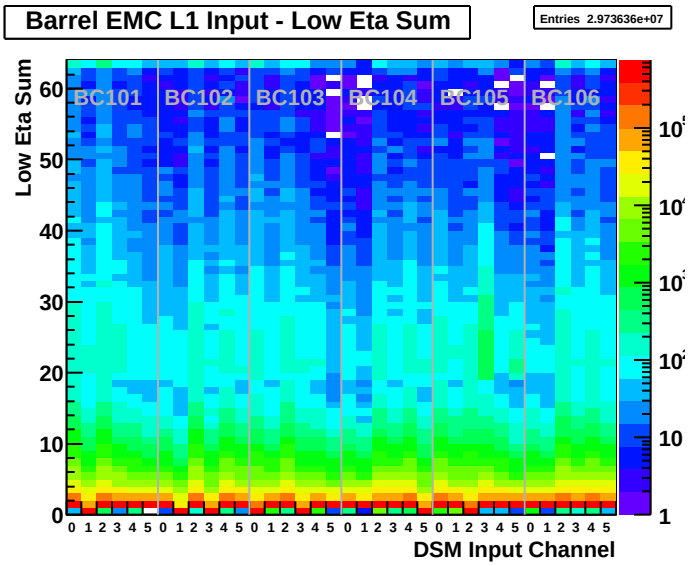
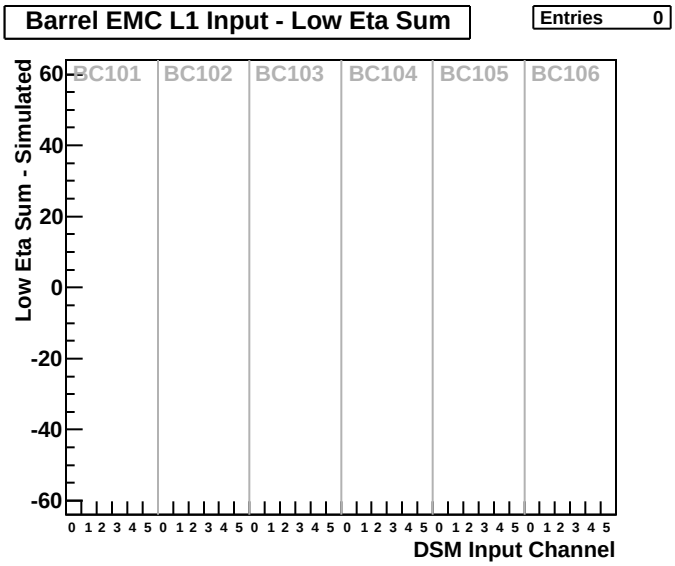
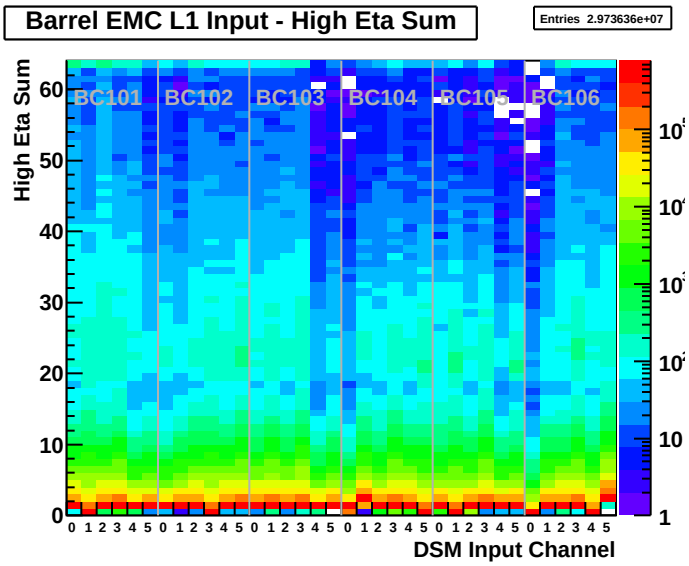
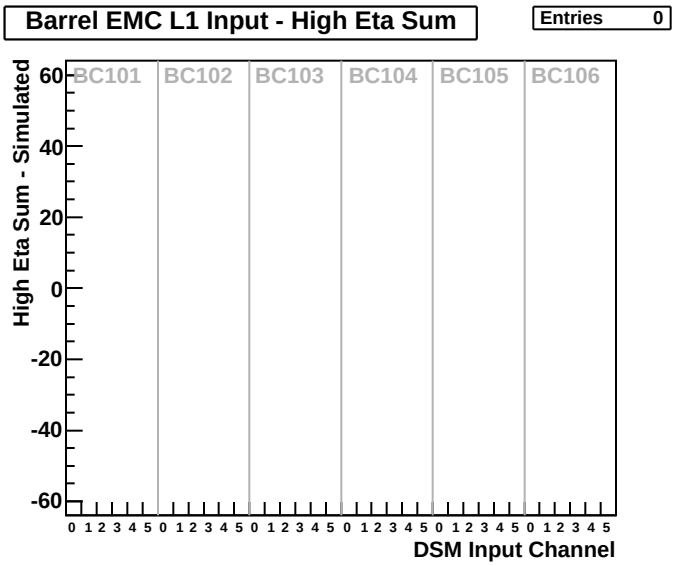
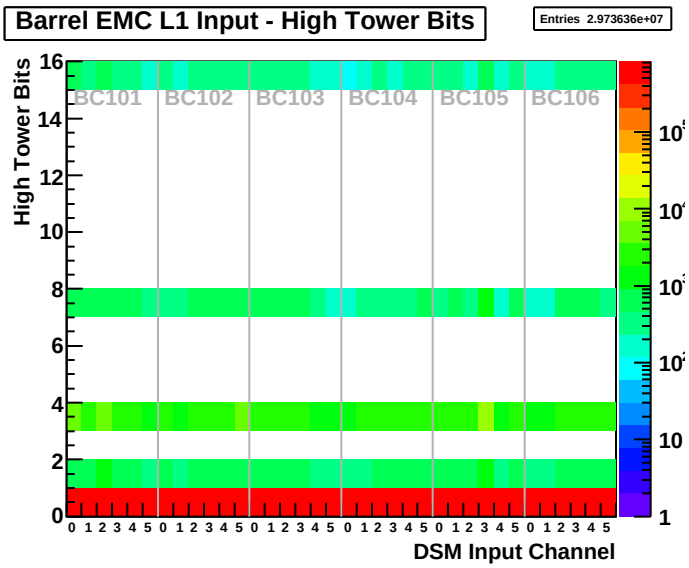
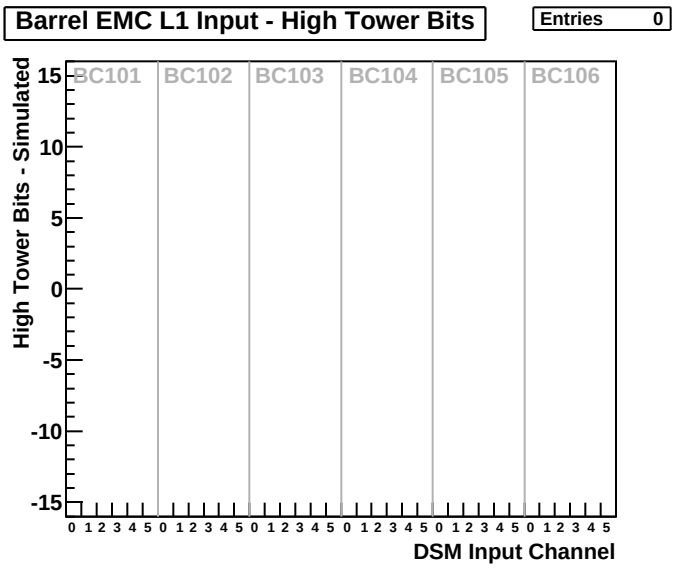

Barrel EMC L1 Input - Low Eta Sum
Entries 2.973636e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

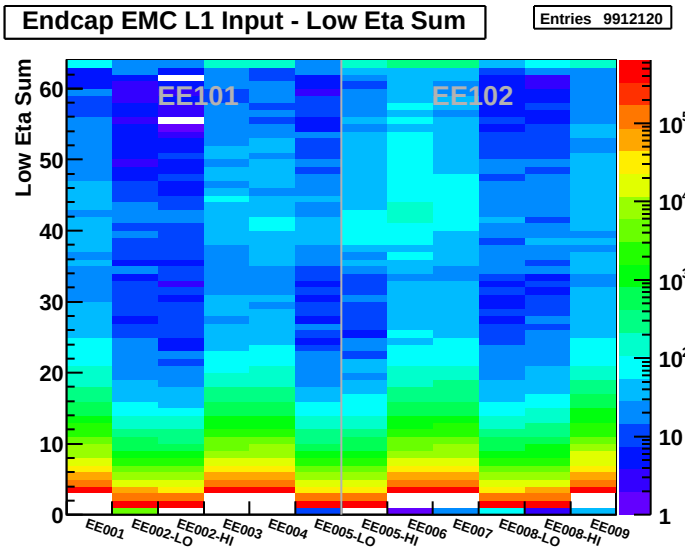

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

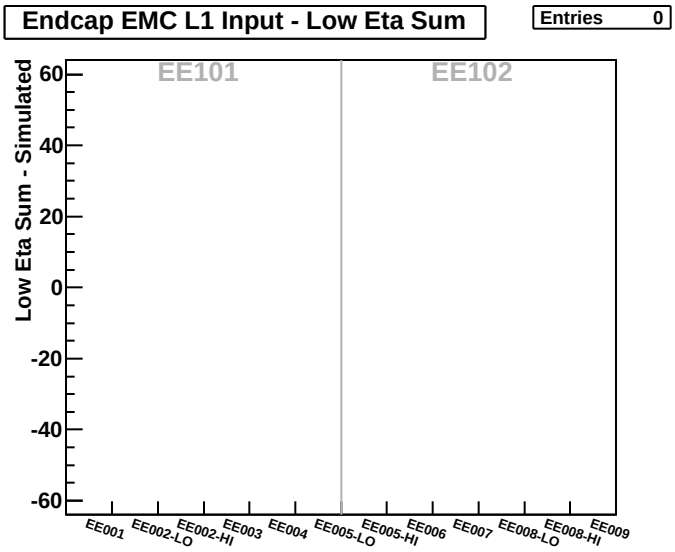

Barrel EMC L1 Input - High Eta Sum
Entries 2.973636e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

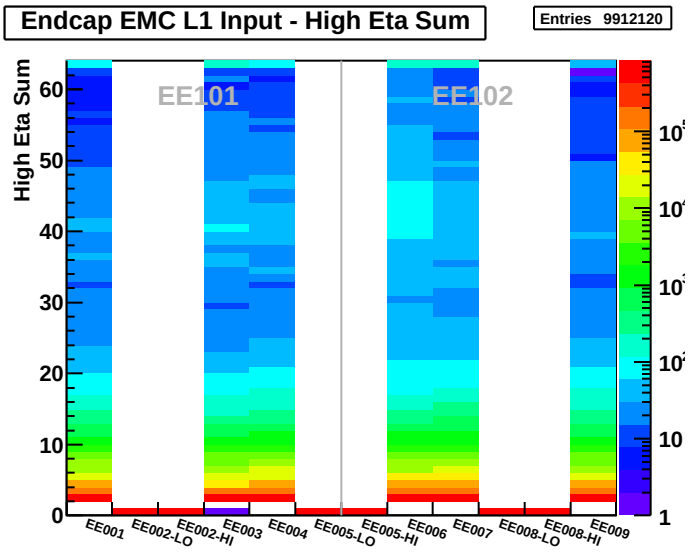

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

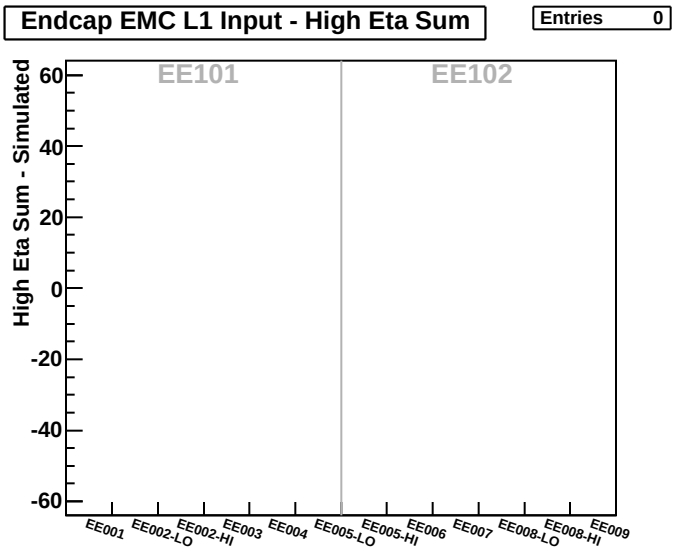

Barrel EMC L1 Input - High Tower Bits
Entries 2.973636e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

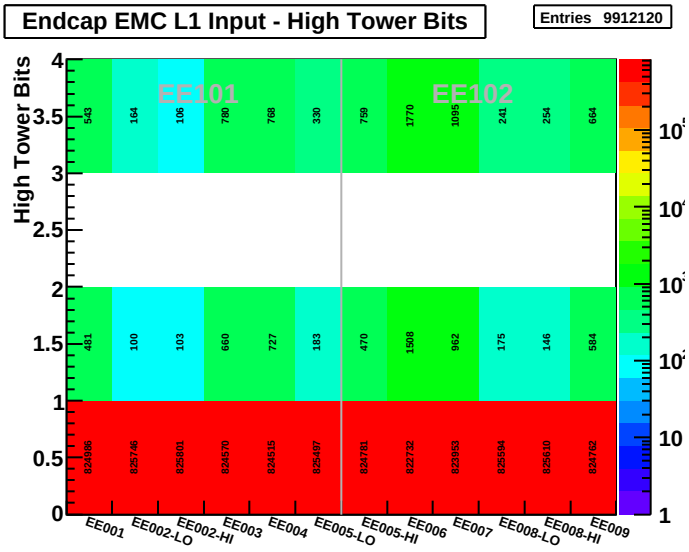

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

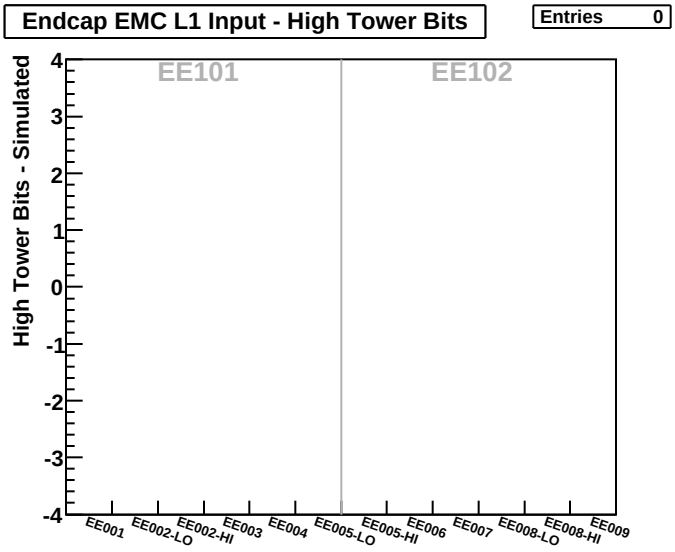

Endcap EMC L1 Input - Low Eta Sum
Entries 9912120
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

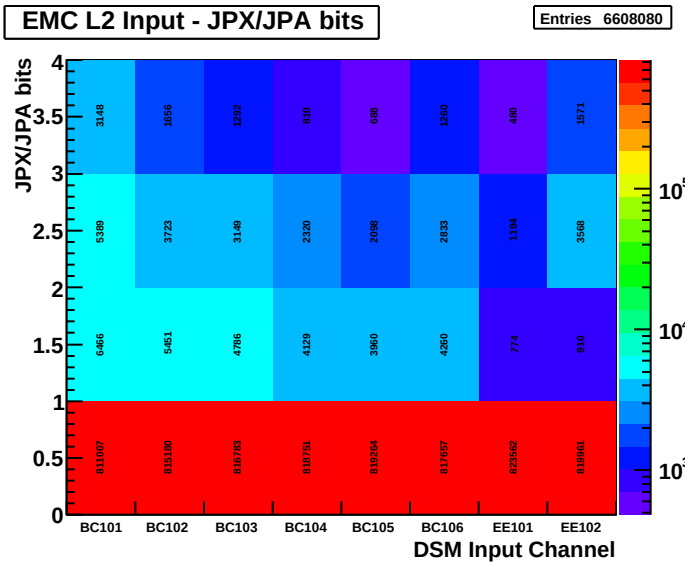

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

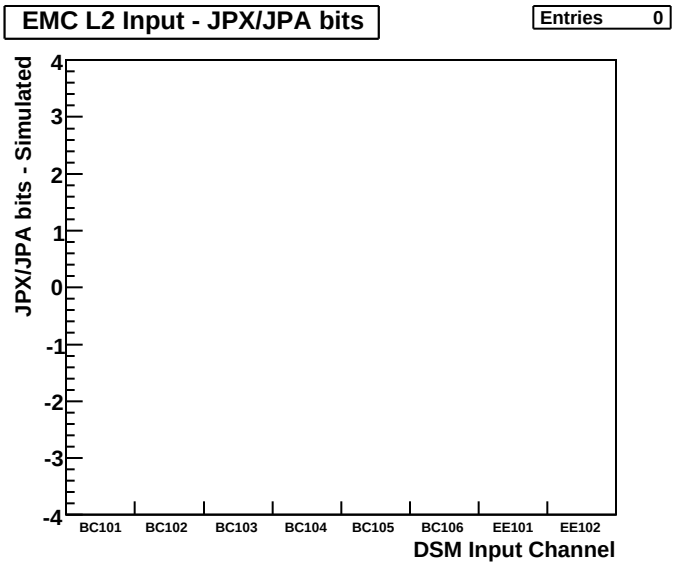

Endcap EMC L1 Input - High Eta Sum
Entries 9912120
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

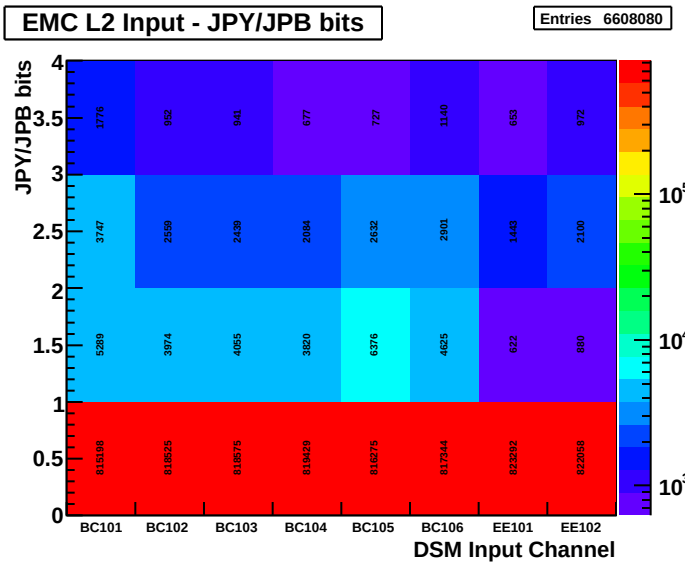

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

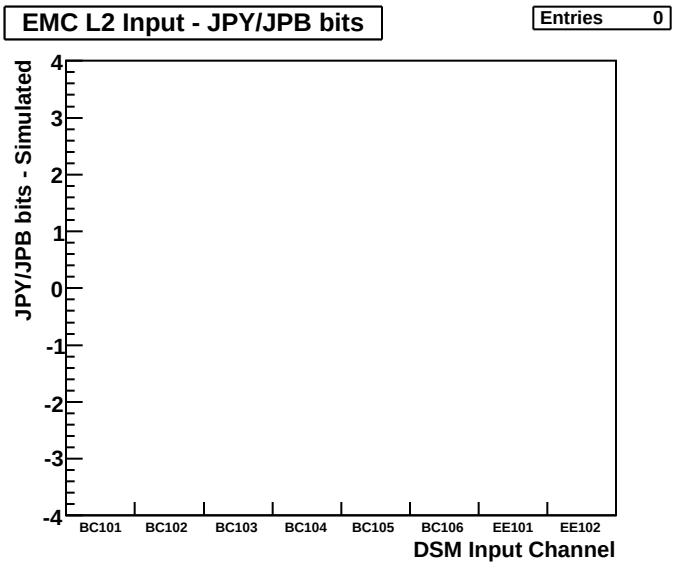

Endcap EMC L1 Input - High Tower Bits
Entries 9912120
High Tower Bits
EE101
EE102
543 164 106 780 768 330 759 1770 1095 241 254 664
481 100 103 660 727 183 470 1508 962 175 146 584
824986 825746 825801 824570 824515 825497 824781 822732 823953 825594 825610 824762
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

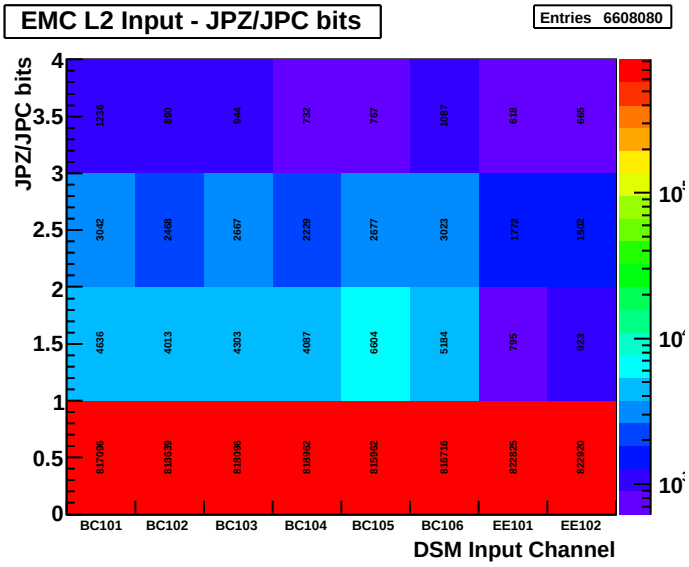

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

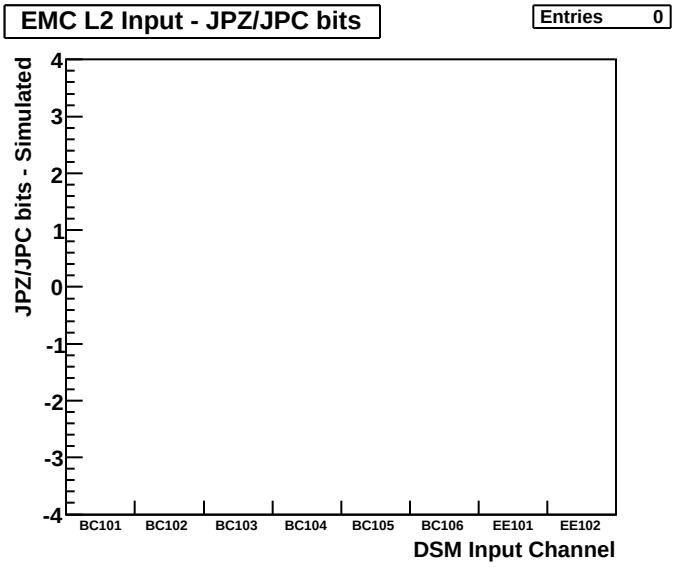

EMC L2 Input - JPX/JPA bits
Entries 6608080
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3148 1656 1292 810 688 1260 490 1571
5389 3723 3149 2320 2098 2833 1194 3568
6466 5451 4786 4129 3960 4260 774 910
811007 815180 818783 818751 819264 817657 823562 819961

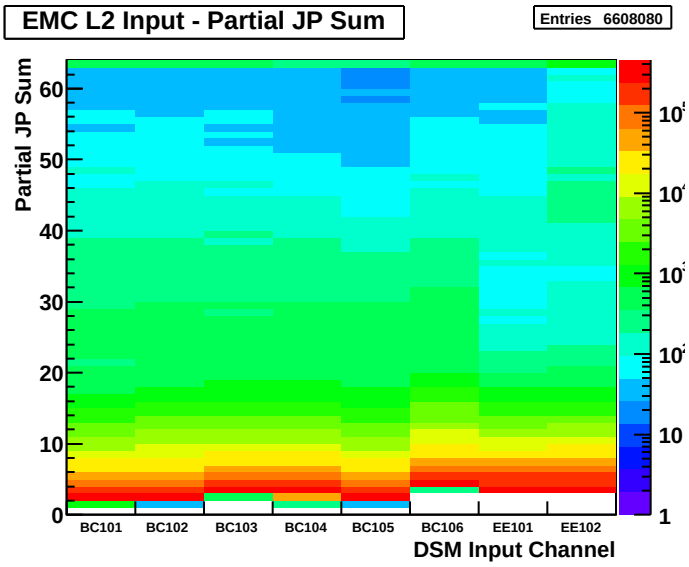

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

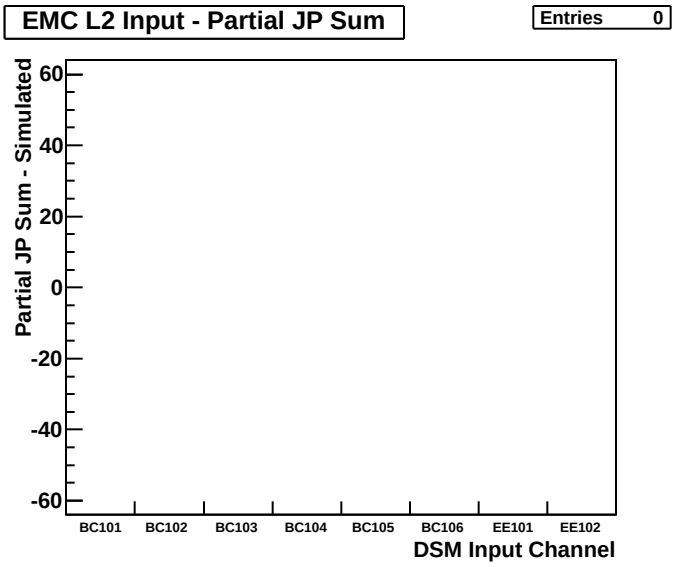

EMC L2 Input - JPY/JPB bits
Entries 6608080
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1776 952 941 677 727 1140 653 972
3747 2559 2439 2084 2632 2901 1443 2100
5289 3974 4055 3820 6376 4625 622 880
815198 818525 818575 819429 818275 817344 823292 822058

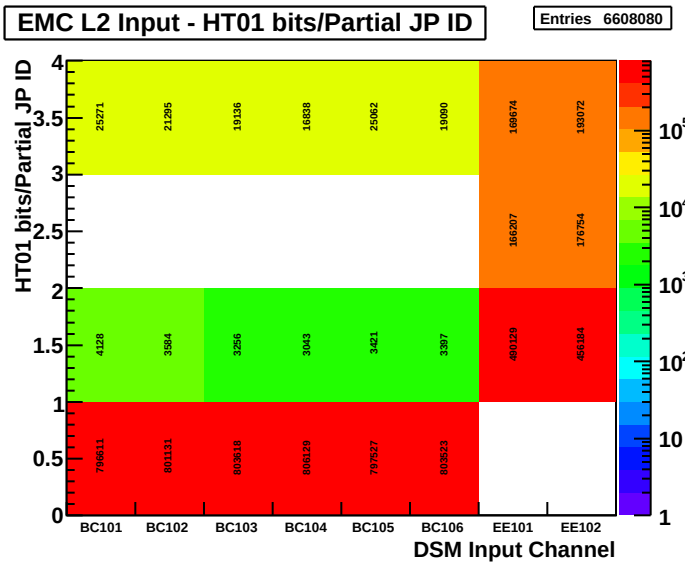

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

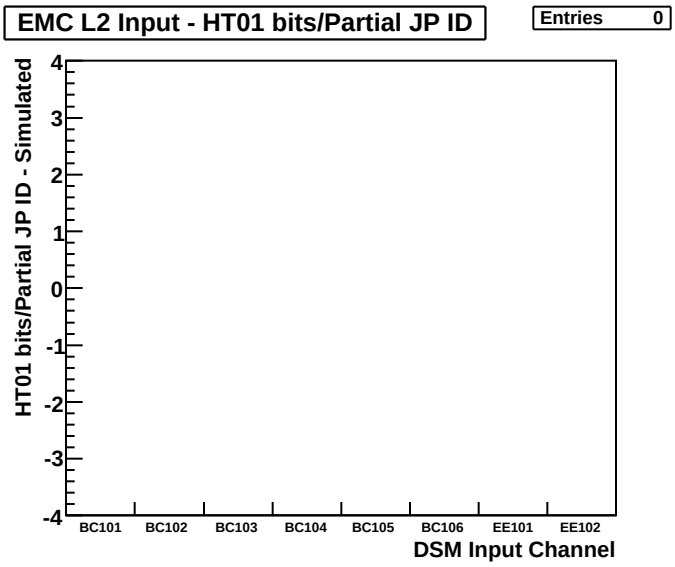

EMC L2 Input - JPZ/JPC bits
Entries 6608080
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1236 830 944 732 767 1087 618 665
3042 2468 2667 2229 2677 3023 1772 1502
4636 4013 4303 4087 6604 5184 795 923
817096 818539 818096 818962 815962 816716 822825 822920

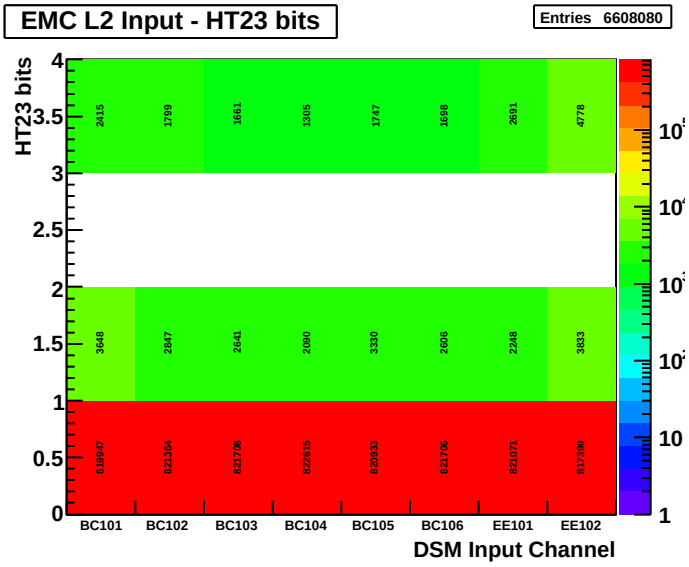

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

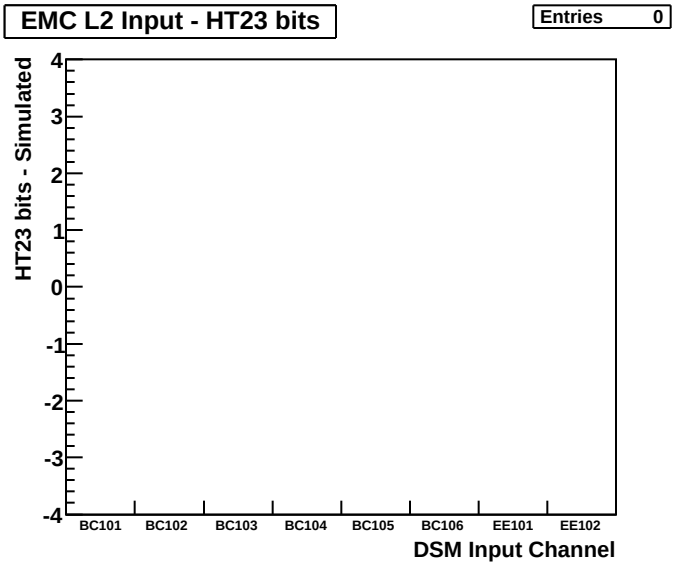

EMC L2 Input - Partial JP Sum
Entries 6608080
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

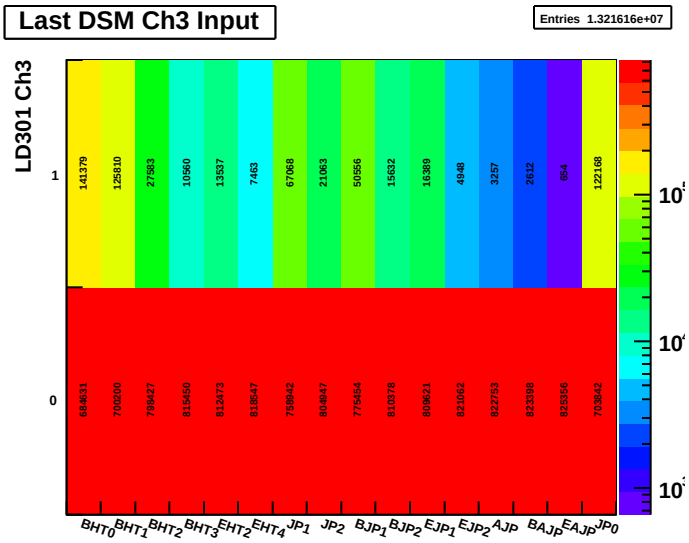

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

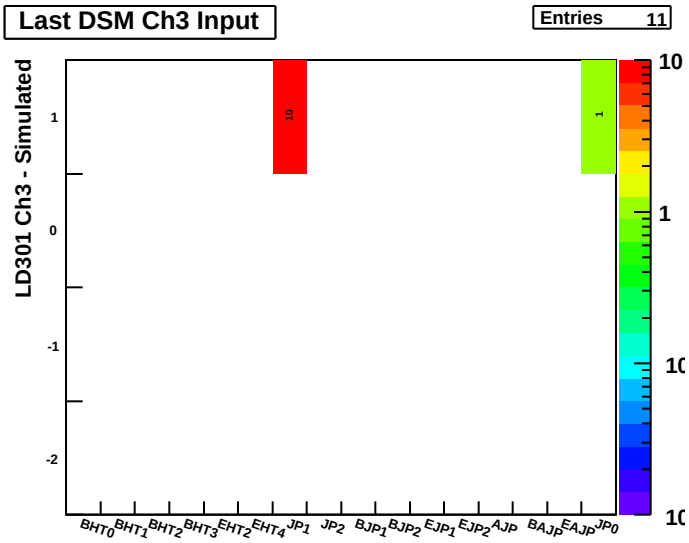

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6608080
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

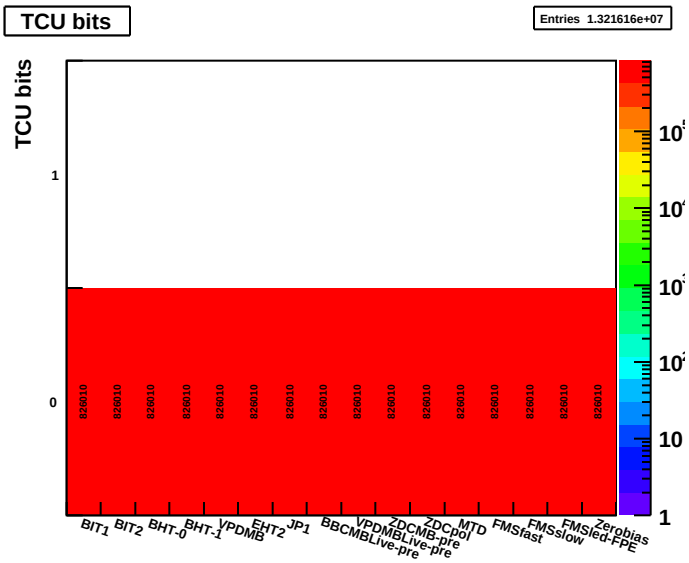

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

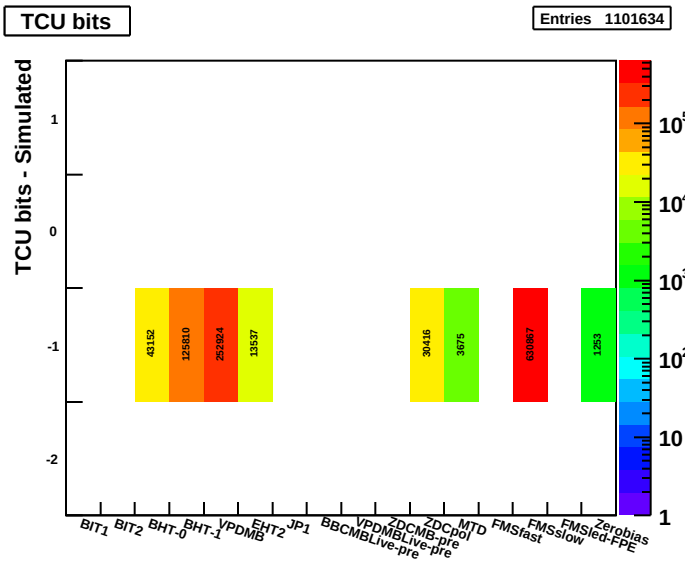

EMC L2 Input - HT23 bits
Entries 6608080
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

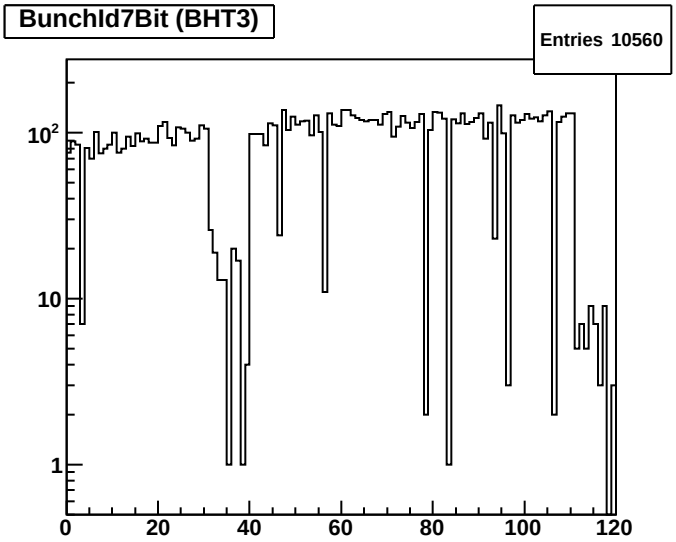

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

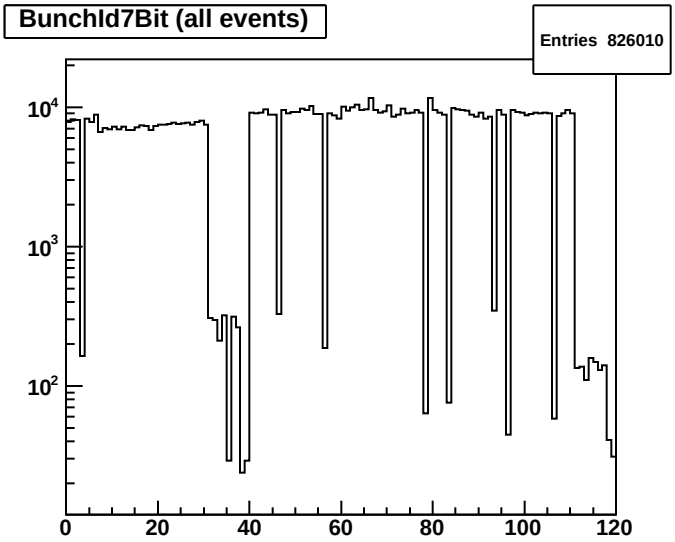

Last DSM Ch3 Input
Entries 1.321616e+07
LD301 Ch3
141379 125810 27593 10560 13537 7463 67068 21063 50556 15632 16389 4948 3257 2612 654 122168
684631 700200 798427 815450 812473 816547 758942 804947 775454 810378 809621 821062 822753 823398 825356 703842
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

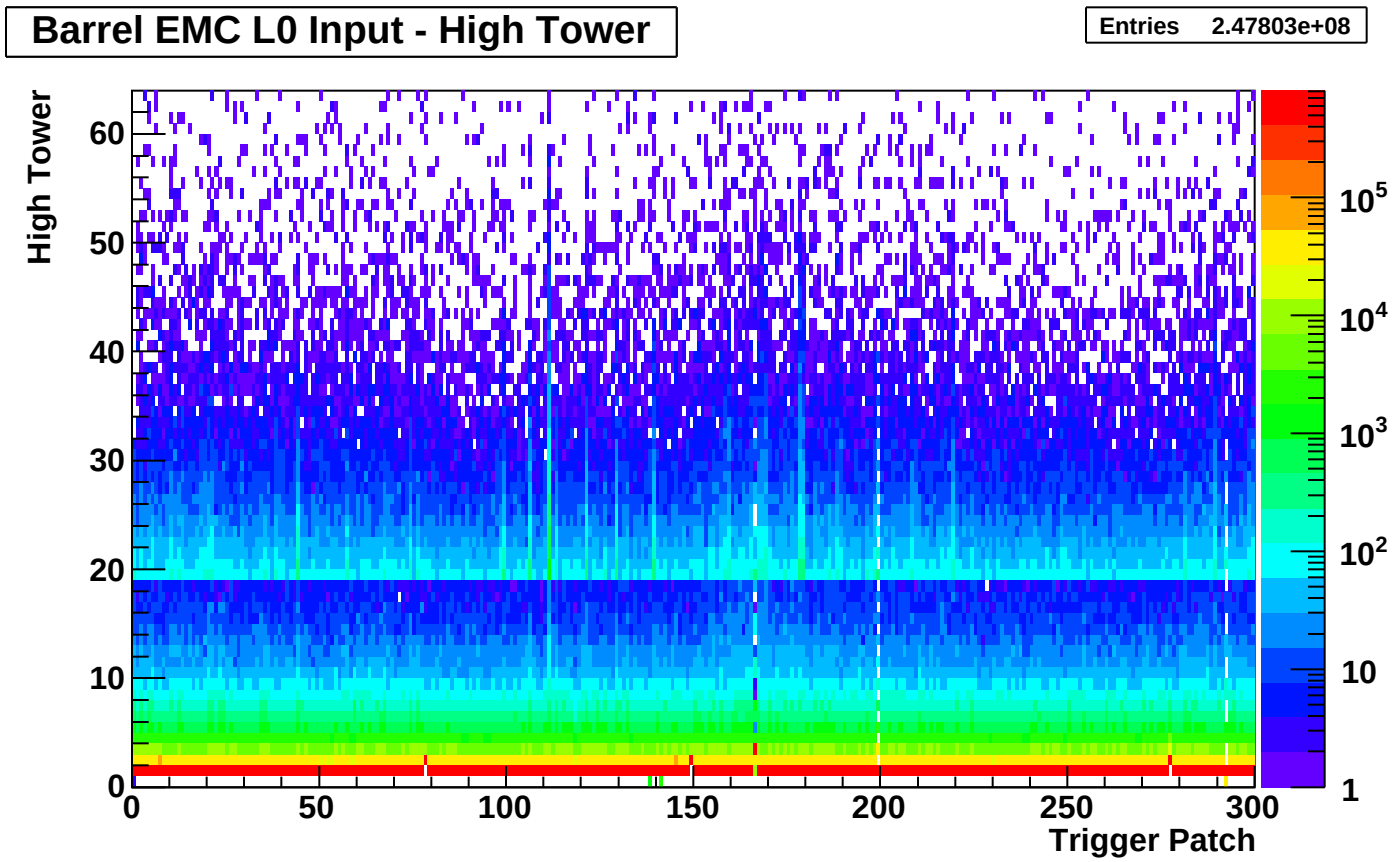

Last DSM Ch3 Input
Entries 11
LD301 Ch3 - Simulated
10
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

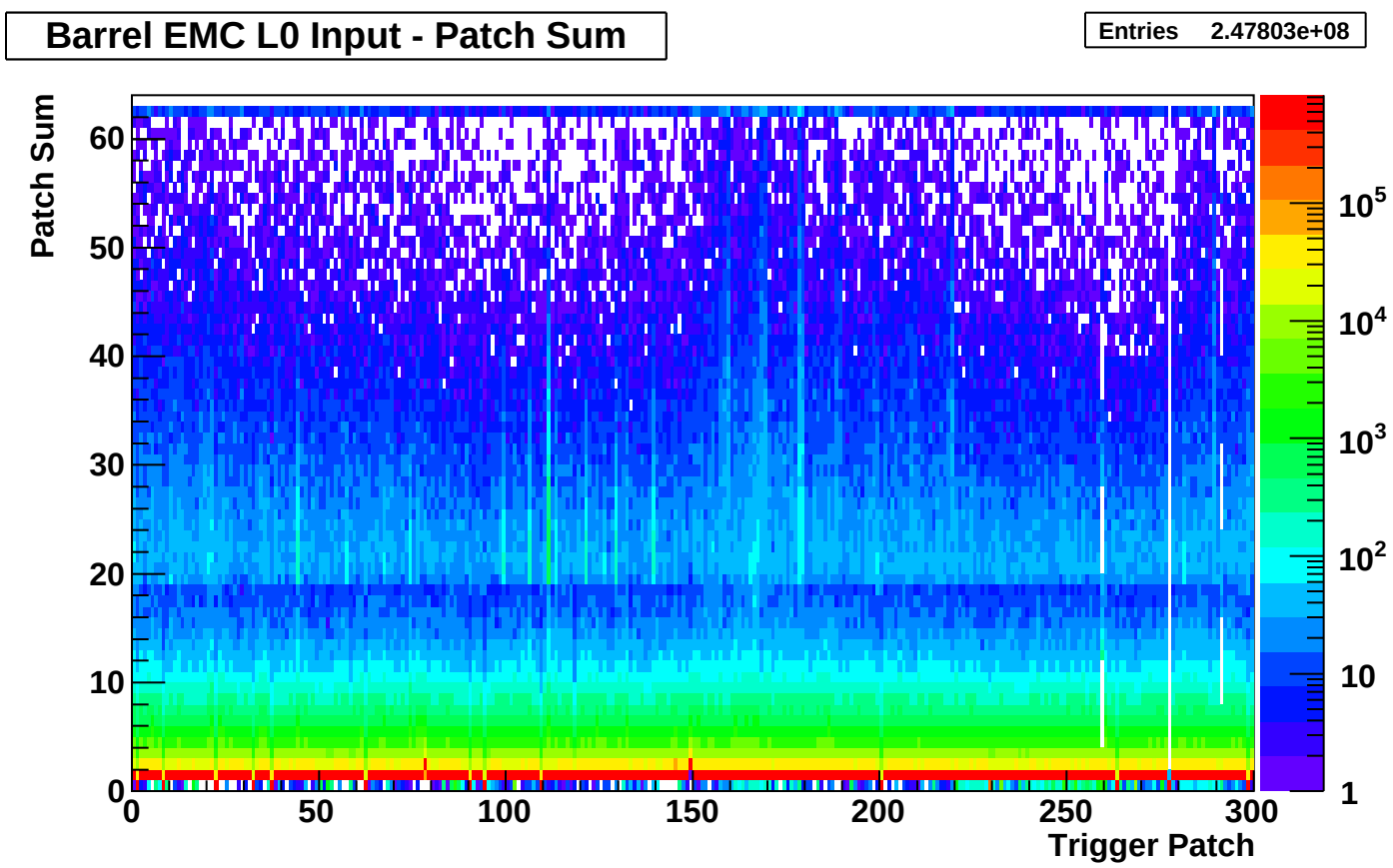

TCU bits
Entries 1.321616e+07
TCU bits
826010
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

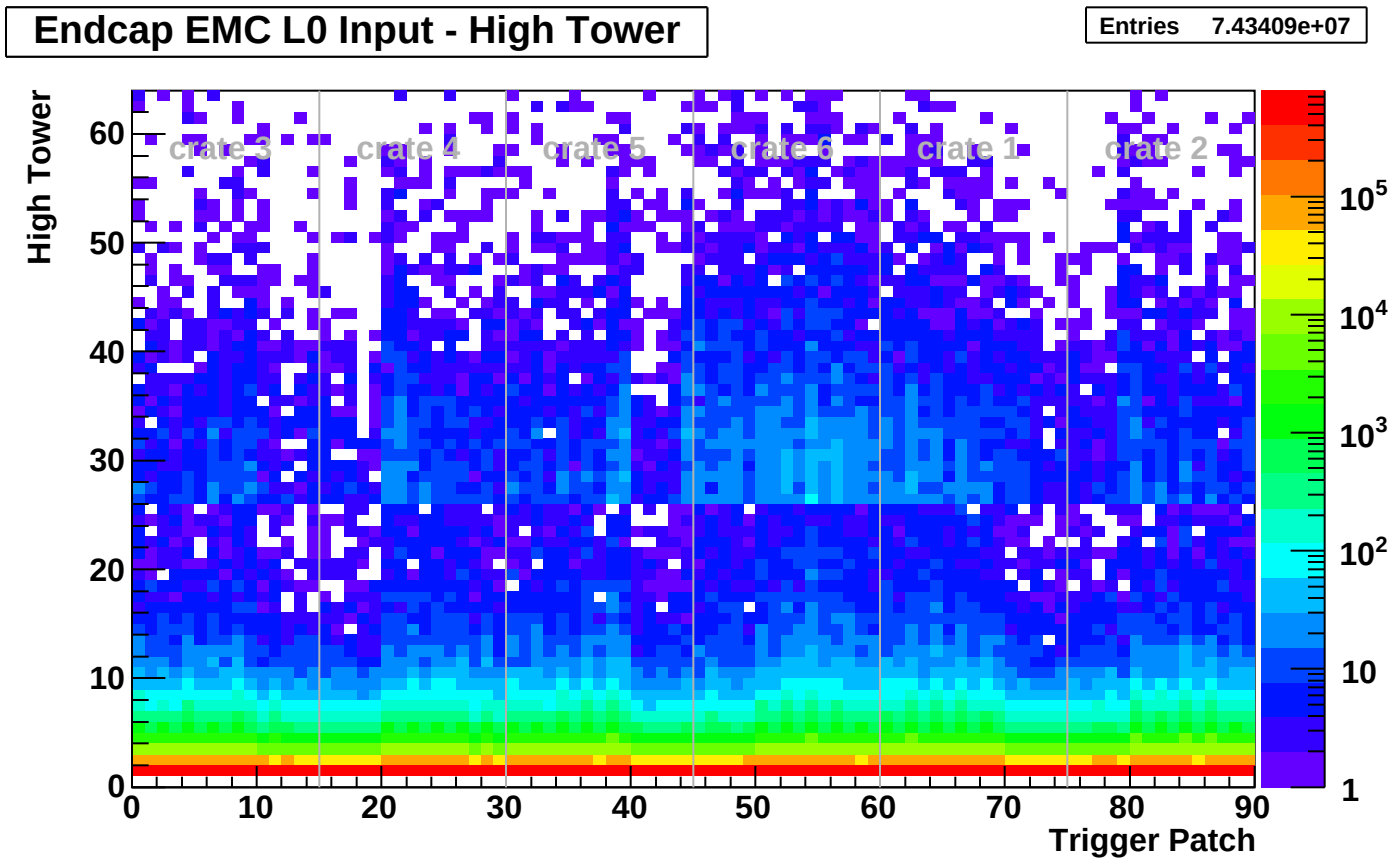

TCU bits
Entries 1101634
TCU bits - Simulated
43152 125810 262924 13537 30416 3675 630867 1253
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

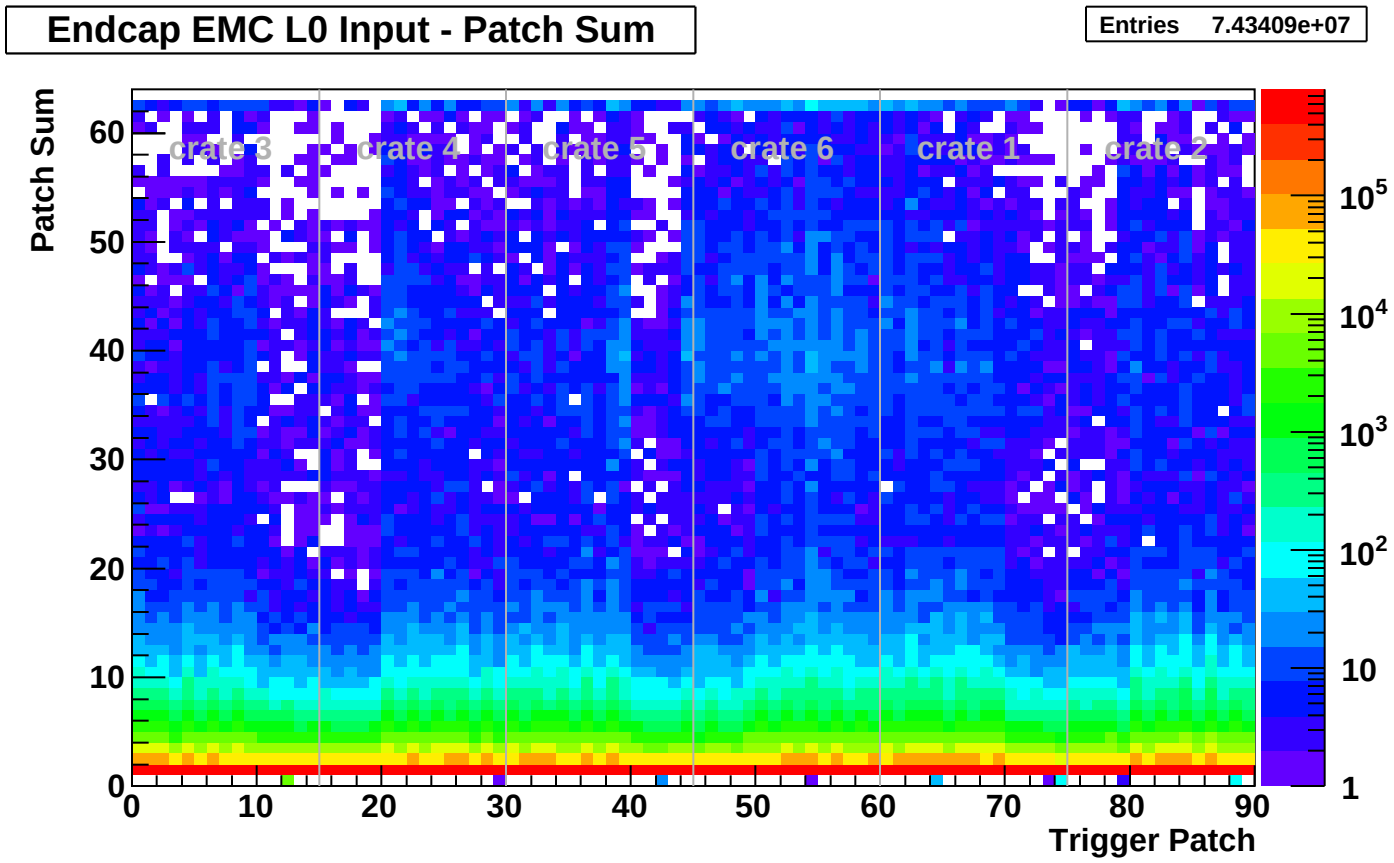

BunchId7Bit (BHT3)
Entries 10560

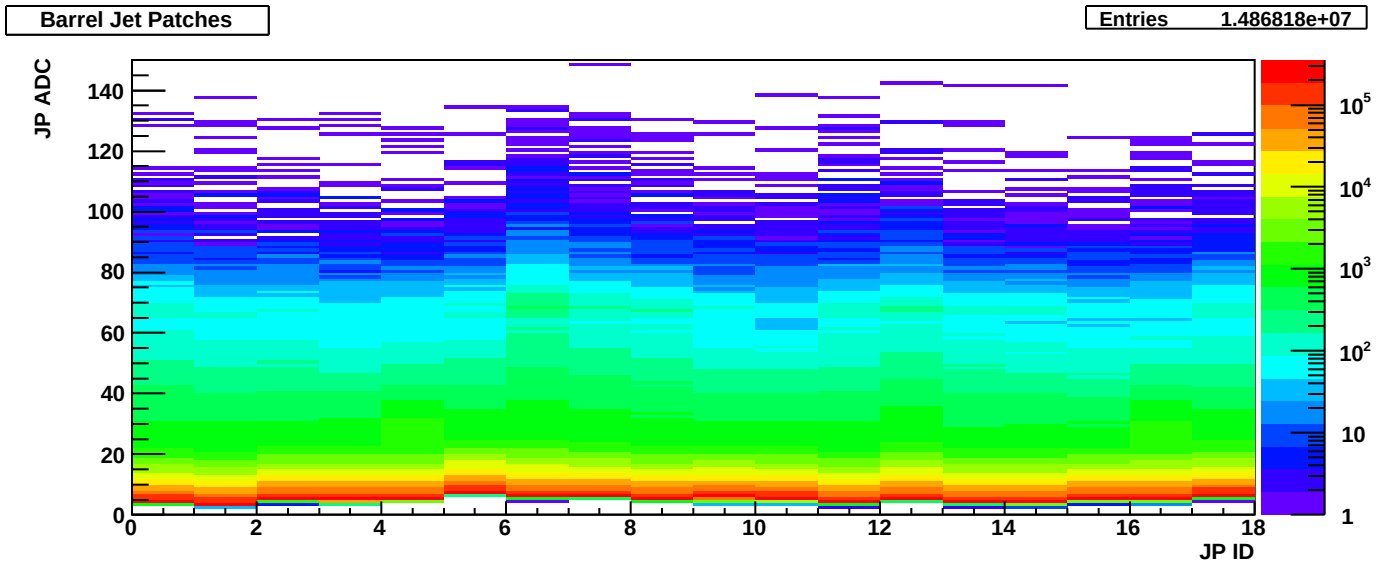

BunchId7Bit (all events)
Entries 826010

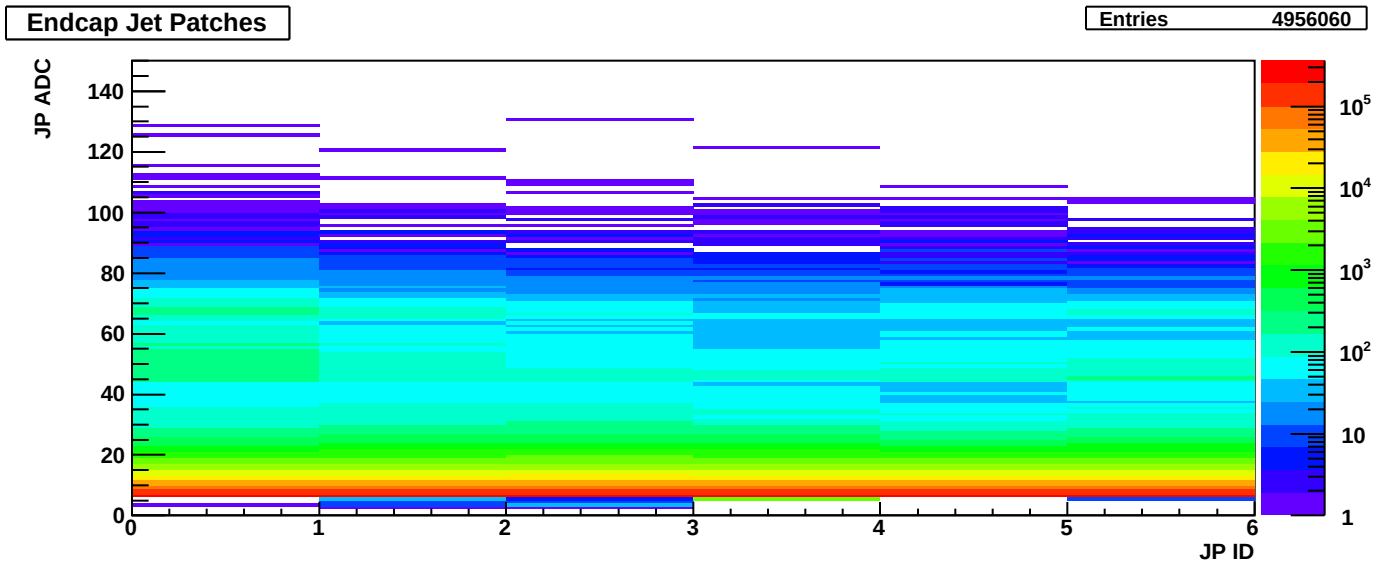

Barrel EMC L0 Input - High Tower
Entries 2.47803e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

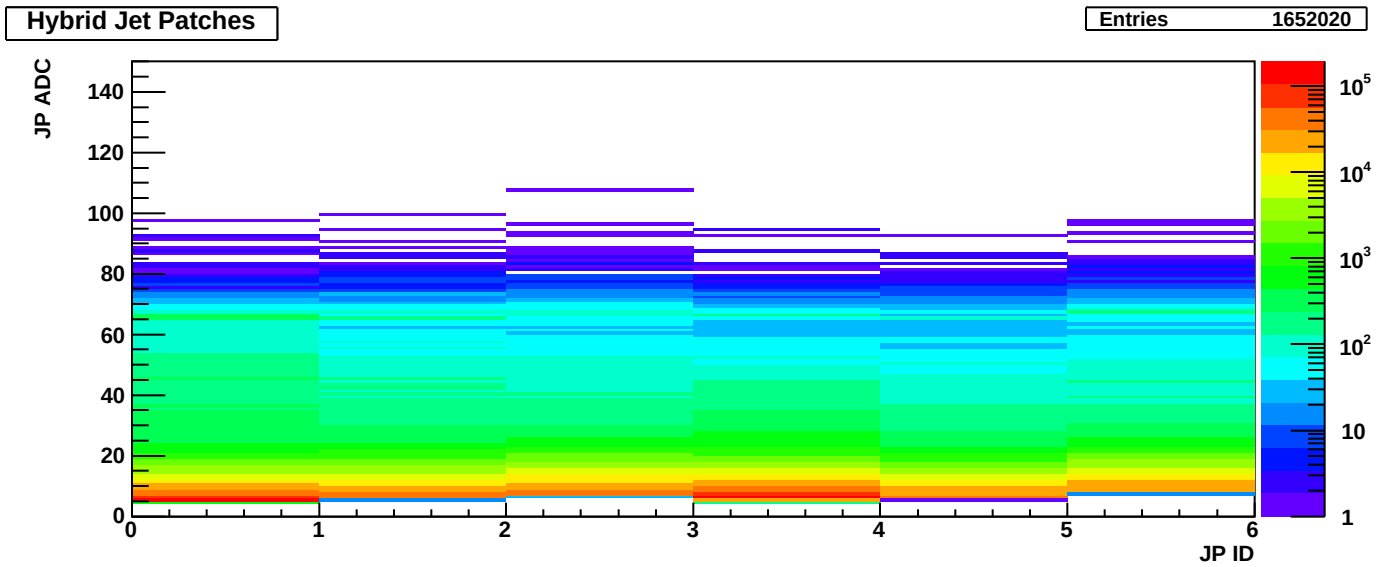

Barrel EMC L0 Input - Patch Sum
Entries 2.47803e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

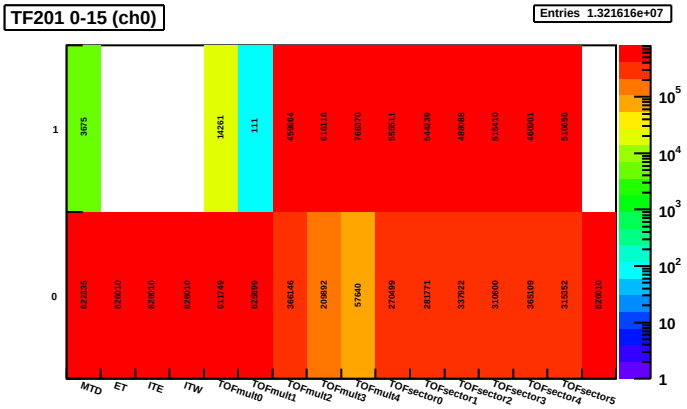

Endcap EMC L0 Input - High Tower
Entries 7.43409e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

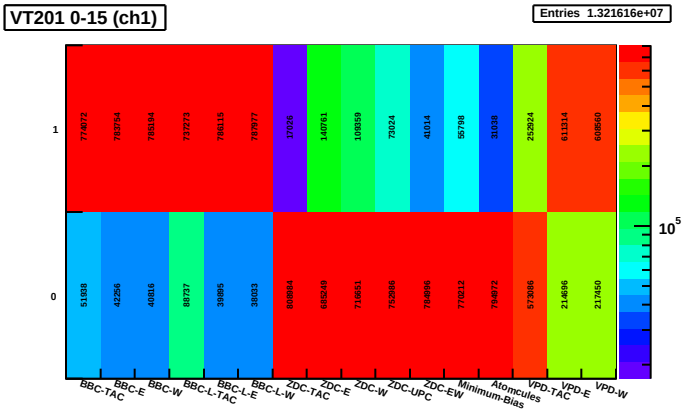

Endcap EMC L0 Input - Patch Sum
Entries 7.43409e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

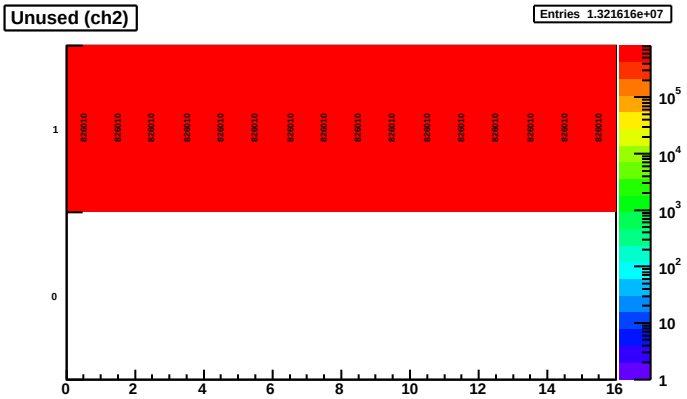

Barrel Jet Patches
Entries 1.486818e+07
JP ADC
JP ID

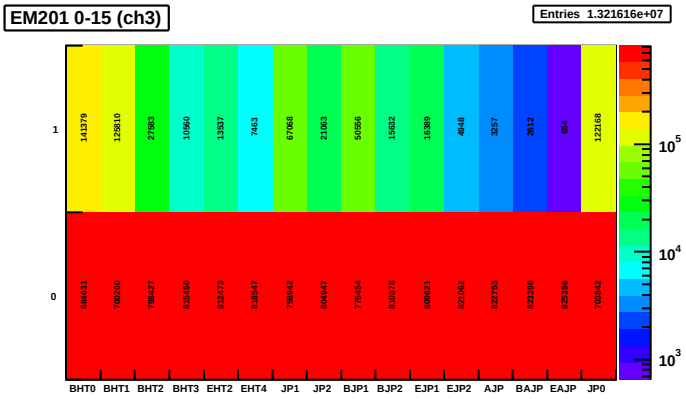

Endcap Jet Patches
Entries 4956060
JP ADC
JP ID

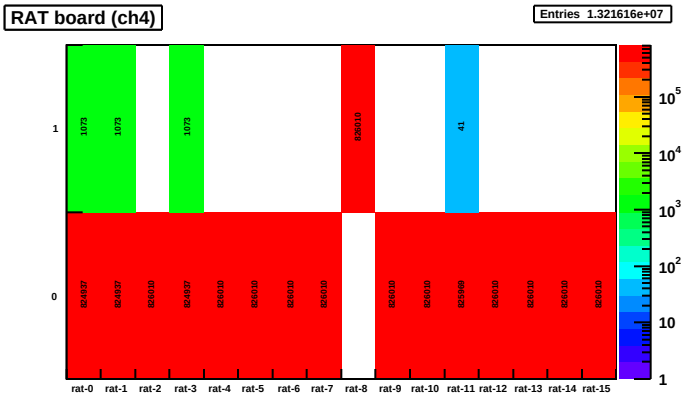

Hybrid Jet Patches
Entries 1652020
JP ADC
JP ID

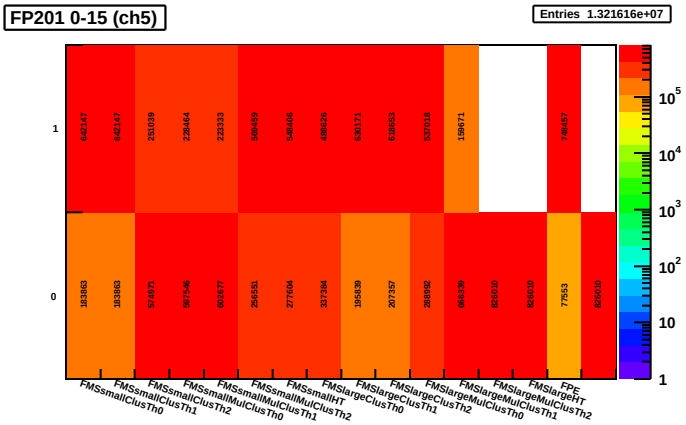

TF201 0-15 (ch0)
Entries 1.321616e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

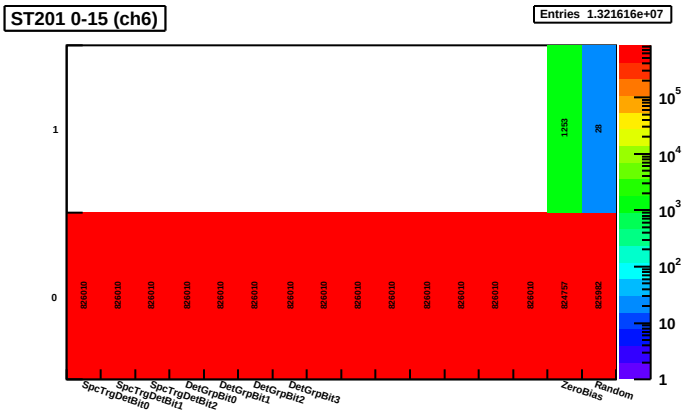

VT201 0-15 (ch1)
Entries 1.321616e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

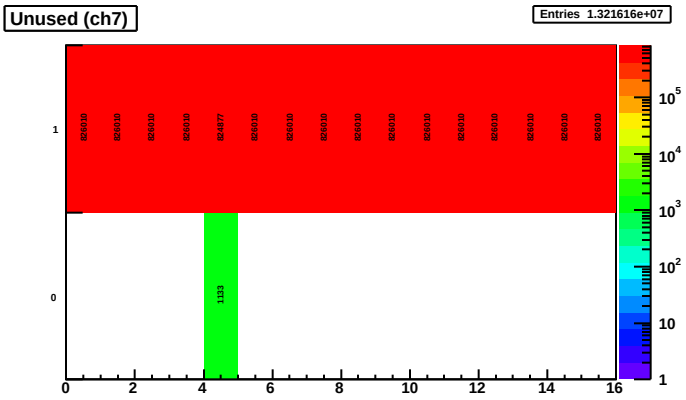

Unused (ch2)
Entries 1.321616e+07

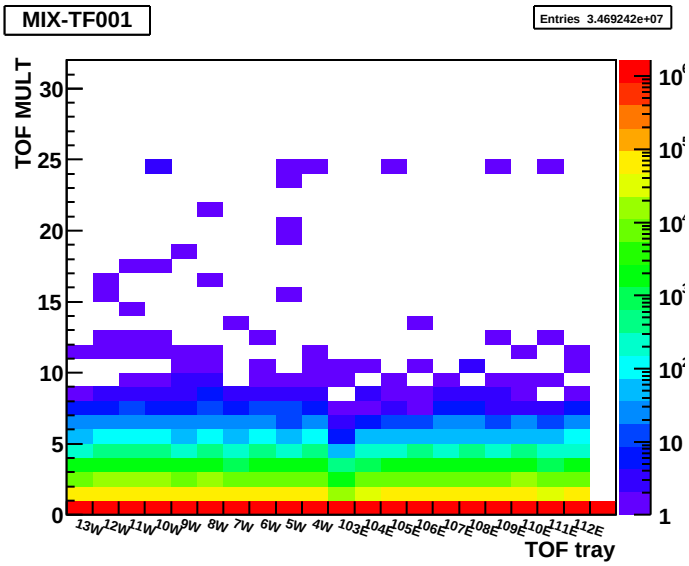

EM201 0-15 (ch3)
Entries 1.321616e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

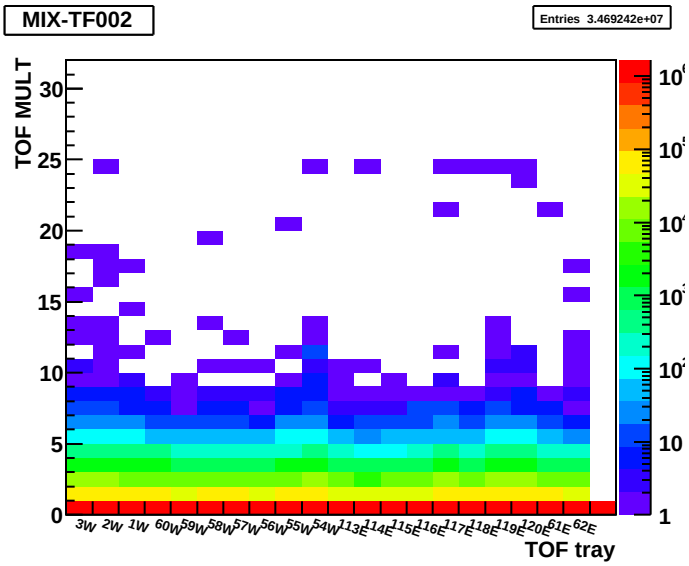

RAT board (ch4)
Entries 1.321616e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

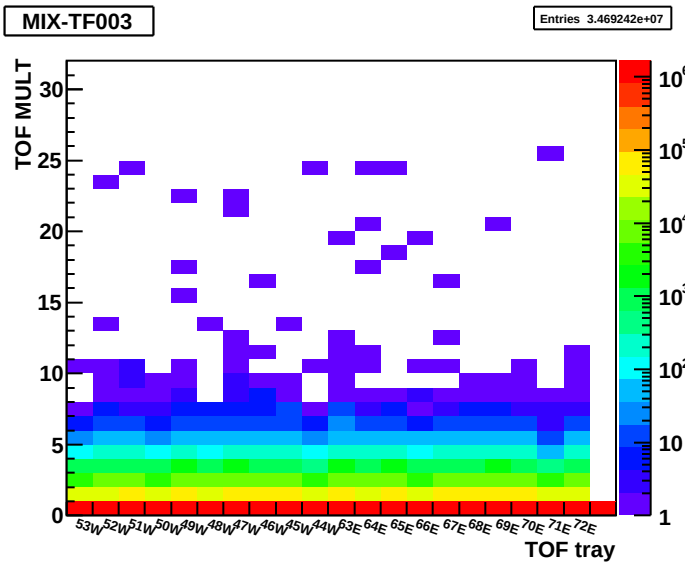

FP201 0-15 (ch5)
Entries 1.321616e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

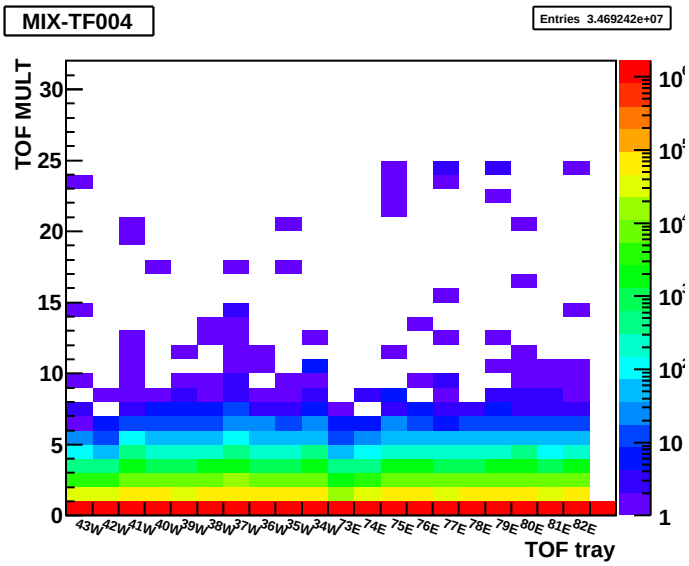

ST201 0-15 (ch6)
Entries 1.321616e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

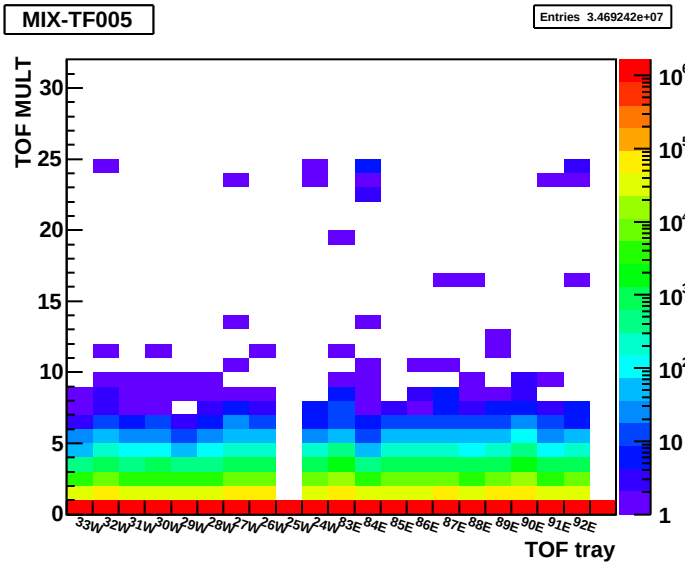

Unused (ch7)
Entries 1.321616e+07

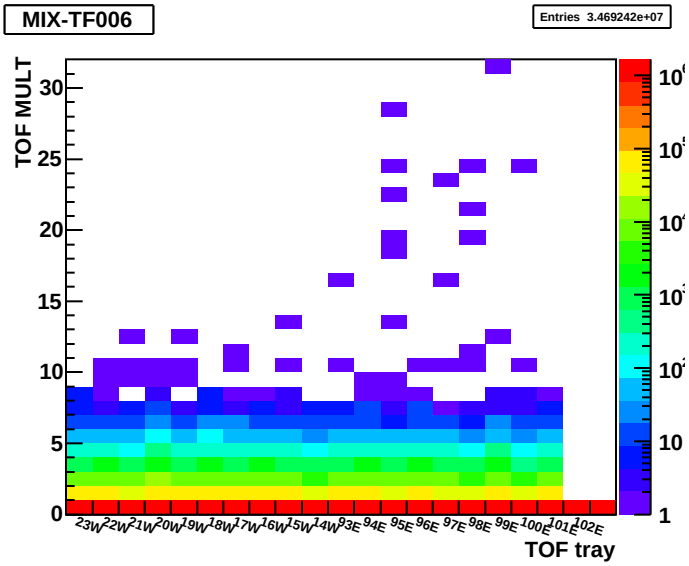

MIX-TF001
Entries 3.469242e+07
TOF MULT
TOF tray

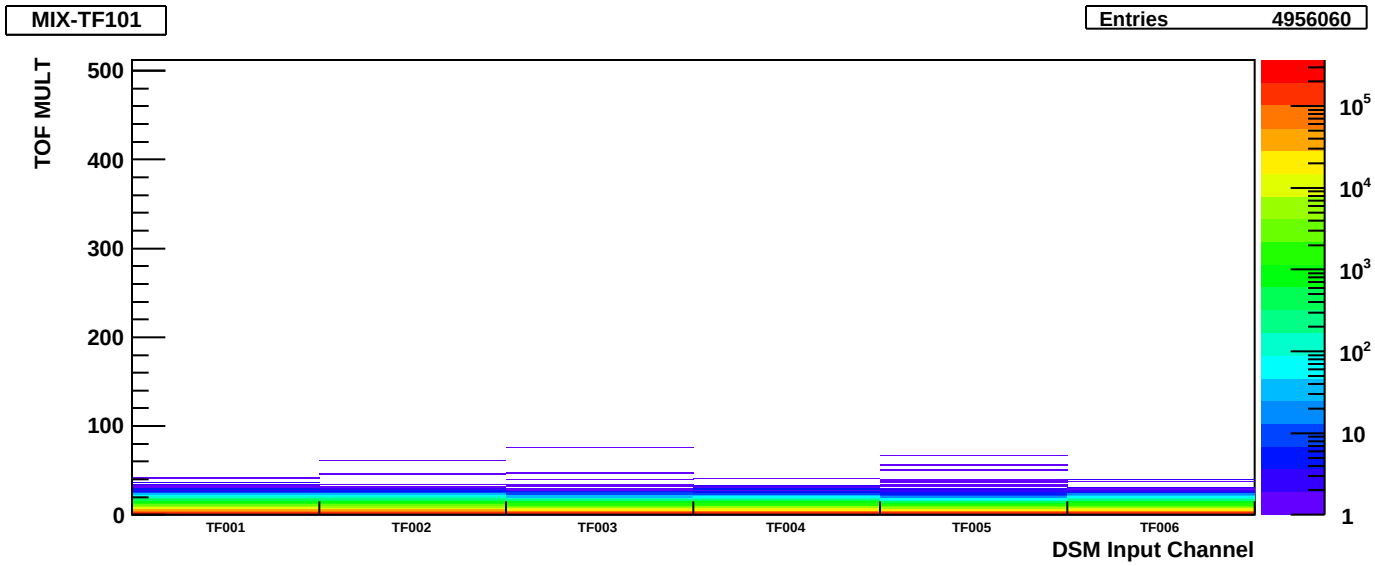

MIX-TF002
Entries 3.469242e+07
TOF MULT
TOF tray

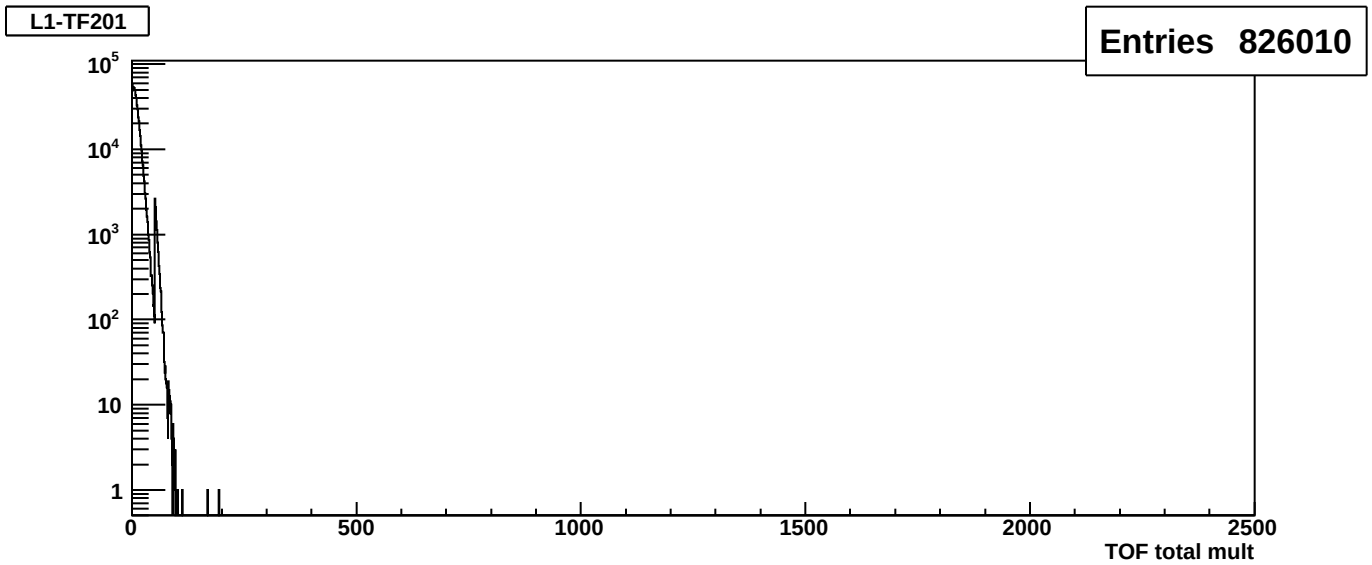

MIX-TF003
Entries 3.469242e+07
TOF MULT
TOF tray

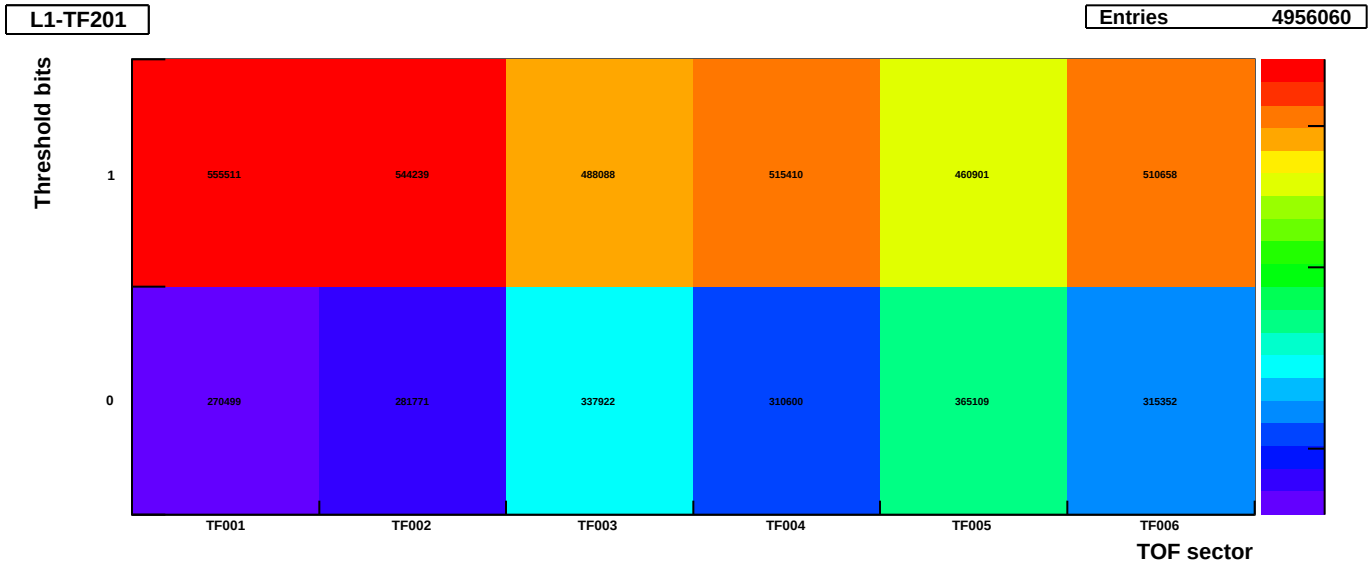

MIX-TF004
Entries 3.469242e+07
TOF MULT
TOF tray

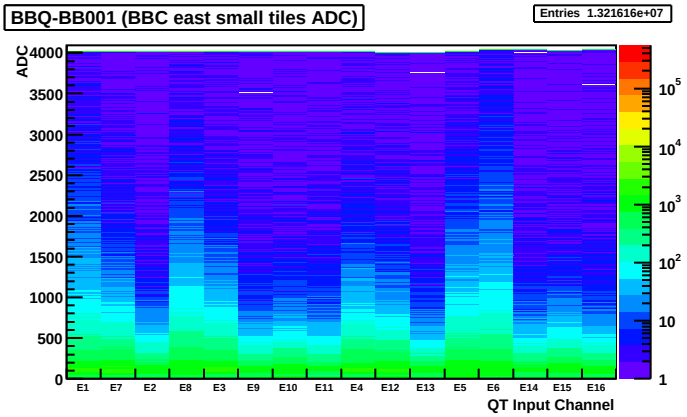

MIX-TF005
Entries 3.469242e+07
TOF MULT
TOF tray

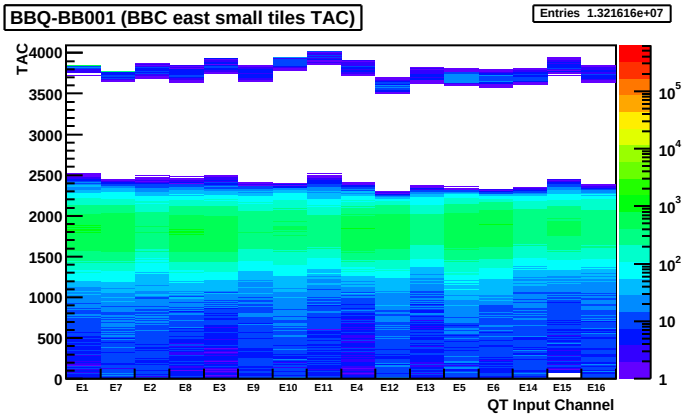

MIX-TF006
Entries 3.469242e+07
TOF MULT
TOF tray

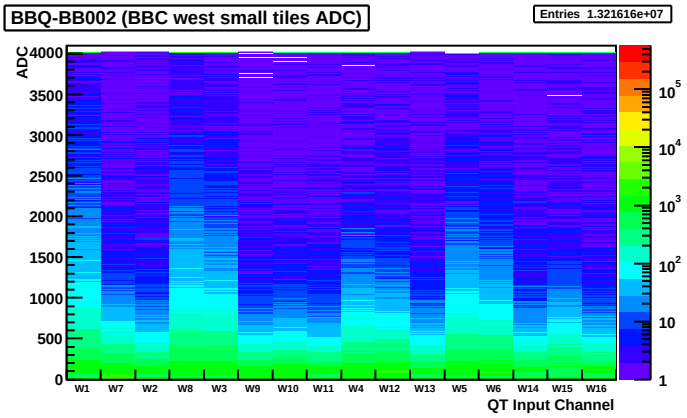

MIX-TF101
Entries 4956060
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

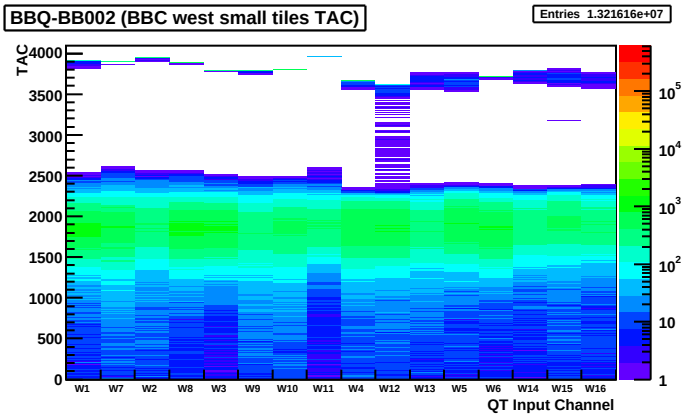

L1-TF201
Entries 826010
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

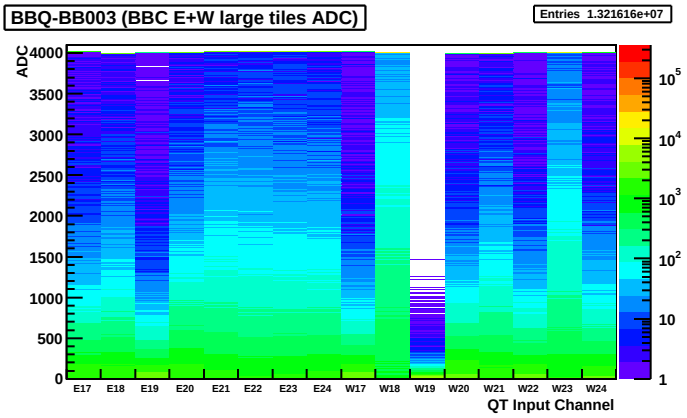

L1-TF201
Entries 4956060
Threshold bits
1
0
555511
544239
488088
515410
460901
510658
270499
281771
337922
310600
365109
315352
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

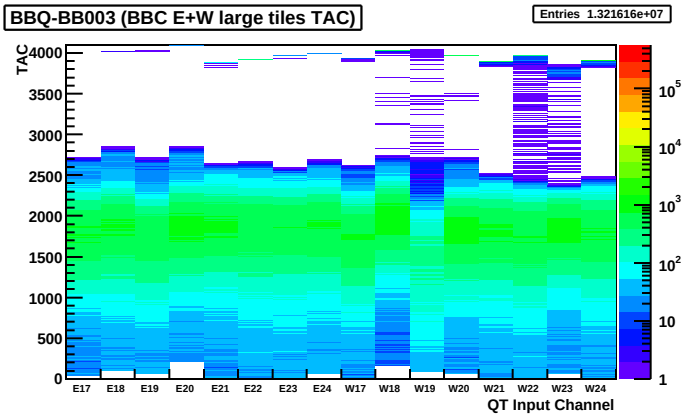

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.321616e+07
ADC
QT Input Channel

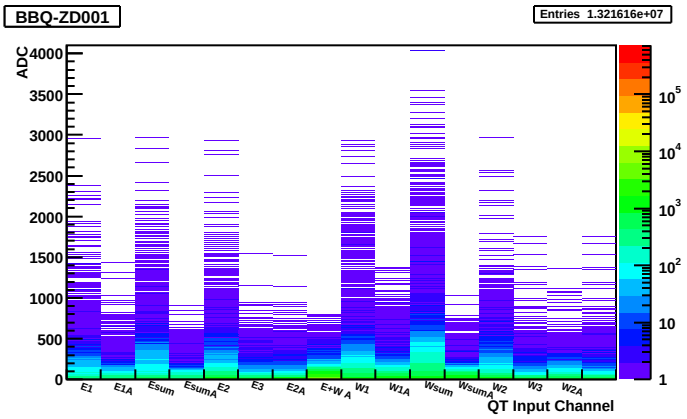

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.321616e+07
TAC
QT Input Channel

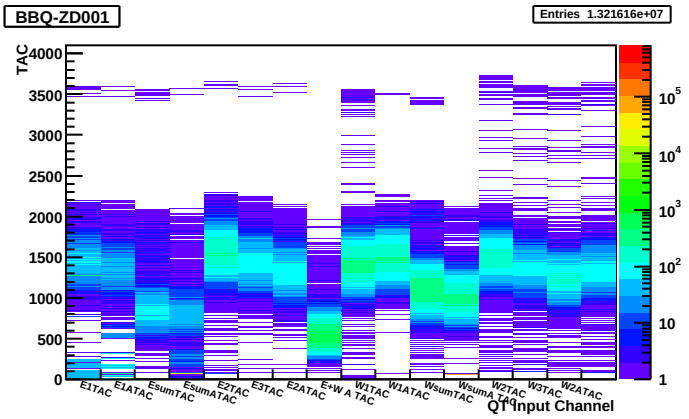

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.321616e+07
ADC
QT Input Channel

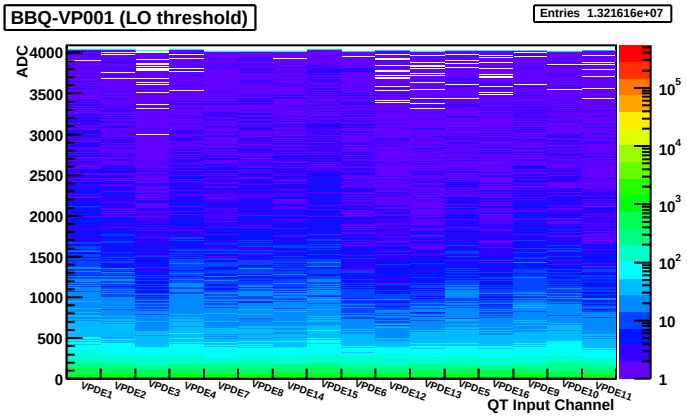

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.321616e+07
TAC
QT Input Channel

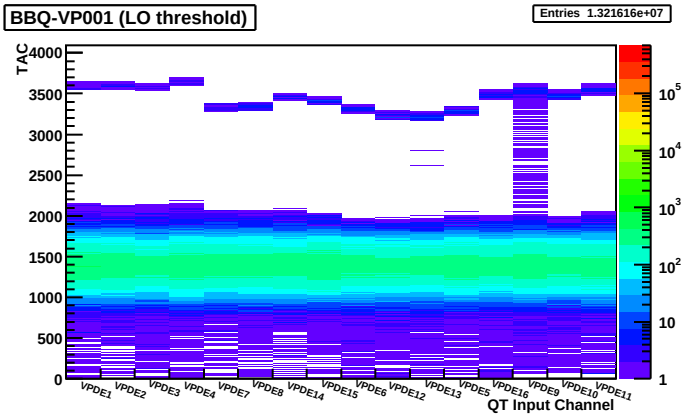

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.321616e+07
ADC
QT Input Channel

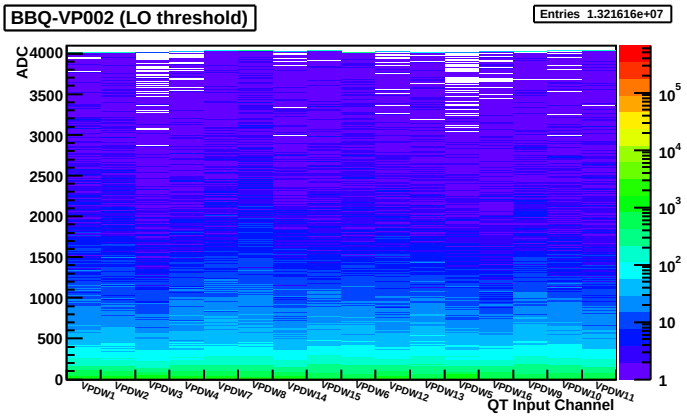

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.321616e+07
TAC
QT Input Channel

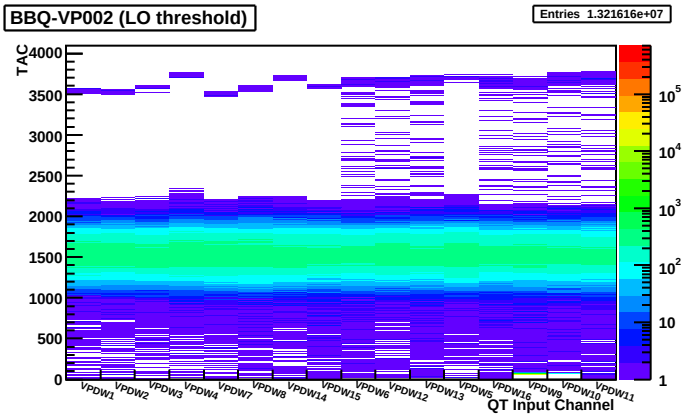

BBQ-ZD001
Entries 1.321616e+07
ADC
QT Input Channel

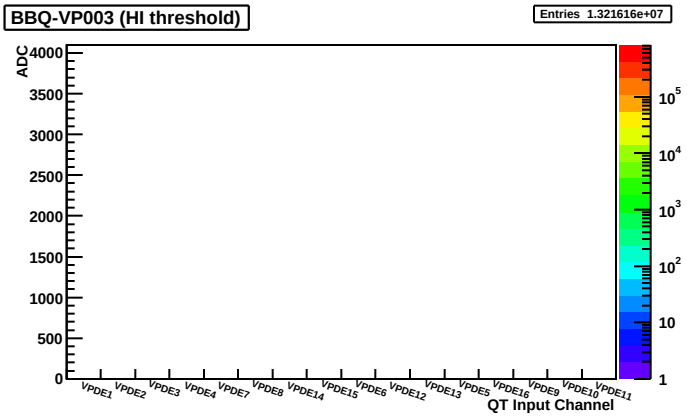

BBQ-ZD001
Entries 1.321616e+07
TAC
QT Input Channel

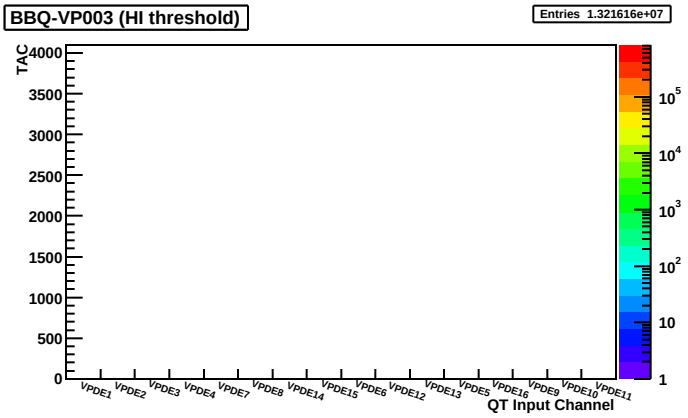

BBQ-VP001 (LO threshold)
Entries 1.321616e+07
ADC
QT Input Channel

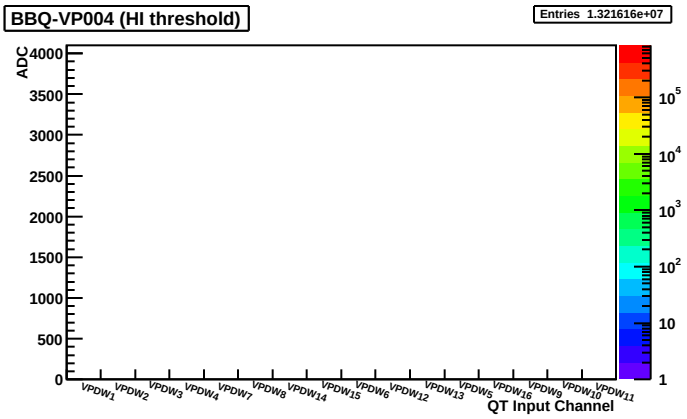

BBQ-VP001 (LO threshold)
Entries 1.321616e+07
TAC
QT Input Channel

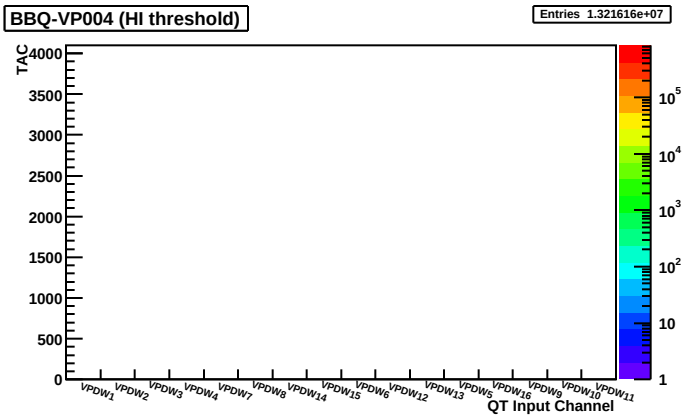

BBQ-VP002 (LO threshold)
Entries 1.321616e+07
ADC
QT Input Channel

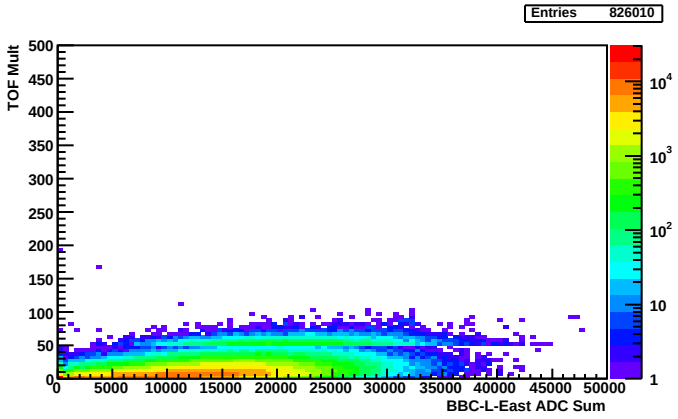

BBQ-VP002 (LO threshold)
Entries 1.321616e+07
TAC
QT Input Channel

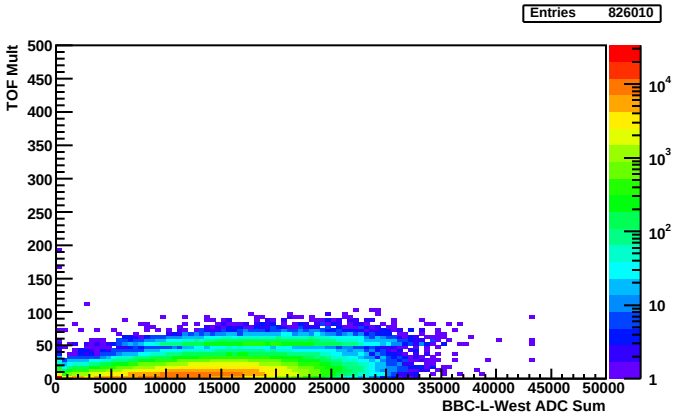

BBQ-VP003 (HI threshold)
Entries 1.321616e+07
ADC
QT Input Channel

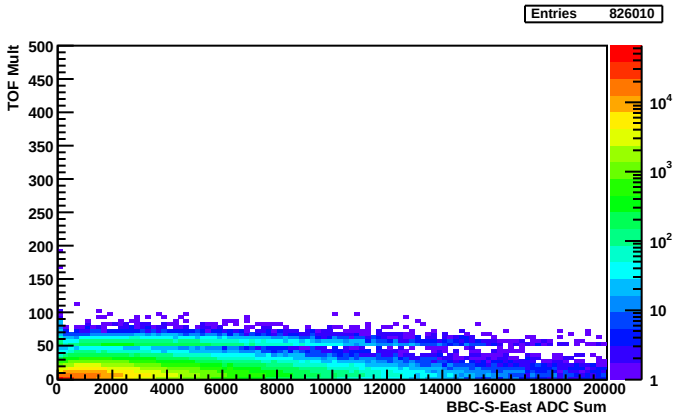

BBQ-VP003 (HI threshold)
Entries 1.321616e+07
TAC
QT Input Channel

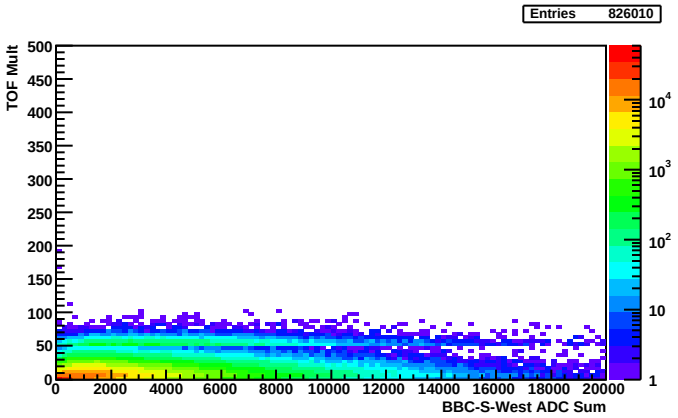

BBQ-VP004 (HI threshold)
Entries 1.321616e+07
ADC
QT Input Channel

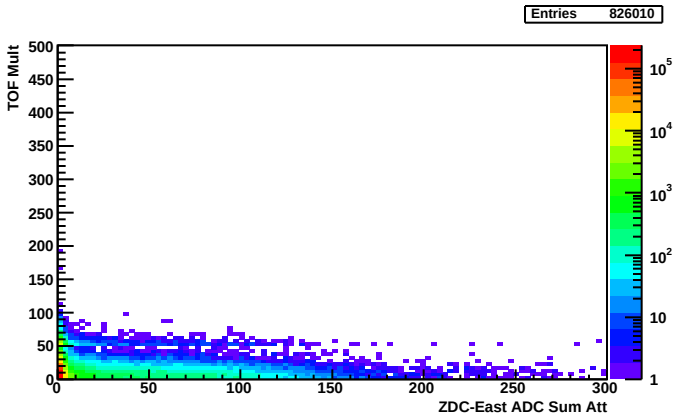

BBQ-VP004 (HI threshold)
Entries 1.321616e+07
TAC
QT Input Channel

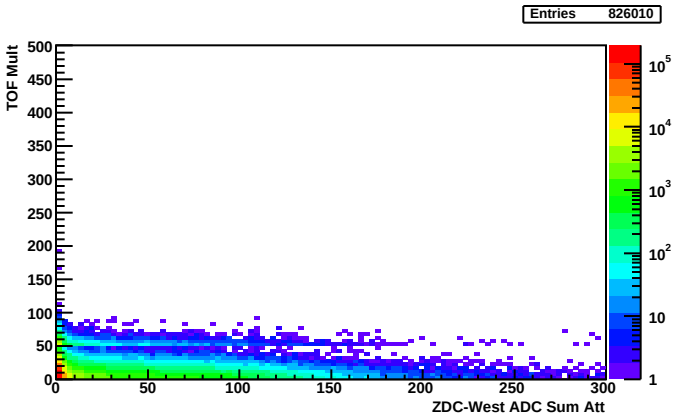

Entries 826010
TOF Mult
BBC-L-East ADC Sum

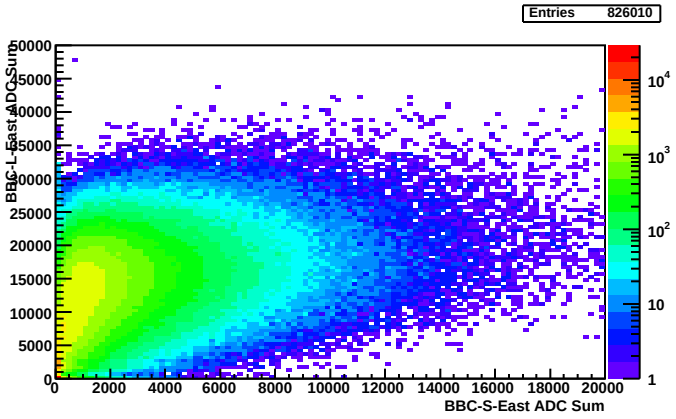

Entries 826010
TOF Mult
BBC-L-West ADC Sum

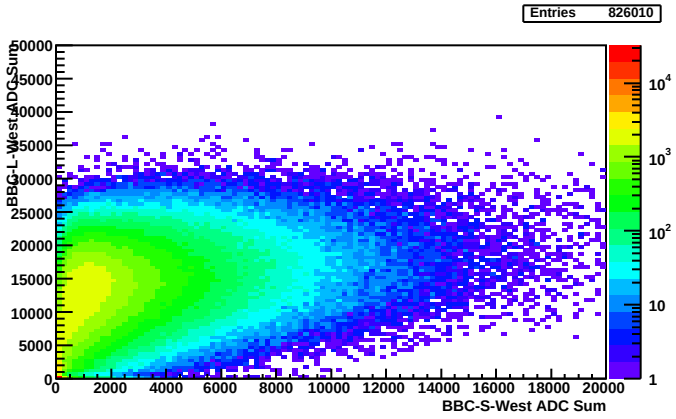

Entries 826010
TOF Mult
BBC-S-East ADC Sum

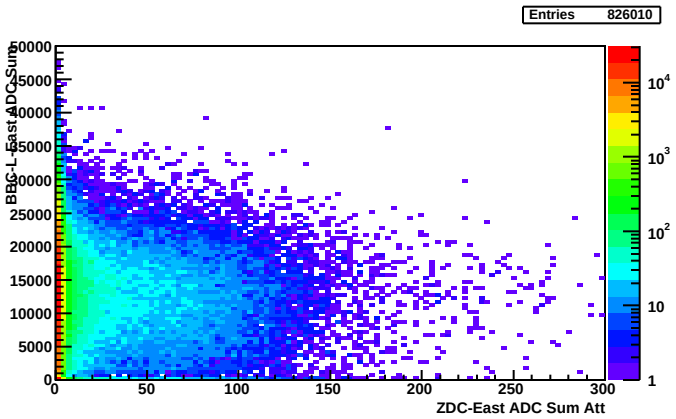

Entries 826010
TOF Mult
BBC-S-West ADC Sum

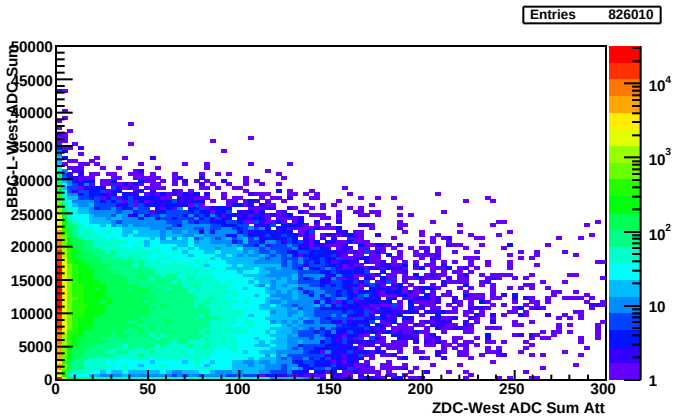

Entries 826010
TOF Mult
ZDC-East ADC Sum Att

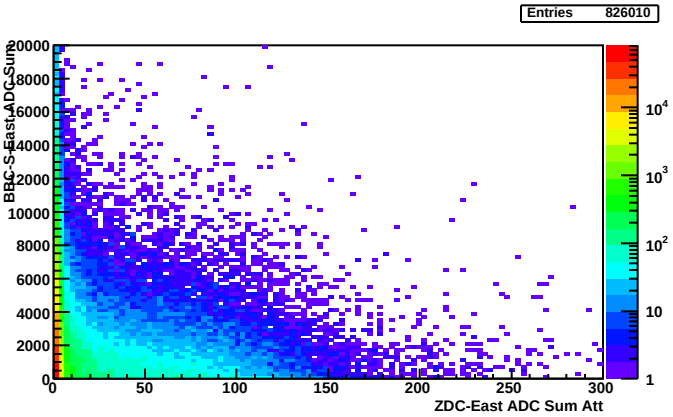

Entries 826010
TOF Mult
ZDC-West ADC Sum Att

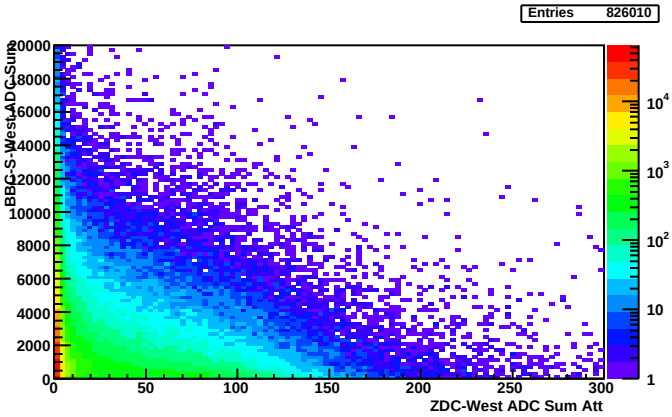

Entries 826010
BBC-L-East ADC Sum
BBC-S-East ADC Sum

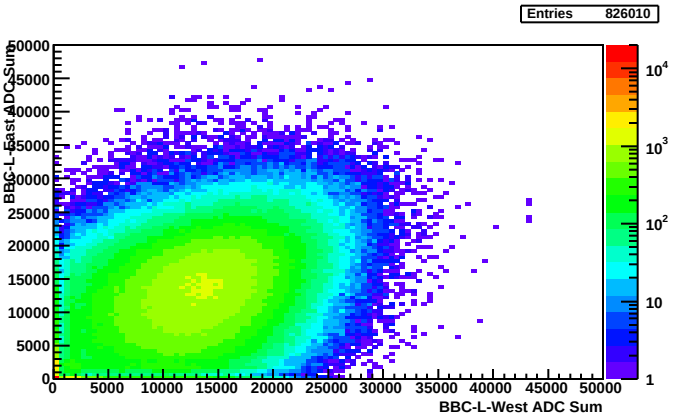

Entries 826010
BBC-L-West ADC Sum
BBC-S-West ADC Sum

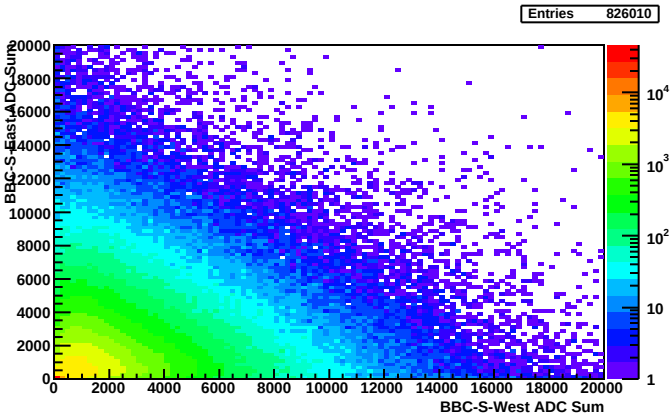

Entries 826010
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

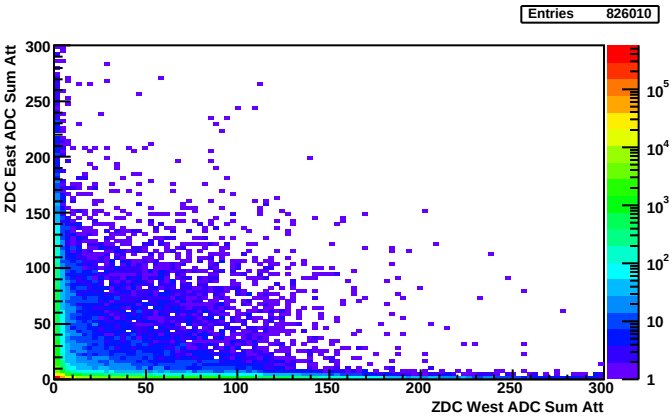

Entries 826010
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

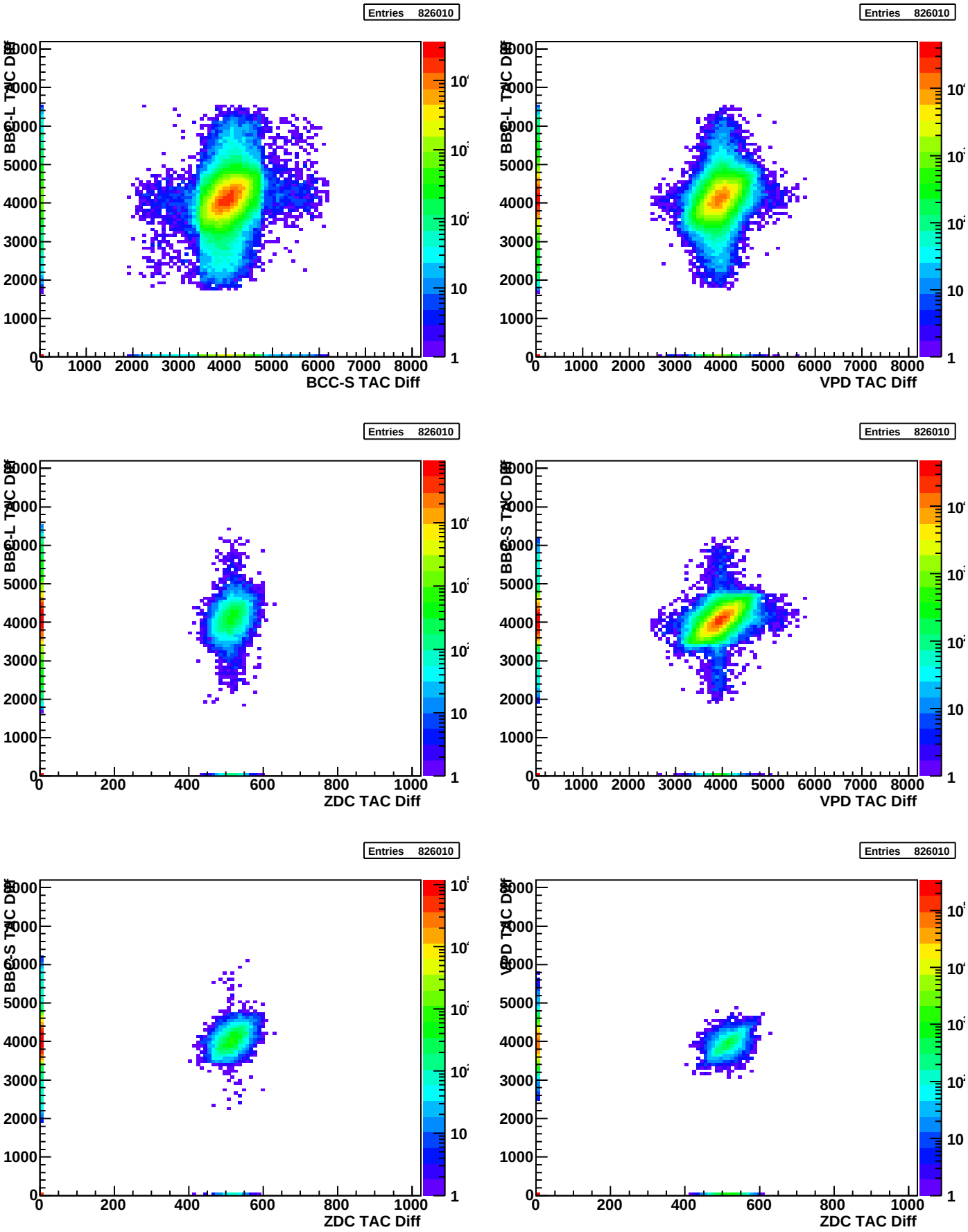

Entries 826010
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

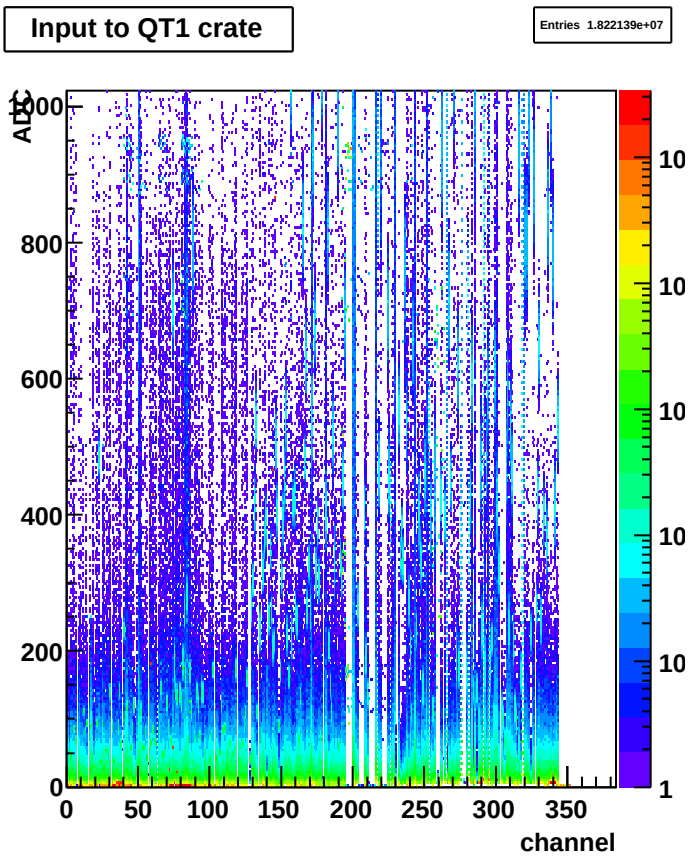

Entries 826010
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

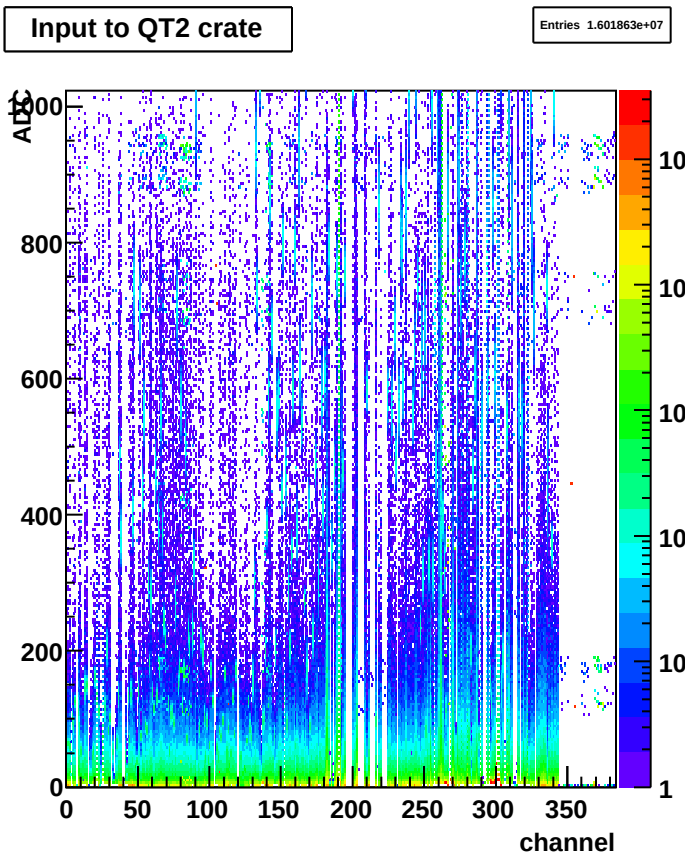

Entries 826010
BBC-L-East ADC Sum
BBC-L-West ADC Sum

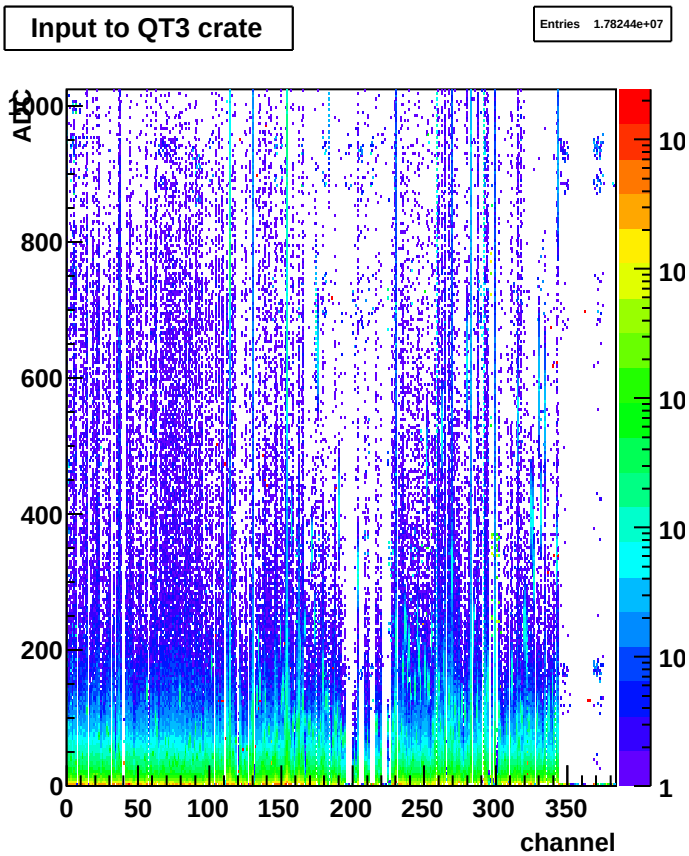

Entries 826010
BBC-S-East ADC Sum
BBC-S-West ADC Sum

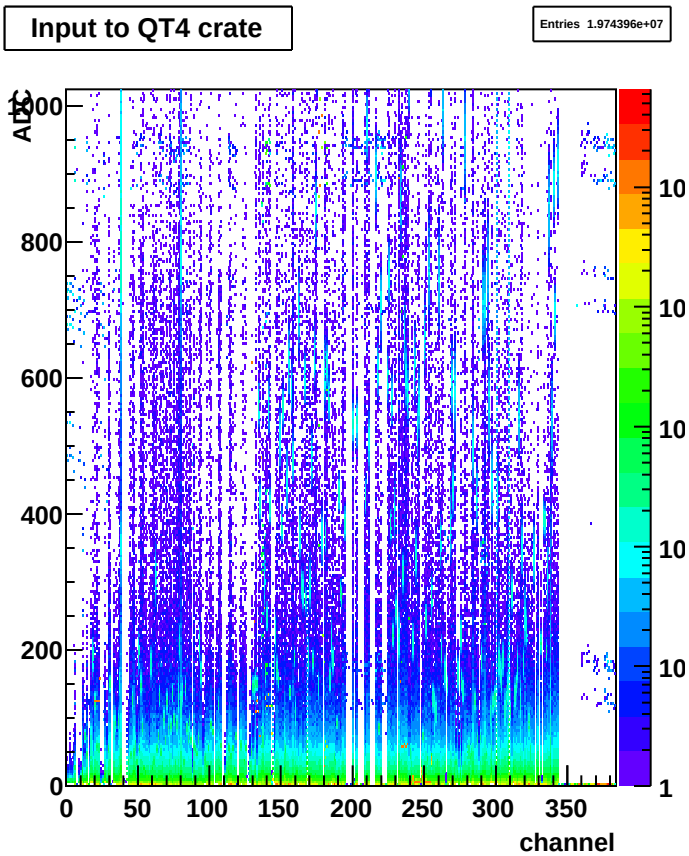

Entries 826010
ZDC East ADC Sum Att
ZDC West ADC Sum Att

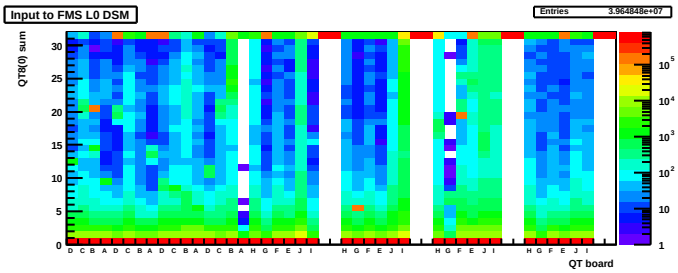

Entries 826010
BBC-L TAC Diff
BCC-S TAC Diff
Entries 826010
BBC-L TAC Diff
VPD TAC Diff
Entries 826010
BBC-L TAC Diff
ZDC TAC Diff
Entries 826010
BBC-S TAC Diff
VPD TAC Diff
Entries 826010
BBC-S TAC Diff
ZDC TAC Diff
Entries 826010
VPD TAC Diff
ZDC TAC Diff

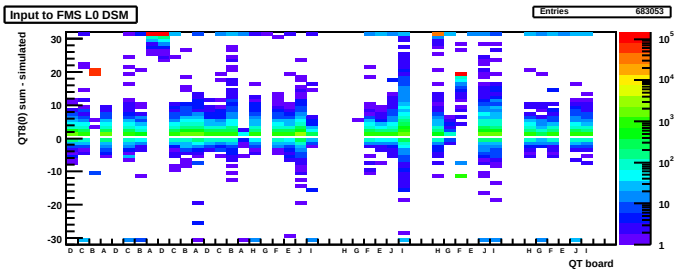

Input to QT1 crate
Entries 1.822139e+07
ADC
channel

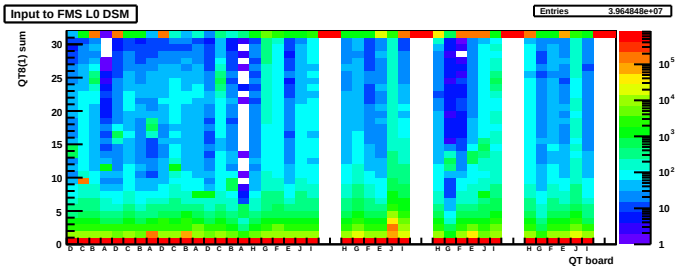

Input to QT2 crate
Entries 1.601863e+07
ADC
channel

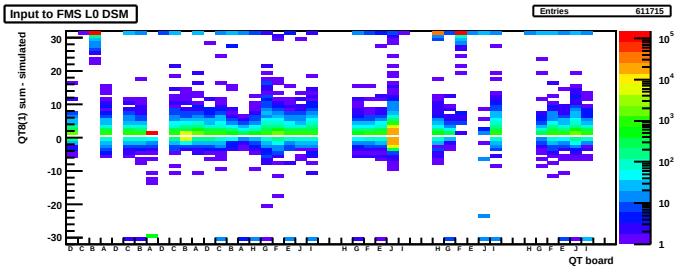

Input to QT3 crate
Entries 1.78244e+07
ADC
channel

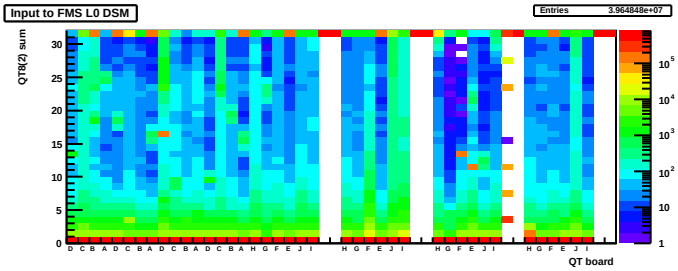

Input to QT4 crate
Entries 1.974396e+07
ADC
channel

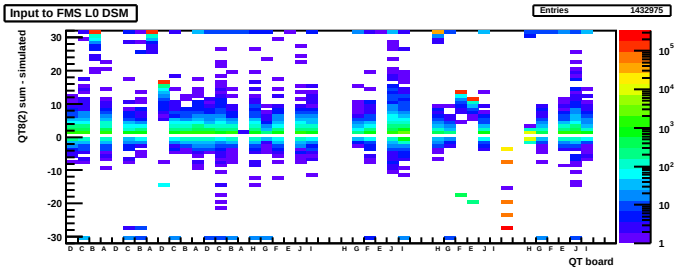

Input to FMS L0 DSM
Entries 3.964848e+07
QT8(0) sum
QT board

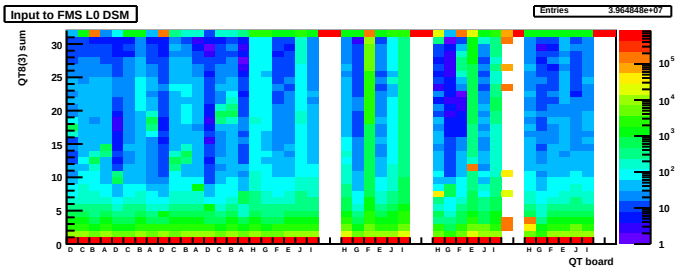

Input to FMS L0 DSM
Entries 683053
QT8(0) sum - simulated
QT board

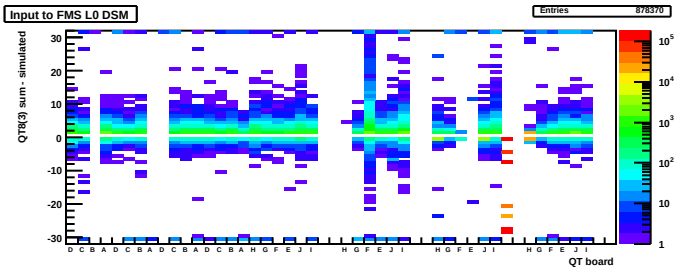

Input to FMS L0 DSM
Entries 3.964848e+07
QT8(1) sum
QT board

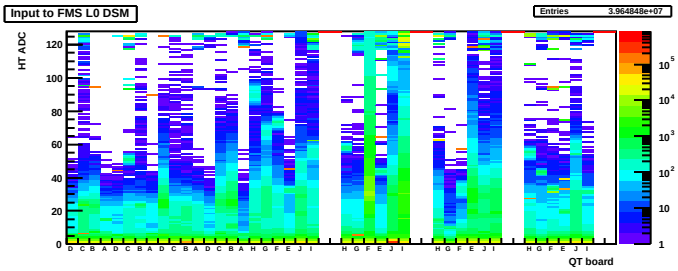

Input to FMS L0 DSM
Entries 611715
QT8(1) sum - simulated
QT board

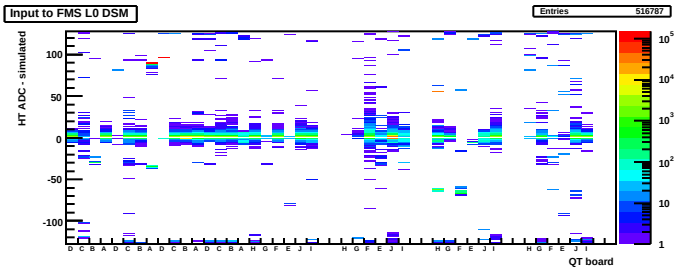

Input to FMS L0 DSM
Entries 3.964848e+07
QT8(2) sum
QT board

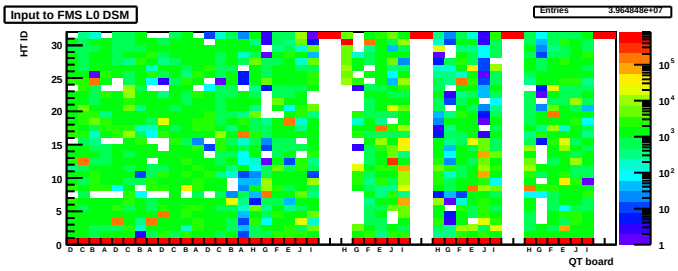

Input to FMS L0 DSM
Entries 1432975
QT8(2) sum - simulated
QT board

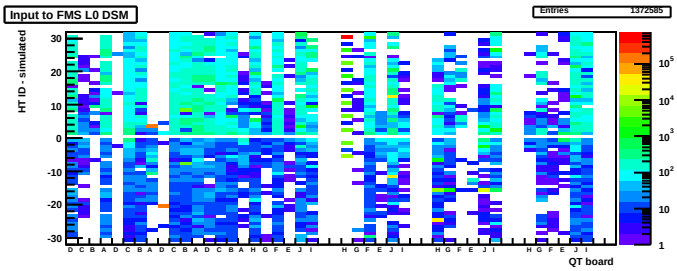

Input to FMS L0 DSM
Entries 3.964848e+07
QT8(3) sum
QT board

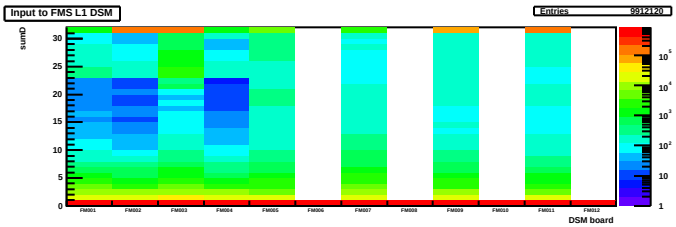

Input to FMS L0 DSM
Entries 878370
QT8(3) sum - simulated
QT board

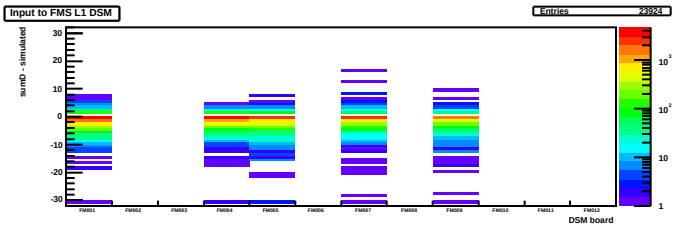

Input to FMS L0 DSM
Entries 3.964848e+07
HT ADC
QT board

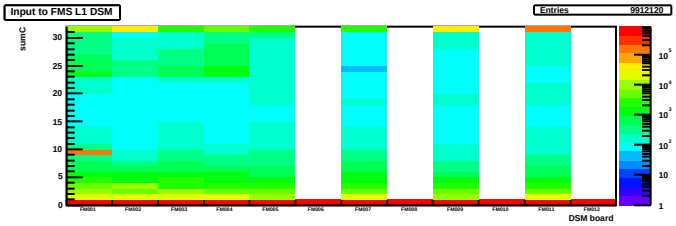

Input to FMS L0 DSM
Entries 516787
HT ADC - simulated
QT board

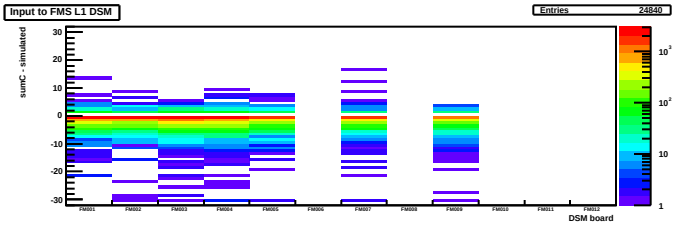

Input to FMS L0 DSM
Entries 3.964848e+07
HT ID
QT board

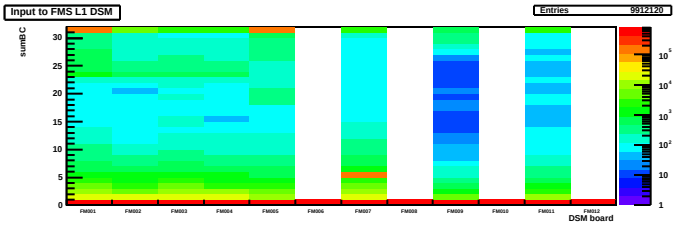

Input to FMS L0 DSM
Entries 1372585
HT ID - simulated
QT board

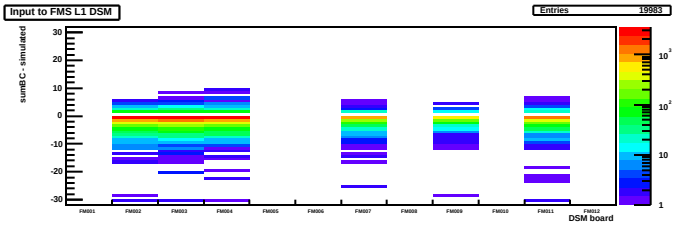

Input to FMS L1 DSM
Entries 9912120
sumD
DSM board

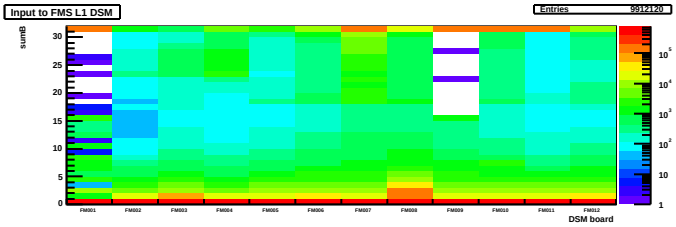

Input to FMS L1 DSM
Entries 23824
sumD - simulated
DSM board

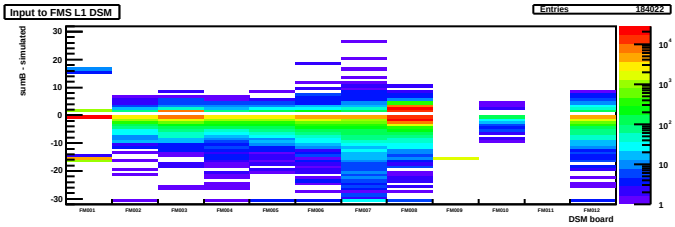

Input to FMS L1 DSM
Entries 9912120
sumC
DSM board

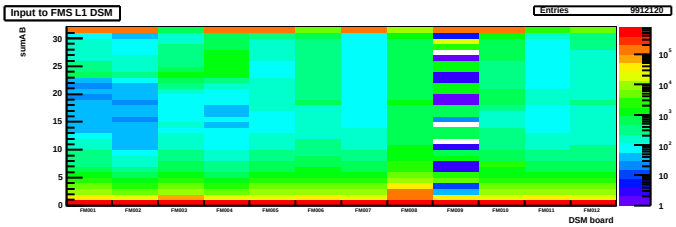

Input to FMS L1 DSM
Entries 24840
sumC - simulated
DSM board

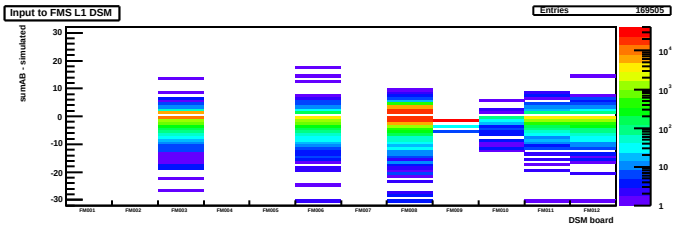

Input to FMS L1 DSM
Entries 9912120
sumBC
DSM board

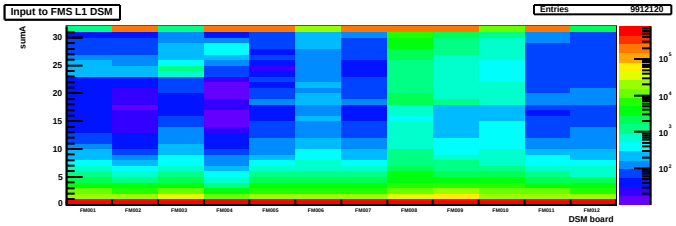

Input to FMS L1 DSM
Entries 19983
sumBC - simulated
DSM board

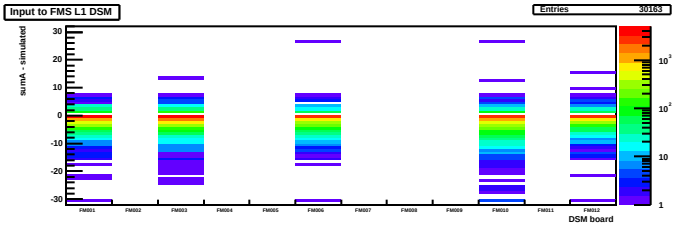

Input to FMS L1 DSM
Entries 9912120
sumB
DSM board

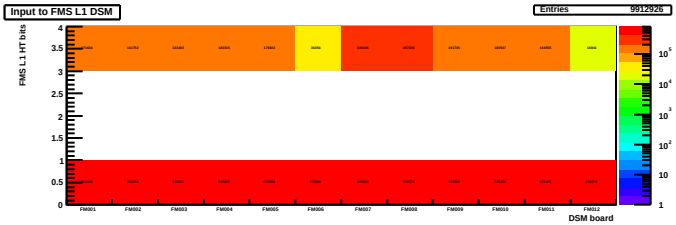

Input to FMS L1 DSM
Entries 184022
sumB - simulated
DSM board

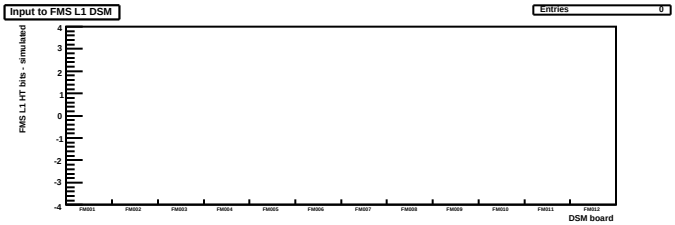

Input to FMS L1 DSM
Entries 9912120
sumAB
DSM board

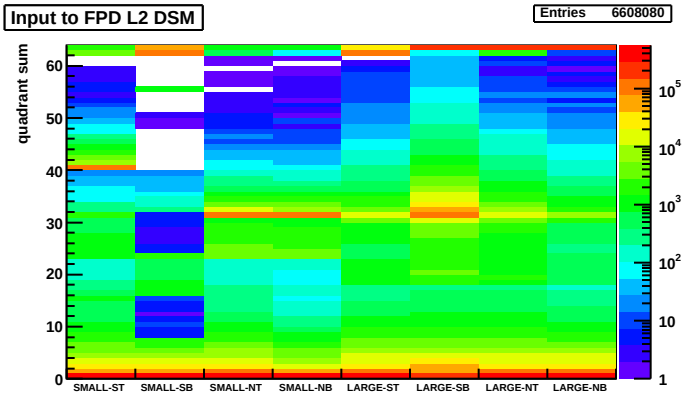

Input to FMS L1 DSM
Entries 169505
sumAB - simulated
DSM board

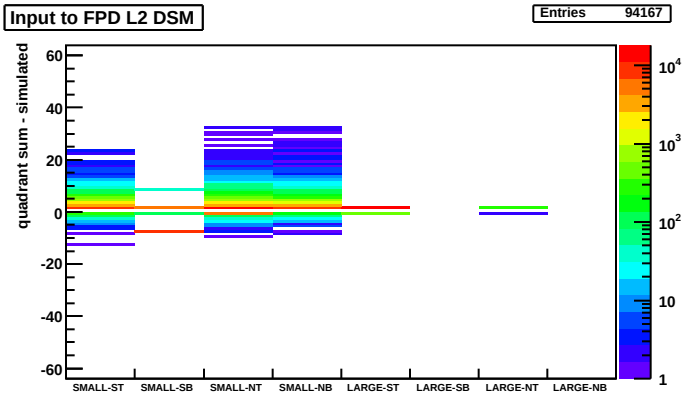

Input to FMS L1 DSM
Entries 9912120
sumA
DSM board

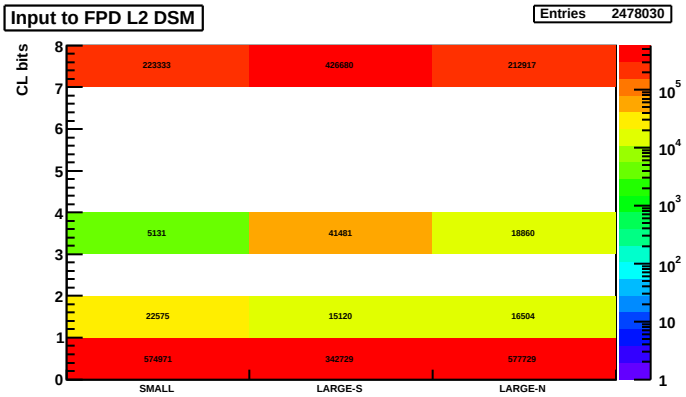

Input to FMS L1 DSM
Entries 30163
sumA - simulated
DSM board

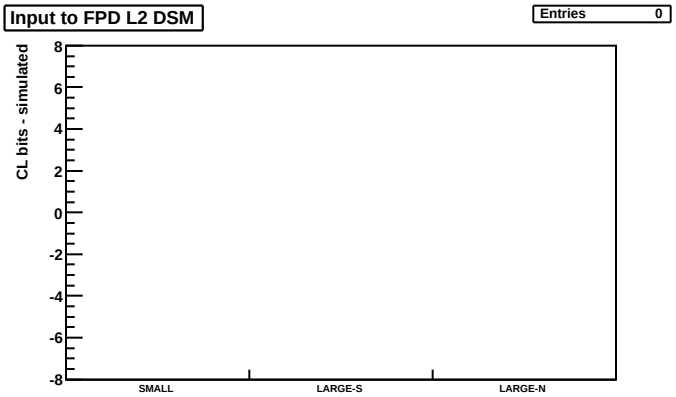

Input to FMS L1 DSM
Entries 9912926
FMS L1 HT bits
DSM board

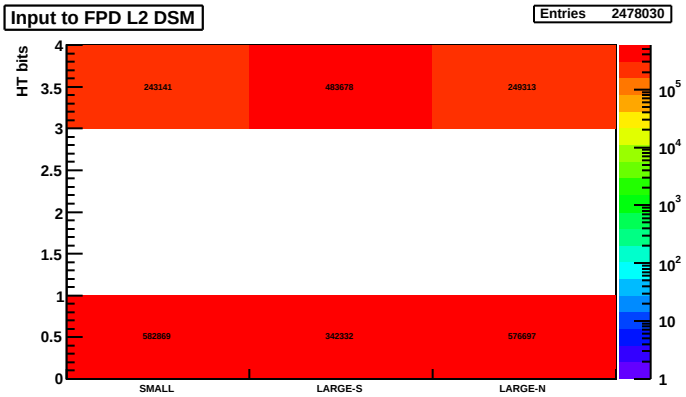

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

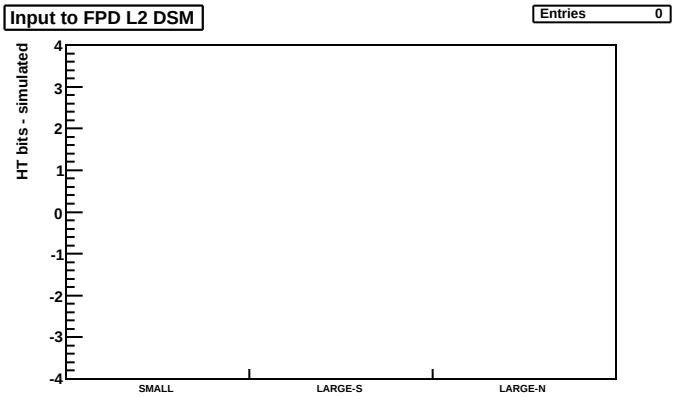

Input to FPD L2 DSM
Entries 6608080
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

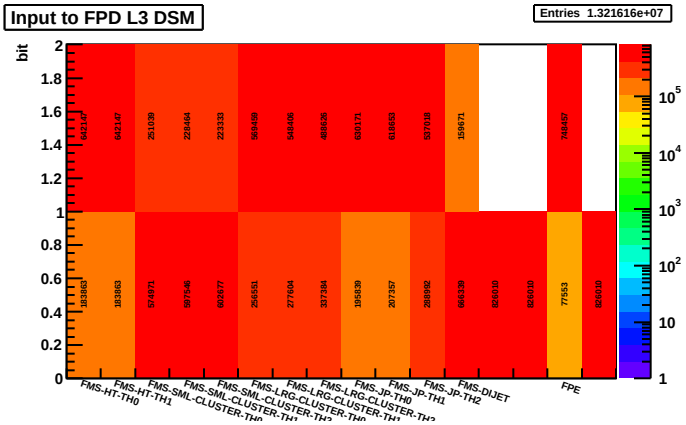

Input to FPD L2 DSM
Entries 94167
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

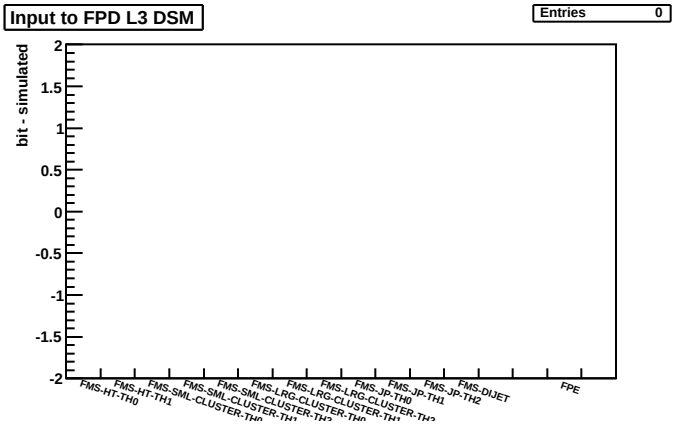

Input to FPD L2 DSM
Entries 2478030
CL bits
223333
426680
212917
5131
41481
18860
22575
15120
16504
574971
342729
577729
SMALL
LARGE-S
LARGE-N

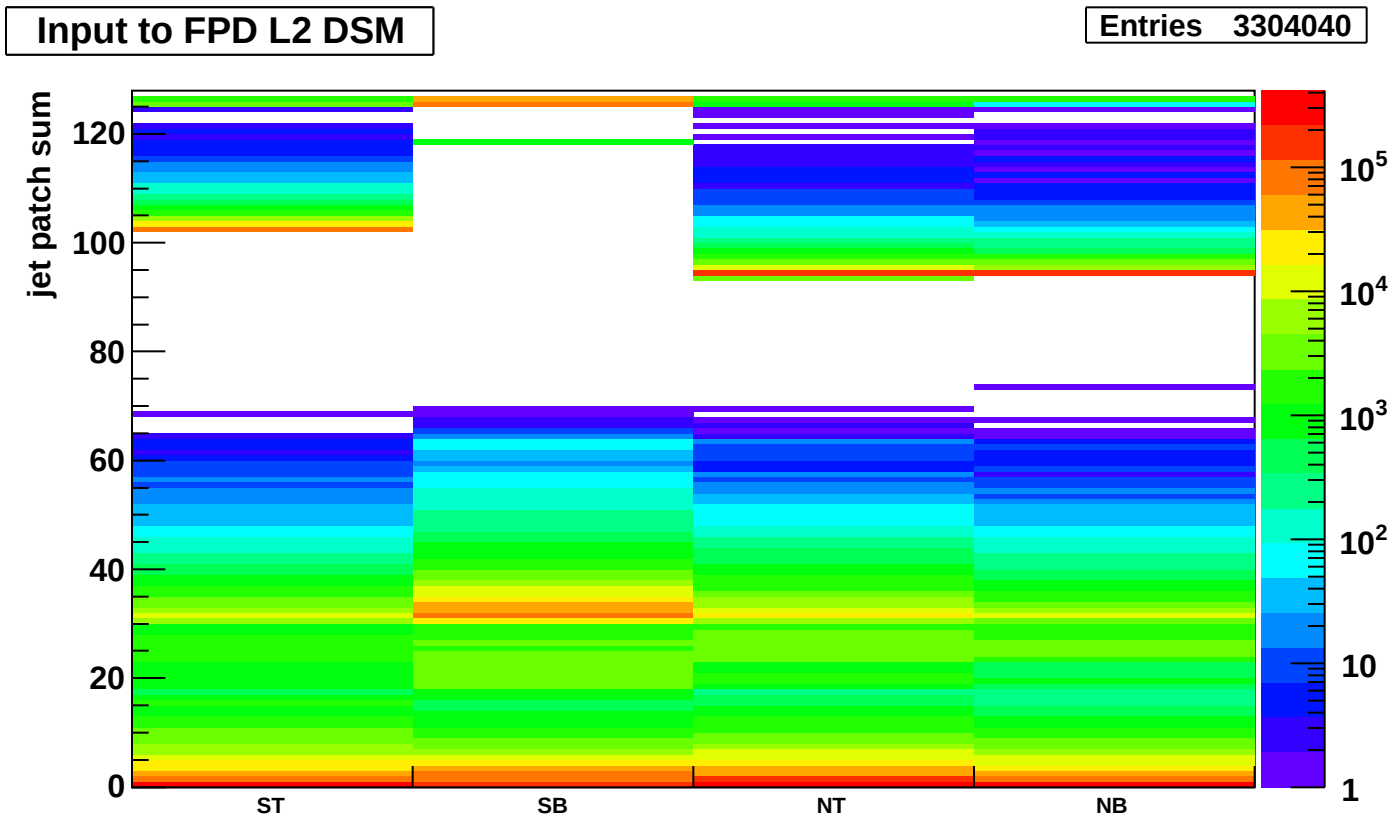

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

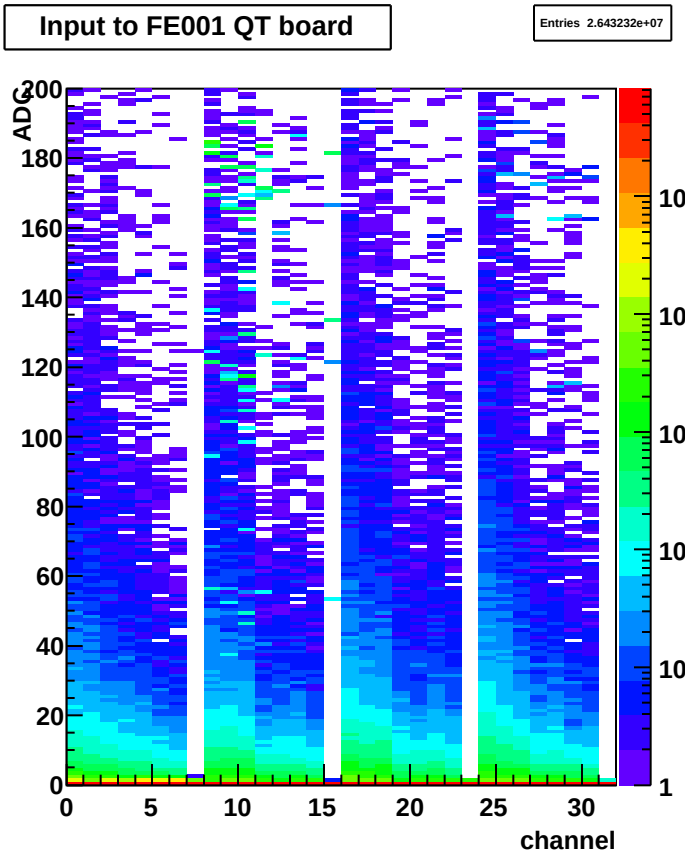

Input to FPD L2 DSM
Entries 2478030
HT bits
243141
483678
249313
582869
342332
576697
SMALL
LARGE-S
LARGE-N

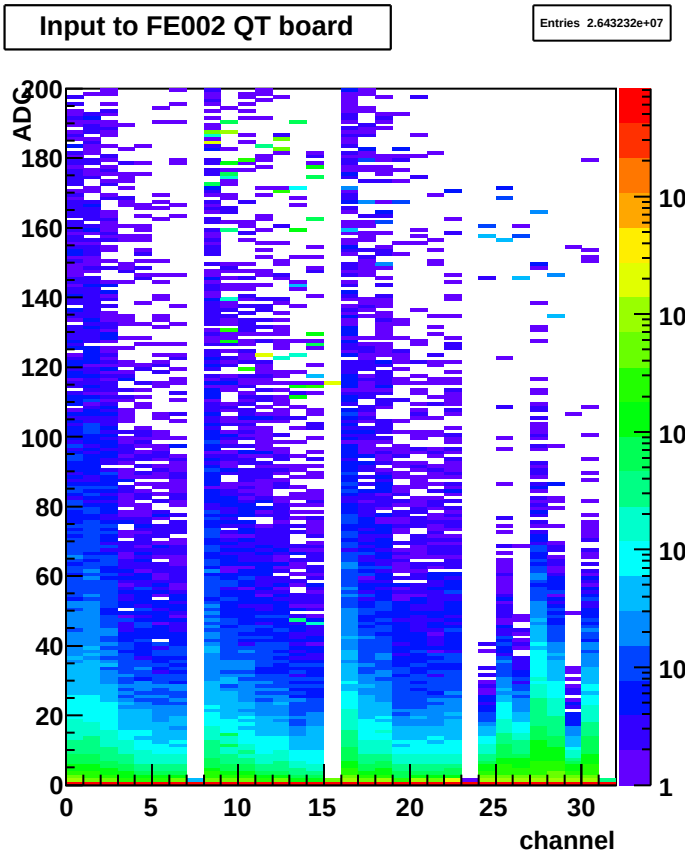

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

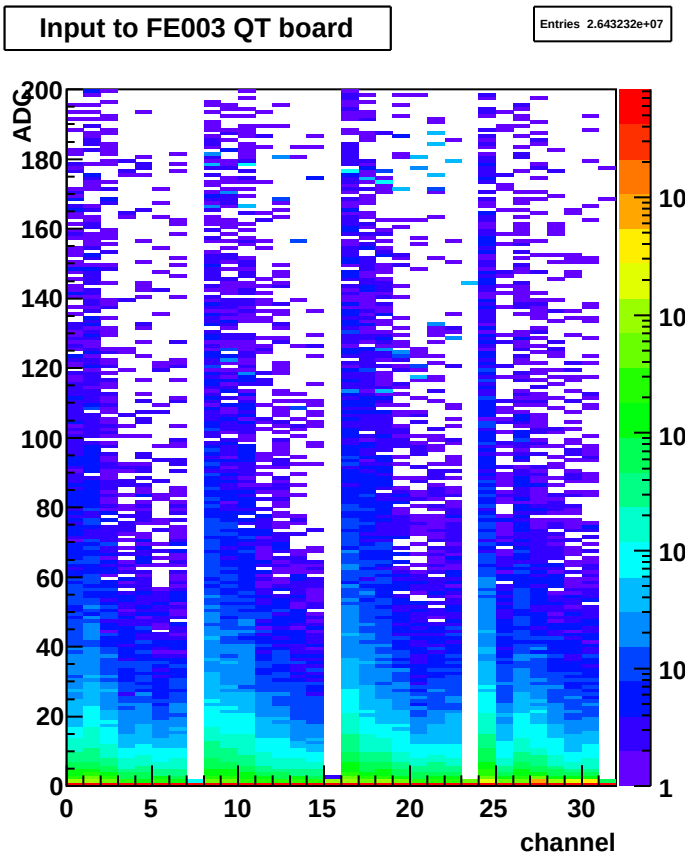

Input to FPD L3 DSM
Entries 1.321616e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

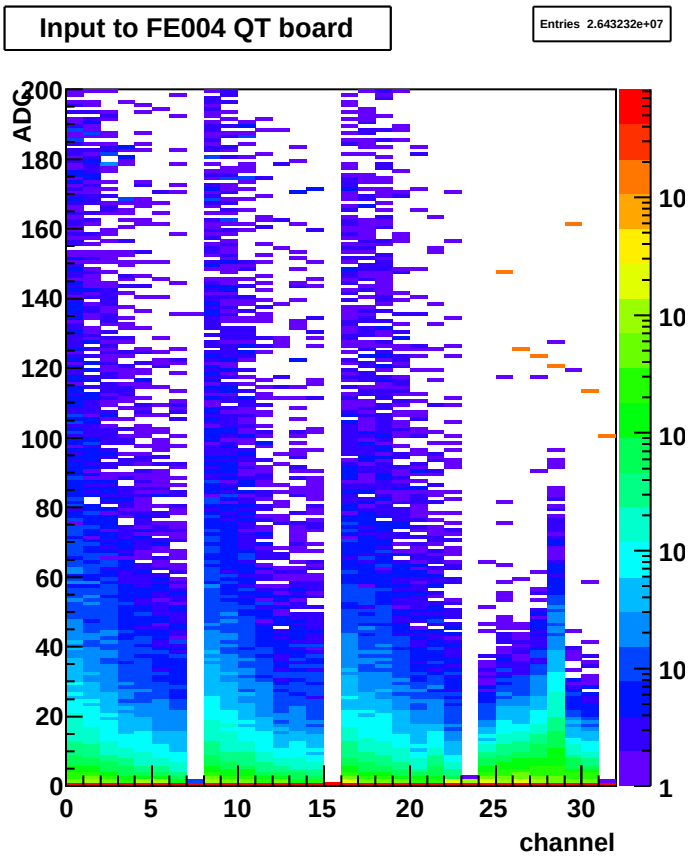

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

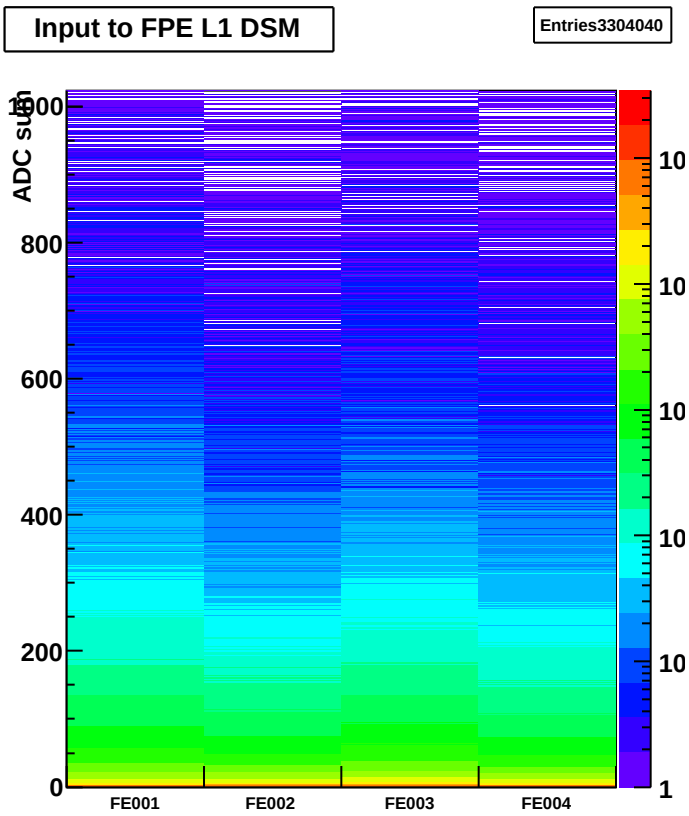

Input to FPD L2 DSM
Entries 3304040
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

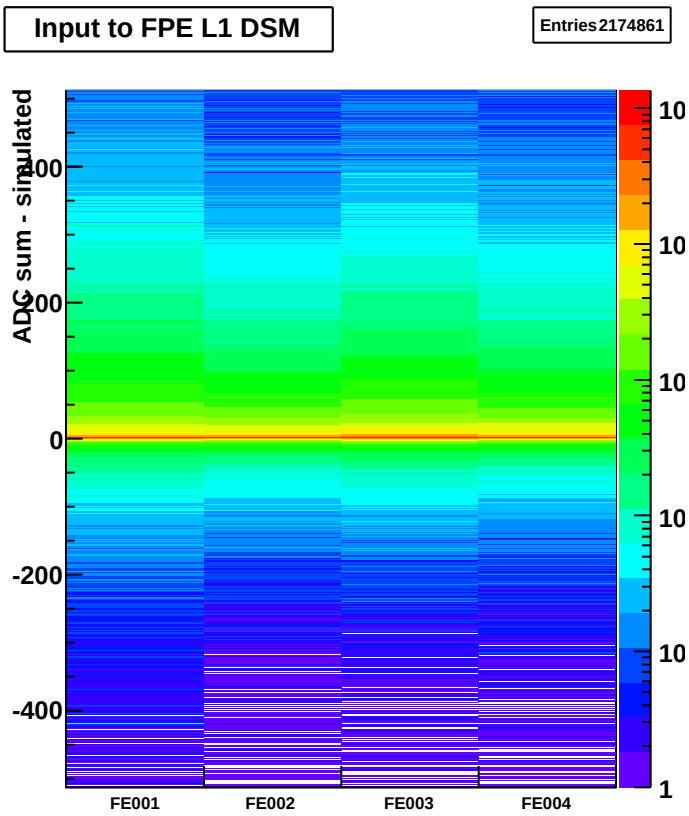

Input to FE001 QT board
Entries 2.643232e+07
ADC
channel

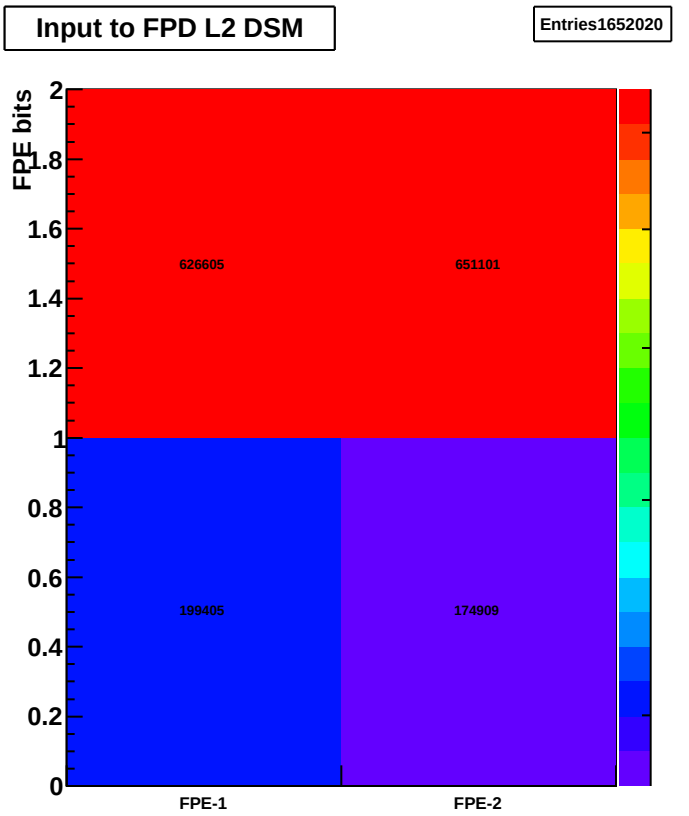

Input to FE002 QT board
Entries 2.643232e+07
ADC
channel

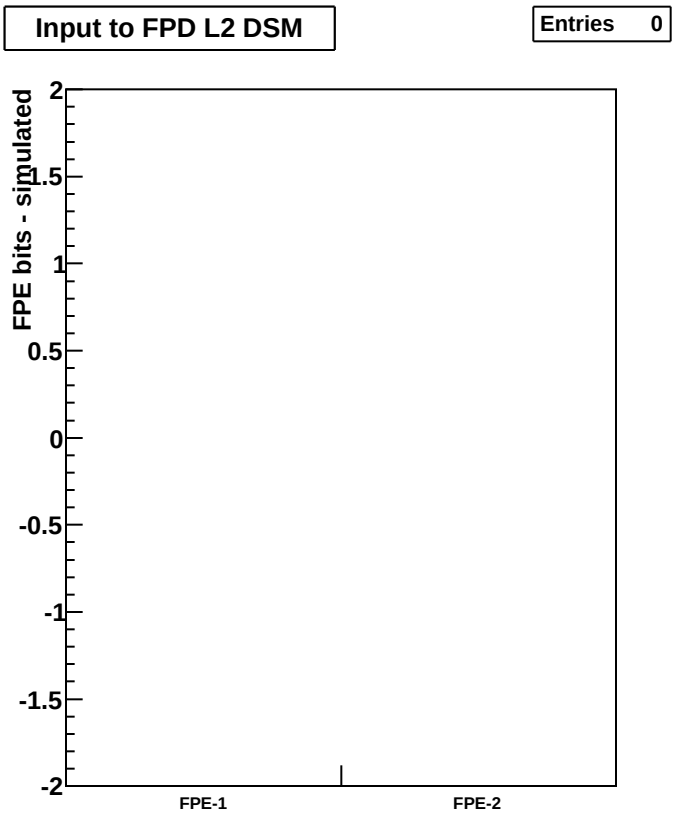

Input to FE003 QT board
Entries 2.643232e+07
ADC
channel


Input to FE004 QT board
Entries 2.643232e+07
ADC
channel


Input to FPE L1 DSM
Entries3304040
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2174861
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries1652020
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
626605
651101
199405
174909
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2