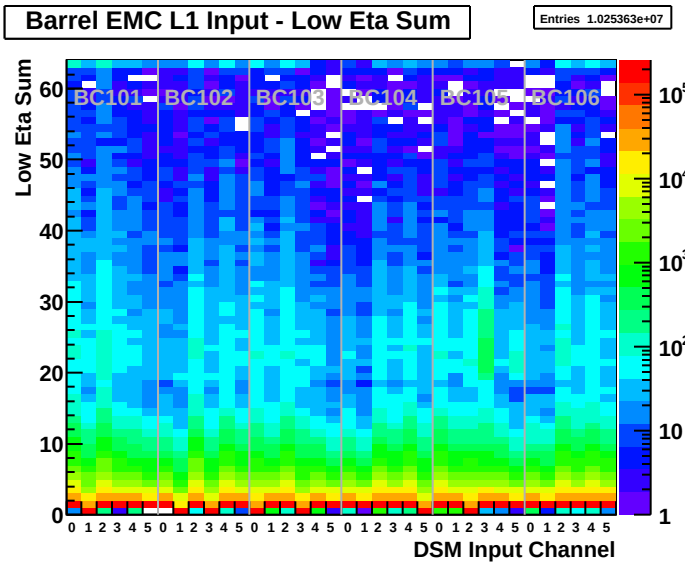
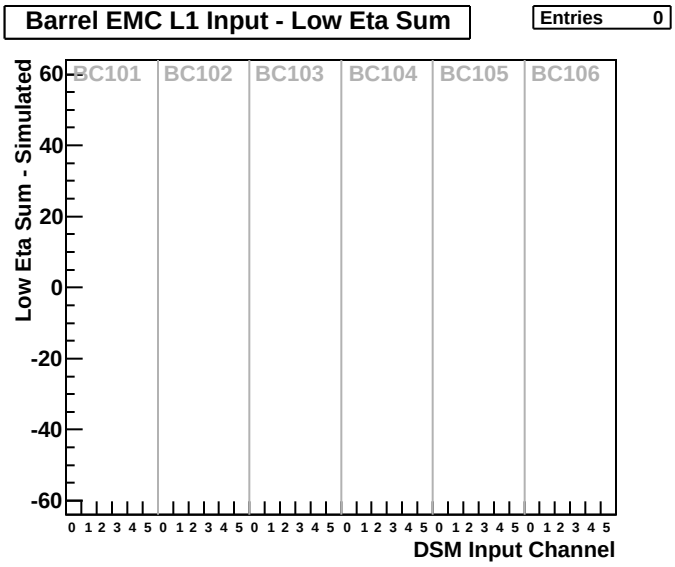
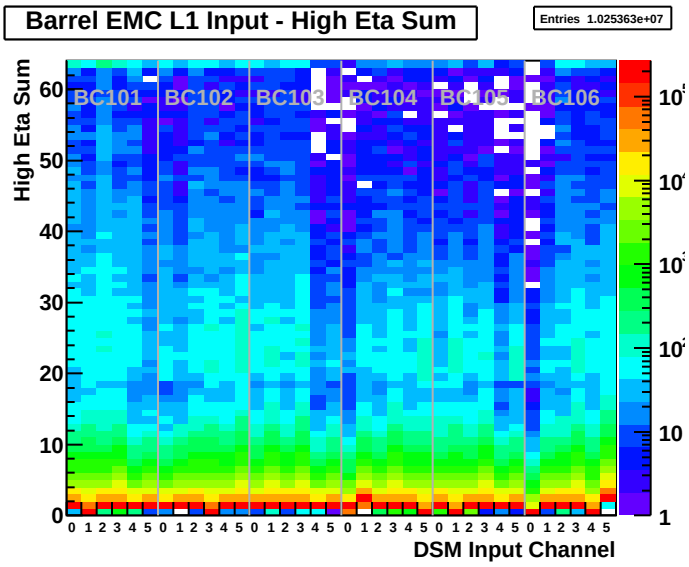
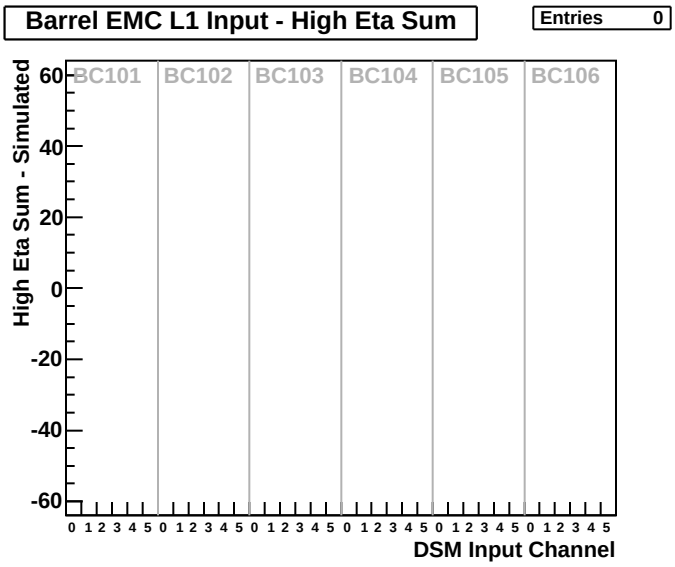
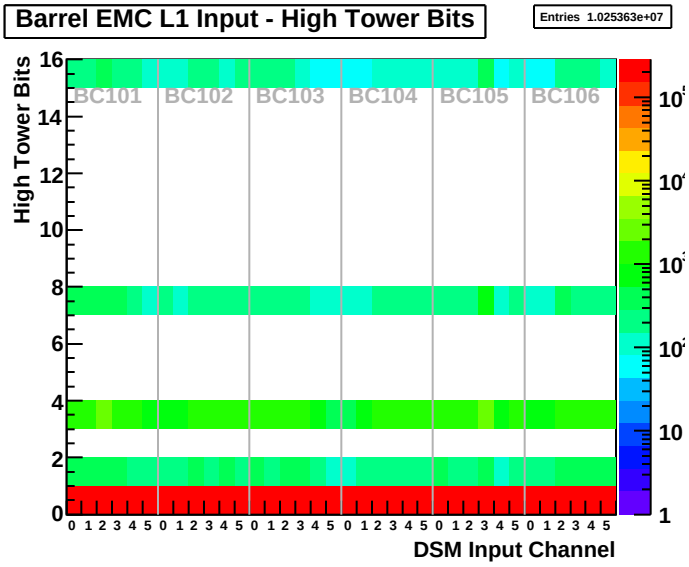
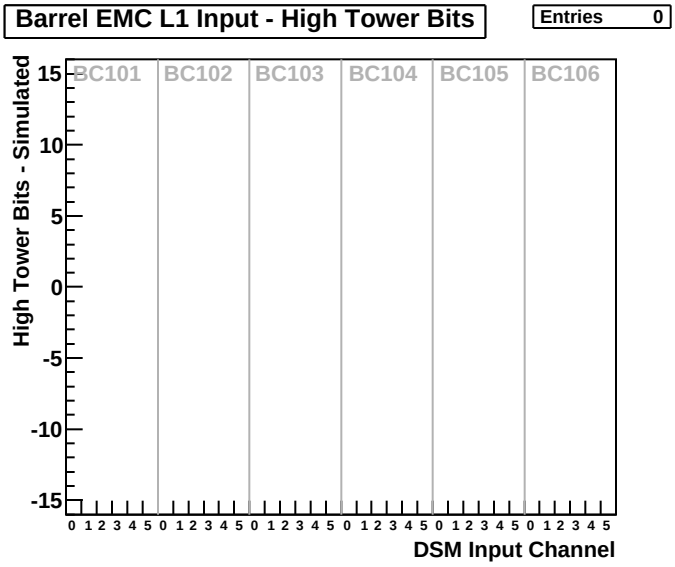

Barrel EMC L1 Input - Low Eta Sum
Entries 1.025363e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

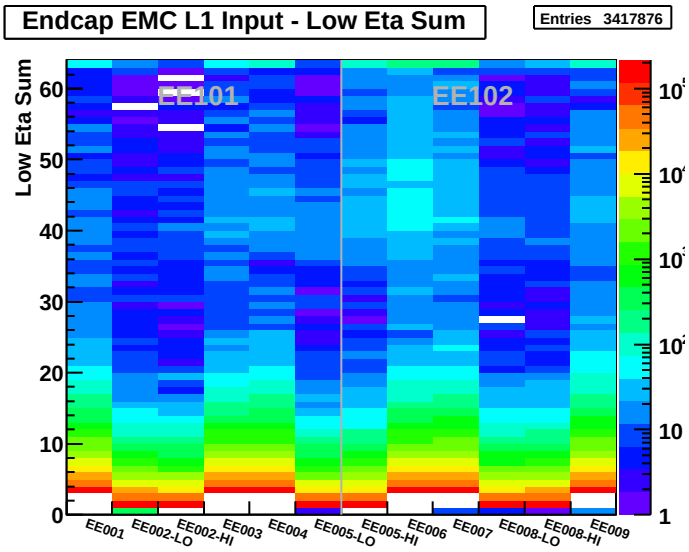

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

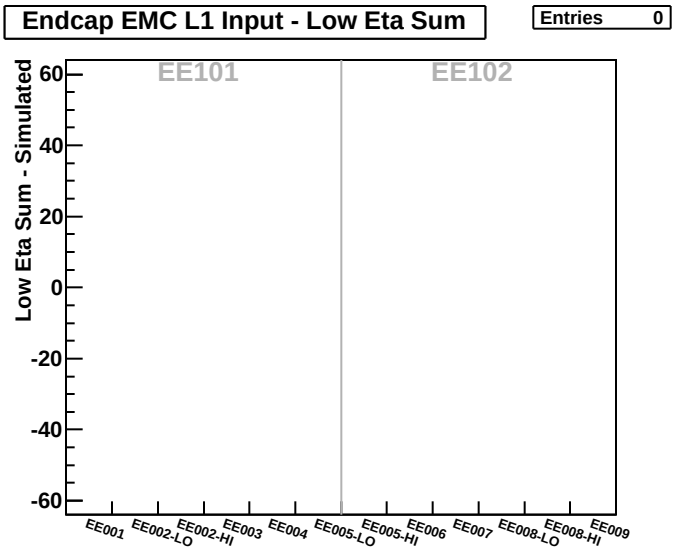

Barrel EMC L1 Input - High Eta Sum
Entries 1.025363e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

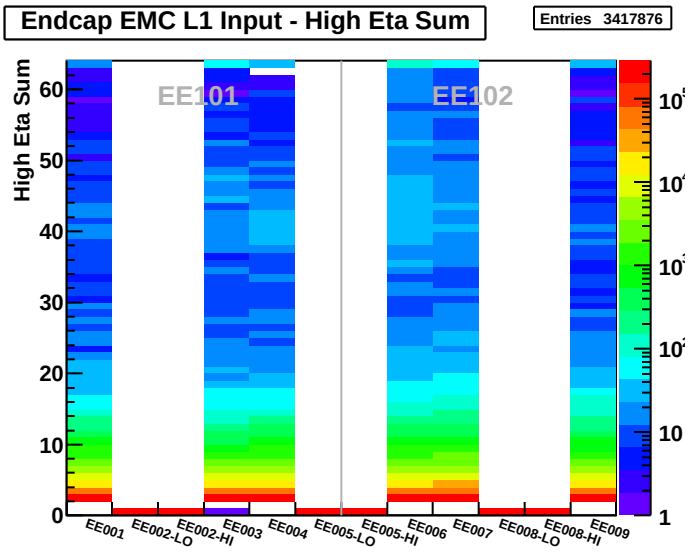

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

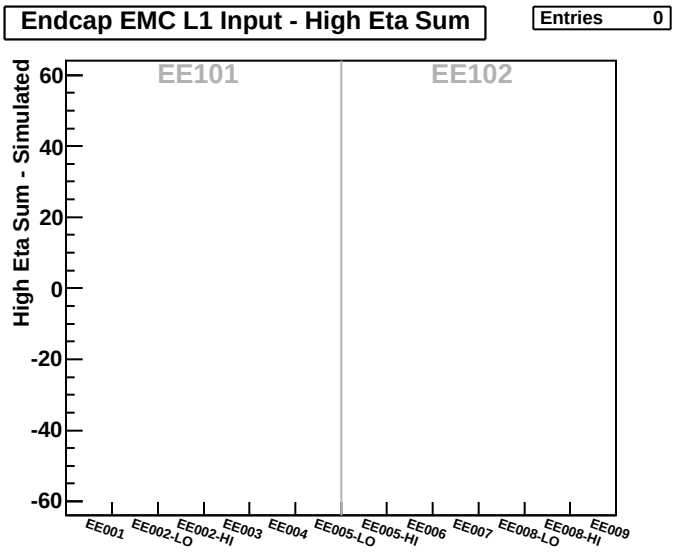

Barrel EMC L1 Input - High Tower Bits
Entries 1.025363e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

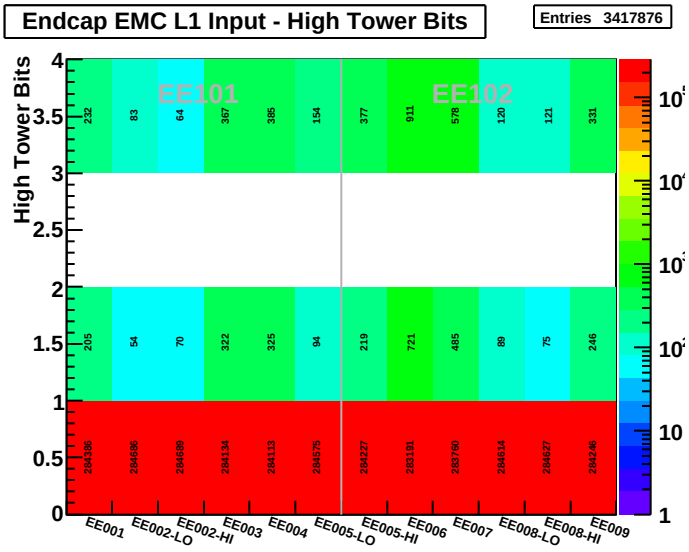

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

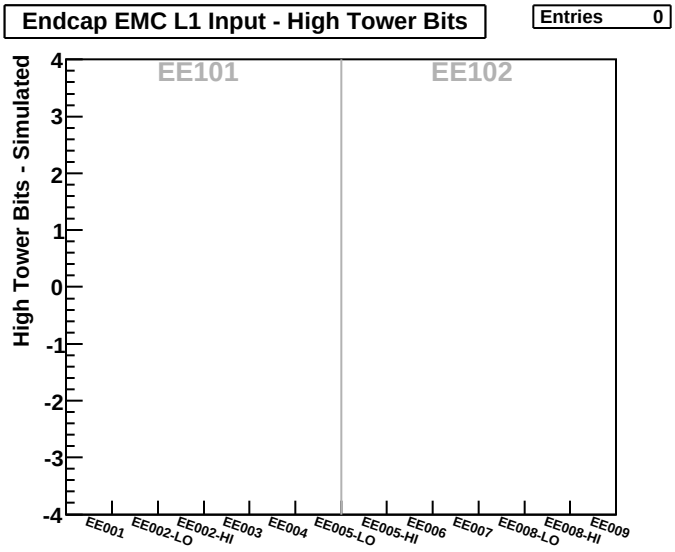

Endcap EMC L1 Input - Low Eta Sum
Entries 3417876
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

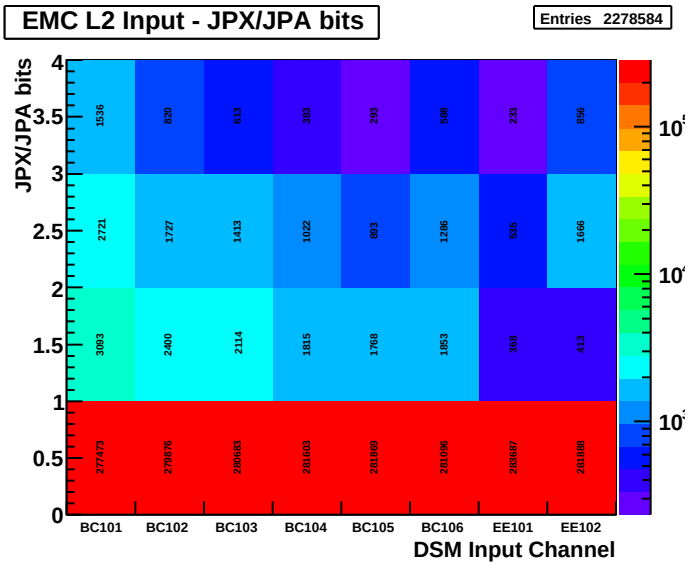

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

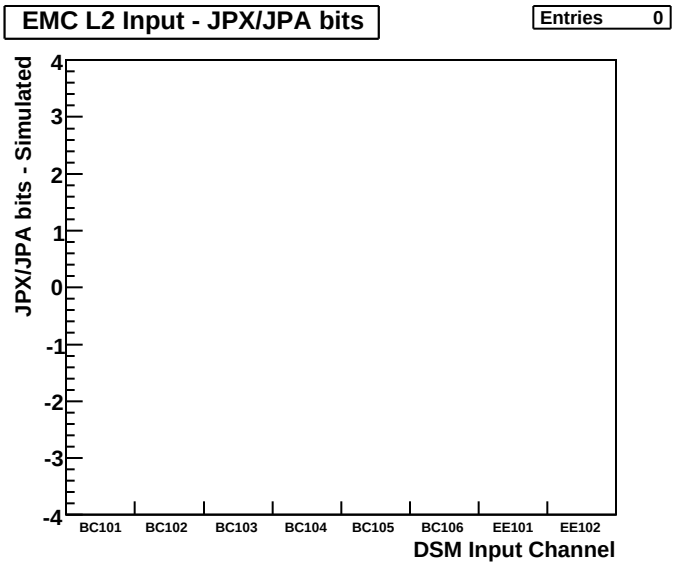

Endcap EMC L1 Input - High Eta Sum
Entries 3417876
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

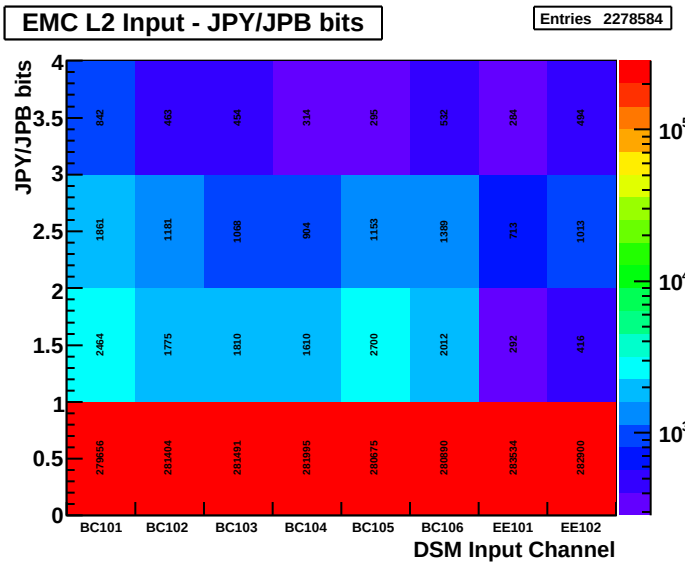

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

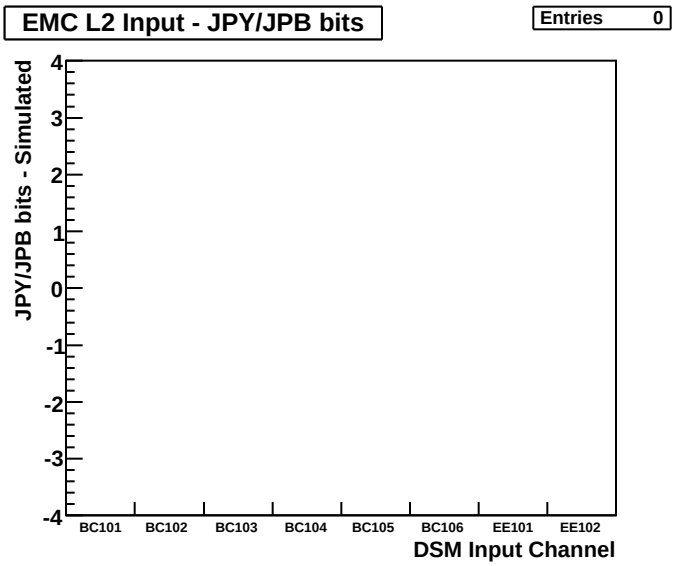

Endcap EMC L1 Input - High Tower Bits
Entries 3417876
High Tower Bits
EE101
EE102
232 83 64 367 385 154 377 911 578 120 121 331
205 54 70 322 325 94 219 721 485 89 75 246
284386 284686 284689 284134 284113 284575 284227 283191 283760 284614 284627 284245
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

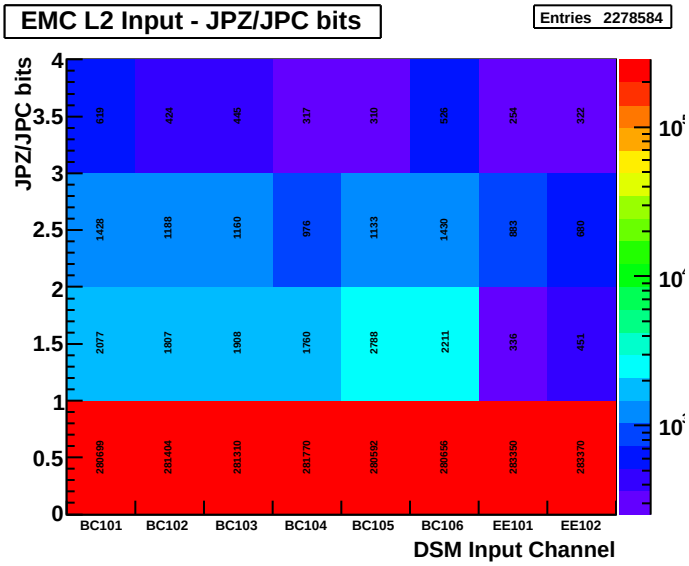

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

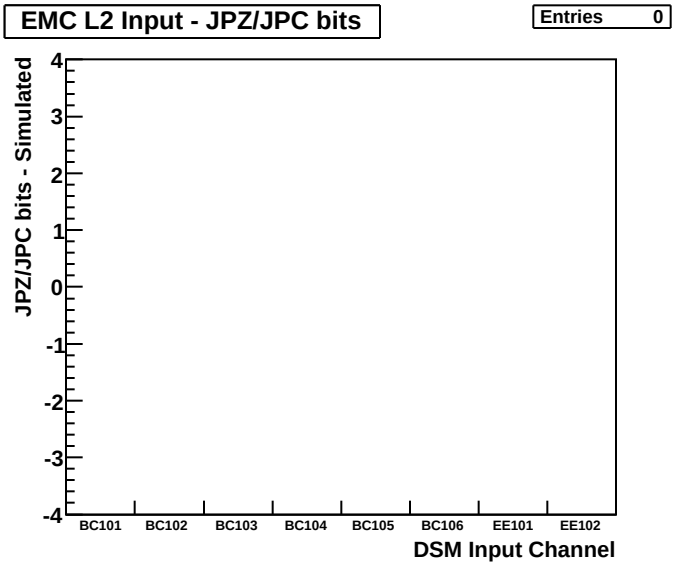

EMC L2 Input - JPX/JPA bits
Entries 2278584
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1536 820 613 383 293 588 233 856
2721 1727 1413 1022 893 1286 535 1666
3093 2400 2114 1815 1768 1853 368 413
277473 279876 280603 281603 281869 281096 283687 281888

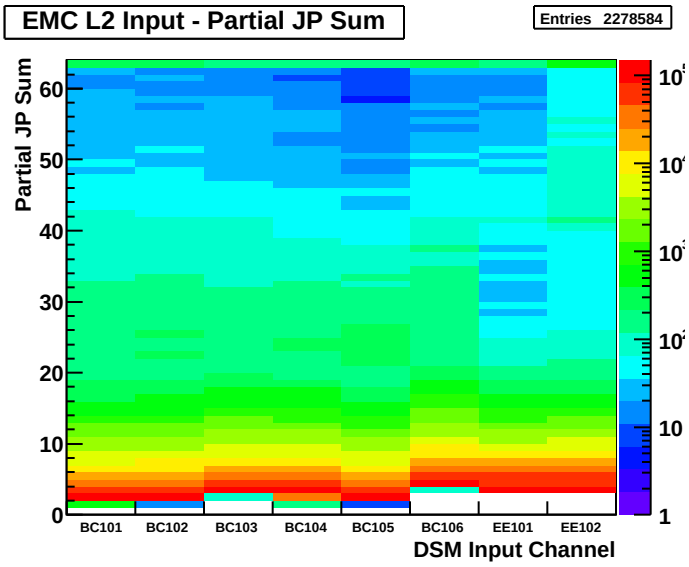

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

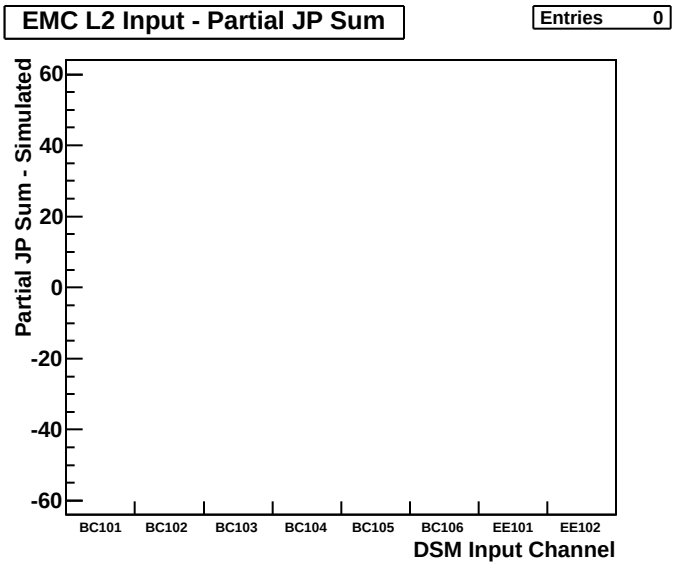

EMC L2 Input - JPY/JPB bits
Entries 2278584
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
842 463 454 314 295 532 284 494
1861 1181 1068 904 1153 1389 713 1013
2464 1775 1810 1610 2700 2012 292 416
279656 281404 281491 281995 280675 280890 283534 282900

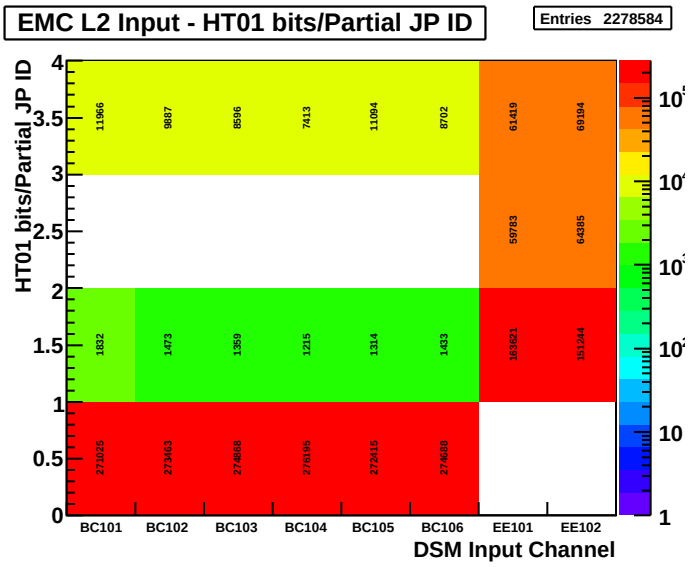

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

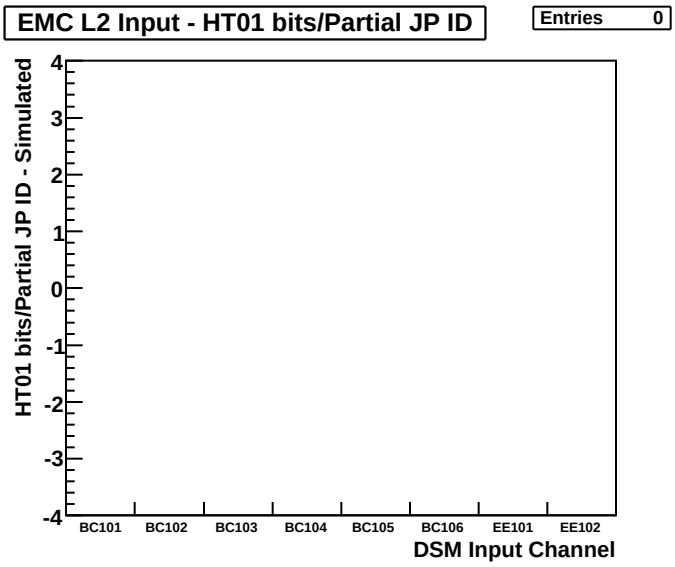

EMC L2 Input - JPZ/JPC bits
Entries 2278584
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
619 424 445 317 310 526 254 322
1428 1188 1160 976 1133 1430 883 680
2077 1807 1908 1760 2788 2211 336 451
280599 281404 281310 281770 280592 280656 283350 283370

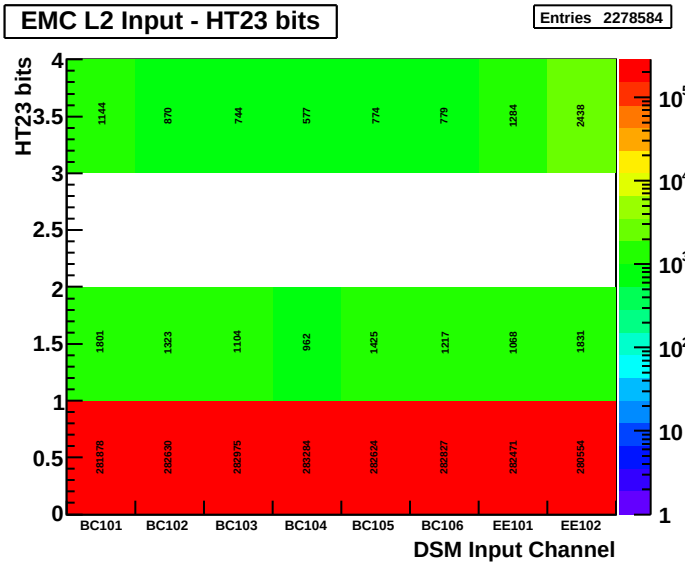

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

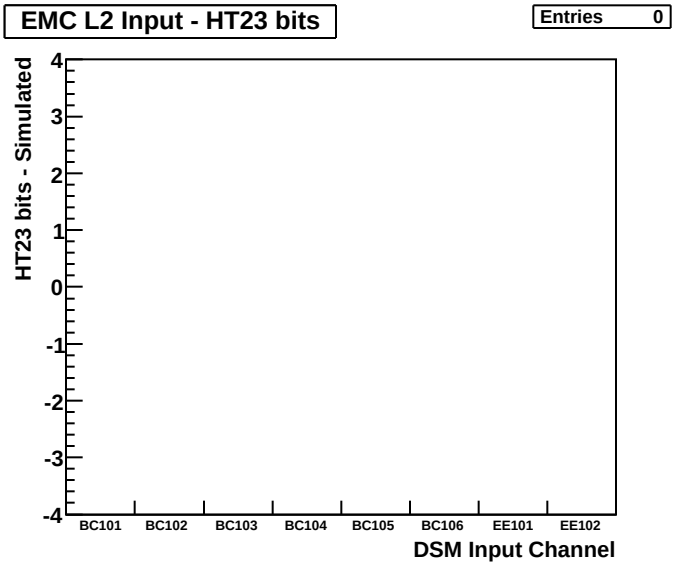

EMC L2 Input - Partial JP Sum
Entries 2278584
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

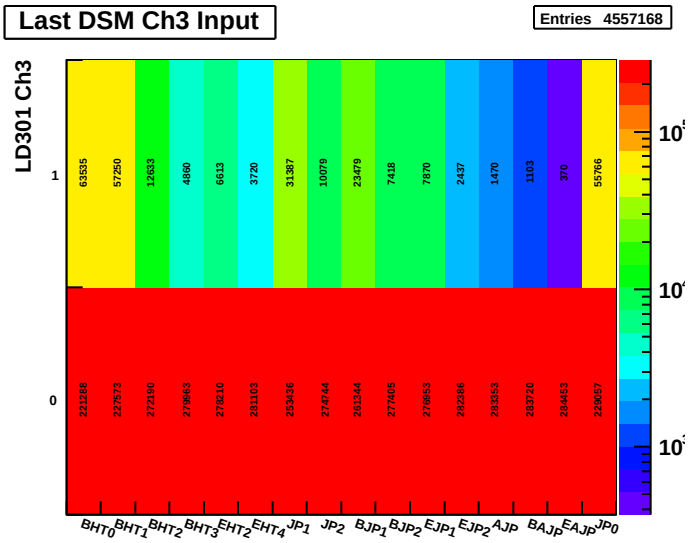

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

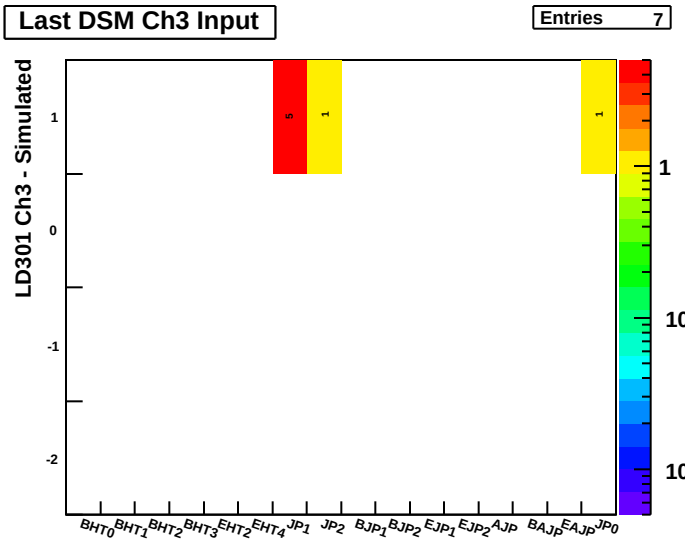

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2278584
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

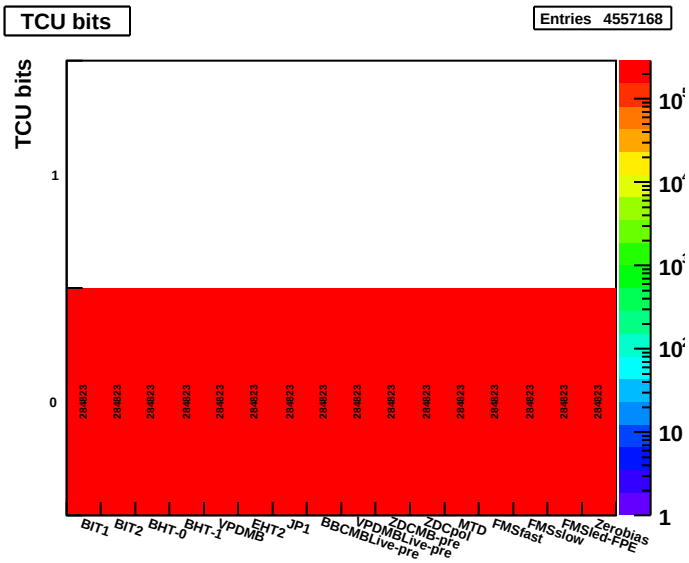

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

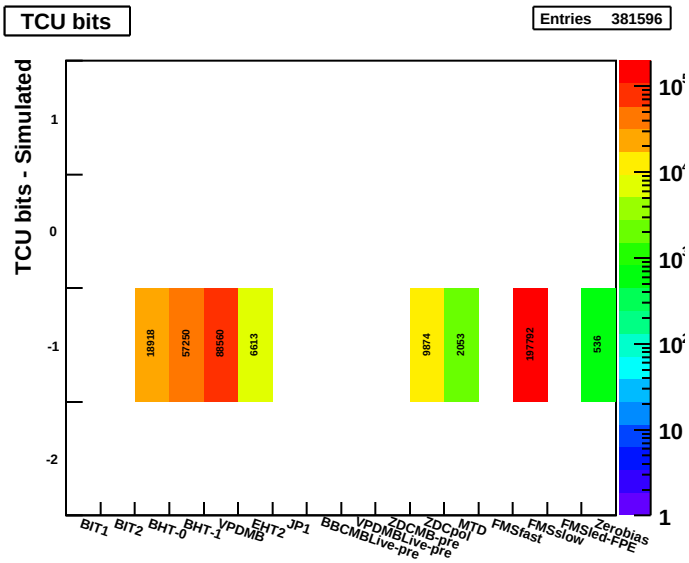

EMC L2 Input - HT23 bits
Entries 2278584
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

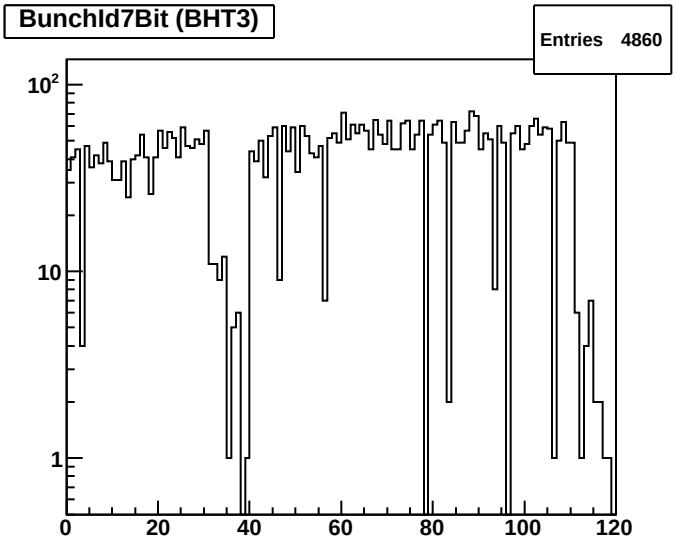

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

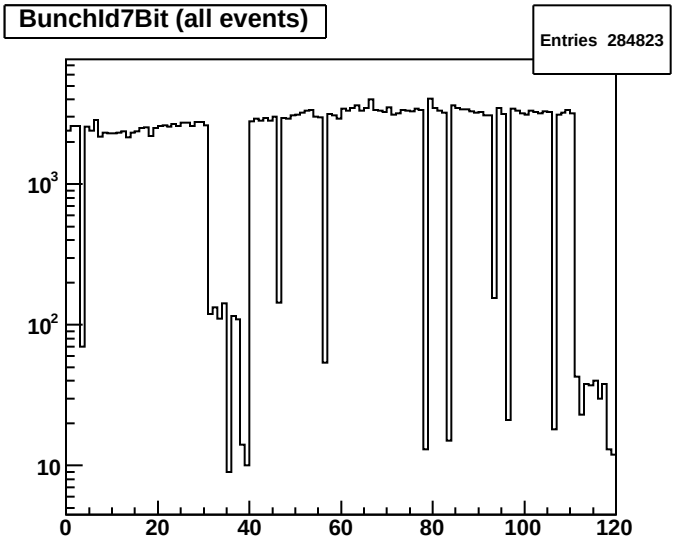

Last DSM Ch3 Input
Entries 4557168
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

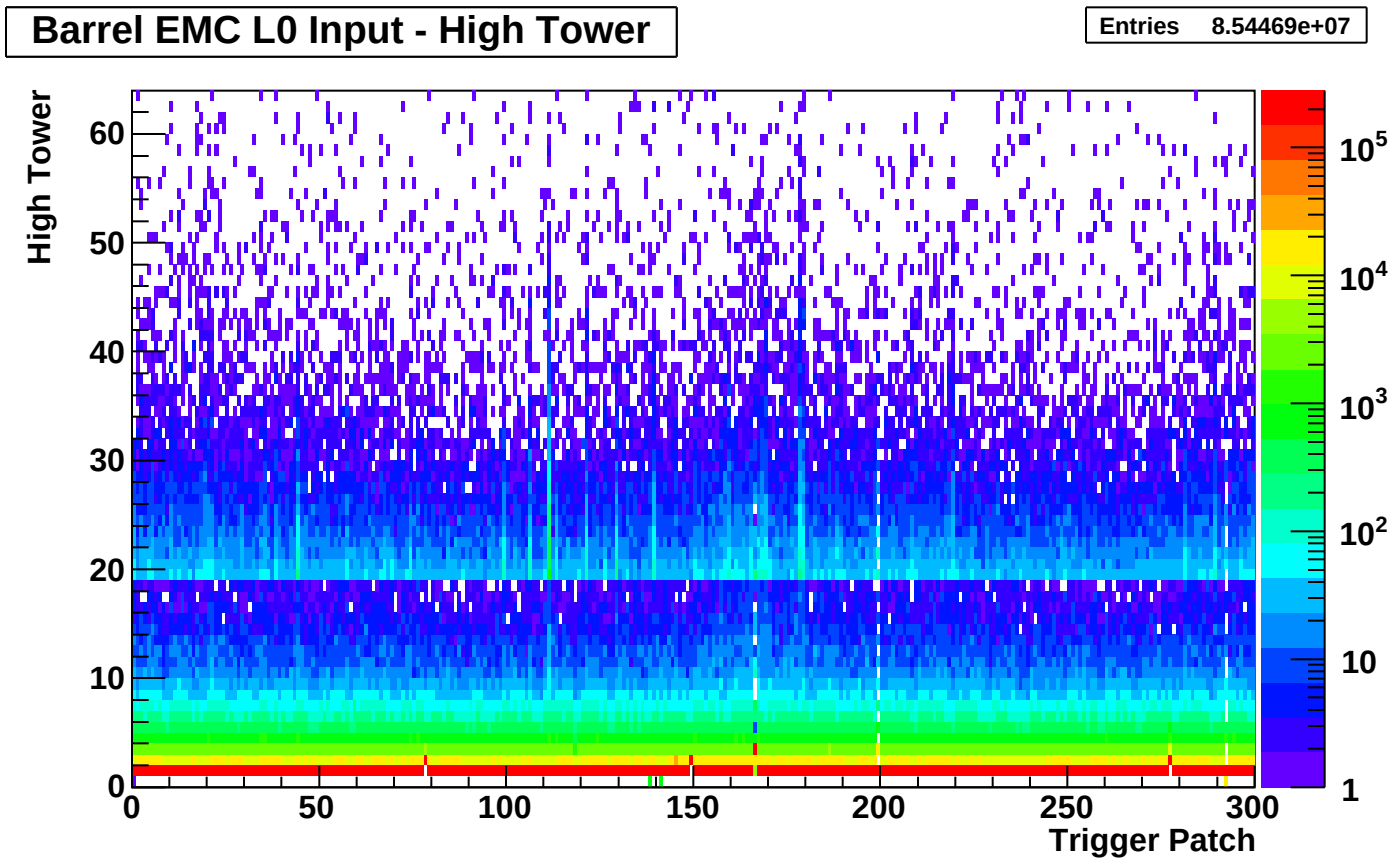

Last DSM Ch3 Input
Entries 7
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

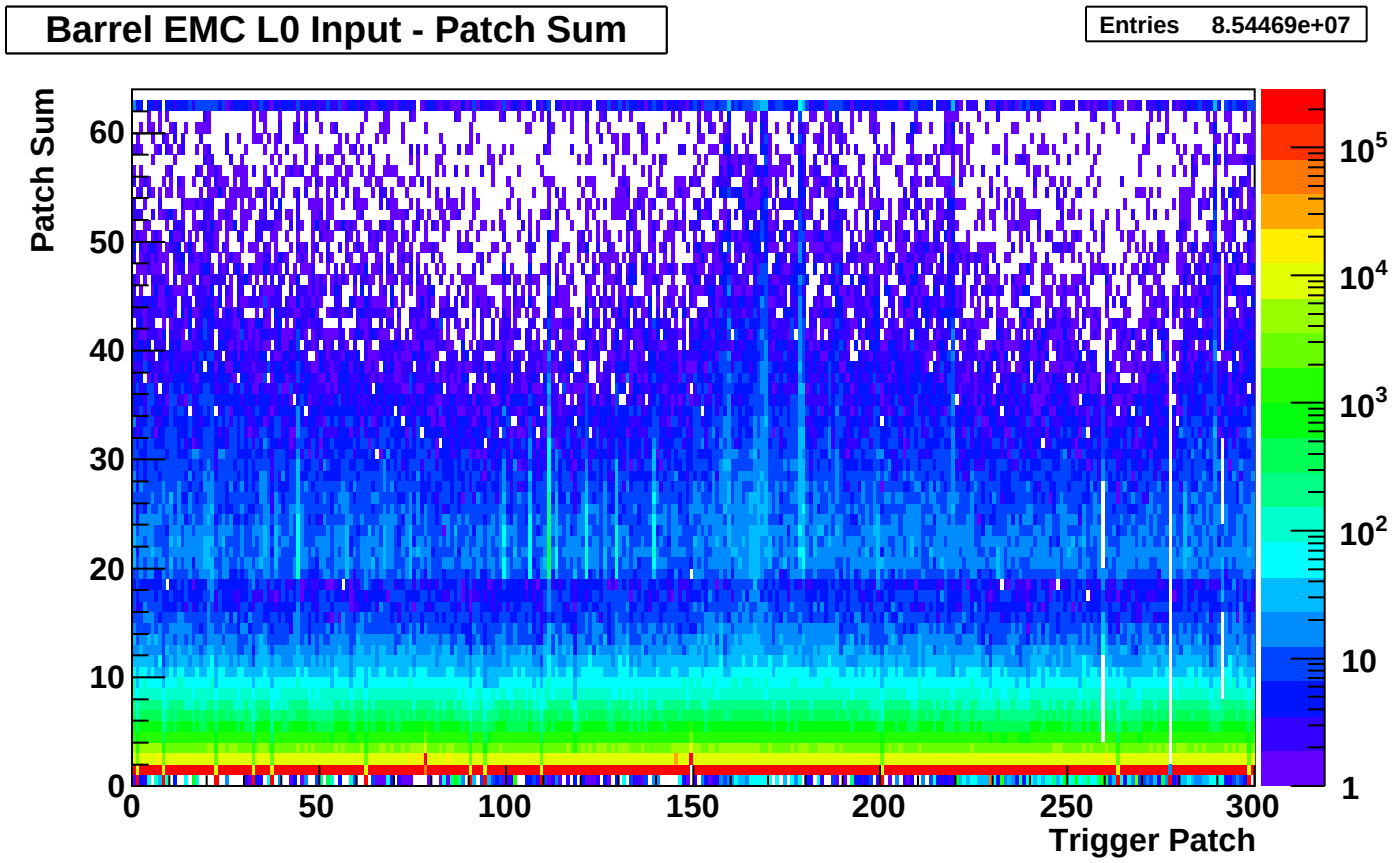

TCU bits
Entries 4557168
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

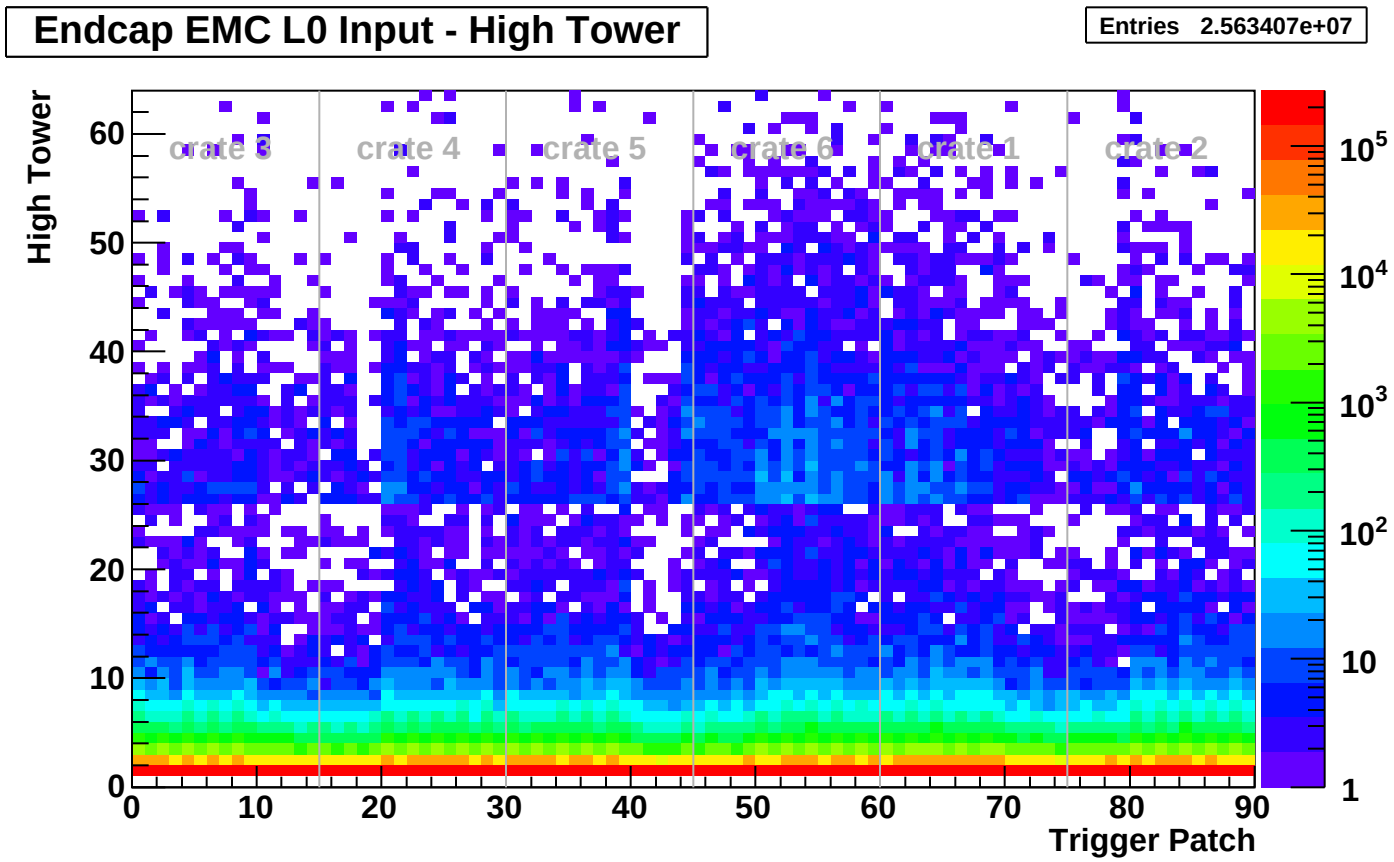

TCU bits
Entries 381596
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

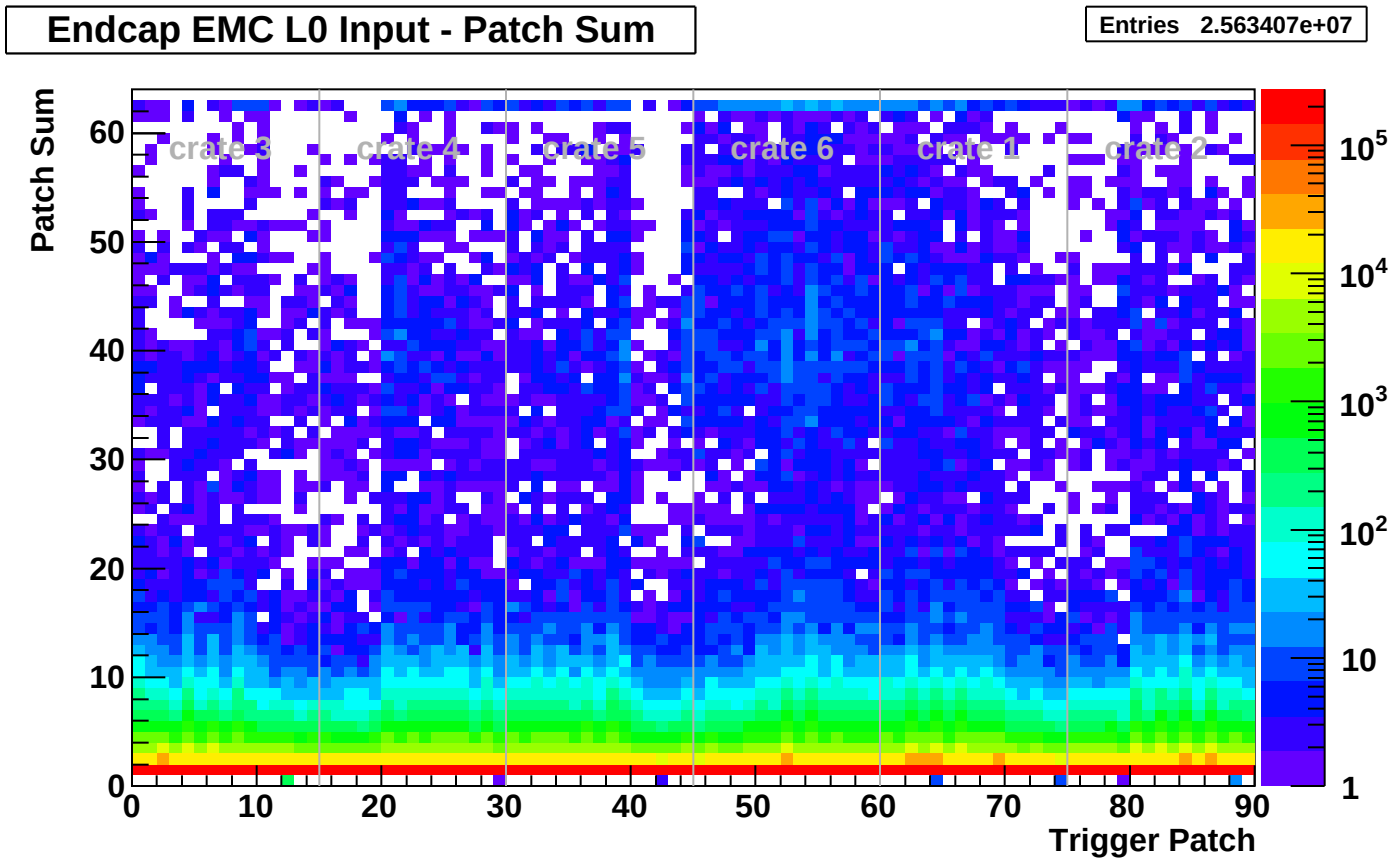

BunchId7Bit (BHT3)
Entries 4860

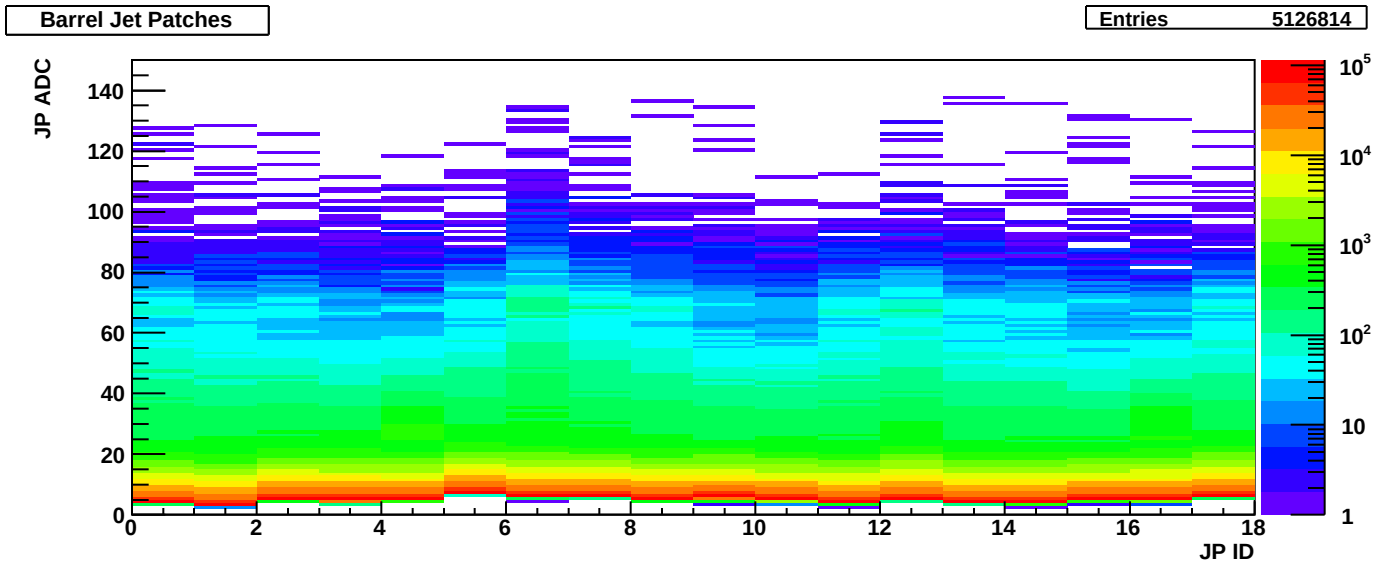

BunchId7Bit (all events)
Entries 284823

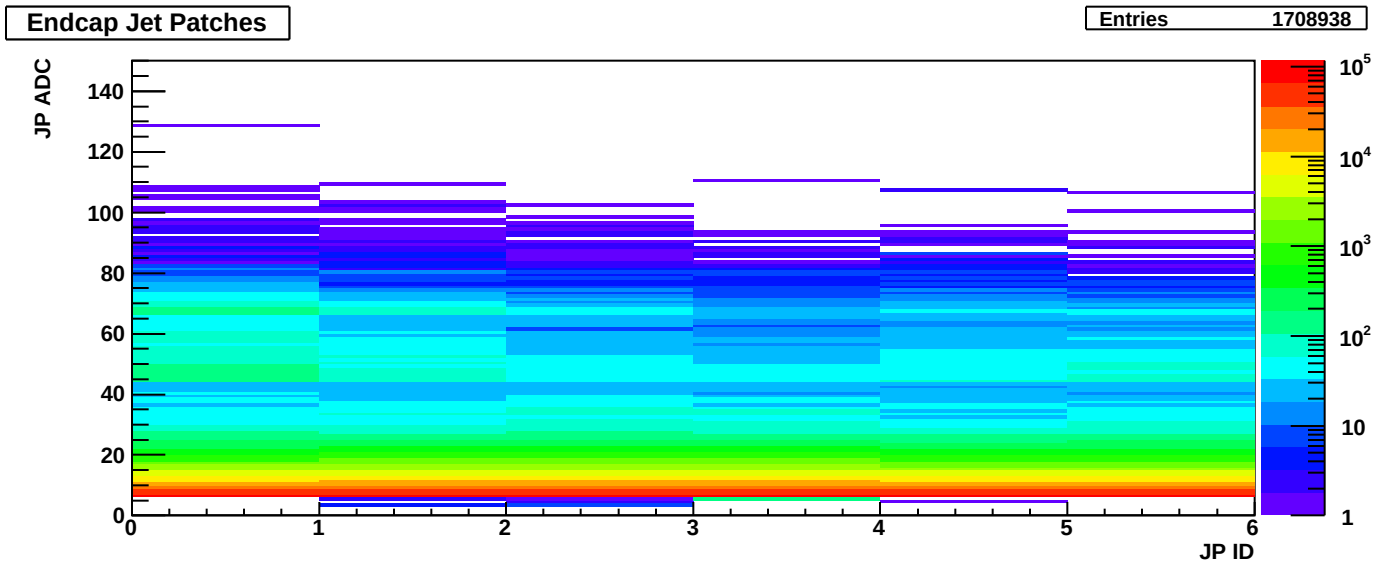

Barrel EMC L0 Input - High Tower
Entries 8.54469e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

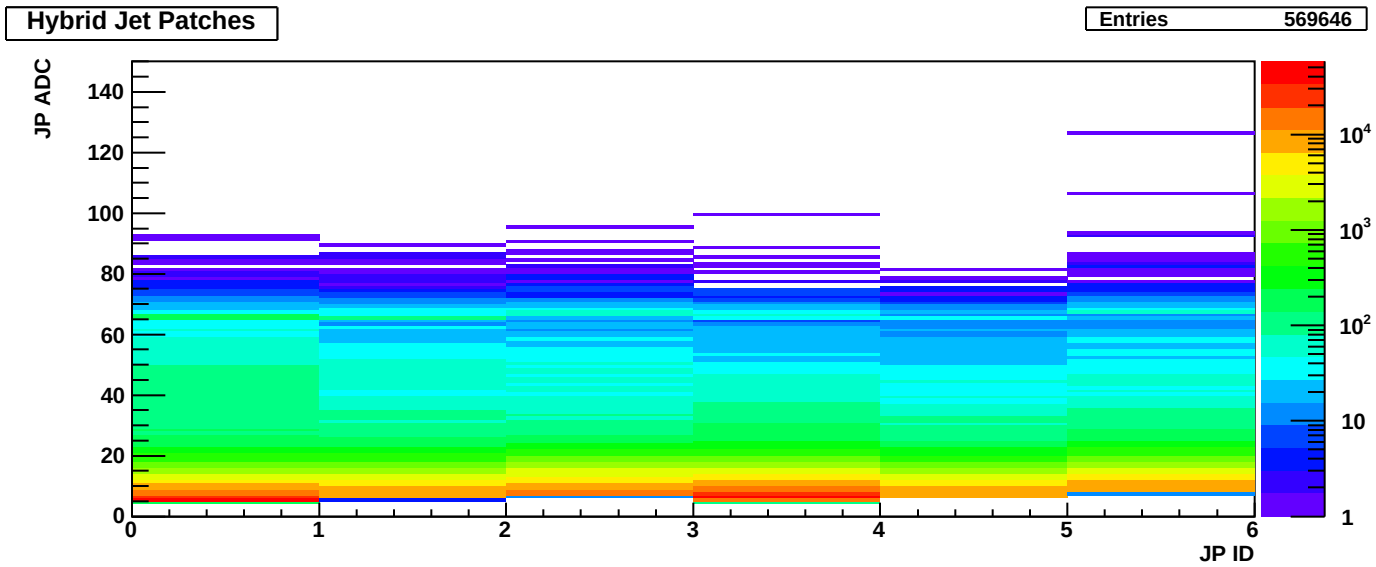

Barrel EMC L0 Input - Patch Sum
Entries 8.54469e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

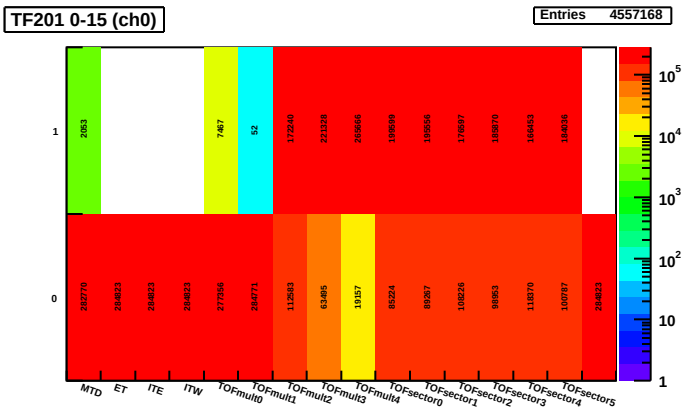

Endcap EMC L0 Input - High Tower
Entries 2.563407e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

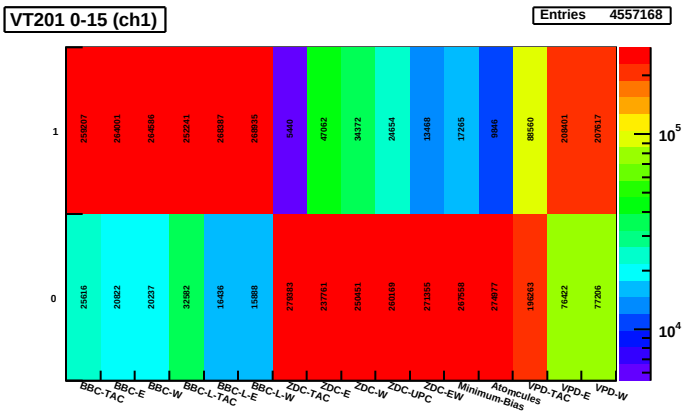

Endcap EMC L0 Input - Patch Sum
Entries 2.563407e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

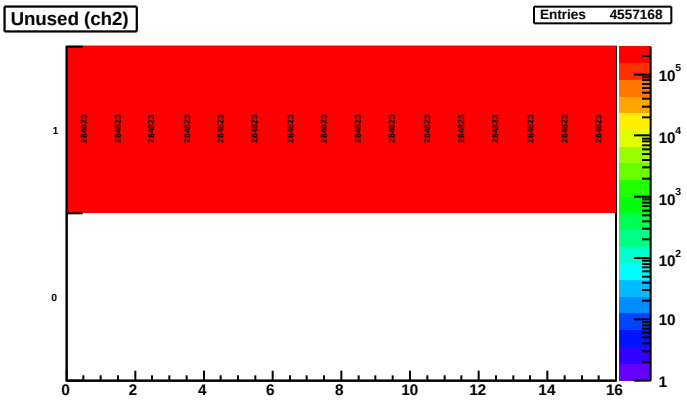

Barrel Jet Patches
Entries 5126814
JP ADC
JP ID

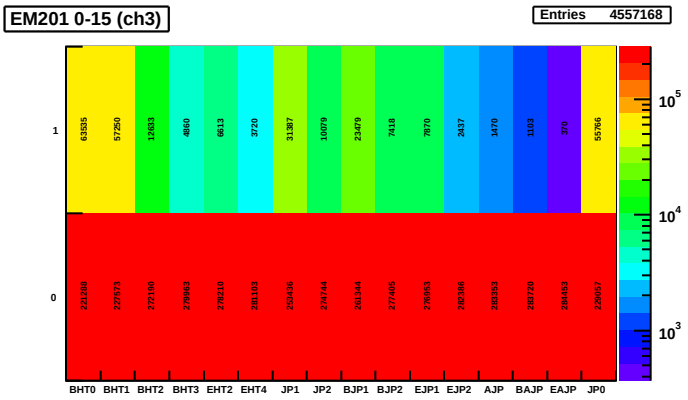

Endcap Jet Patches
Entries 1708938
JP ADC
JP ID

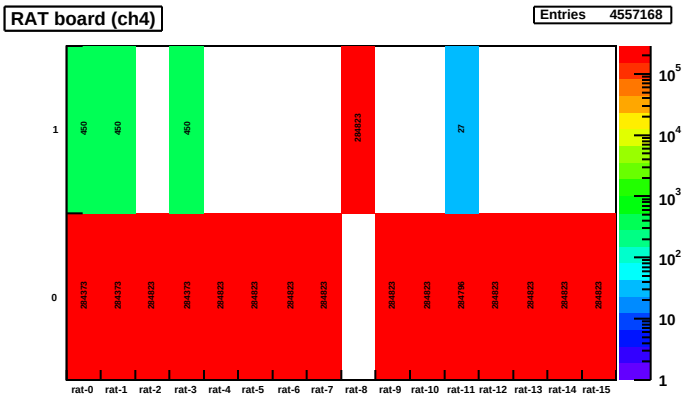

Hybrid Jet Patches
Entries 569646
JP ADC
JP ID

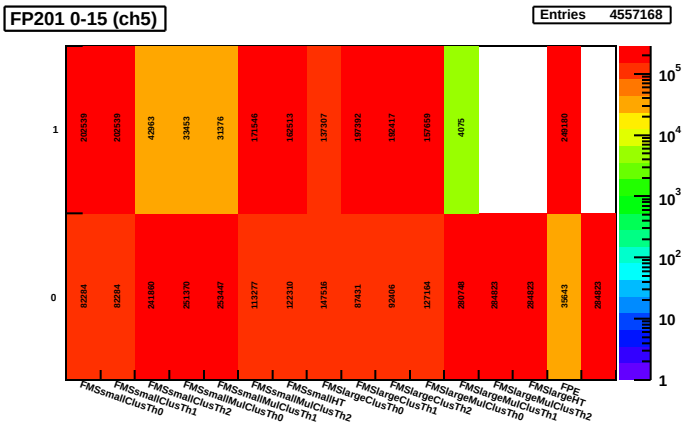

TF201 0-15 (ch0)
Entries 4557168
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

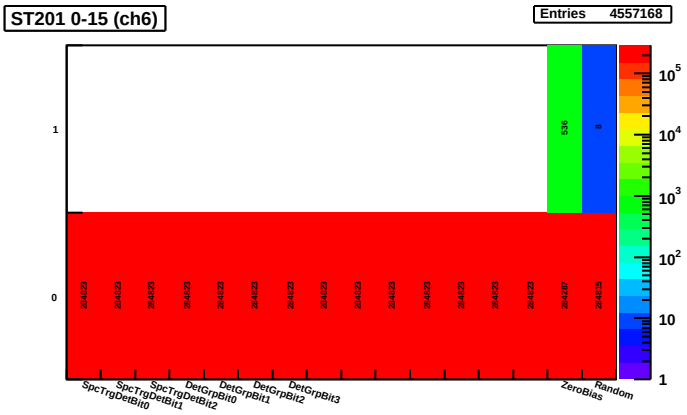

VT201 0-15 (ch1)
Entries 4557168
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

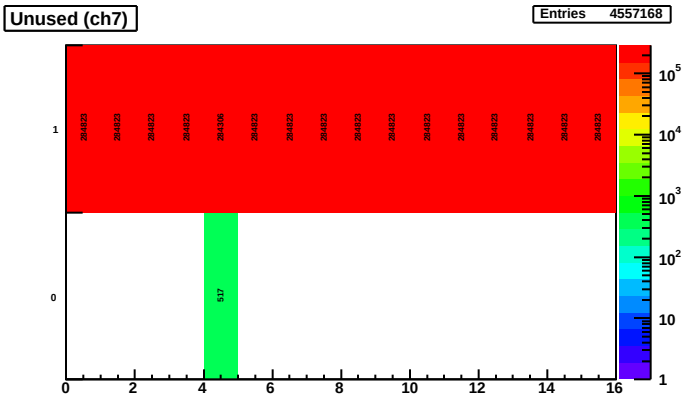

Unused (ch2)
Entries 4557168

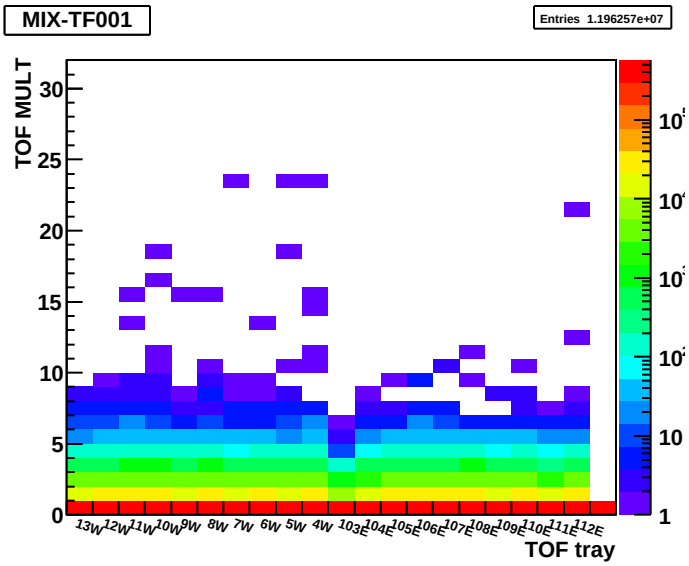

EM201 0-15 (ch3)
Entries 4557168
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

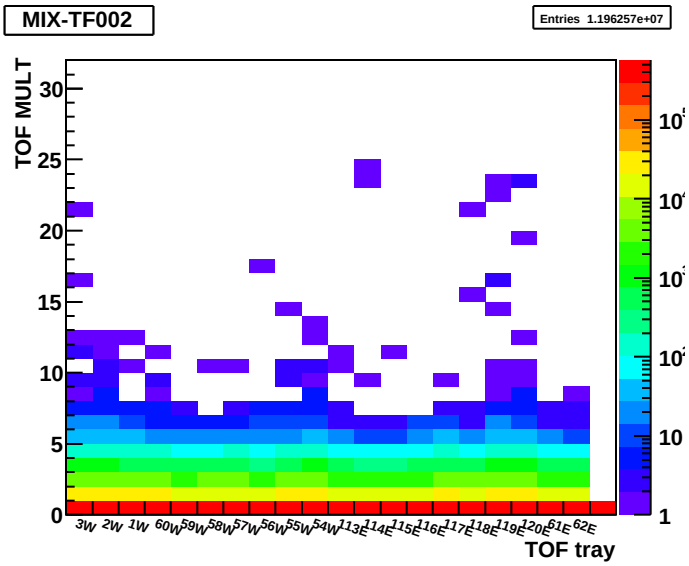

RAT board (ch4)
Entries 4557168
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

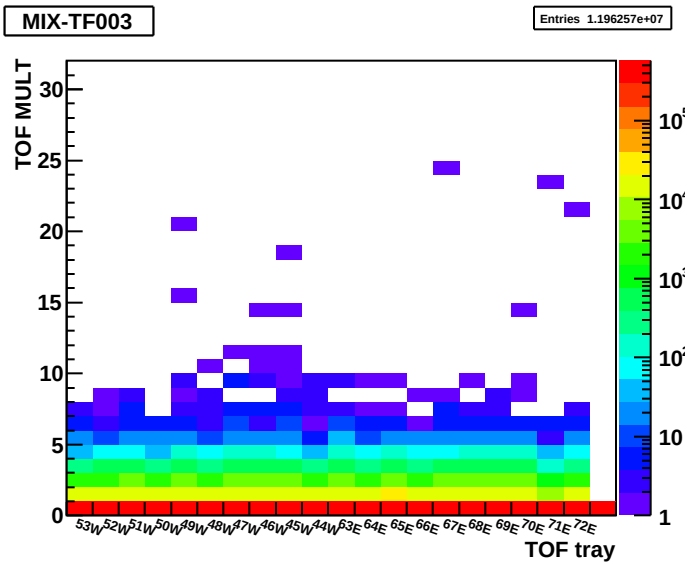

FP201 0-15 (ch5)
Entries 4557168
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

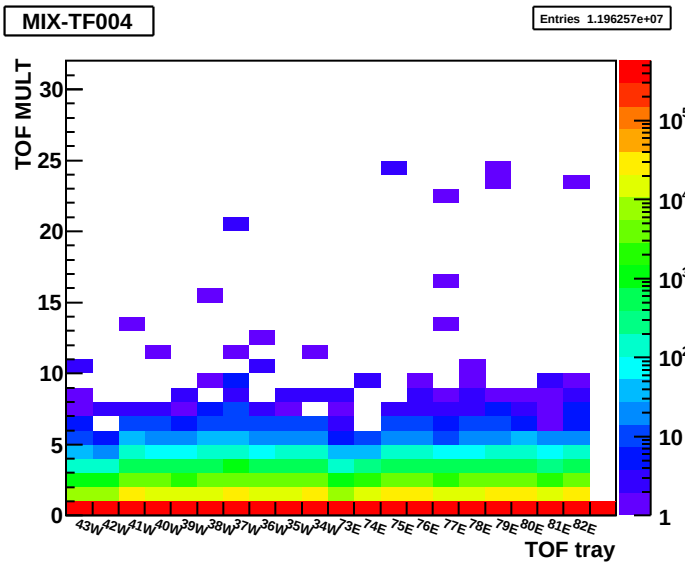

ST201 0-15 (ch6)
Entries 4557168
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

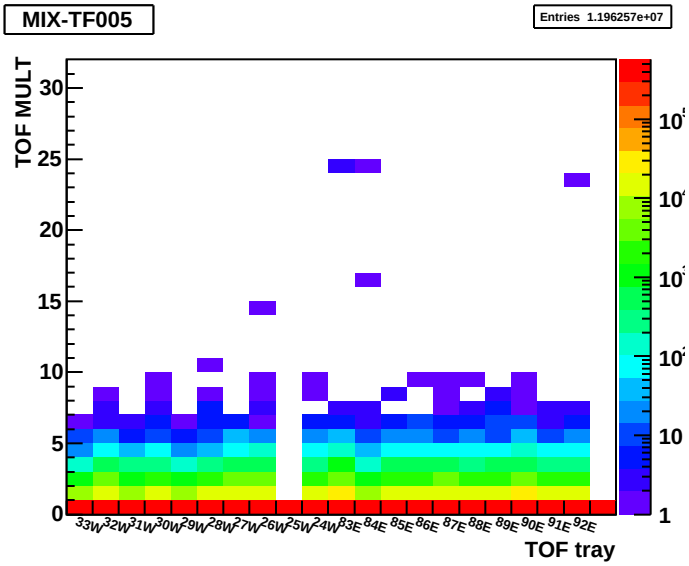

Unused (ch7)
Entries 4557168

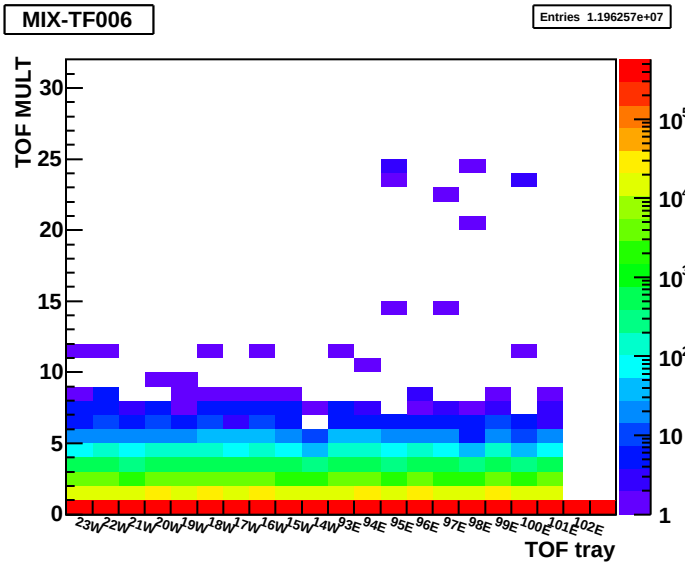

MIX-TF001
Entries 1.196257e+07
TOF MULT
TOF tray

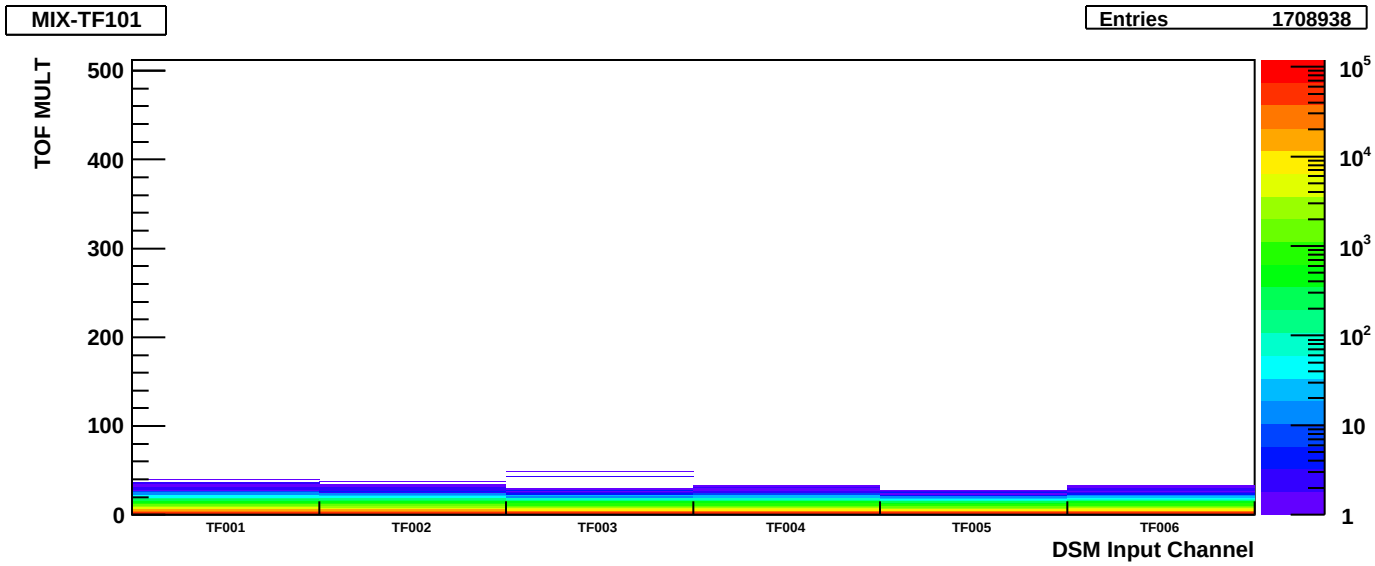

MIX-TF002
Entries 1.196257e+07
TOF MULT
TOF tray

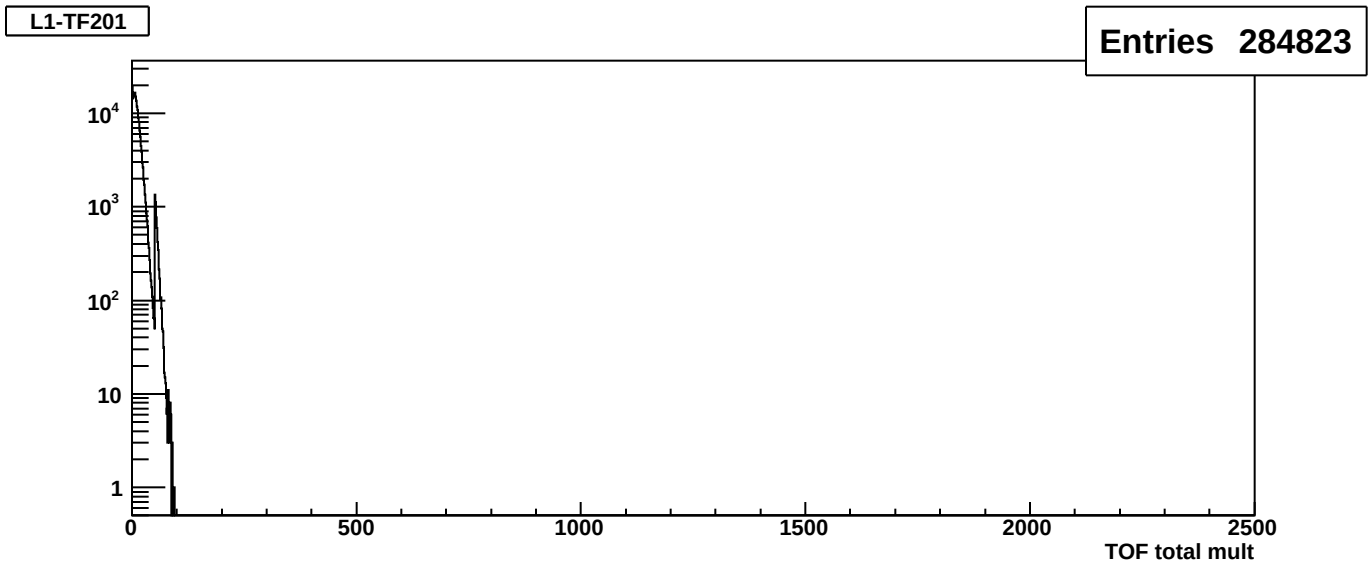

MIX-TF003
Entries 1.196257e+07
TOF MULT
TOF tray

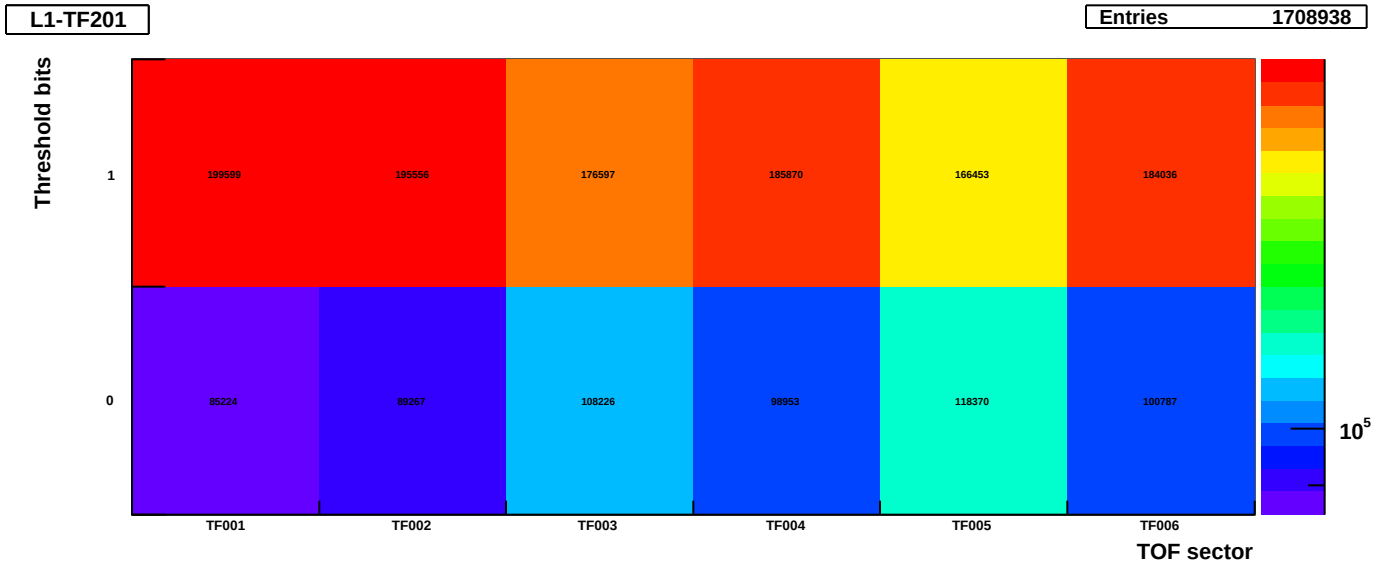

MIX-TF004
Entries 1.196257e+07
TOF MULT
TOF tray

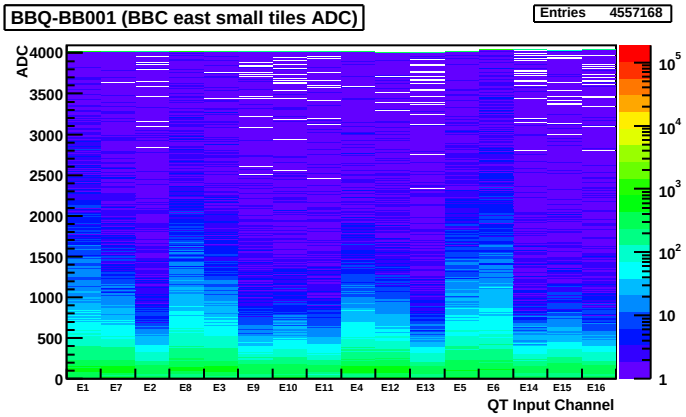

MIX-TF005
Entries 1.196257e+07
TOF MULT
TOF tray

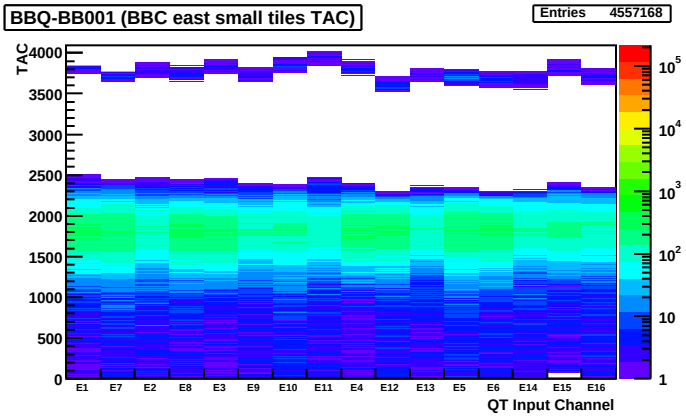

MIX-TF006
Entries 1.196257e+07
TOF MULT
TOF tray

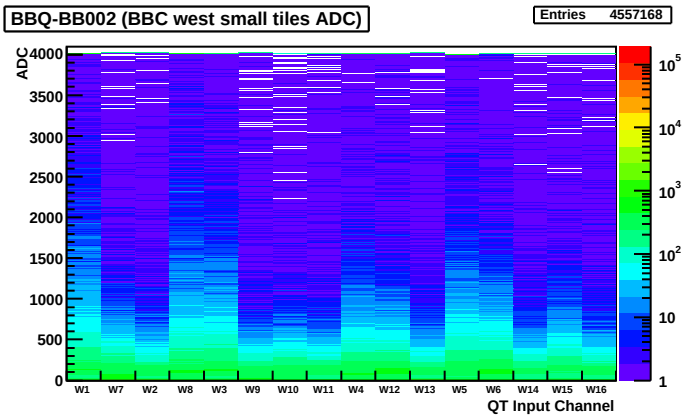

MIX-TF101
Entries 1708938
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

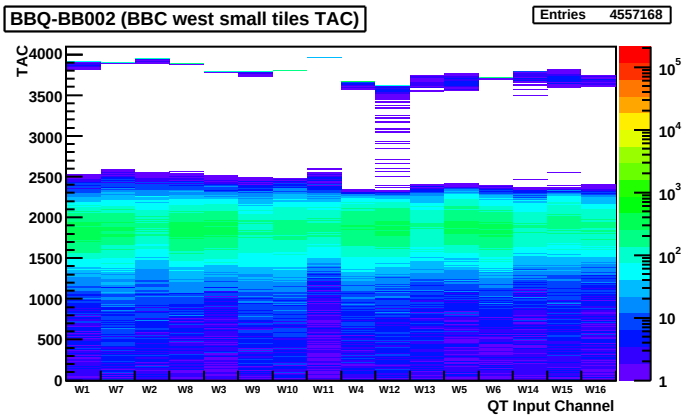

L1-TF201
Entries 284823
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

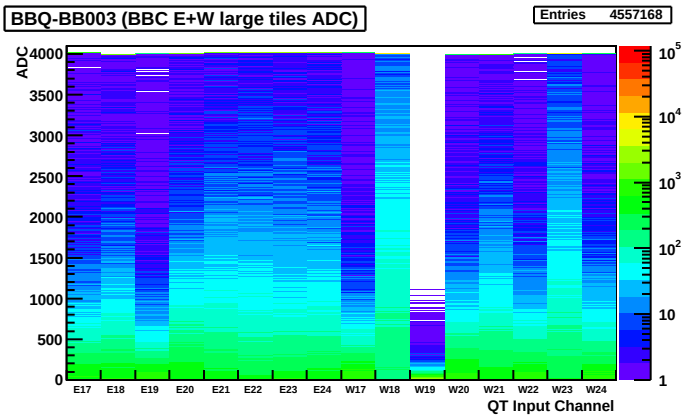

L1-TF201
Entries 1708938
Threshold bits
1
0
199599
195556
176597
185870
166453
184036
85224
89267
108226
98953
118370
100787
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

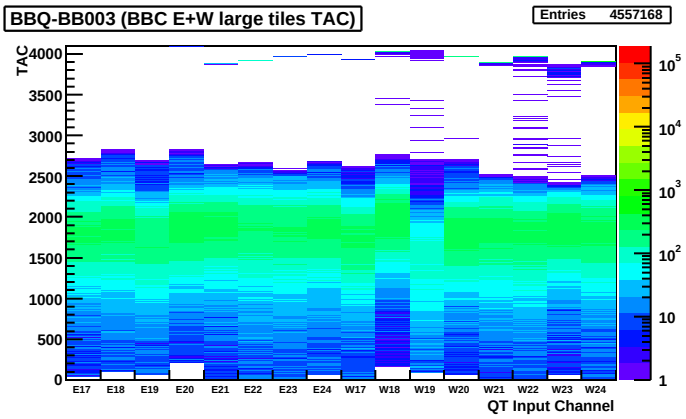

BBQ-BB001 (BBC east small tiles ADC)
Entries 4557168
ADC
QT Input Channel

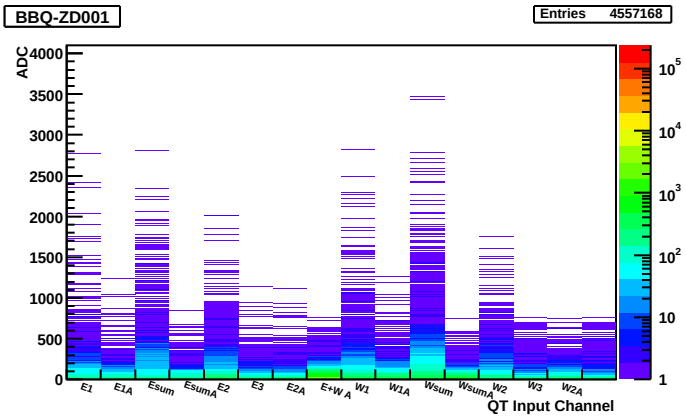

BBQ-BB001 (BBC east small tiles TAC)
Entries 4557168
TAC
QT Input Channel

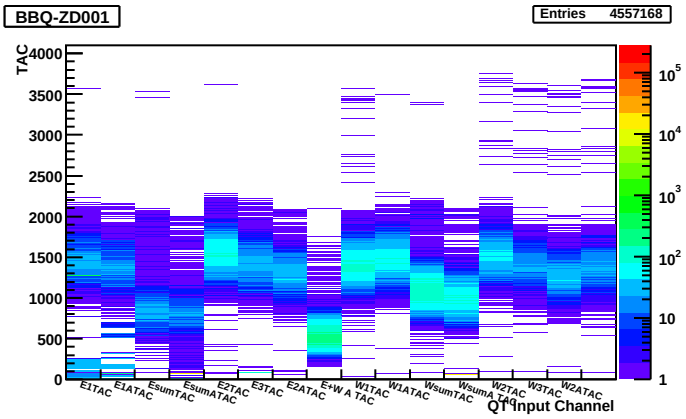

BBQ-BB002 (BBC west small tiles ADC)
Entries 4557168
ADC
QT Input Channel

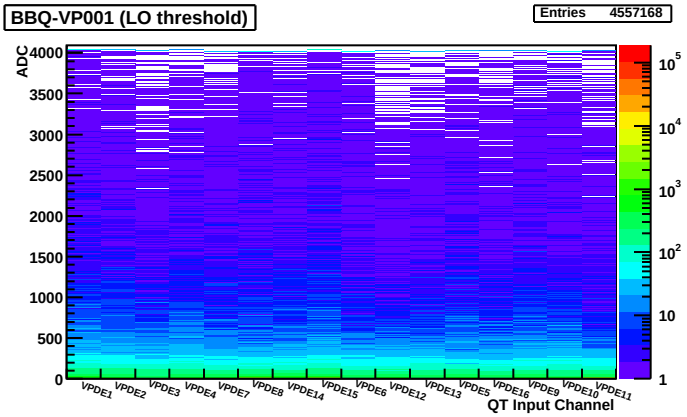

BBQ-BB002 (BBC west small tiles TAC)
Entries 4557168
TAC
QT Input Channel

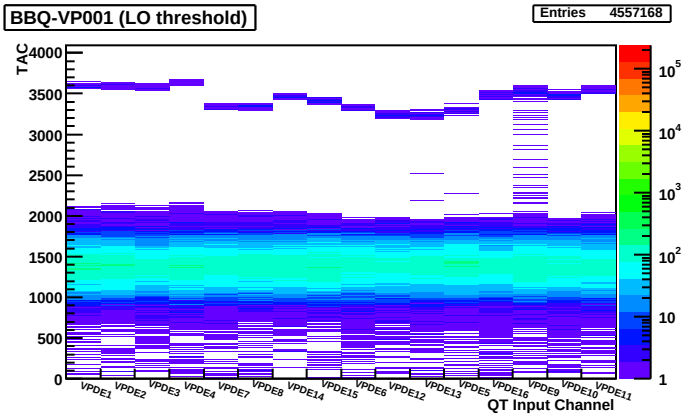

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4557168
ADC
QT Input Channel

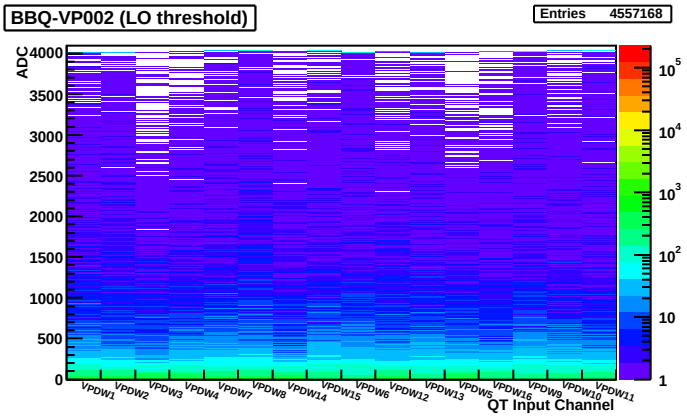

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4557168
TAC
QT Input Channel

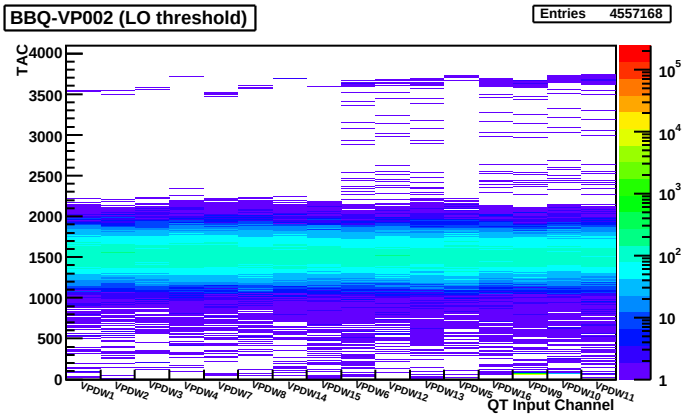

BBQ-ZD001
Entries 4557168
ADC
QT Input Channel

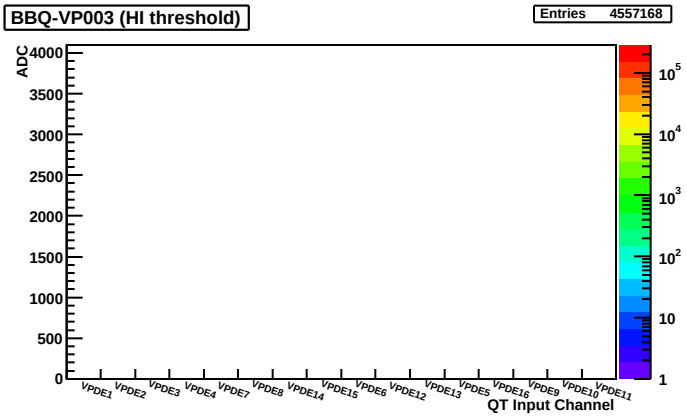

BBQ-ZD001
Entries 4557168
TAC
QT Input Channel

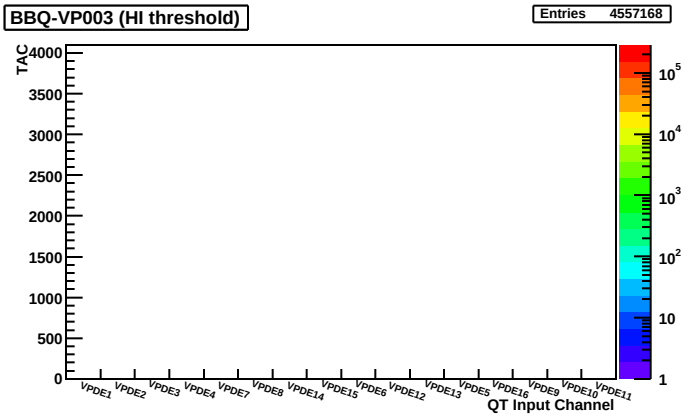

BBQ-VP001 (LO threshold)
Entries 4557168
ADC
QT Input Channel

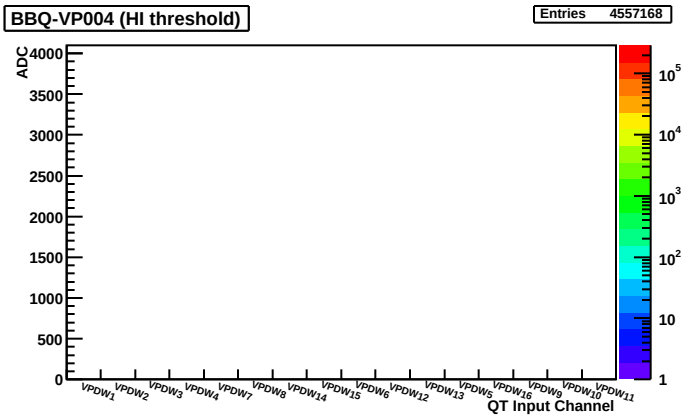

BBQ-VP001 (LO threshold)
Entries 4557168
TAC
QT Input Channel

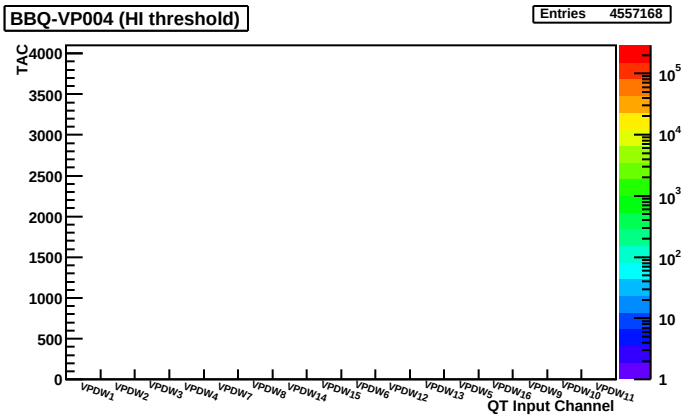

BBQ-VP002 (LO threshold)
Entries 4557168
ADC
QT Input Channel

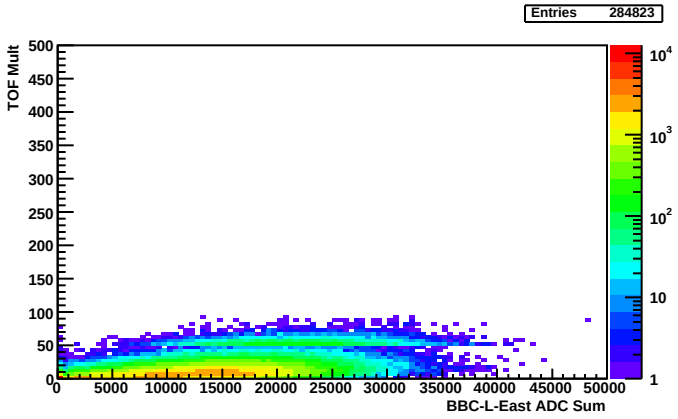

BBQ-VP002 (LO threshold)
Entries 4557168
TAC
QT Input Channel

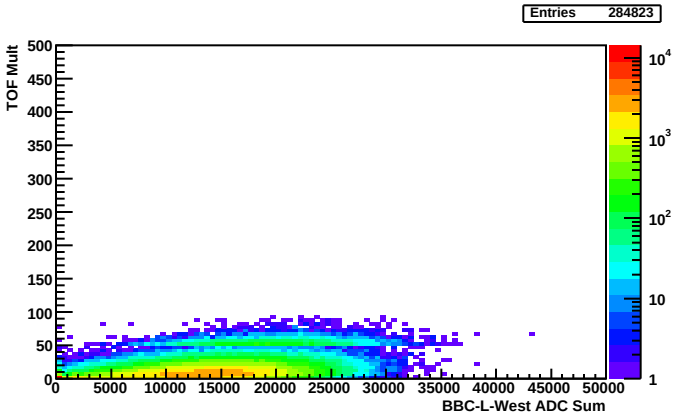

BBQ-VP003 (HI threshold)
Entries 4557168
ADC
QT Input Channel

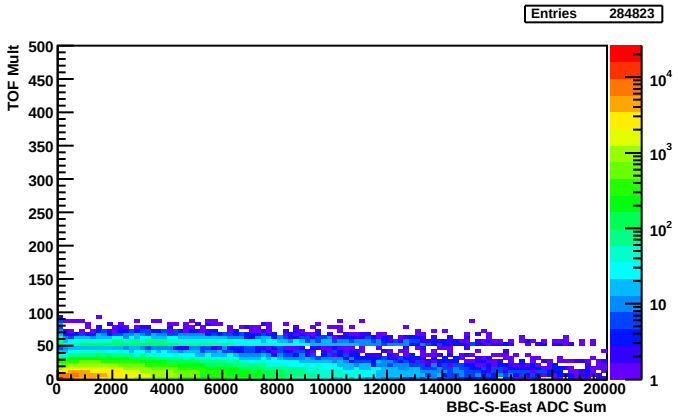

BBQ-VP003 (HI threshold)
Entries 4557168
TAC
QT Input Channel

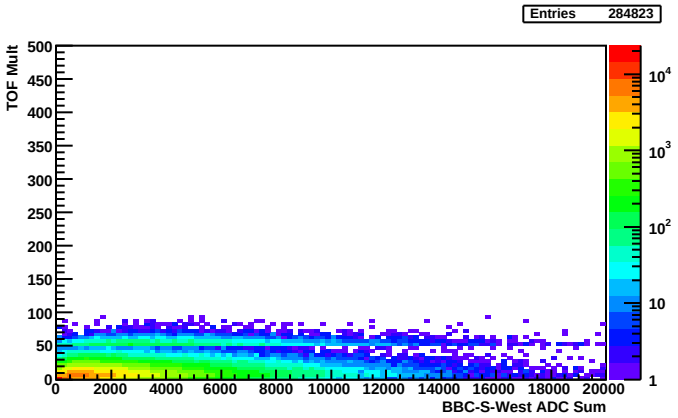

BBQ-VP004 (HI threshold)
Entries 4557168
ADC
QT Input Channel

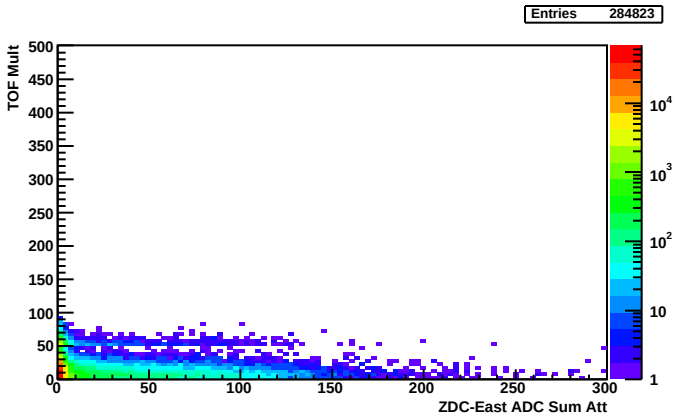

BBQ-VP004 (HI threshold)
Entries 4557168
TAC
QT Input Channel

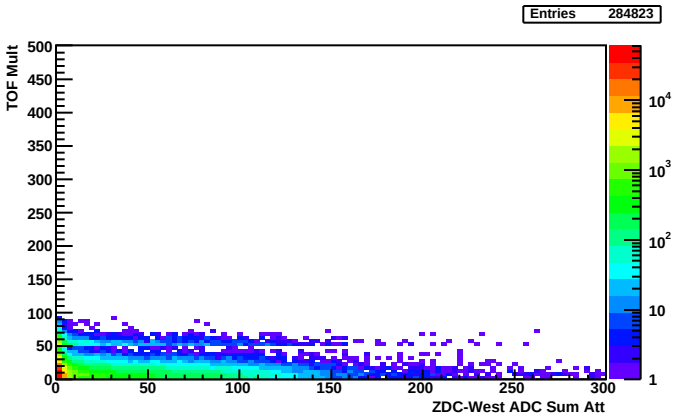

Entries 284823
TOF Mult
BBC-L-East ADC Sum

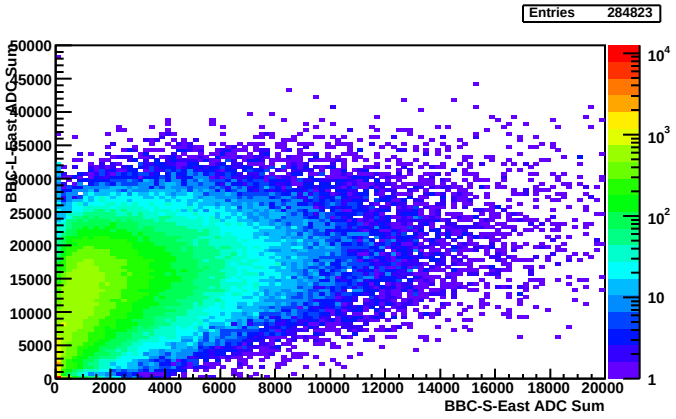

Entries 284823
TOF Mult
BBC-L-West ADC Sum

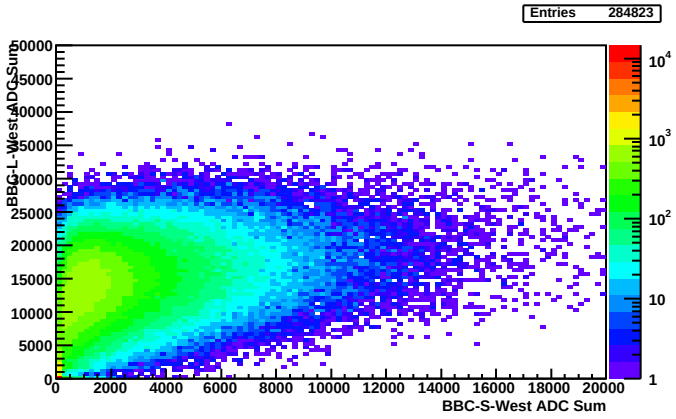

Entries 284823
TOF Mult
BBC-S-East ADC Sum

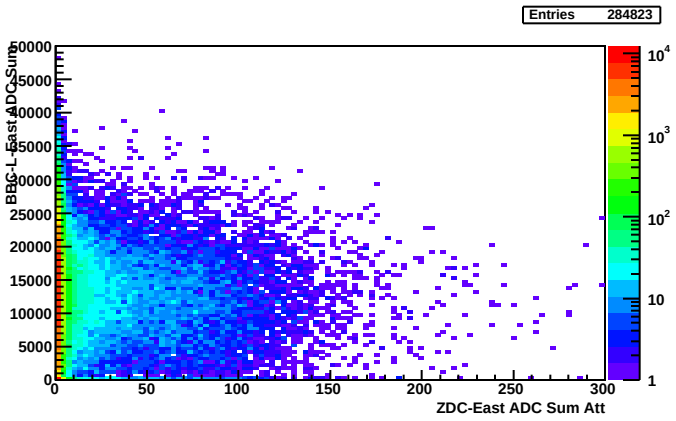

Entries 284823
TOF Mult
BBC-S-West ADC Sum

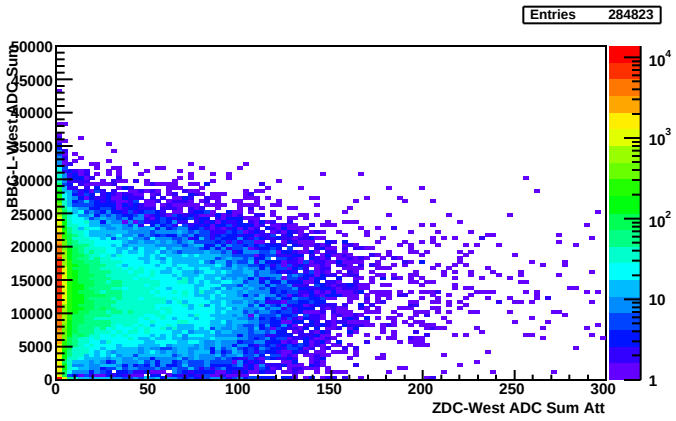

Entries 284823
TOF Mult
ZDC-East ADC Sum Att

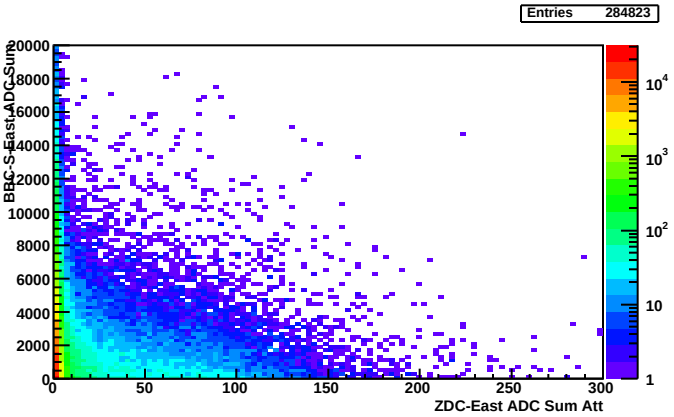

Entries 284823
TOF Mult
ZDC-West ADC Sum Att

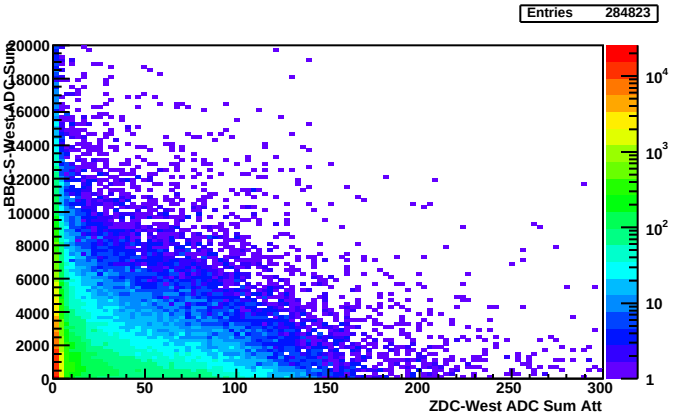

Entries 284823
BBC-L-East ADC Sum
BBC-S-East ADC Sum

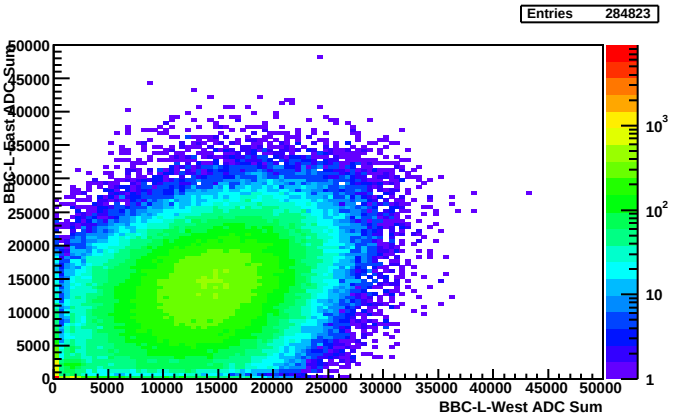

Entries 284823
BBC-L-West ADC Sum
BBC-S-West ADC Sum

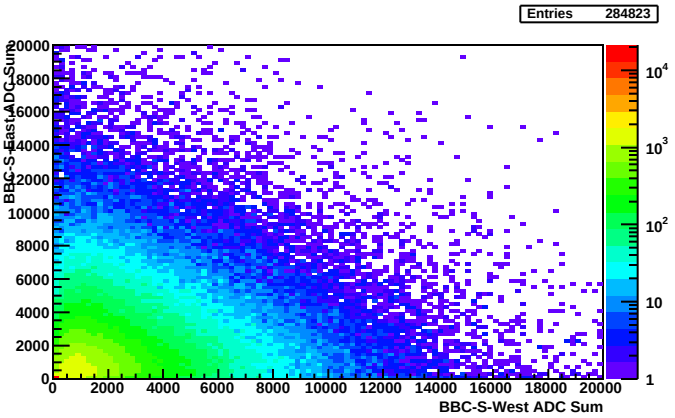

Entries 284823
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

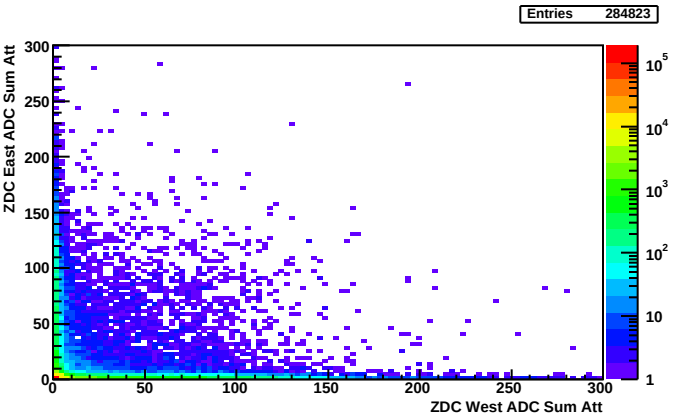

Entries 284823
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

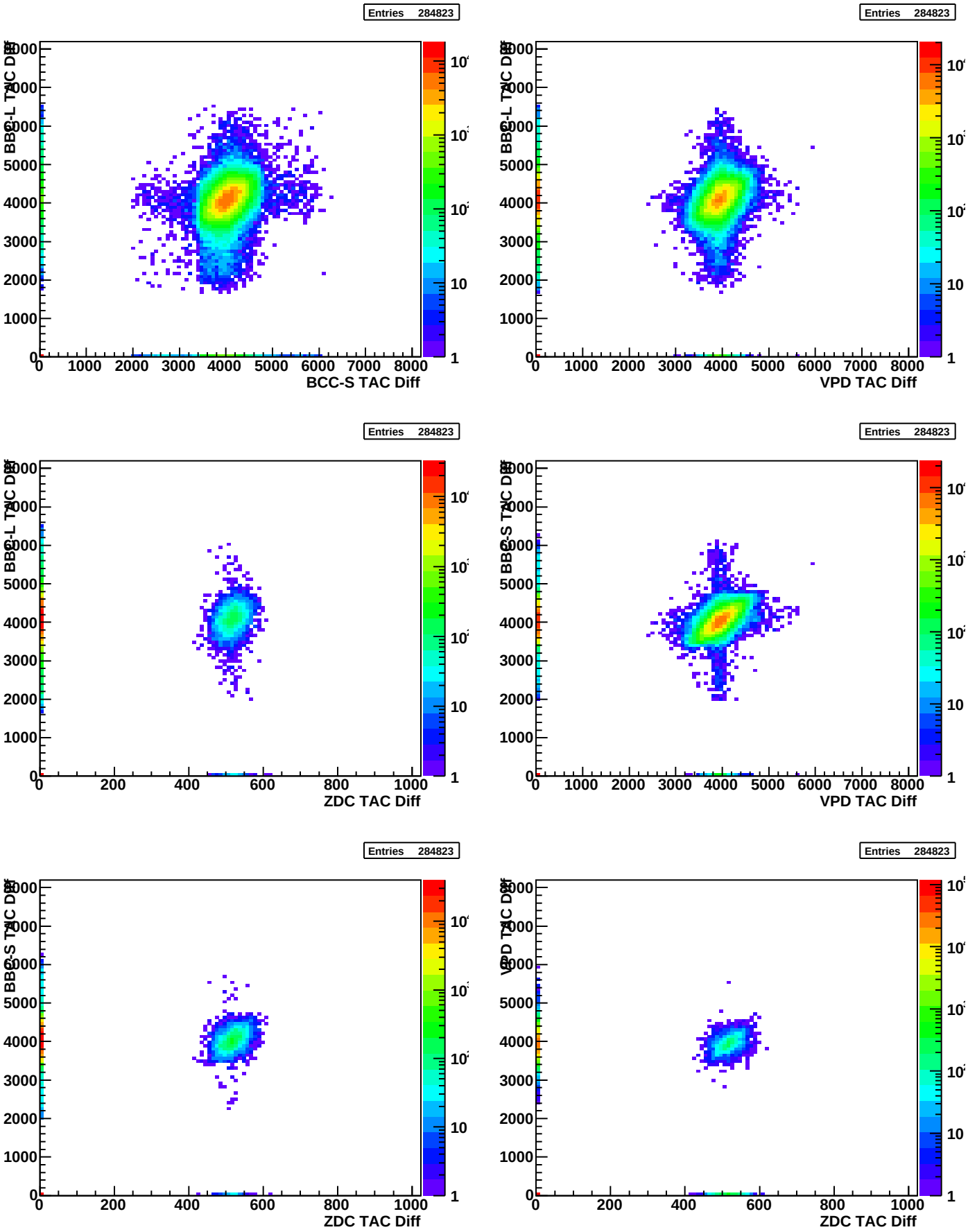

Entries 284823
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

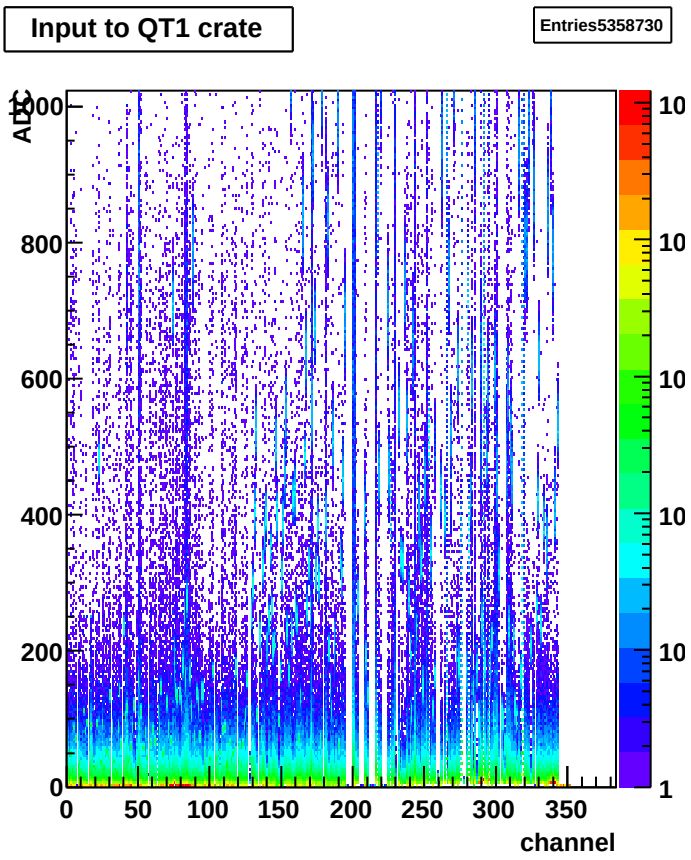

Entries 284823
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

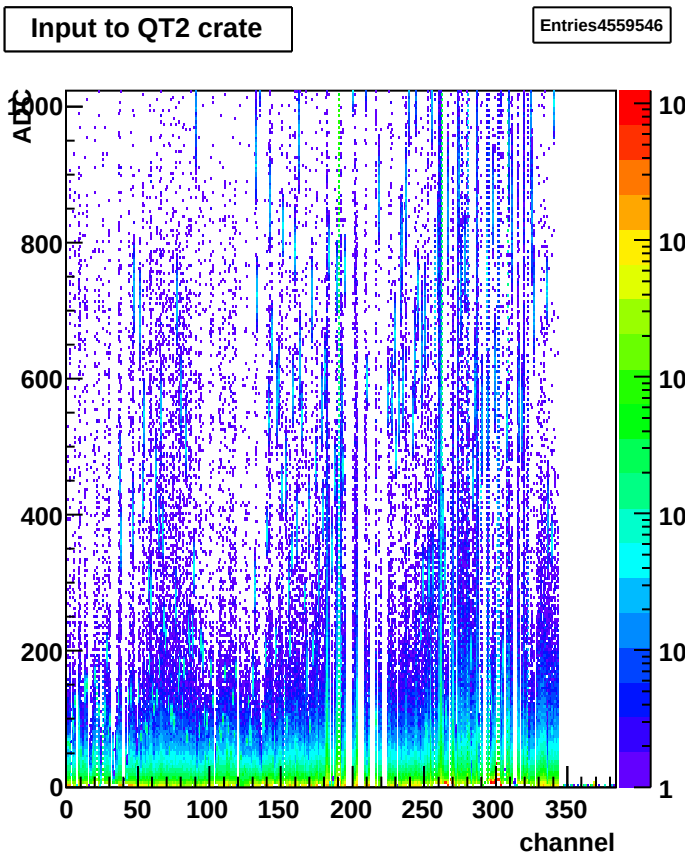

Entries 284823
BBC-L-East ADC Sum
BBC-L-West ADC Sum

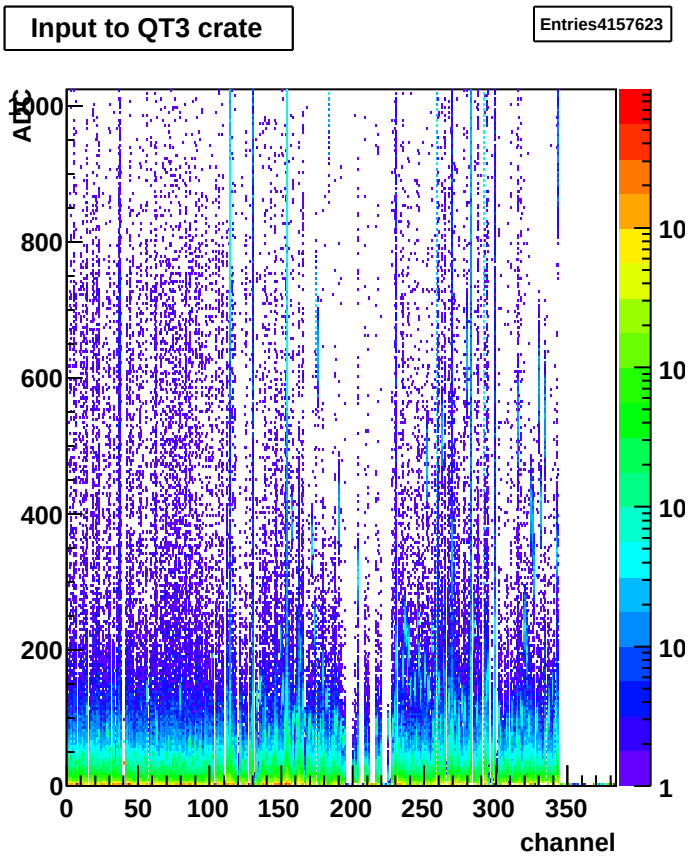

Entries 284823
BBC-S-East ADC Sum
BBC-S-West ADC Sum

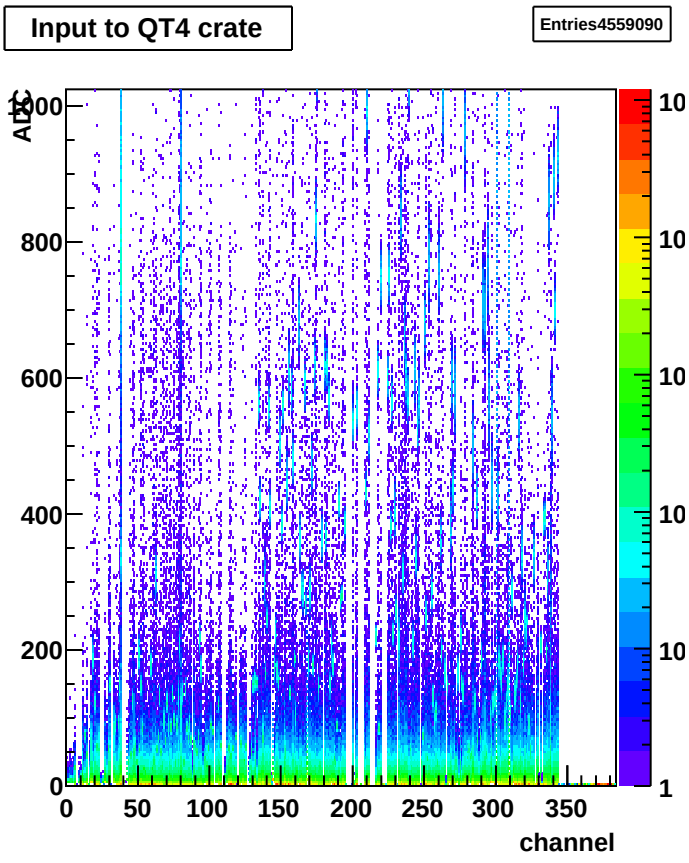

Entries 284823
ZDC East ADC Sum Att
ZDC West ADC Sum Att

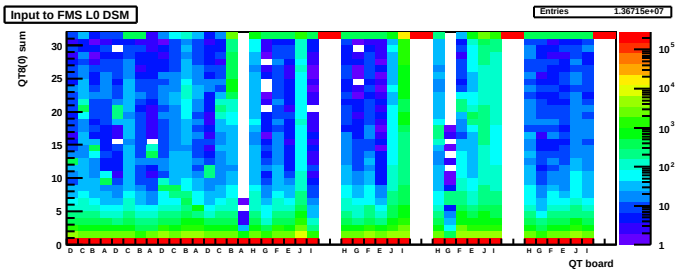

Entries 284823
BBC-L TAC Diff
BCC-S TAC Diff
Entries 284823
BBC-L TAC Diff
VPD TAC Diff
Entries 284823
BBC-L TAC Diff
ZDC TAC Diff
Entries 284823
BBC-S TAC Diff
VPD TAC Diff
Entries 284823
BBC-S TAC Diff
ZDC TAC Diff
Entries 284823
VPD TAC Diff
ZDC TAC Diff

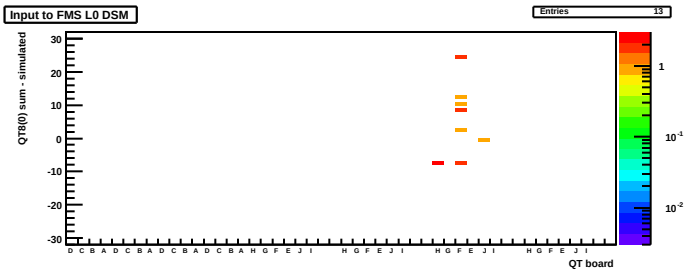

Input to QT1 crate
Entries5358730
ADC
channel

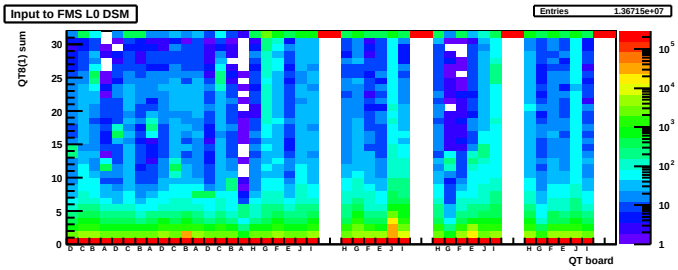

Input to QT2 crate
Entries4559546
ADC
channel

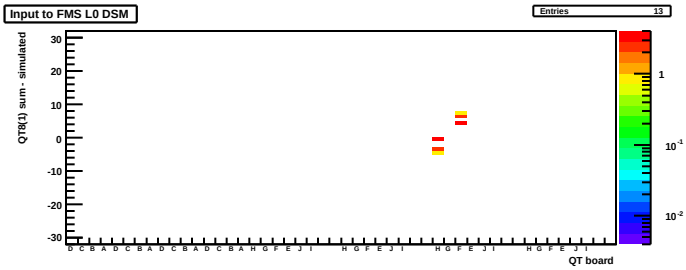

Input to QT3 crate
Entries4157623
ADC
channel

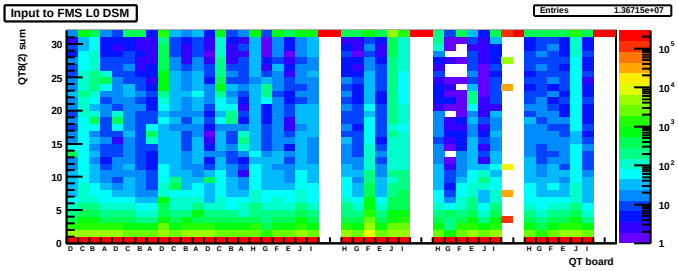

Input to QT4 crate
Entries4559090
ADC
channel

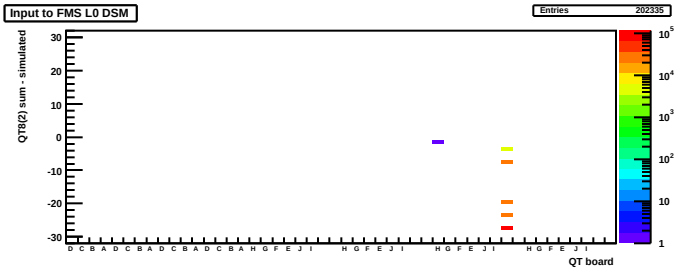

Input to FMS L0 DSM
Entries 1.36715e+07
QT8(0) sum
QT board

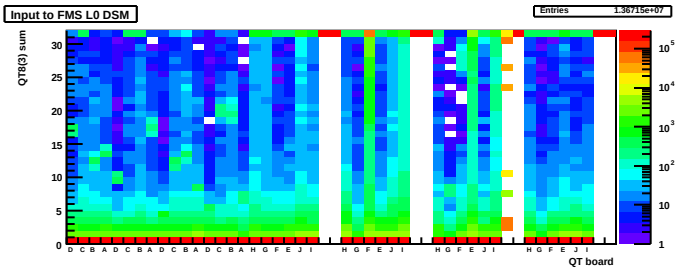

Input to FMS L0 DSM
Entries 13
QT8(0) sum - simulated
QT board

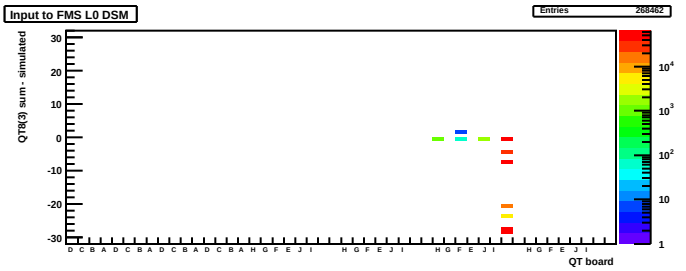

Input to FMS L0 DSM
Entries 1.36715e+07
QT8(1) sum
QT board

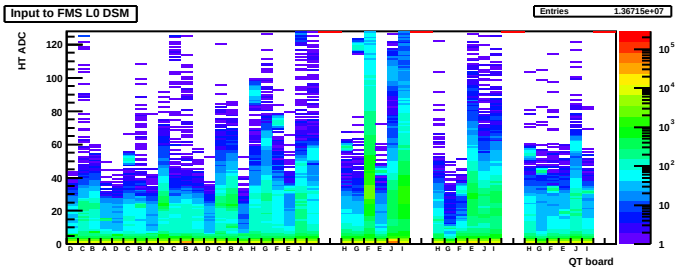

Input to FMS L0 DSM
Entries 13
QT8(1) sum - simulated
QT board

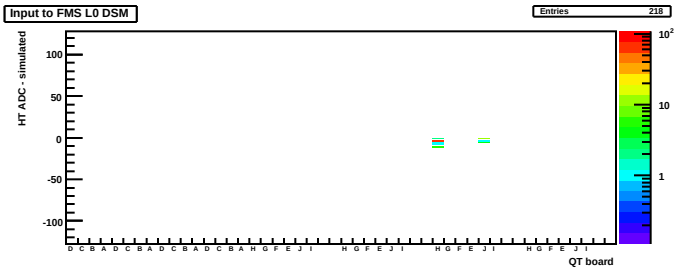

Input to FMS L0 DSM
Entries 1.36715e+07
QT8(2) sum
QT board

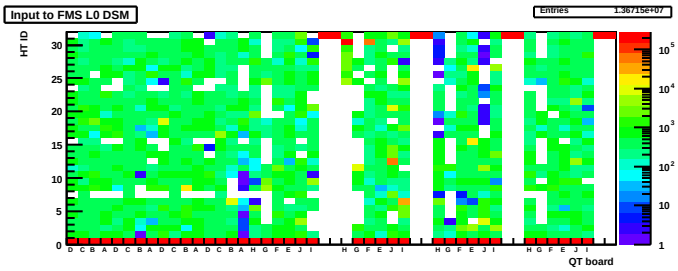

Input to FMS L0 DSM
Entries 202335
QT8(2) sum - simulated
QT board

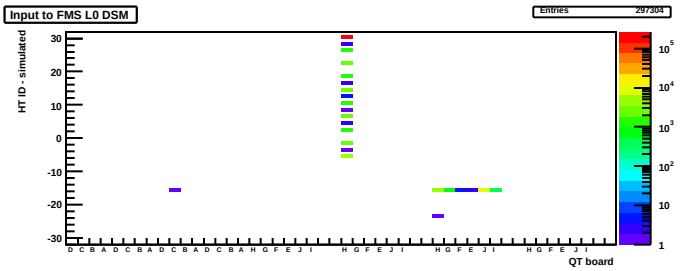

Input to FMS L0 DSM
Entries 1.36715e+07
QT8(3) sum
QT board

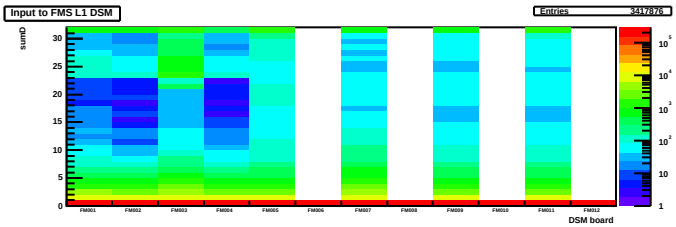

Input to FMS L0 DSM
Entries 268462
QT8(3) sum - simulated
QT board

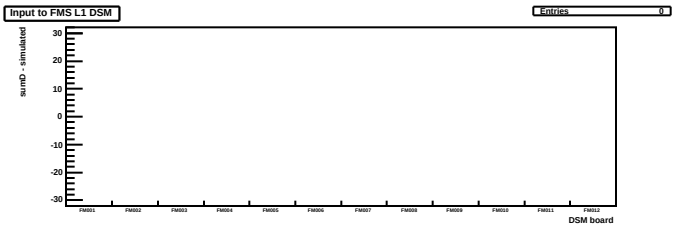

Input to FMS L0 DSM
Entries 1.36715e+07
HT ADC
QT board

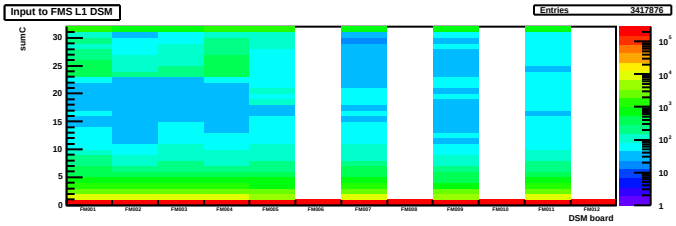

Input to FMS L0 DSM
Entries 218
HT ADC - simulated
QT board

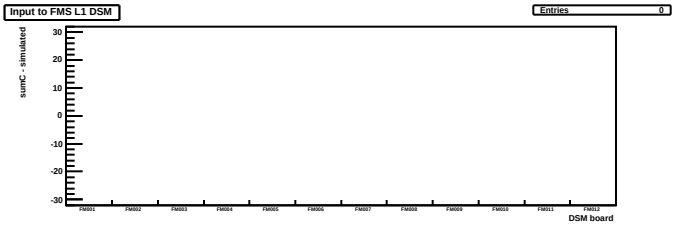

Input to FMS L0 DSM
Entries 1.36715e+07
HT ID
QT board

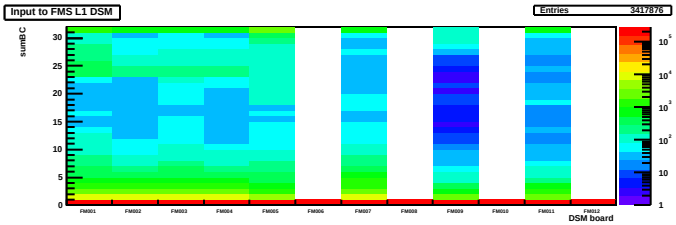

Input to FMS L0 DSM
Entries 297304
HT ID - simulated
QT board

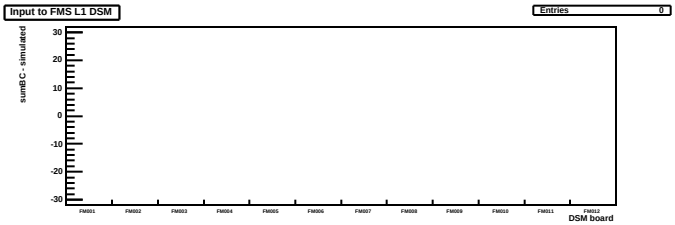

Input to FMS L1 DSM
Entries 3417876
sumD
DSM board

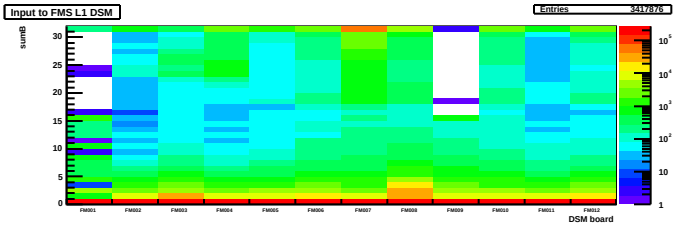

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

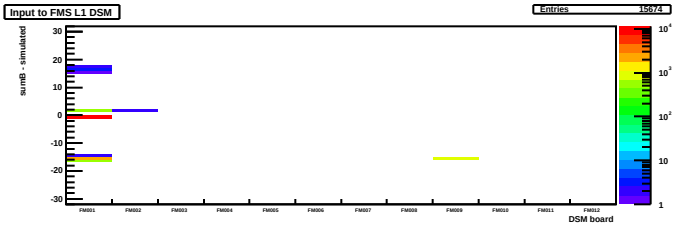

Input to FMS L1 DSM
Entries 3417876
sumC
DSM board

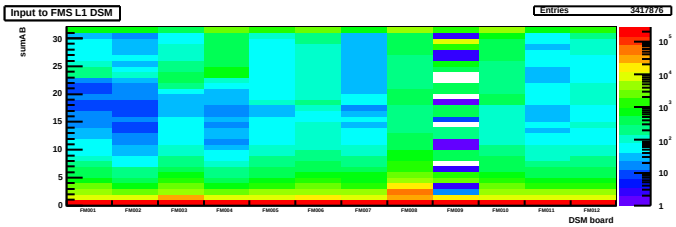

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

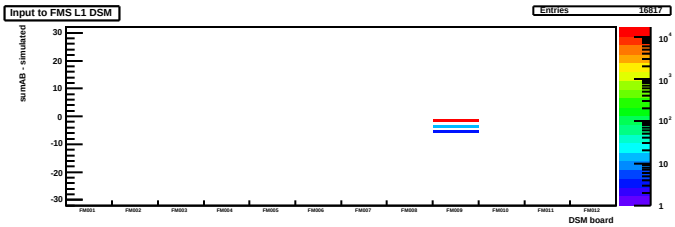

Input to FMS L1 DSM
Entries 3417876
sumBC
DSM board

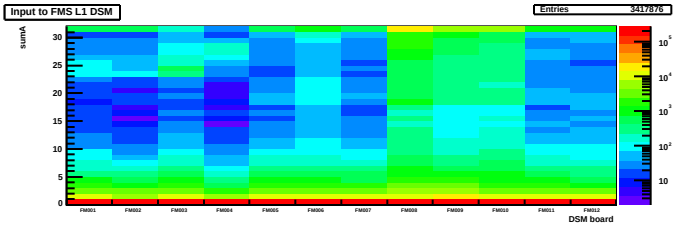

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

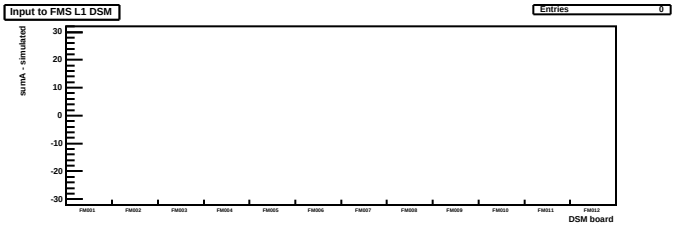

Input to FMS L1 DSM
Entries 3417876
sumB
DSM board

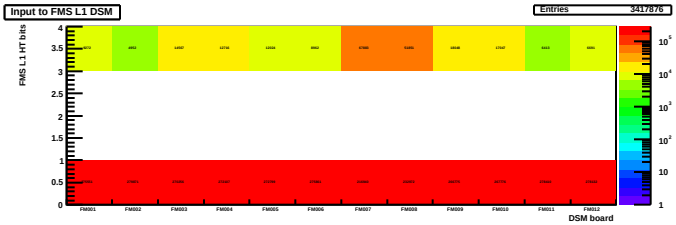

Input to FMS L1 DSM
Entries 15674
sumB - simulated
DSM board

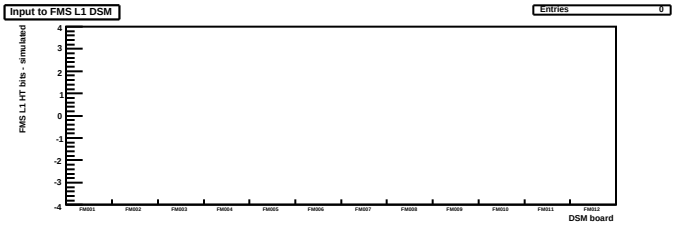

Input to FMS L1 DSM
Entries 3417876
sumAB
DSM board

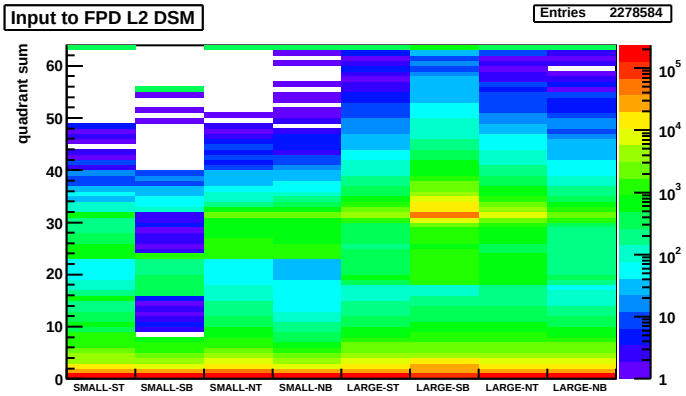

Input to FMS L1 DSM
Entries 16817
sumAB - simulated
DSM board

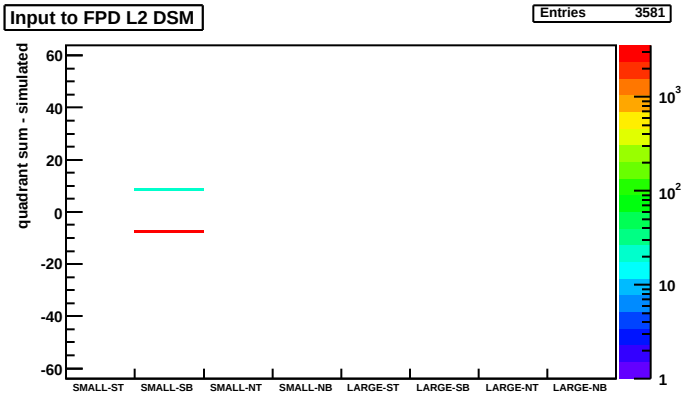

Input to FMS L1 DSM
Entries 3417876
sumA
DSM board

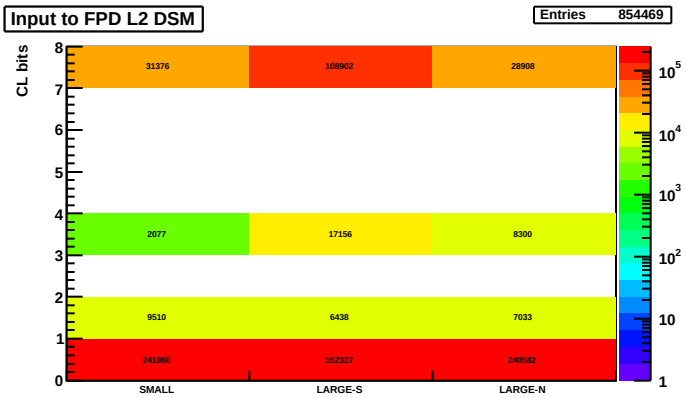

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

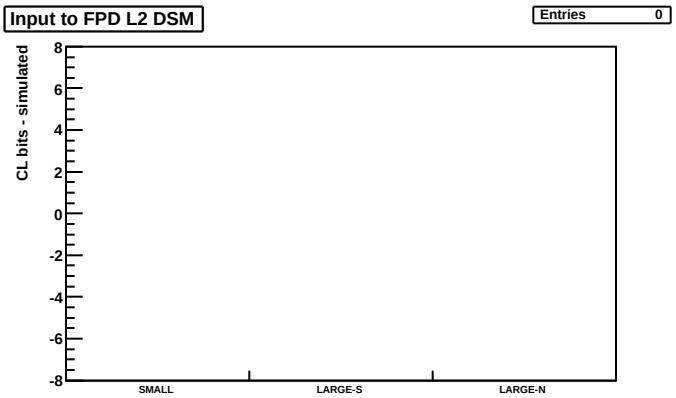

Input to FMS L1 DSM
Entries 3417876
FMS L1 HT bits
DSM board

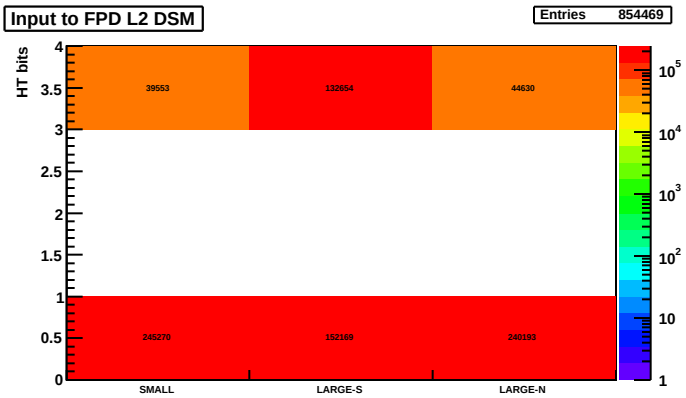

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

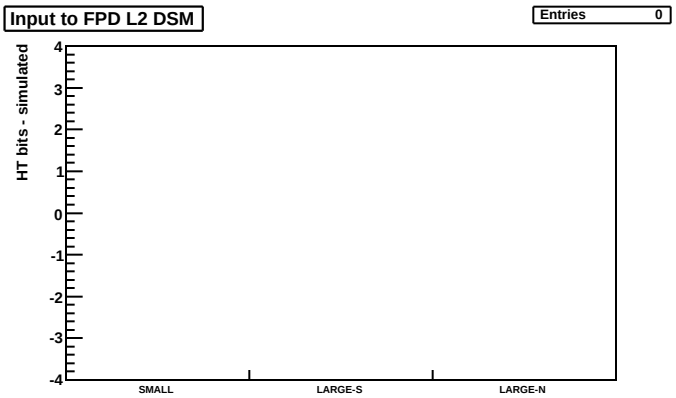

Input to FPD L2 DSM
Entries 2278584
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

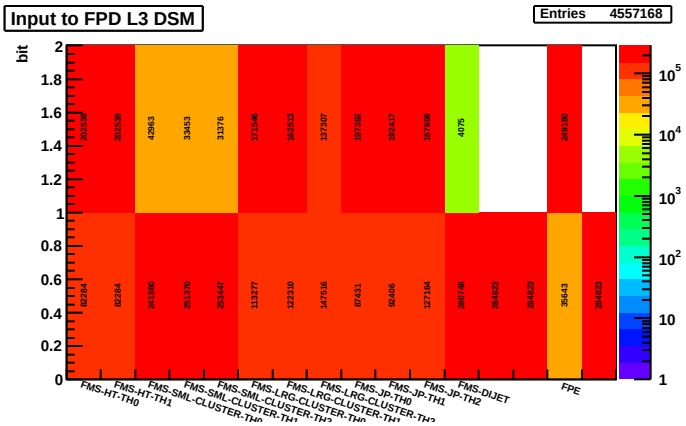

Input to FPD L2 DSM
Entries 3581
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

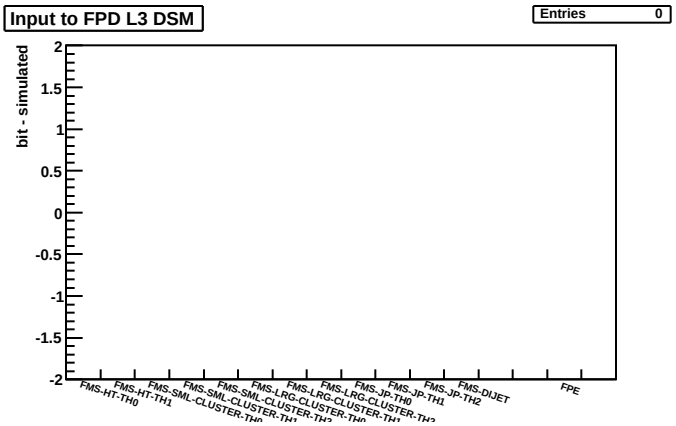

Input to FPD L2 DSM
Entries 854469
CL bits
31376
108902
28908
2077
17156
8300
9510
6438
7033
241860
152327
240582
SMALL
LARGE-S
LARGE-N

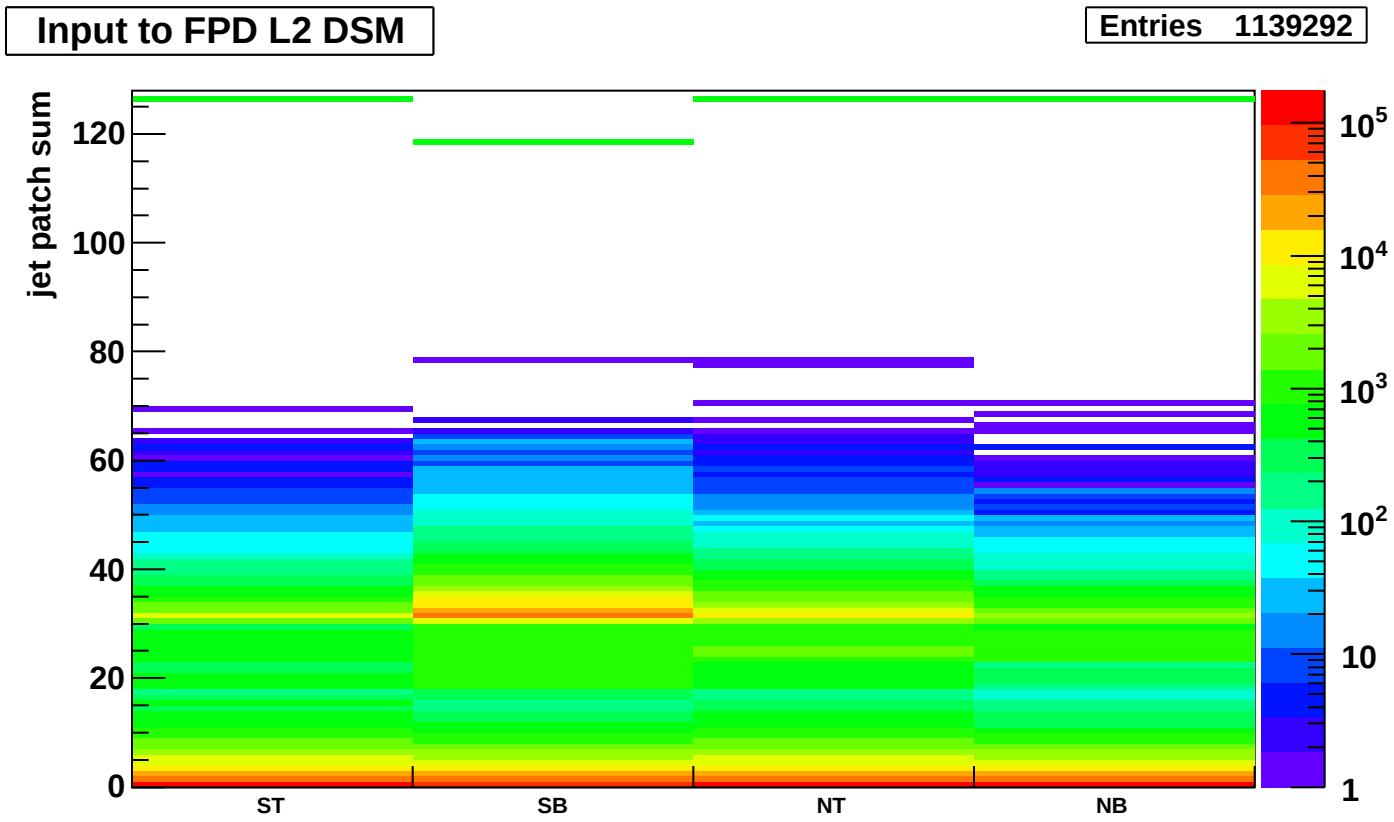

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

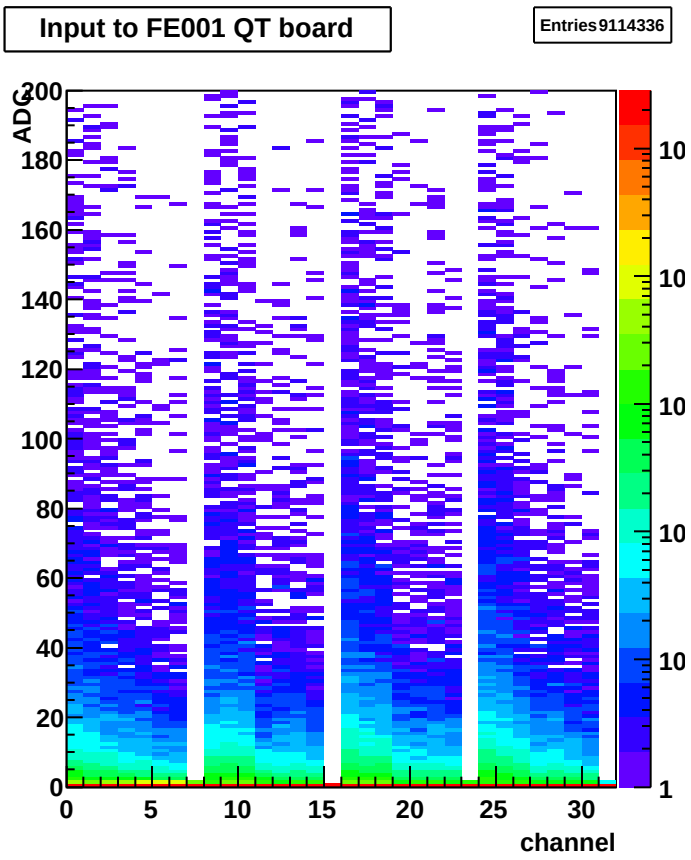

Input to FPD L2 DSM
Entries 854469
HT bits
39553
132654
44630
245270
152169
240193
SMALL
LARGE-S
LARGE-N

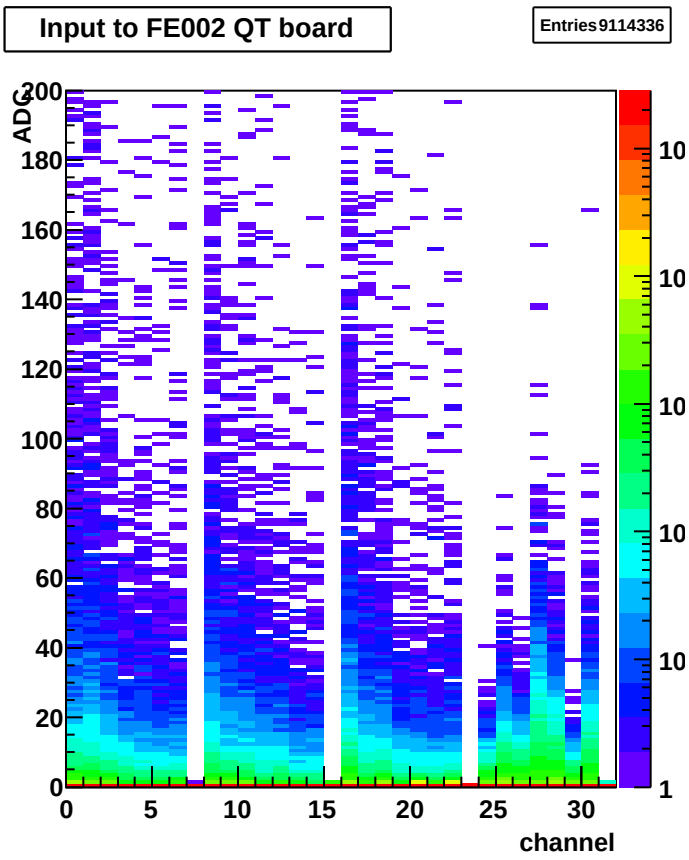

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

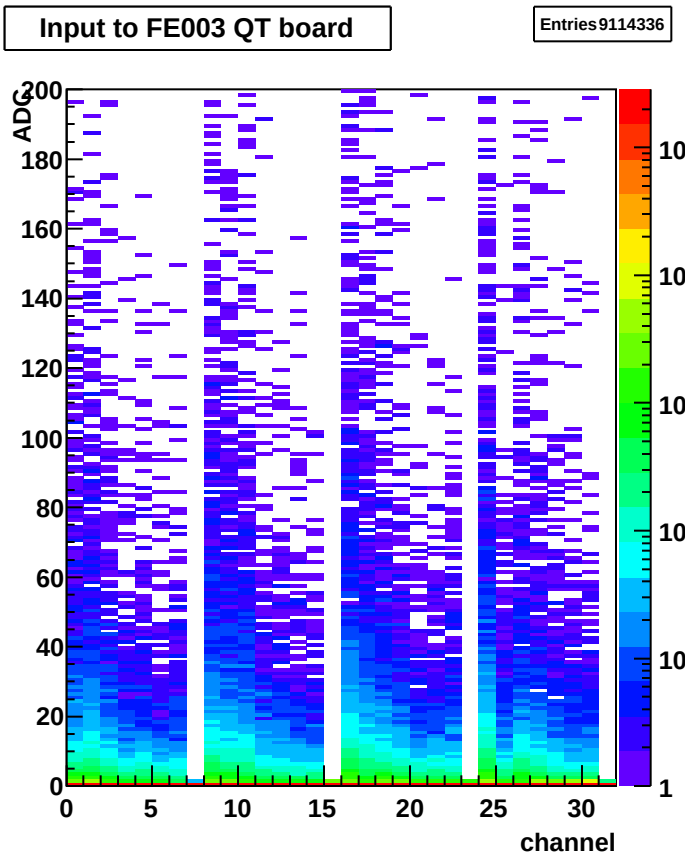

Input to FPD L3 DSM
Entries 4557168
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

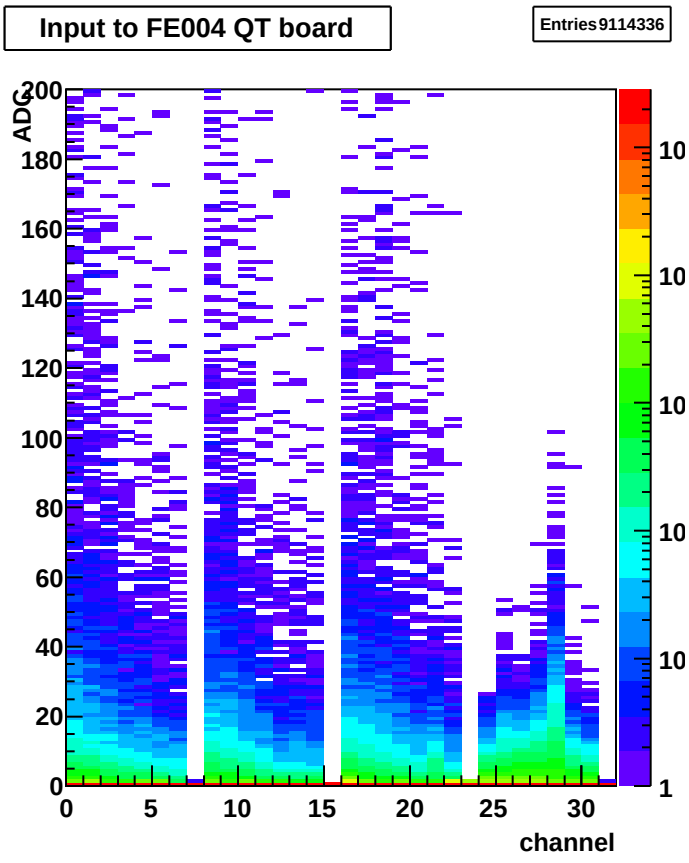

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

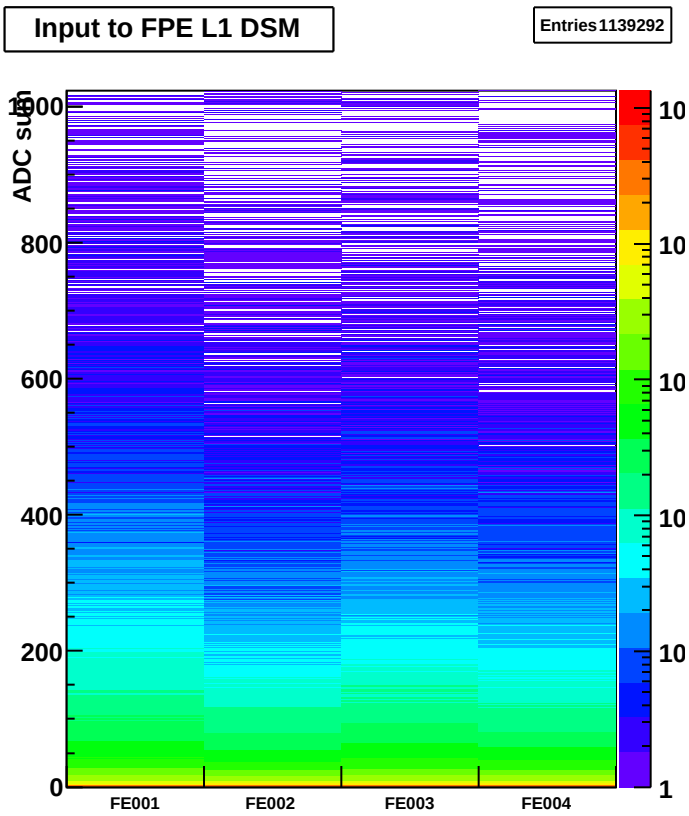

Input to FPD L2 DSM
Entries 1139292
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

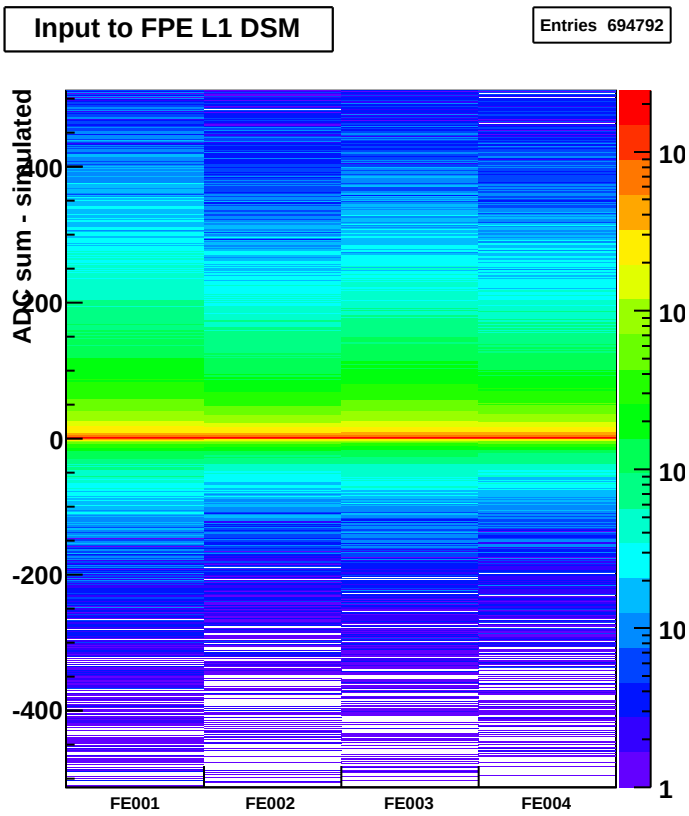

Input to FE001 QT board
Entries 9114336
ADC
channel

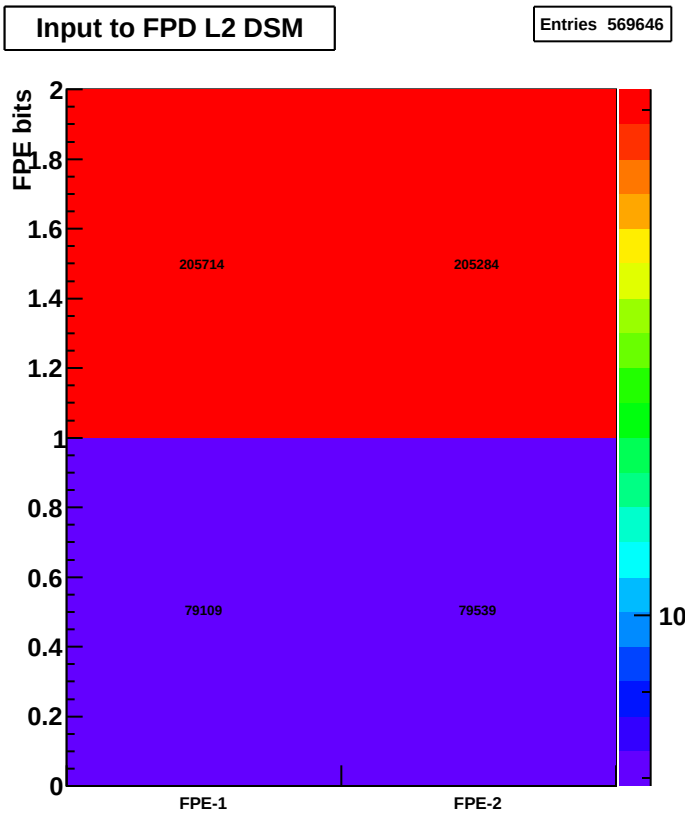

Input to FE002 QT board
Entries 9114336
ADC
channel

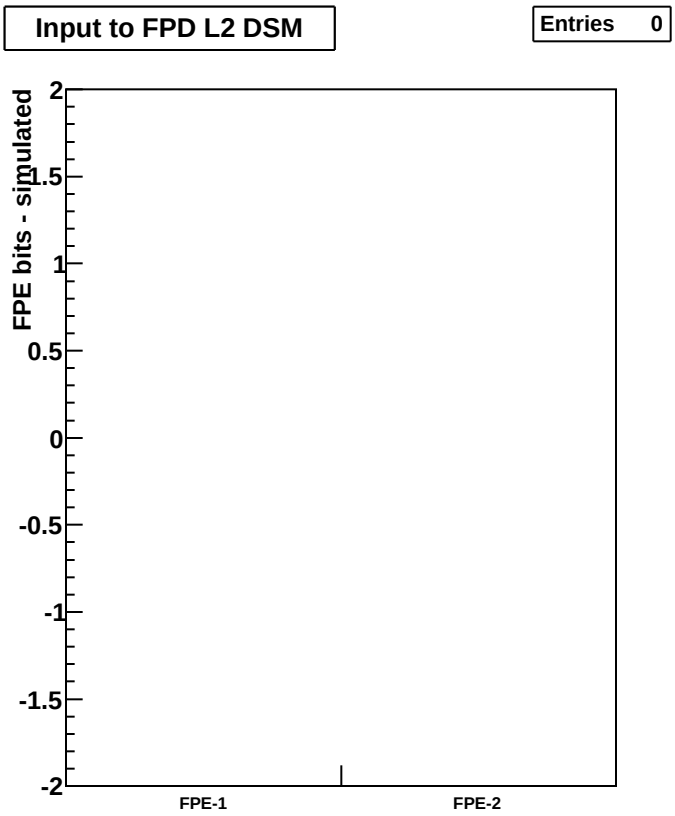

Input to FE003 QT board
Entries 9114336
ADC
channel


Input to FE004 QT board
Entries 9114336
ADC
channel


Input to FPE L1 DSM
Entries 1139292
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 694792
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 569646
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
205714
205284
79109
79539
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2