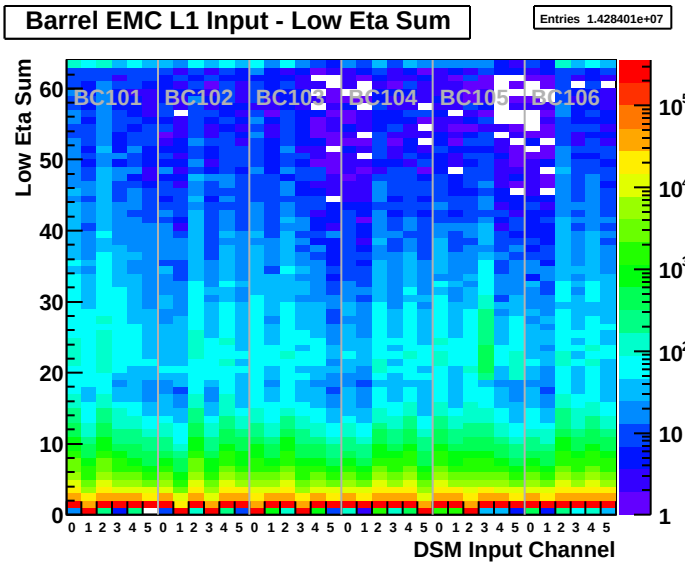
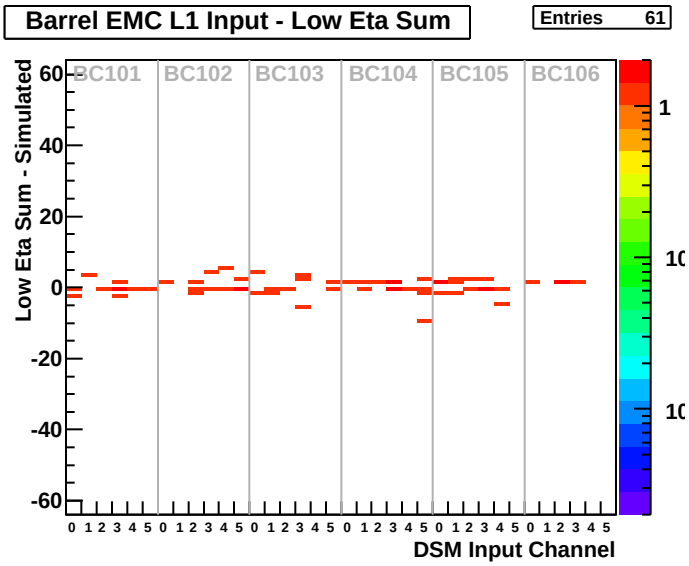
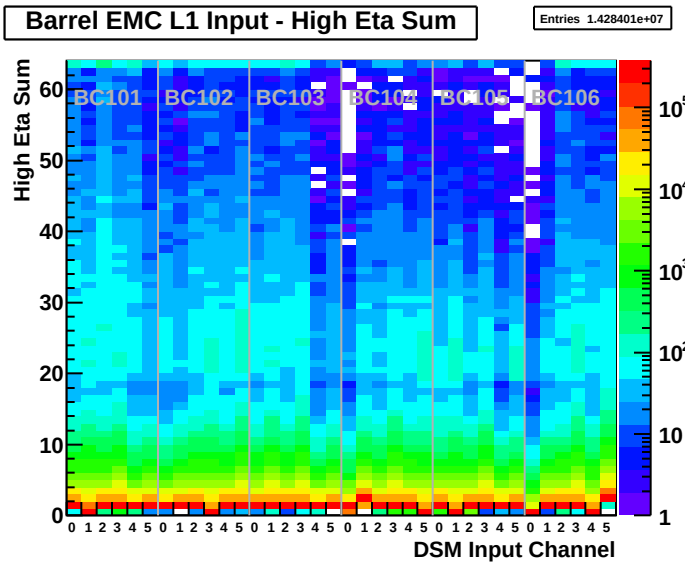
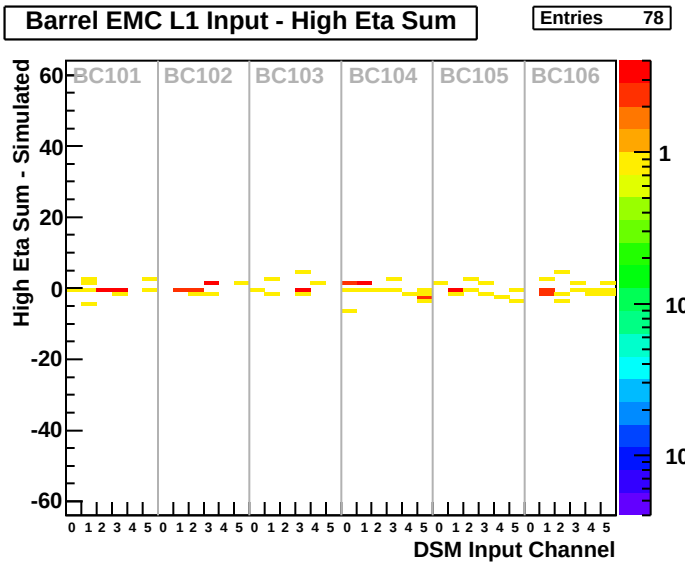
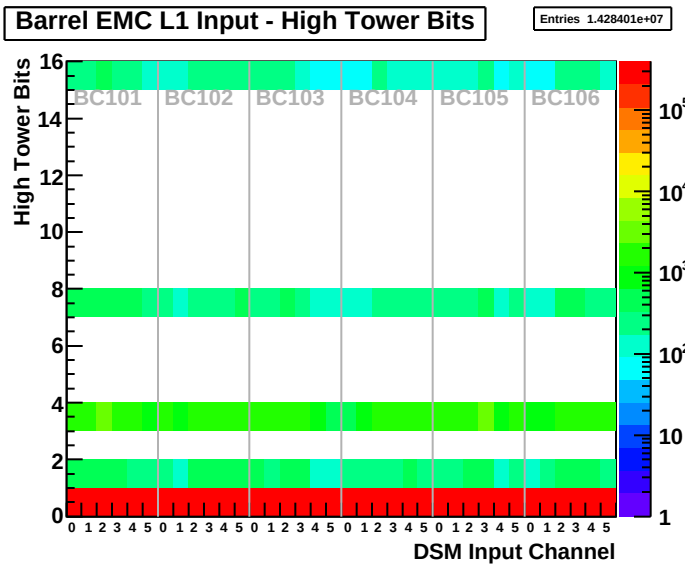
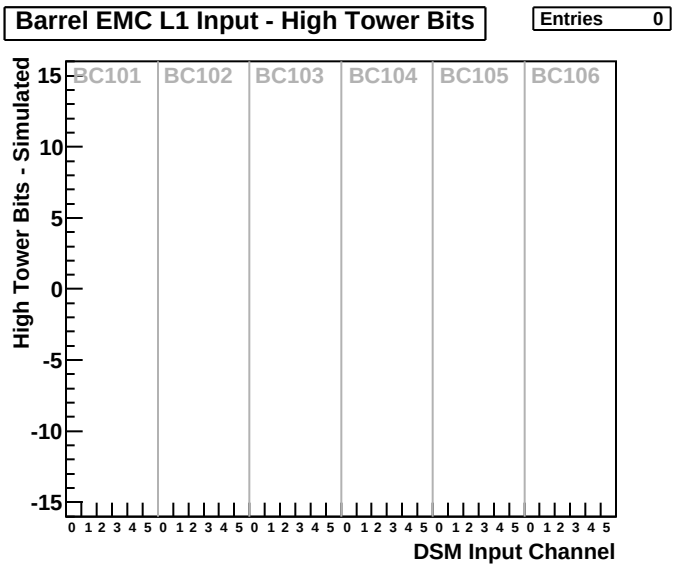

Barrel EMC L1 Input - Low Eta Sum
Entries 1.428401e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

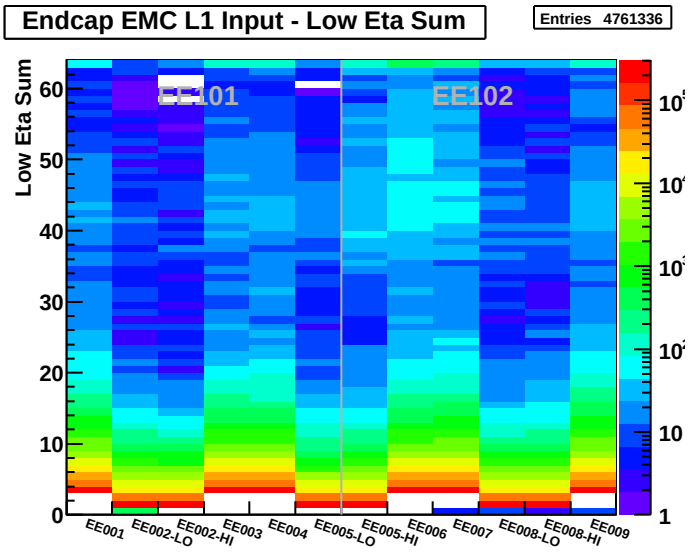

Barrel EMC L1 Input - Low Eta Sum
Entries 61
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

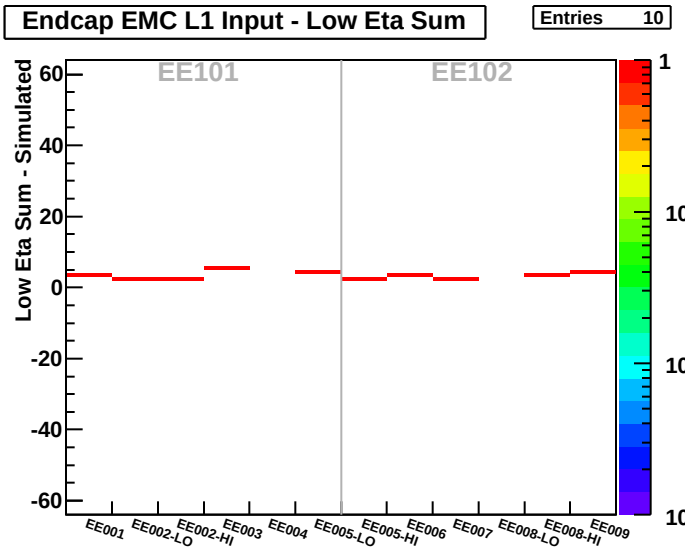

Barrel EMC L1 Input - High Eta Sum
Entries 1.428401e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

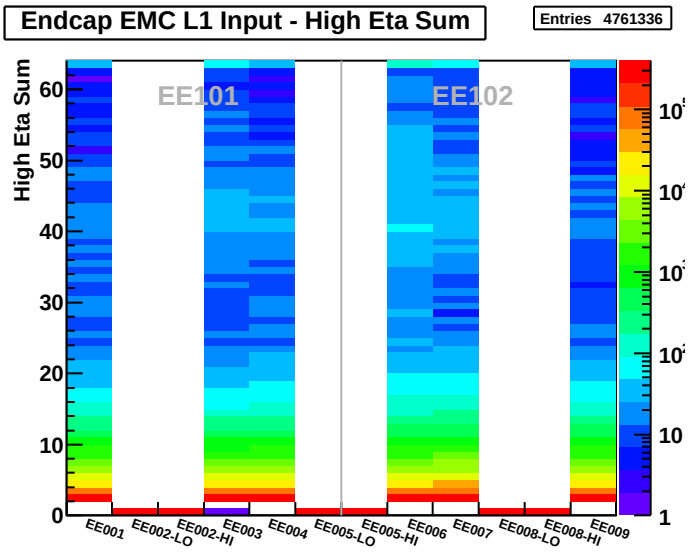

Barrel EMC L1 Input - High Eta Sum
Entries 78
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

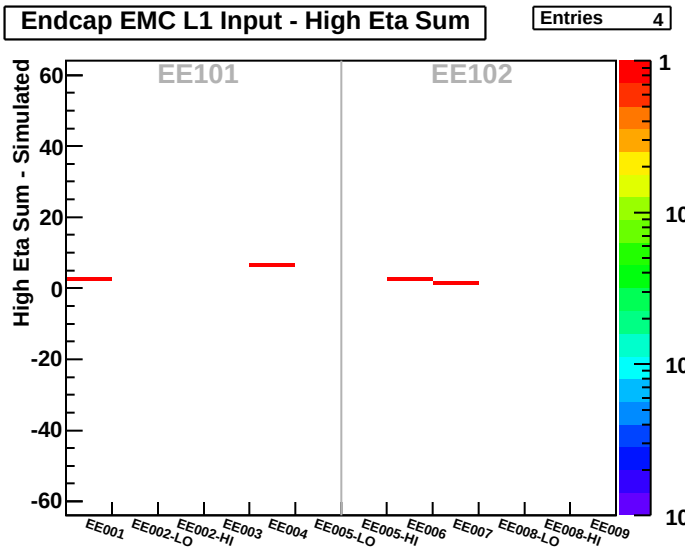

Barrel EMC L1 Input - High Tower Bits
Entries 1.428401e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

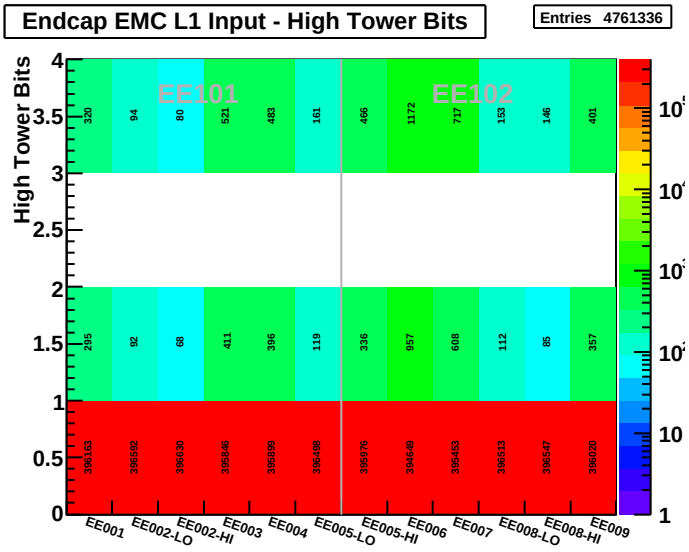

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

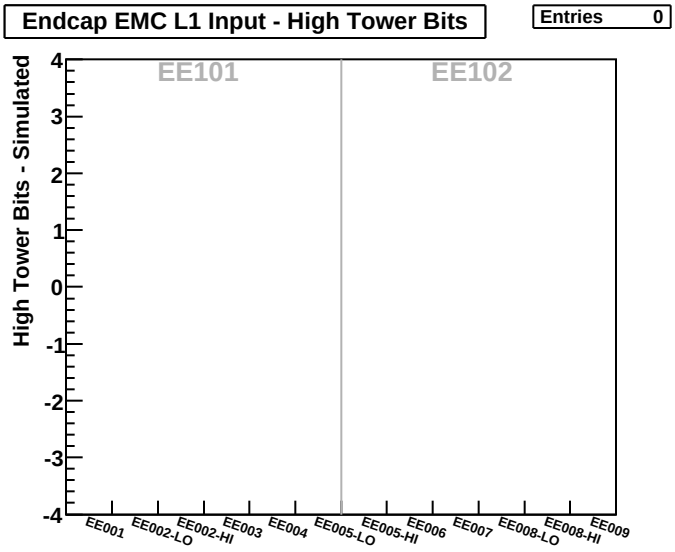

Endcap EMC L1 Input - Low Eta Sum
Entries 4761336
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

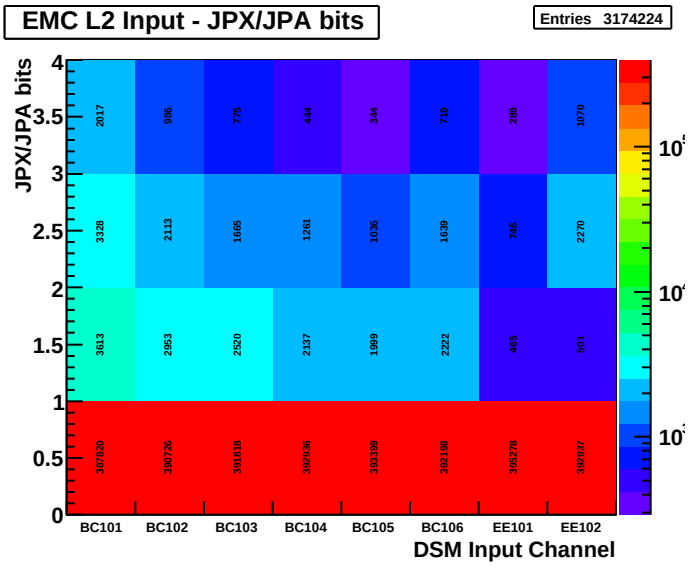

Endcap EMC L1 Input - Low Eta Sum
Entries 10
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

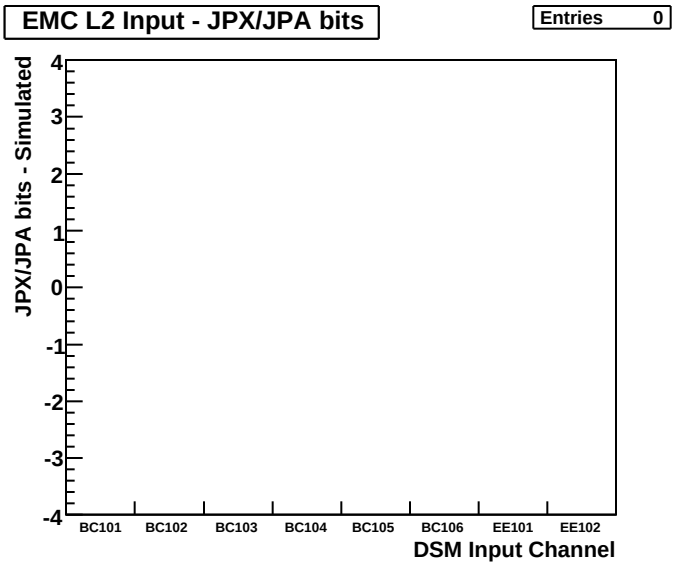

Endcap EMC L1 Input - High Eta Sum
Entries 4761336
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

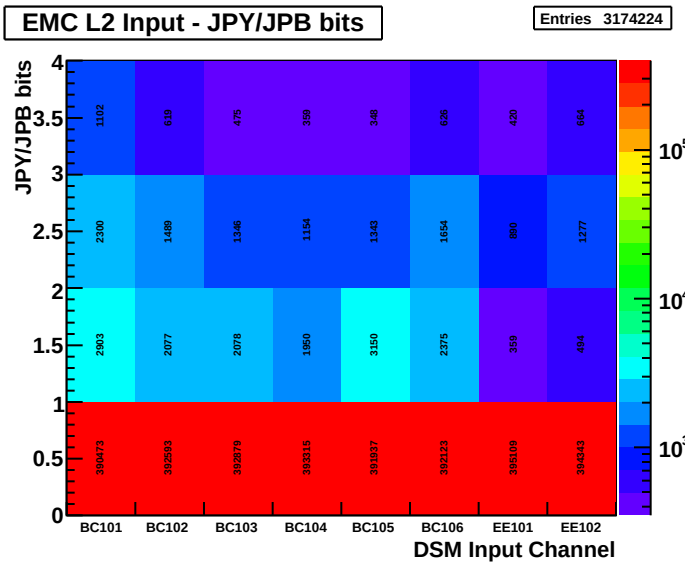

Endcap EMC L1 Input - High Eta Sum
Entries 4
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

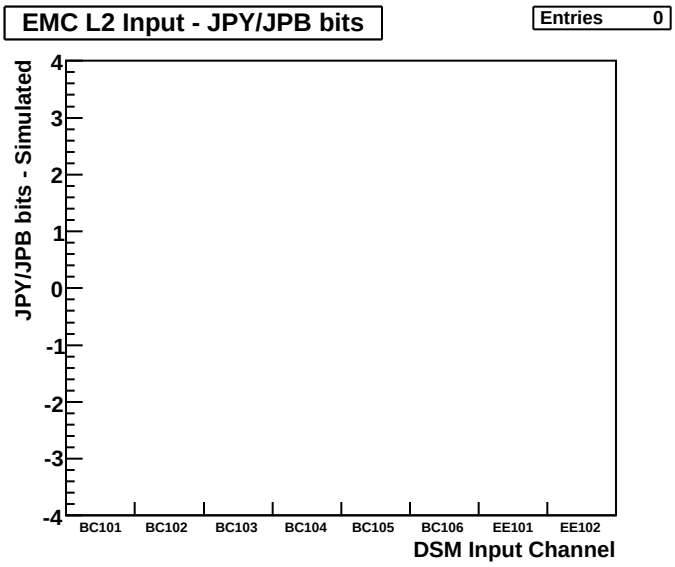

Endcap EMC L1 Input - High Tower Bits
Entries 4761336
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

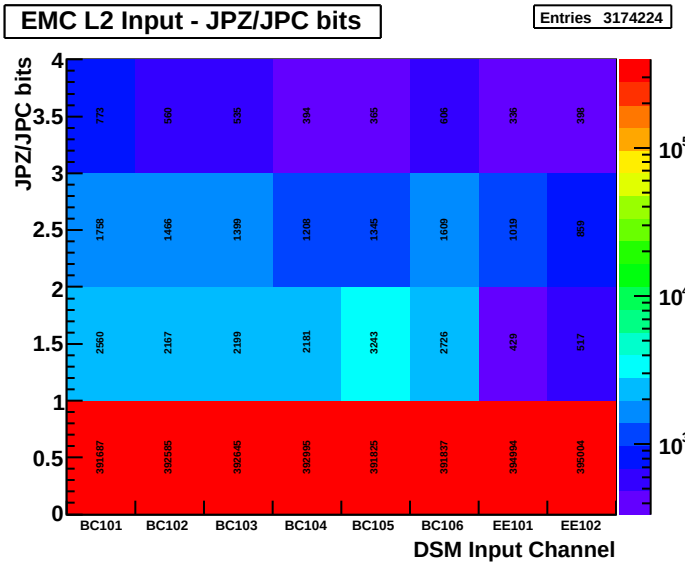

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

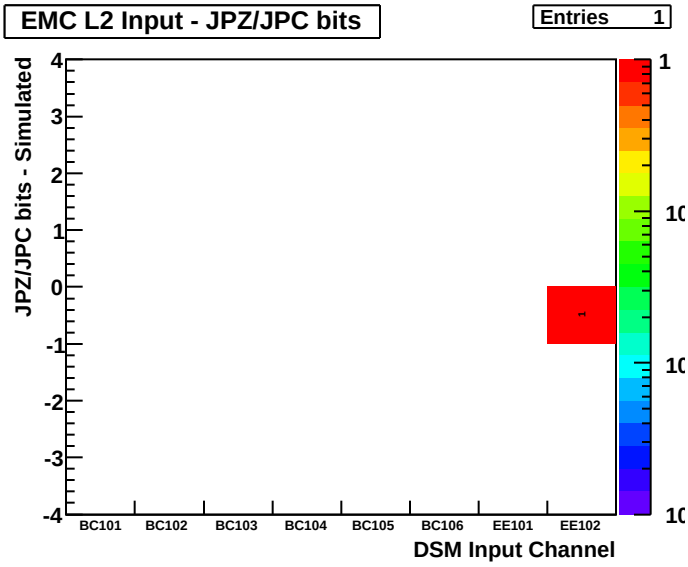

EMC L2 Input - JPX/JPA bits
Entries 3174224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2017 986 775 444 344 719 289 1070
3328 2113 1665 1261 1036 1639 746 2270
3613 2953 2520 2137 1999 2222 465 501
387620 390726 391818 392036 393399 392198 395278 392937

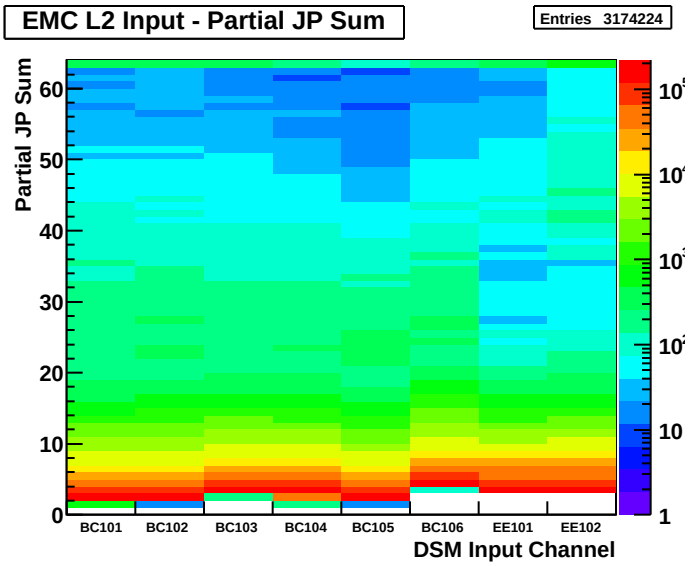

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

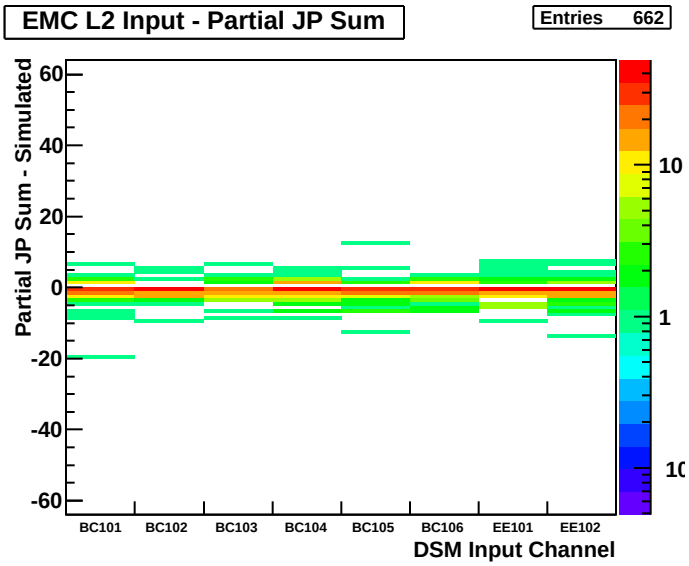

EMC L2 Input - JPY/JPB bits
Entries 3174224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1102 610 475 359 348 626 420 664
2300 1489 1346 1154 1343 1654 880 1277
2903 2077 2078 1950 3150 2375 359 494
390473 392593 392879 393315 391937 392123 395109 394343

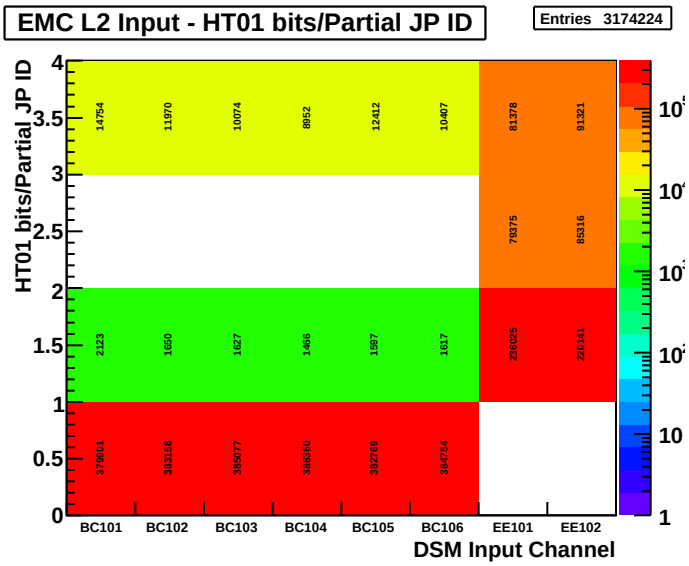

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

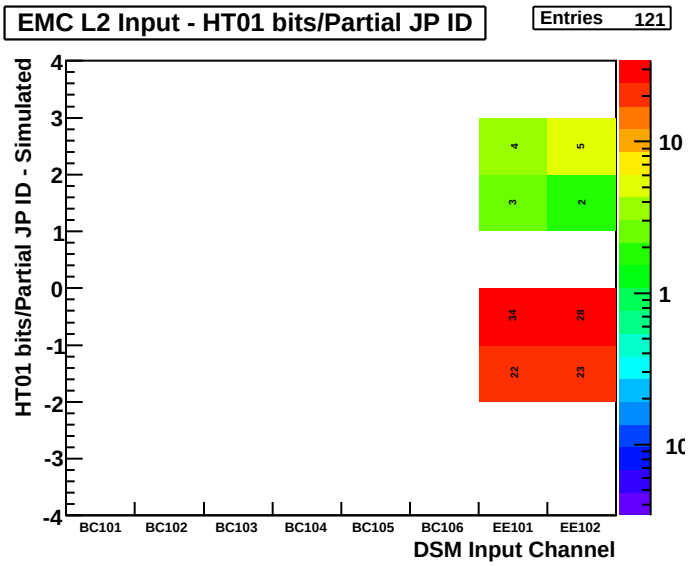

EMC L2 Input - JPZ/JPC bits
Entries 3174224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
773 560 535 394 365 606 336 388
1758 1466 1399 1208 1345 1609 1019 859
2960 2167 2199 2181 3243 2726 429 517
391687 392585 392845 392995 391825 391837 394994 395004

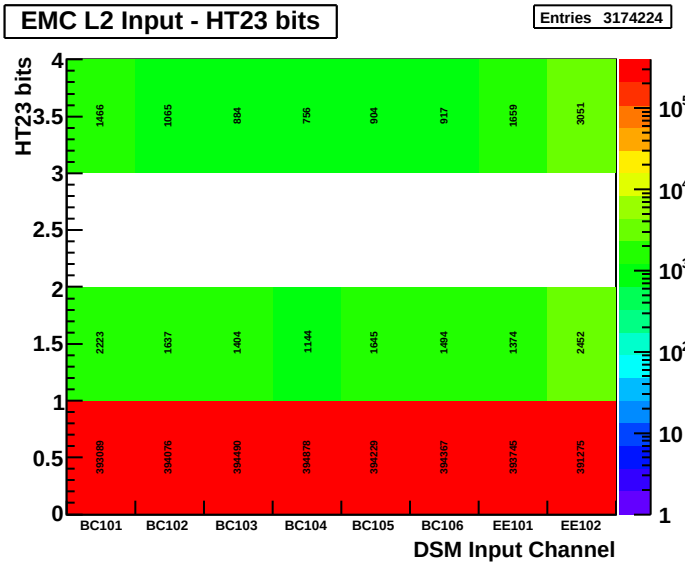

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

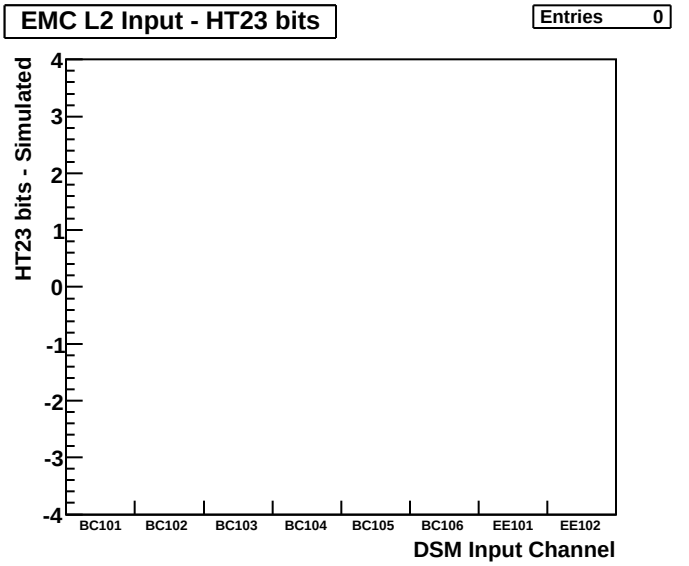

EMC L2 Input - Partial JP Sum
Entries 3174224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

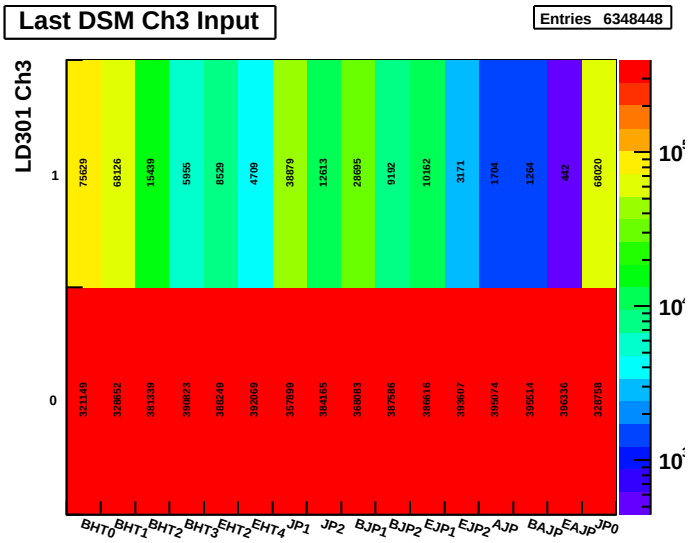

EMC L2 Input - Partial JP Sum
Entries 662
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

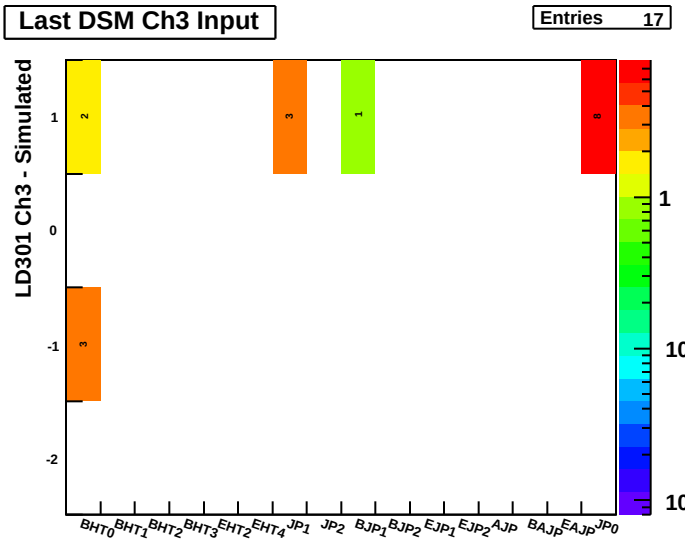

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3174224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

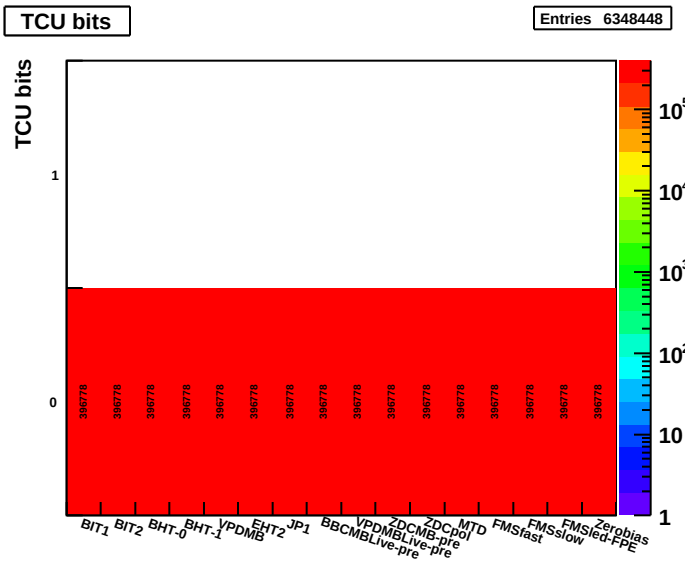

EMC L2 Input - HT01 bits/Partial JP ID
Entries 121
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

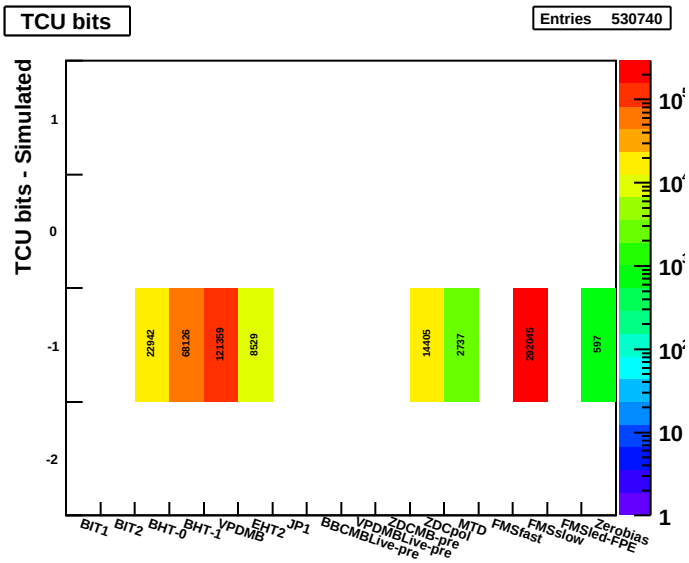

EMC L2 Input - HT23 bits
Entries 3174224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

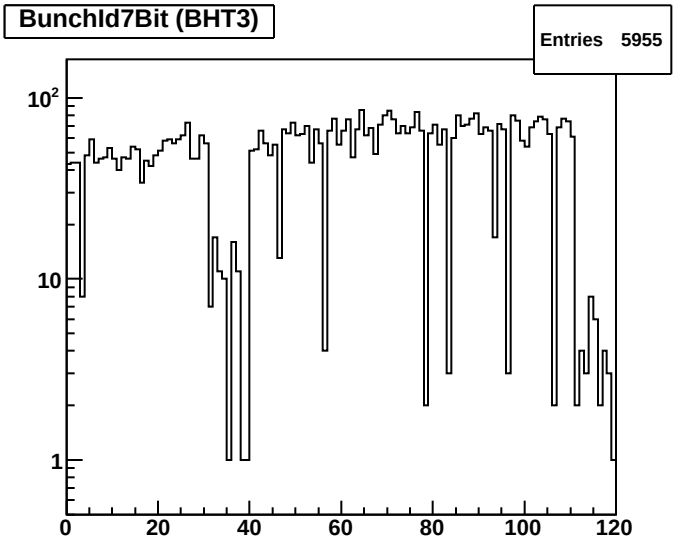

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

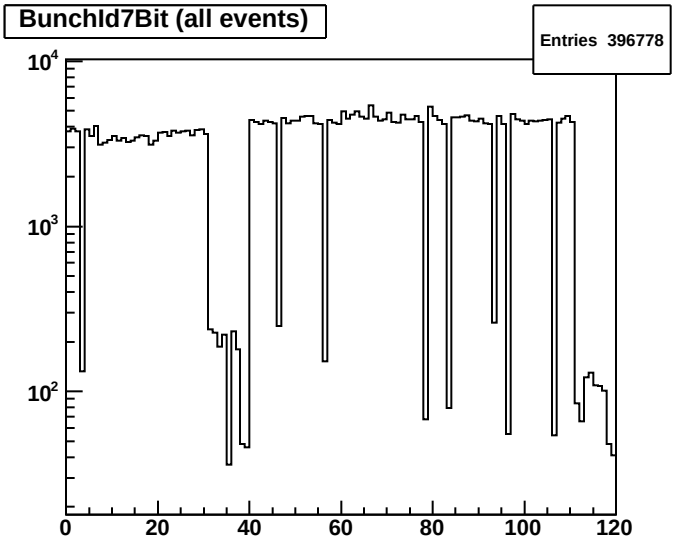

Last DSM Ch3 Input
Entries 6348448
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

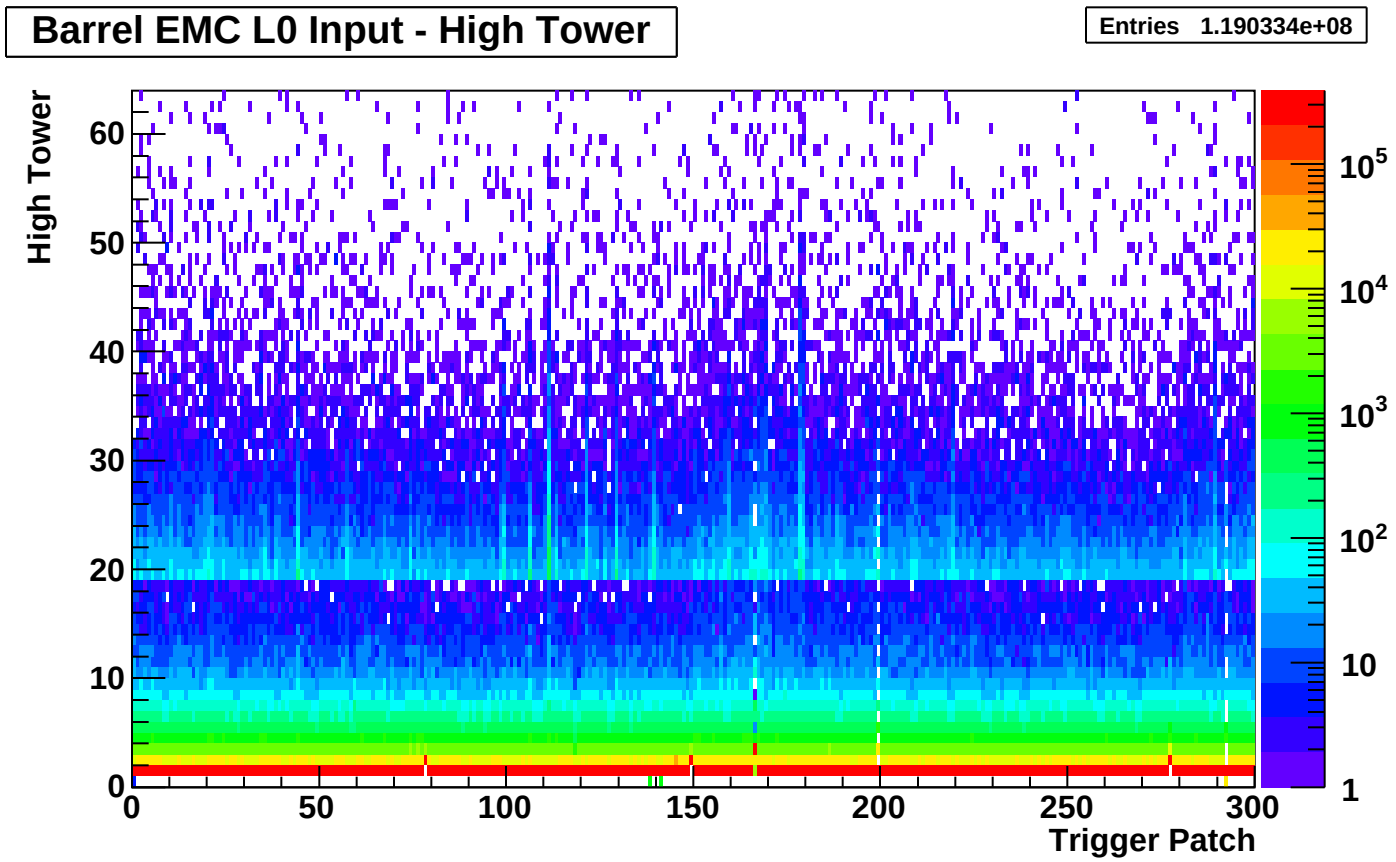

Last DSM Ch3 Input
Entries 17
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

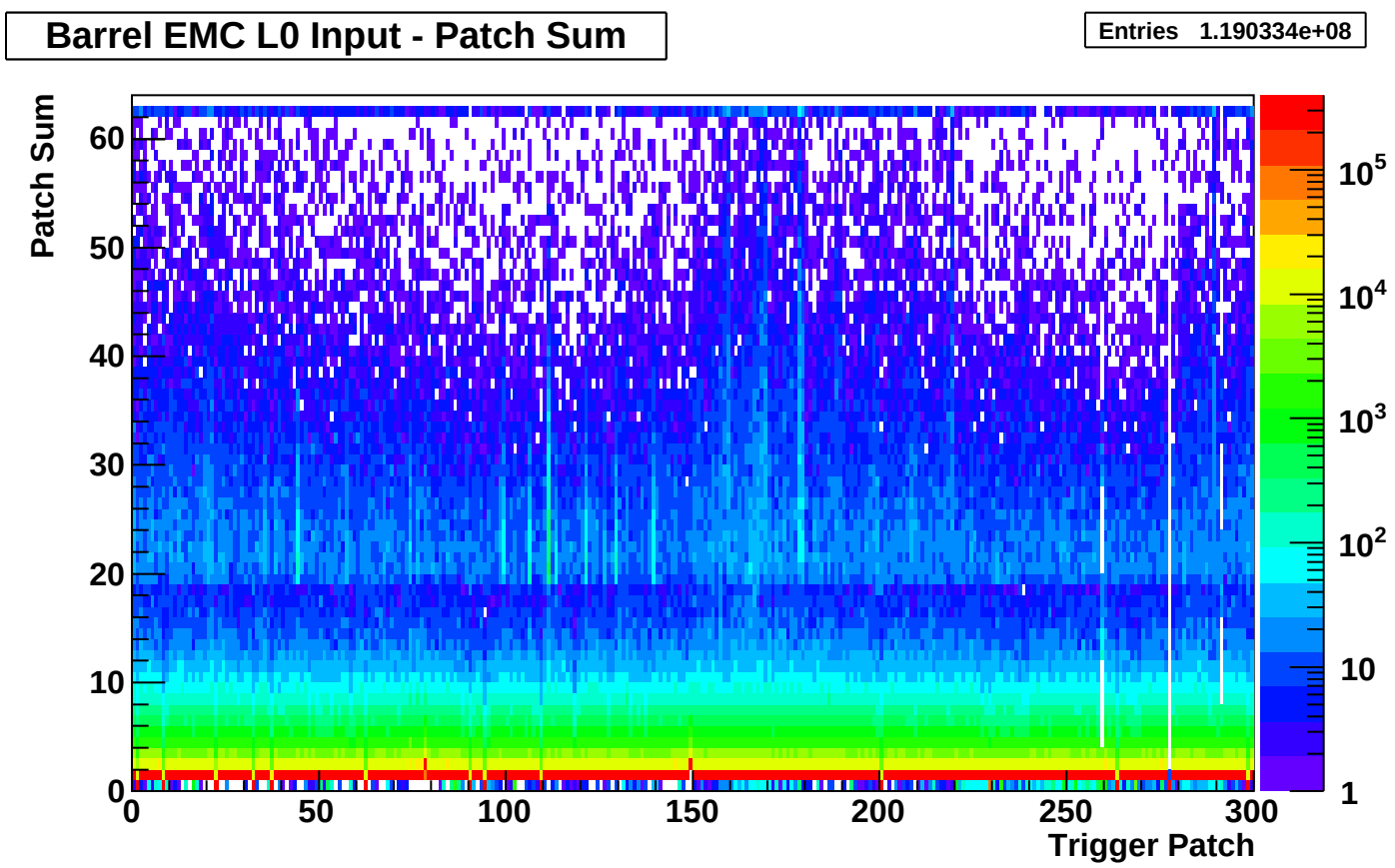

TCU bits
Entries 6348448
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

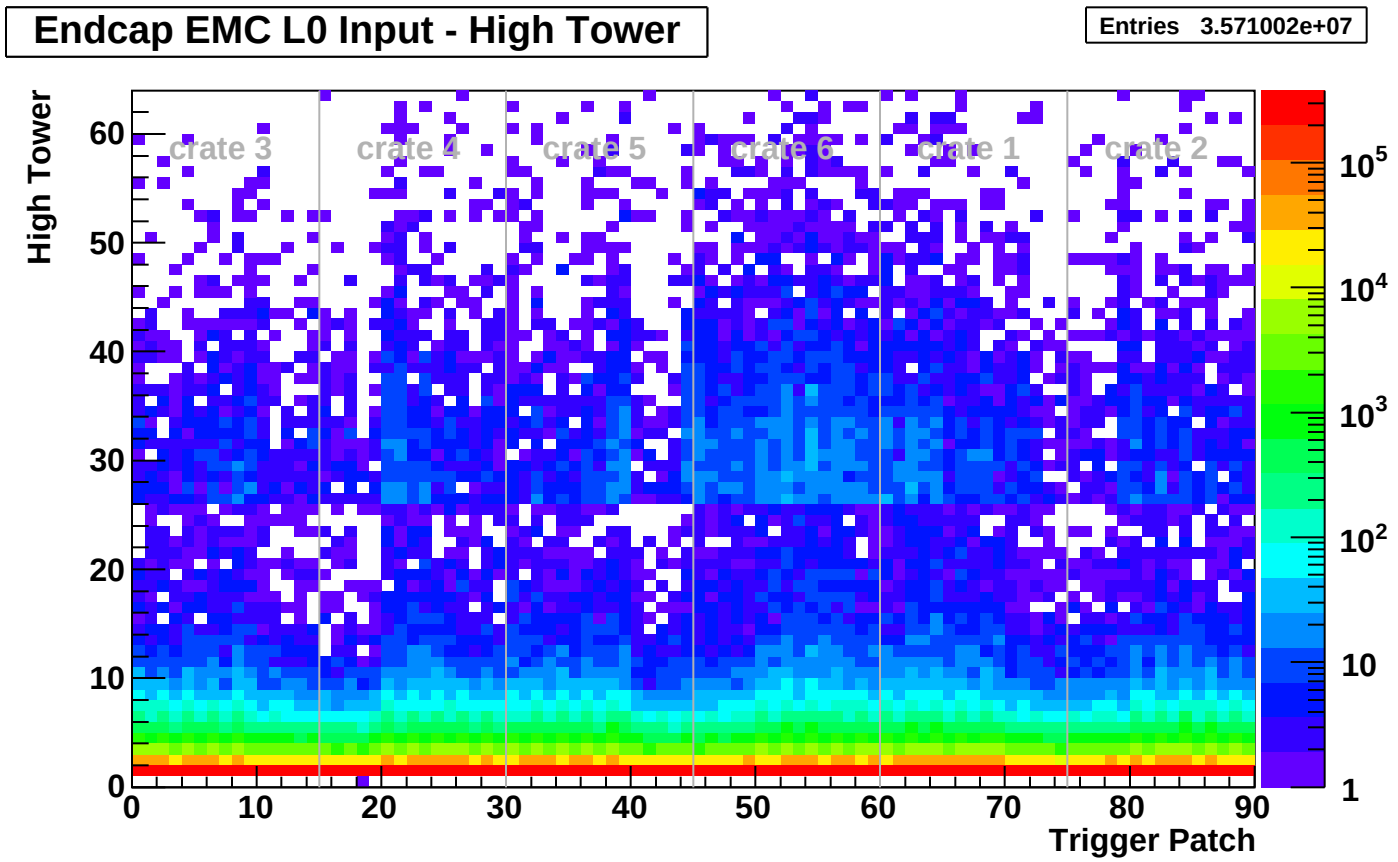

TCU bits
Entries 530740
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

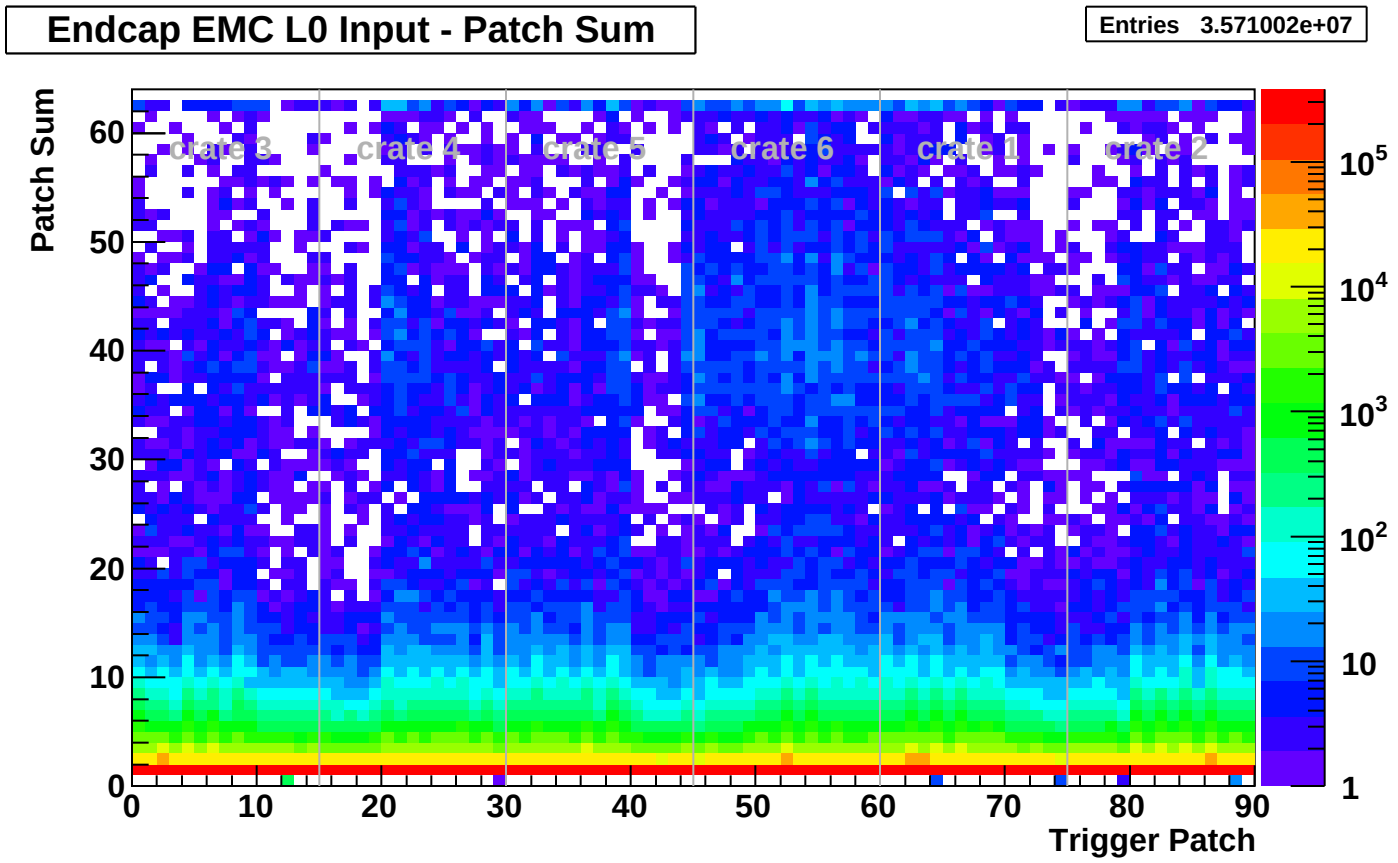

BunchId7Bit (BHT3)
Entries 5955

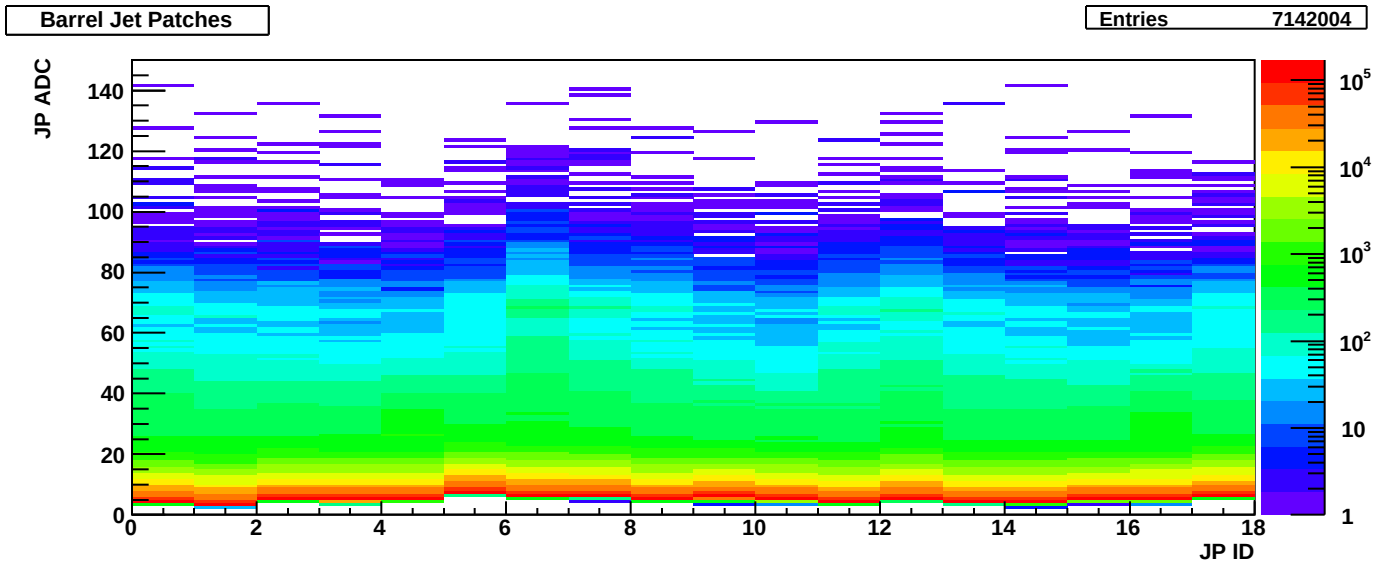

BunchId7Bit (all events)
Entries 396778

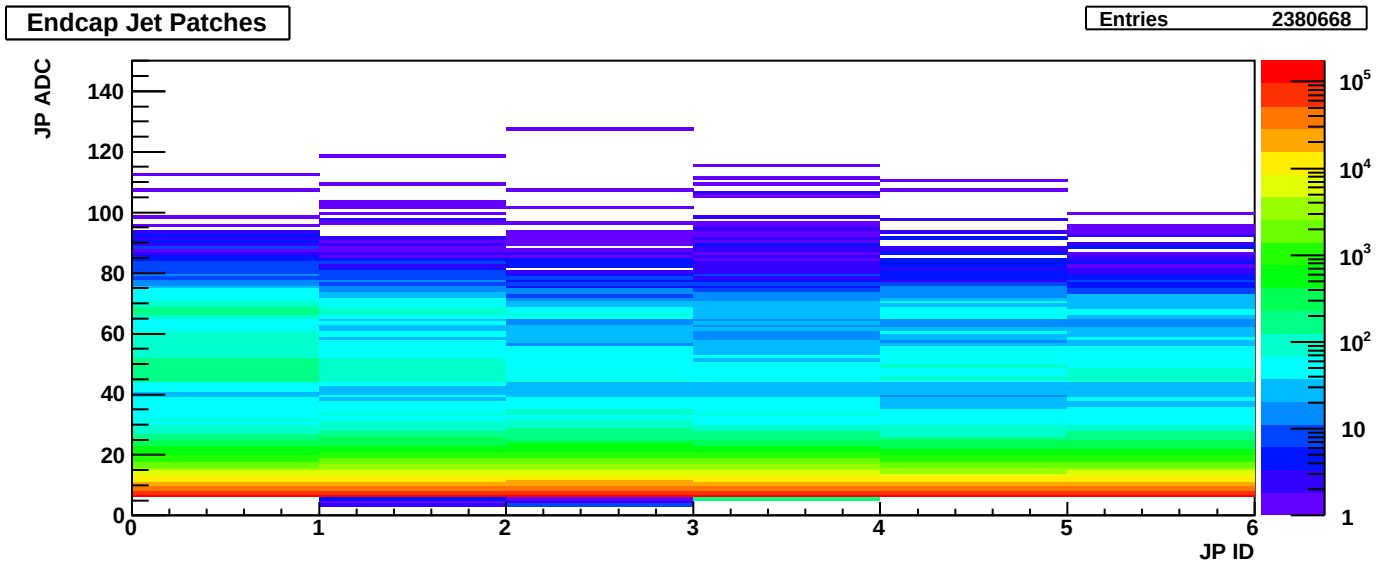

Barrel EMC L0 Input - High Tower
Entries 1.190334e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

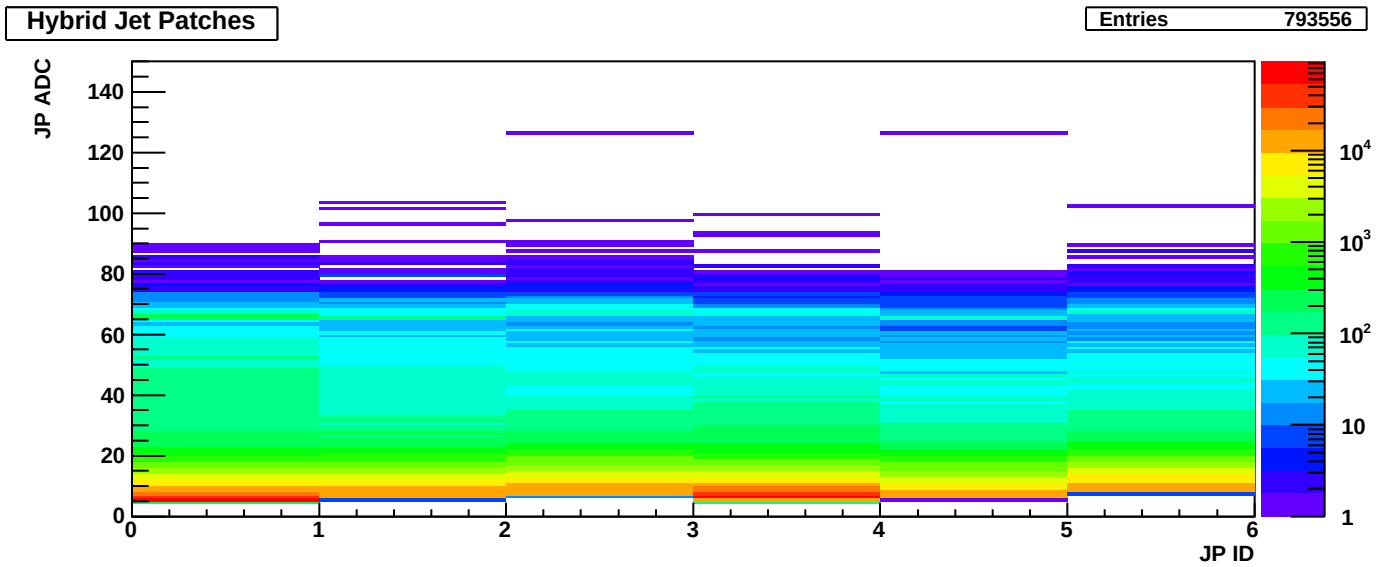

Barrel EMC L0 Input - Patch Sum
Entries 1.190334e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

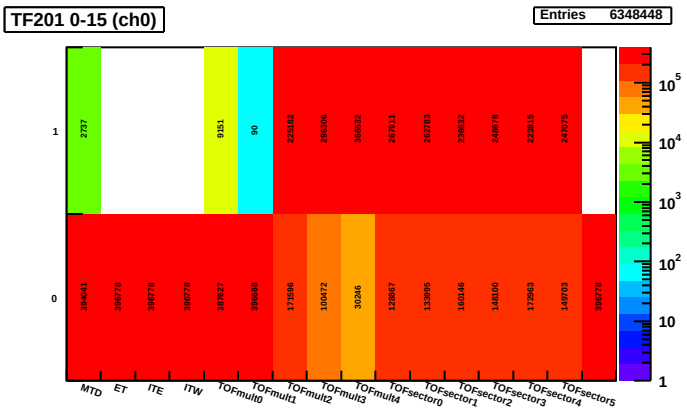

Endcap EMC L0 Input - High Tower
Entries 3.571002e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

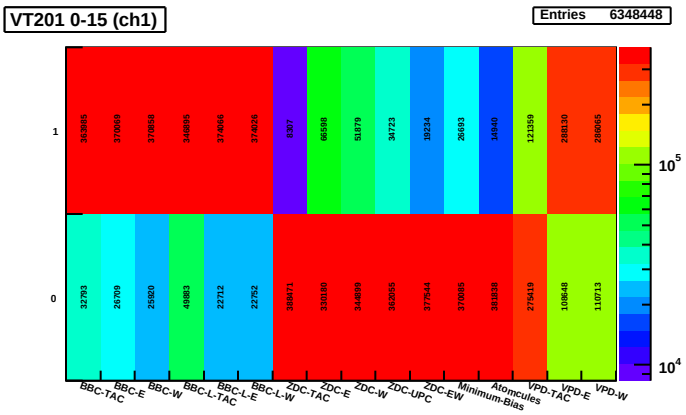

Endcap EMC L0 Input - Patch Sum
Entries 3.571002e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

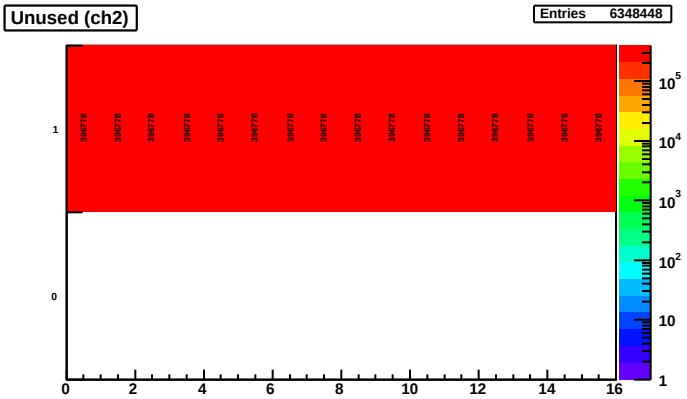

Barrel Jet Patches
Entries 7142004
JP ADC
JP ID

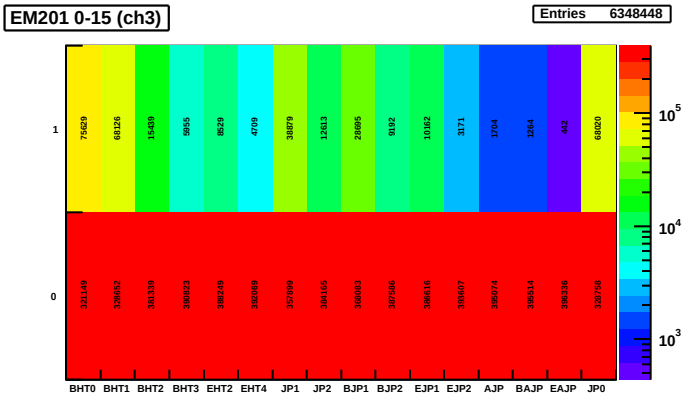

Endcap Jet Patches
Entries 2380668
JP ADC
JP ID

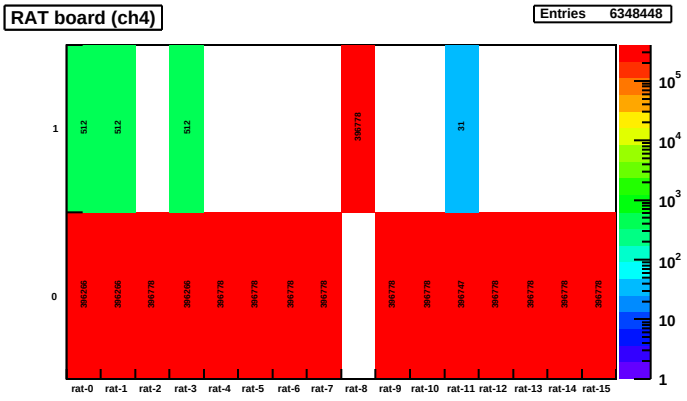

Hybrid Jet Patches
Entries 793556
JP ADC
JP ID

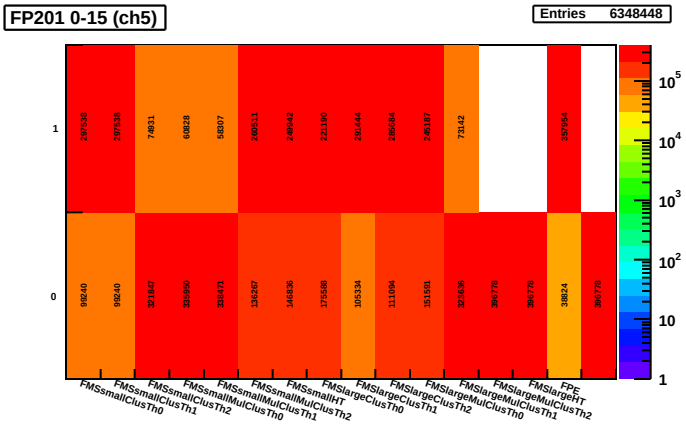

TF201 0-15 (ch0)
Entries 6348448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

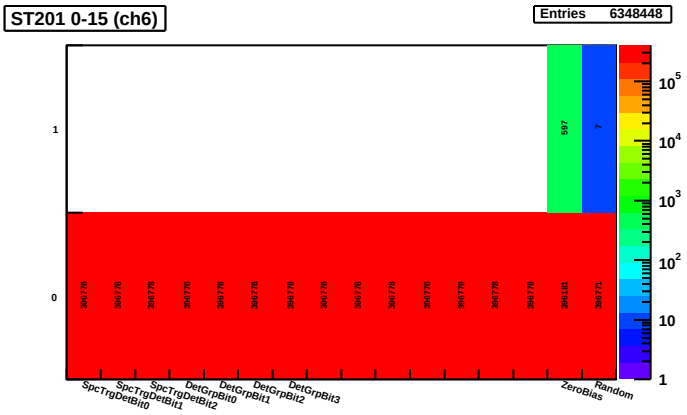

VT201 0-15 (ch1)
Entries 6348448
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

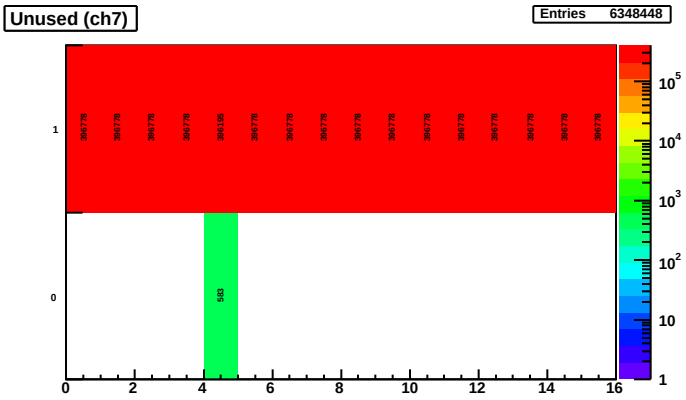

Unused (ch2)
Entries 6348448

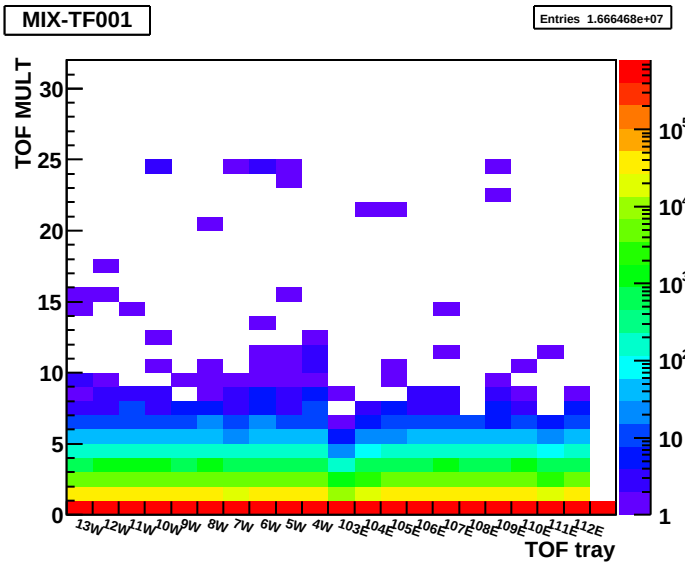

EM201 0-15 (ch3)
Entries 6348448
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

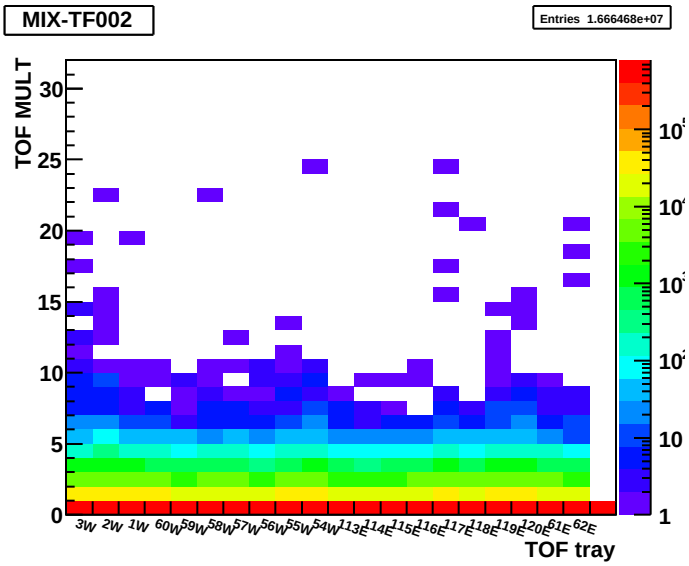

RAT board (ch4)
Entries 6348448
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

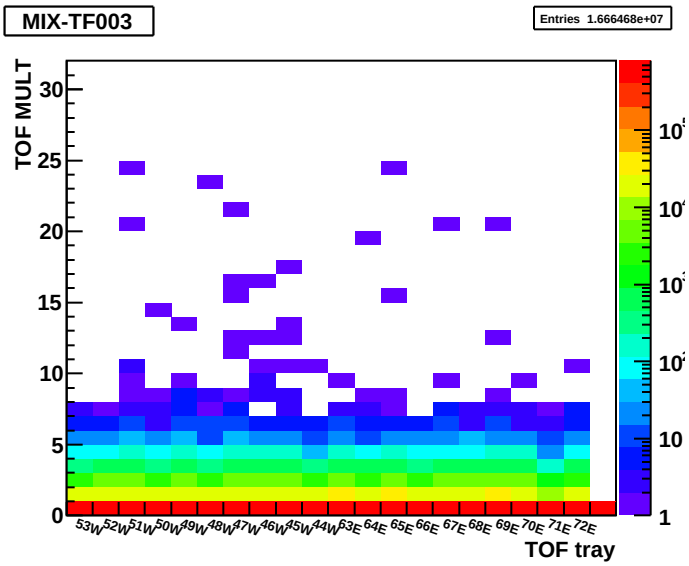

FP201 0-15 (ch5)
Entries 6348448
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

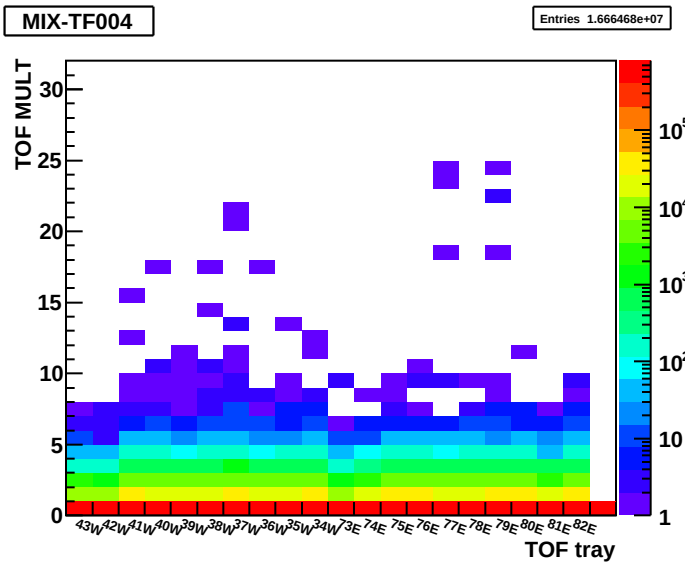

ST201 0-15 (ch6)
Entries 6348448
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

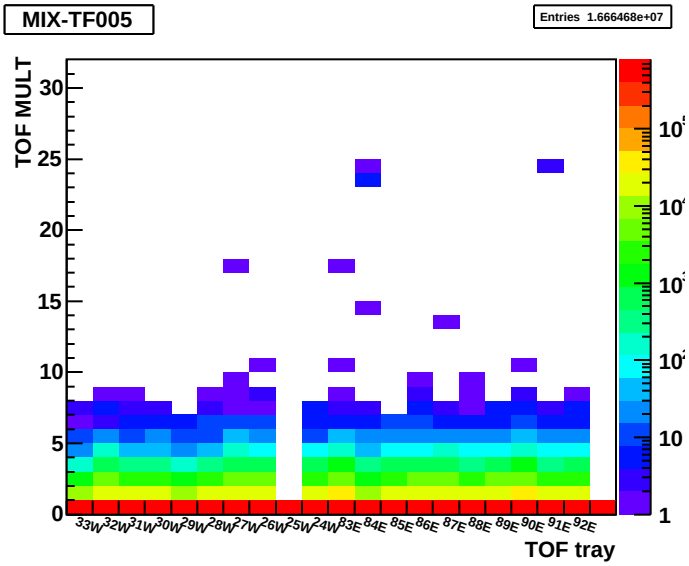

Unused (ch7)
Entries 6348448

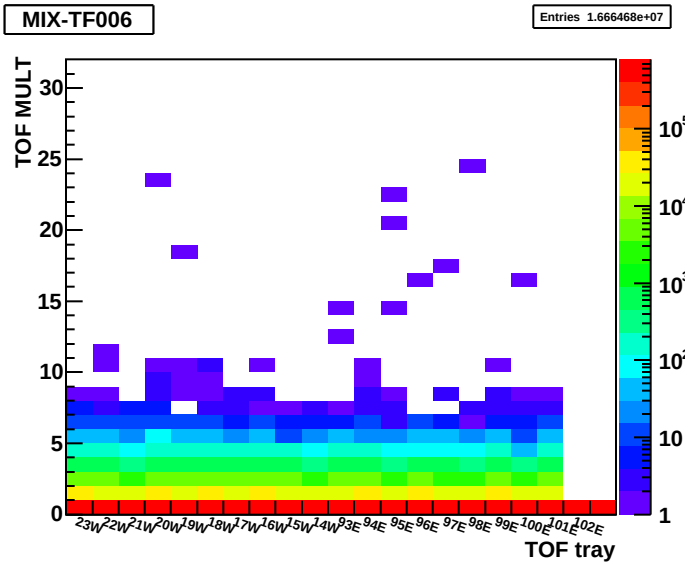

MIX-TF001
Entries 1.666468e+07
TOF MULT
TOF tray

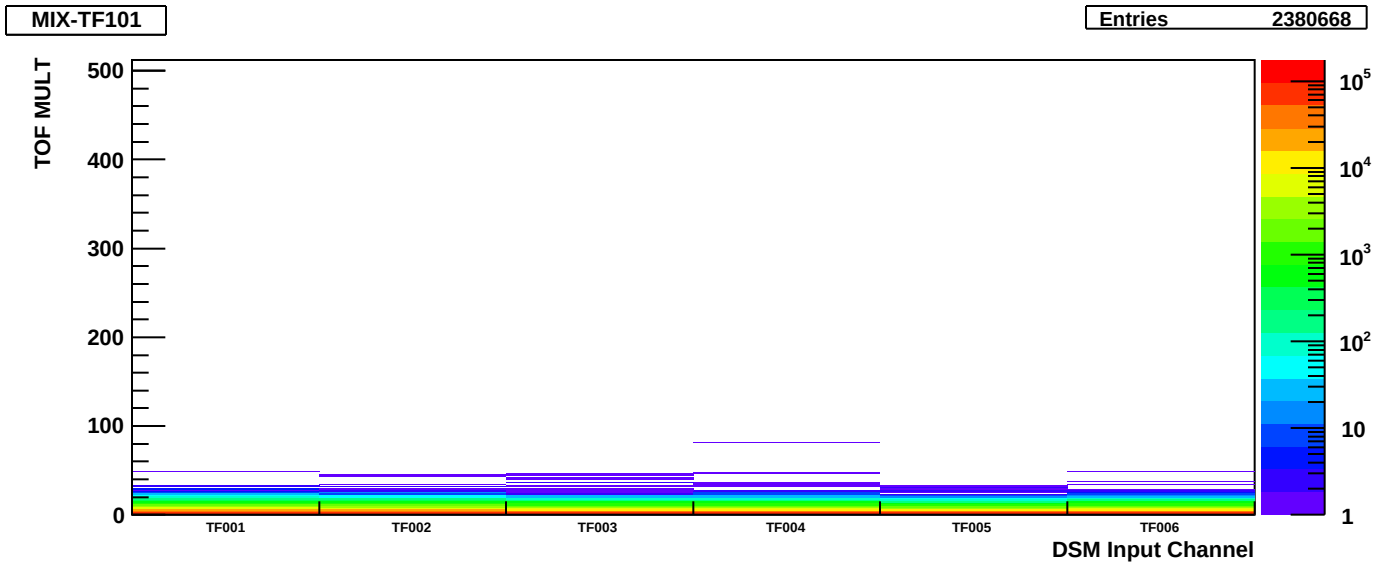

MIX-TF002
Entries 1.666468e+07
TOF MULT
TOF tray

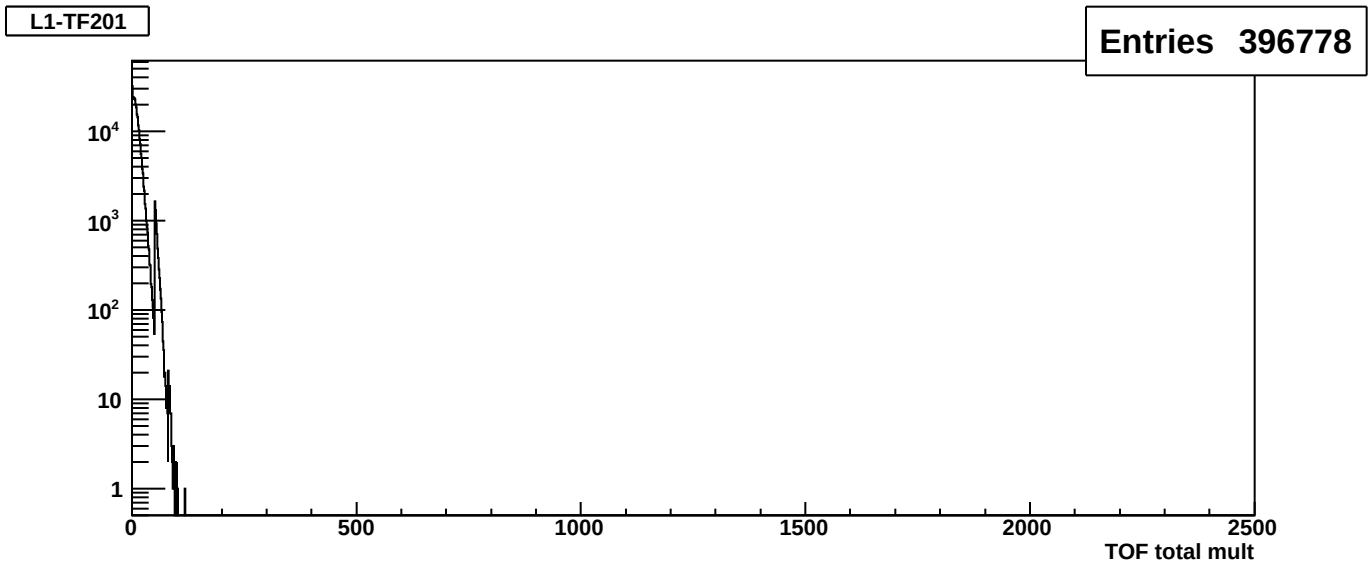

MIX-TF003
Entries 1.666468e+07
TOF MULT
TOF tray

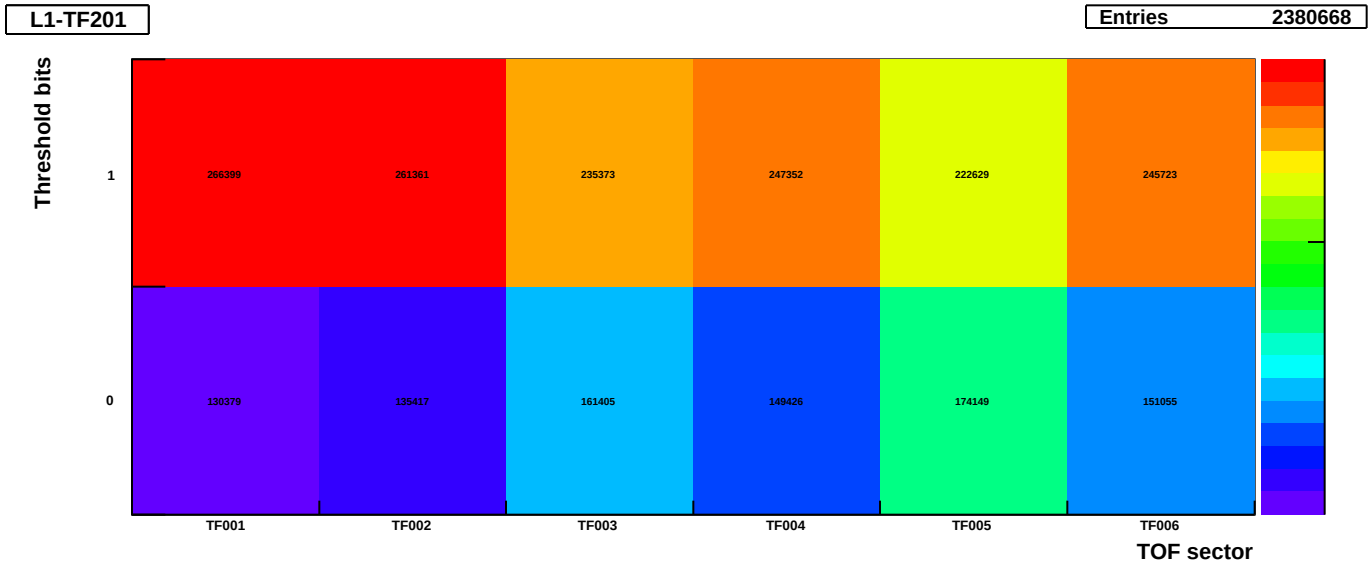

MIX-TF004
Entries 1.666468e+07
TOF MULT
TOF tray

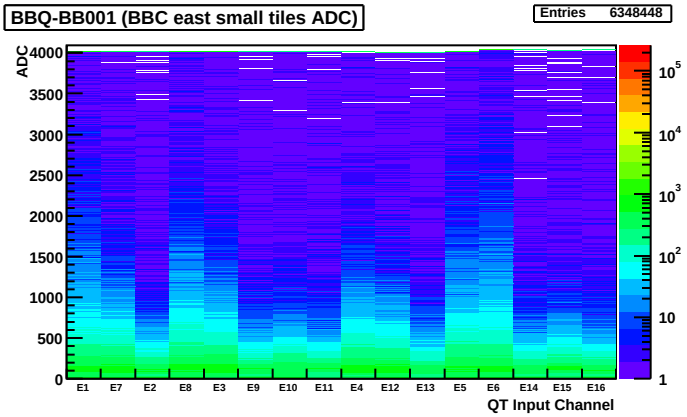

MIX-TF005
Entries 1.666468e+07
TOF MULT
TOF tray

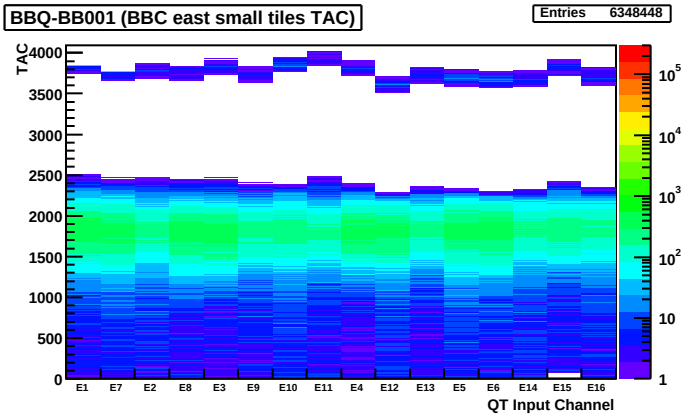

MIX-TF006
Entries 1.666468e+07
TOF MULT
TOF tray

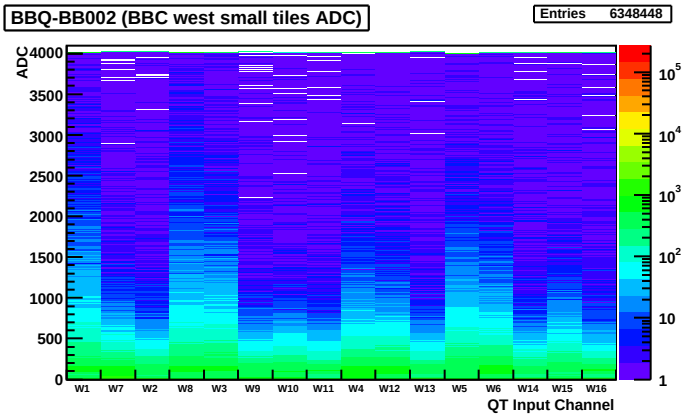

MIX-TF101
Entries 2380668
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

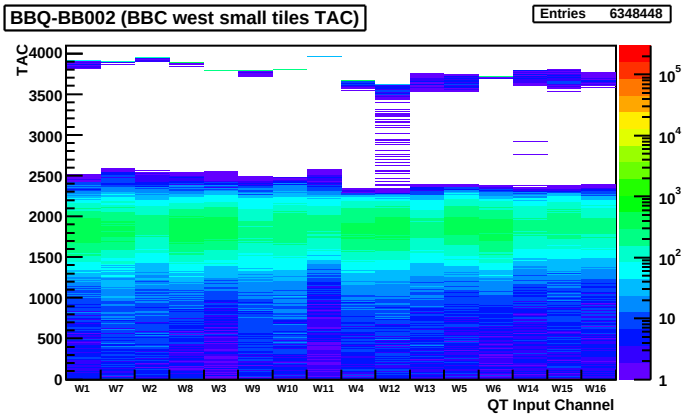

L1-TF201
Entries 396778
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

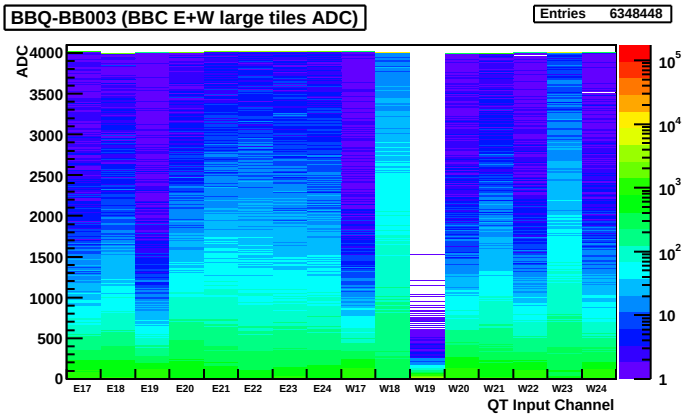

L1-TF201
Entries 2380668
Threshold bits
1
0
266399
261361
235373
247352
222629
245723
130379
135417
161405
149426
174149
151055
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

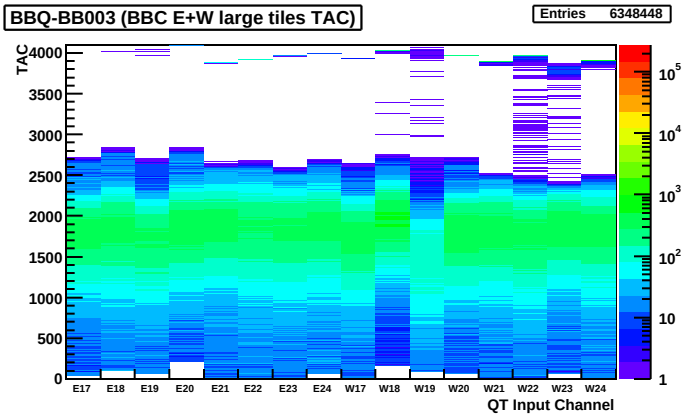

BBQ-BB001 (BBC east small tiles ADC)
Entries 6348448
ADC
QT Input Channel

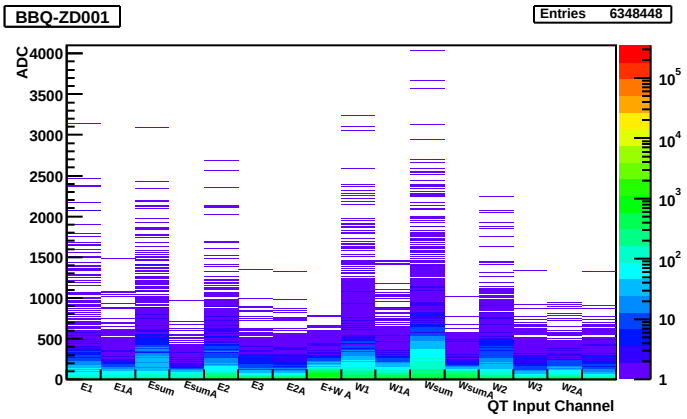

BBQ-BB001 (BBC east small tiles TAC)
Entries 6348448
TAC
QT Input Channel

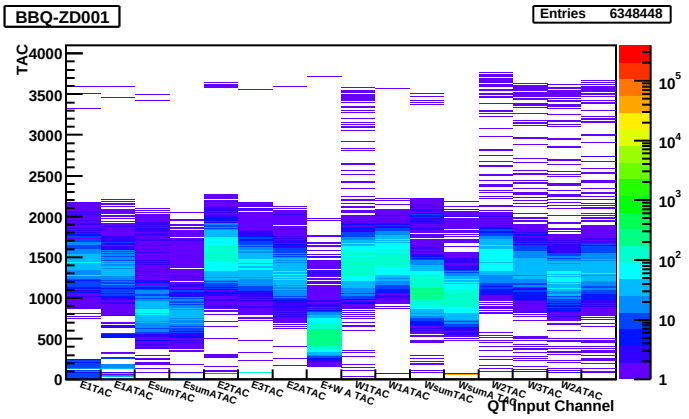

BBQ-BB002 (BBC west small tiles ADC)
Entries 6348448
ADC
QT Input Channel

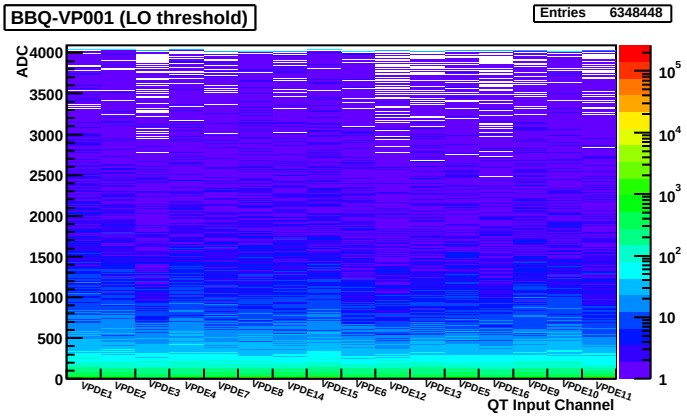

BBQ-BB002 (BBC west small tiles TAC)
Entries 6348448
TAC
QT Input Channel

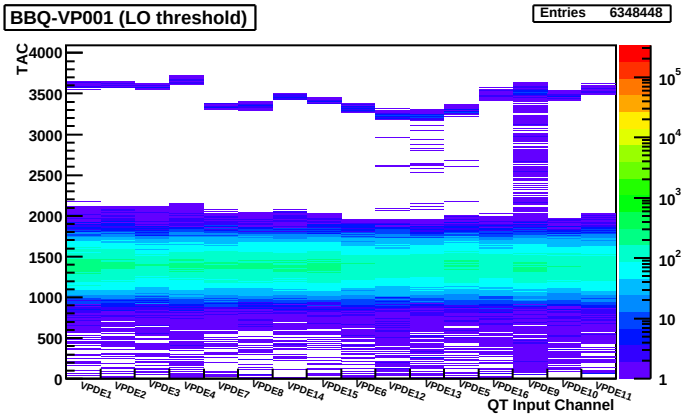

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6348448
ADC
QT Input Channel

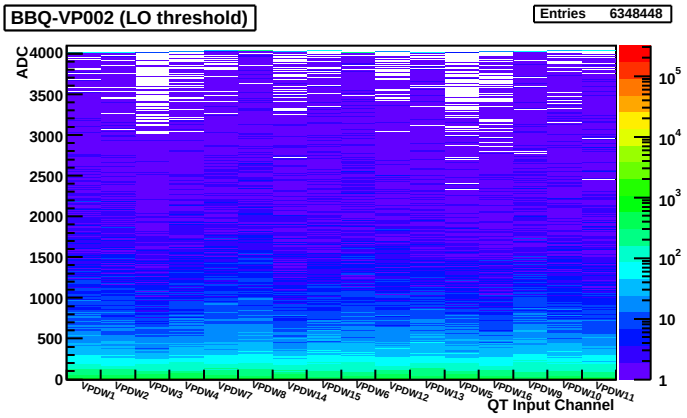

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6348448
TAC
QT Input Channel

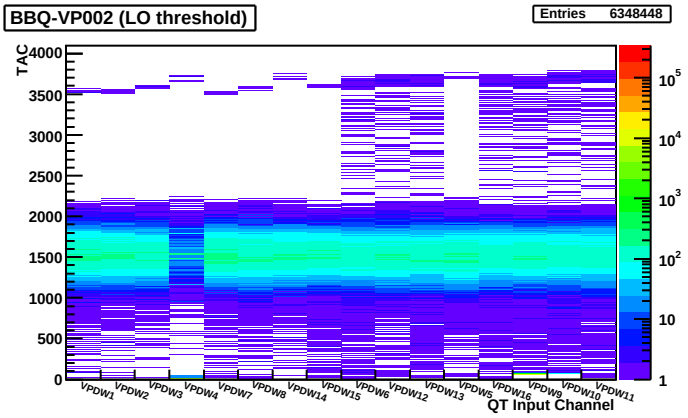

BBQ-ZD001
Entries 6348448
ADC
QT Input Channel

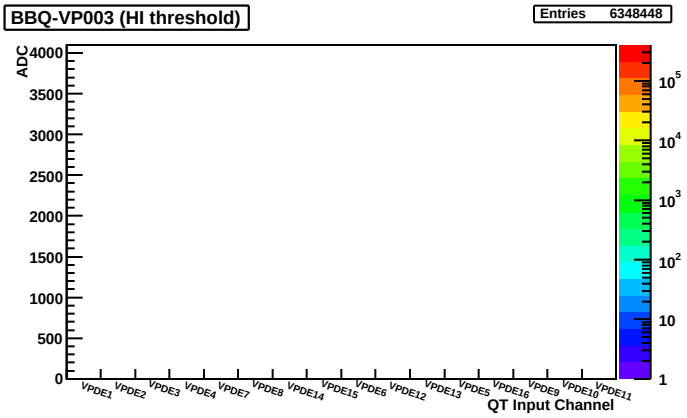

BBQ-ZD001
Entries 6348448
TAC
QT Input Channel

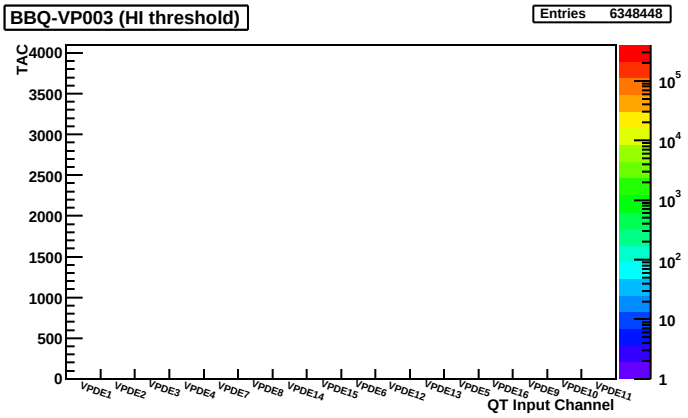

BBQ-VP001 (LO threshold)
Entries 6348448
ADC
QT Input Channel

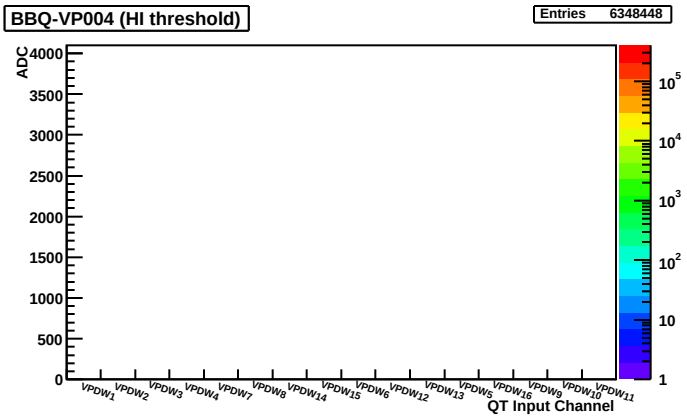

BBQ-VP001 (LO threshold)
Entries 6348448
TAC
QT Input Channel

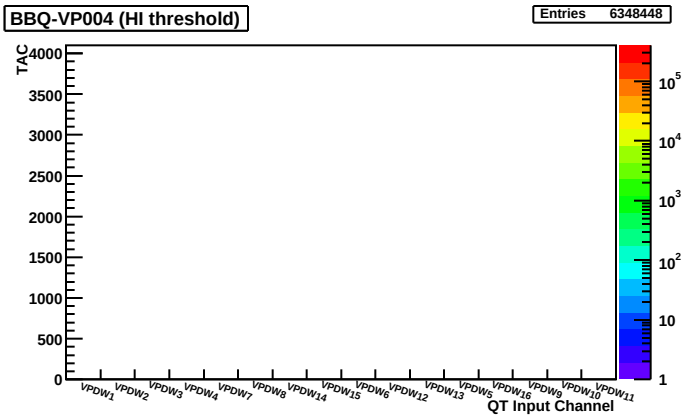

BBQ-VP002 (LO threshold)
Entries 6348448
ADC
QT Input Channel

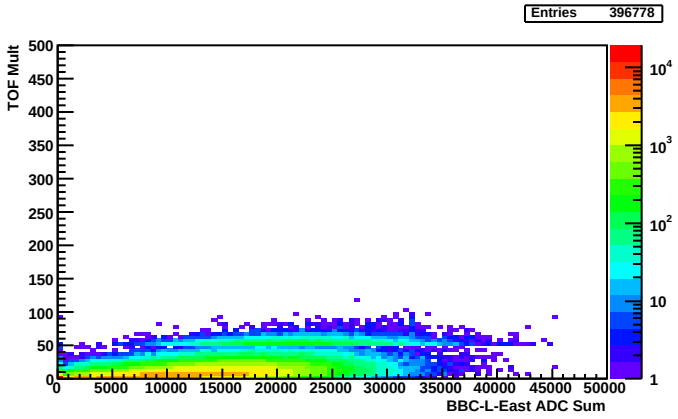

BBQ-VP002 (LO threshold)
Entries 6348448
TAC
QT Input Channel

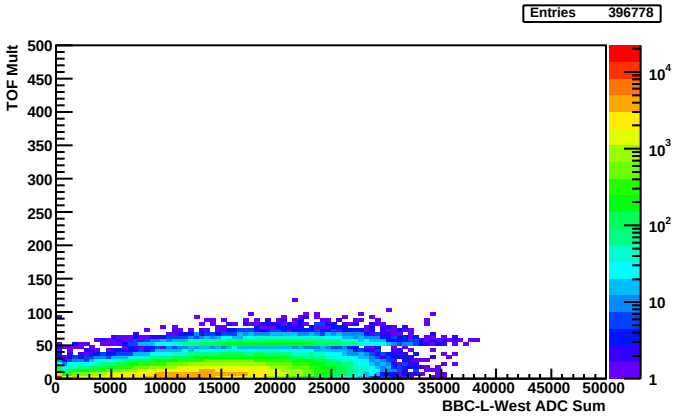

BBQ-VP003 (HI threshold)
Entries 6348448
ADC
QT Input Channel

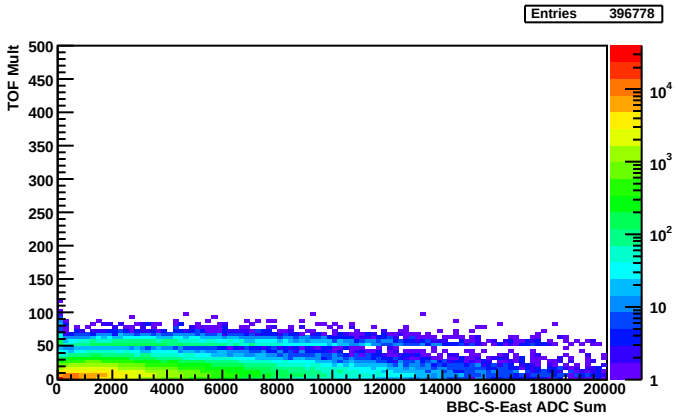

BBQ-VP003 (HI threshold)
Entries 6348448
TAC
QT Input Channel

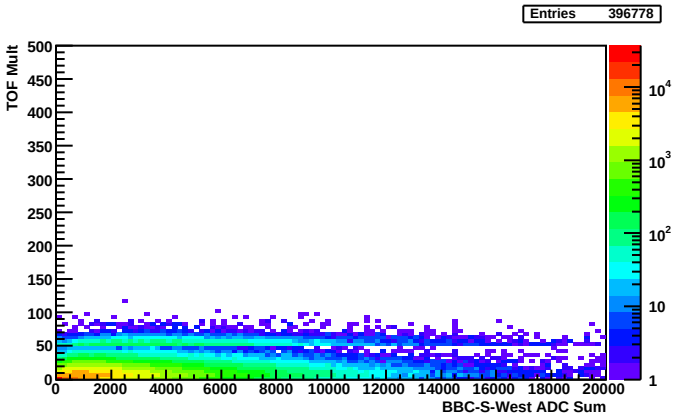

BBQ-VP004 (HI threshold)
Entries 6348448
ADC
QT Input Channel

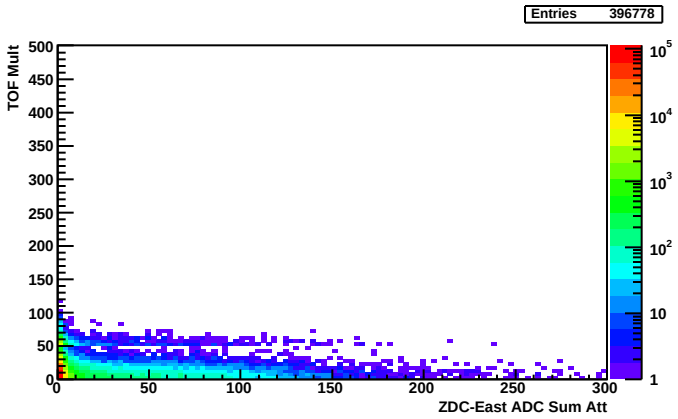

BBQ-VP004 (HI threshold)
Entries 6348448
TAC
QT Input Channel

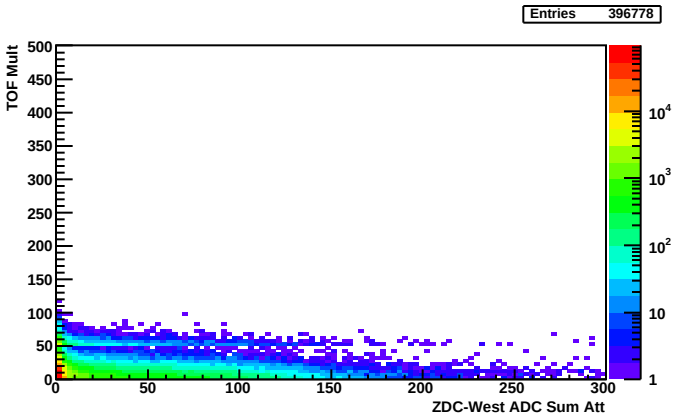

Entries 396778
TOF Mult
BBC-L-East ADC Sum

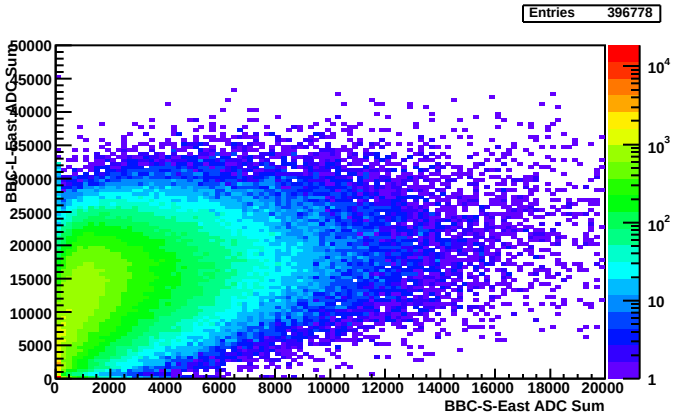

Entries 396778
TOF Mult
BBC-L-West ADC Sum

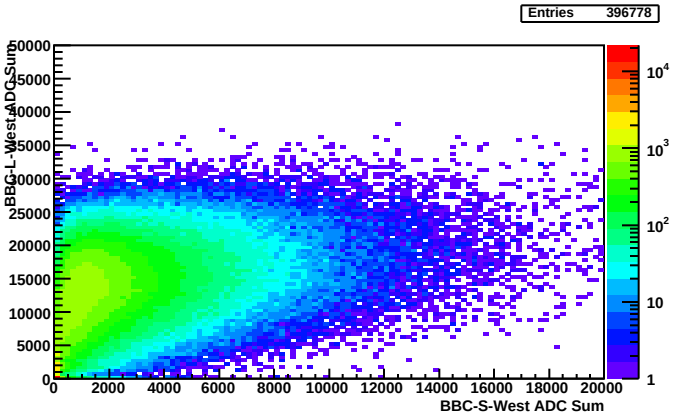

Entries 396778
TOF Mult
BBC-S-East ADC Sum

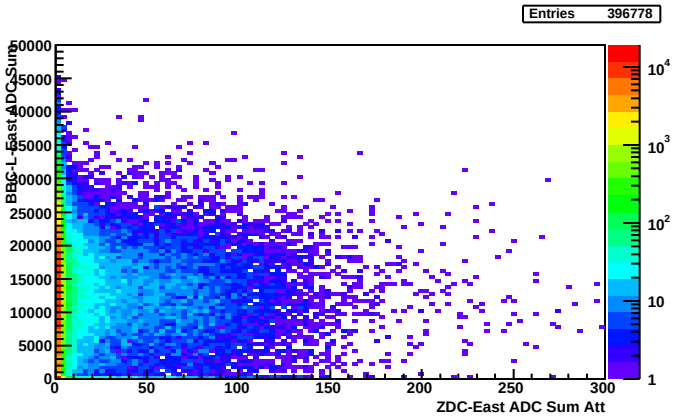

Entries 396778
TOF Mult
BBC-S-West ADC Sum

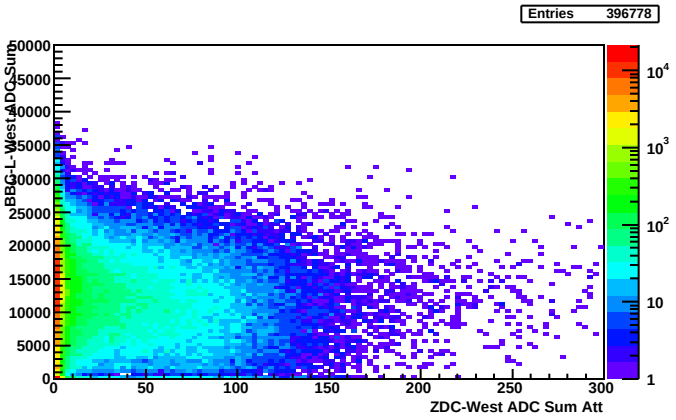

Entries 396778
TOF Mult
ZDC-East ADC Sum Att

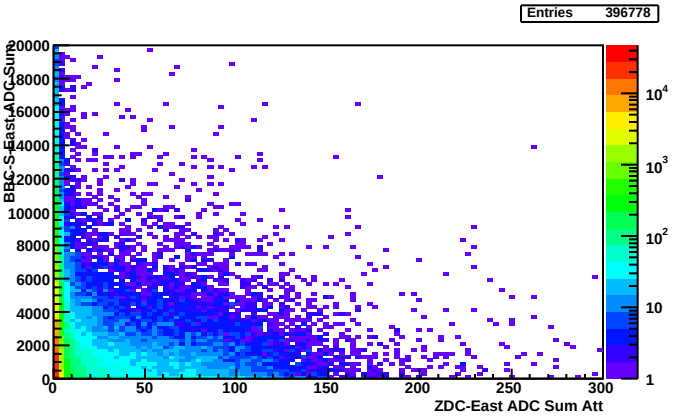

Entries 396778
TOF Mult
ZDC-West ADC Sum Att

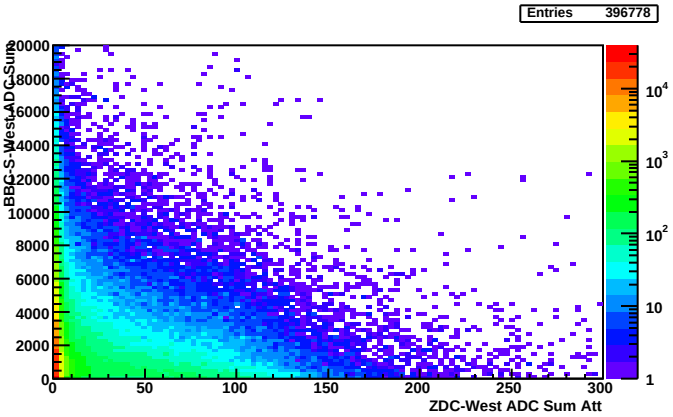

Entries 396778
BBC-L-East ADC Sum
BBC-S-East ADC Sum

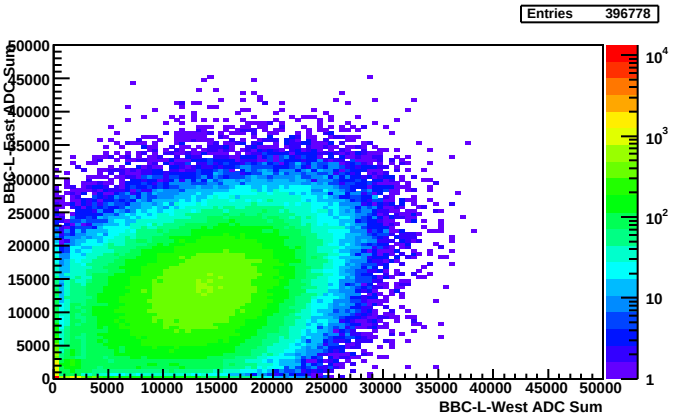

Entries 396778
BBC-L-West ADC Sum
BBC-S-West ADC Sum

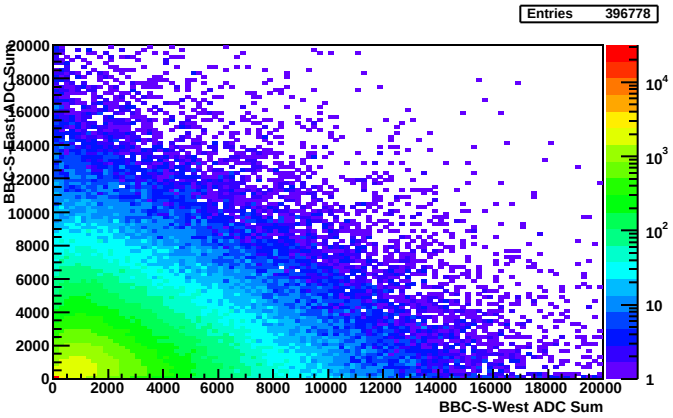

Entries 396778
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

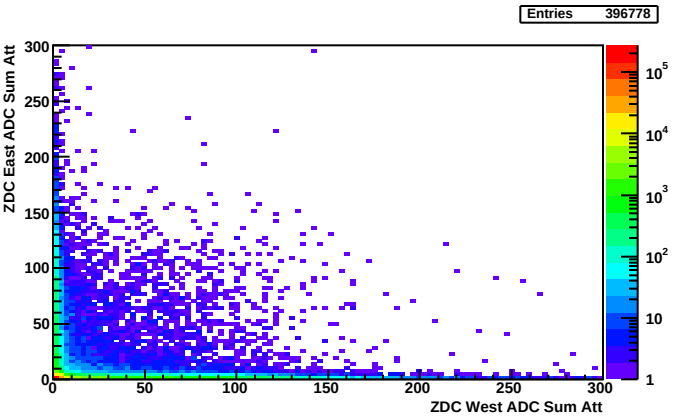

Entries 396778
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

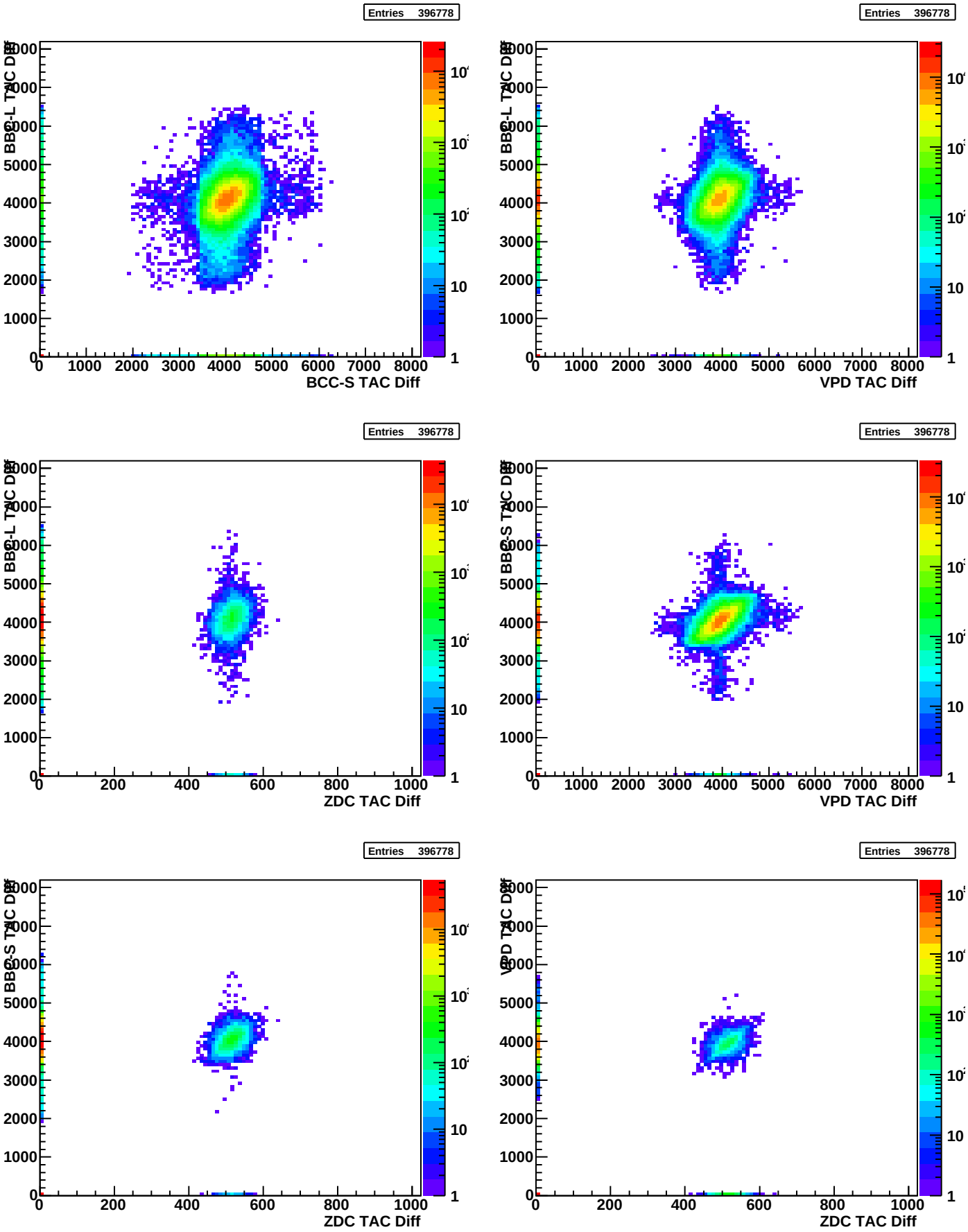

Entries 396778
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

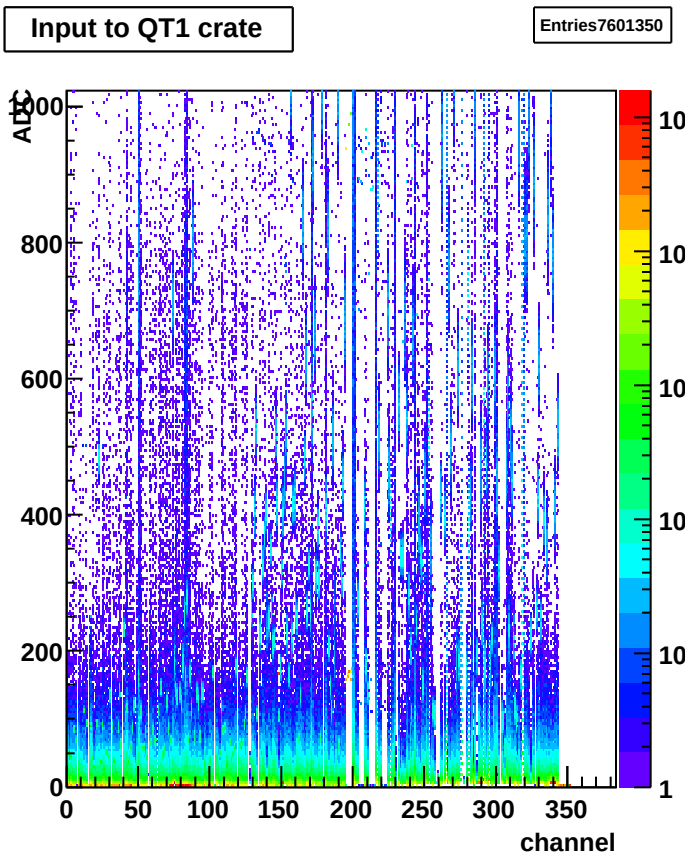

Entries 396778
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

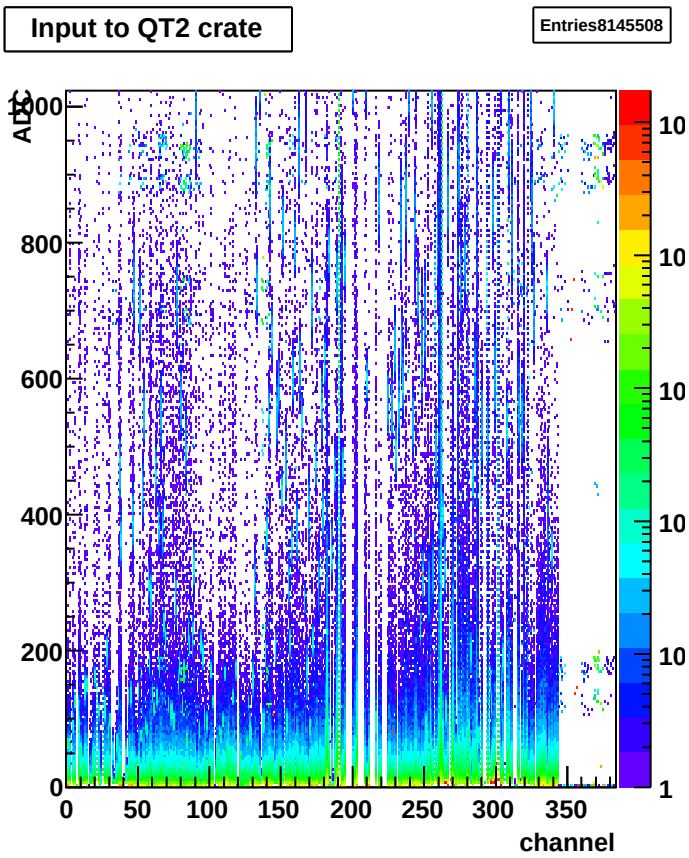

Entries 396778
BBC-L-East ADC Sum
BBC-L-West ADC Sum

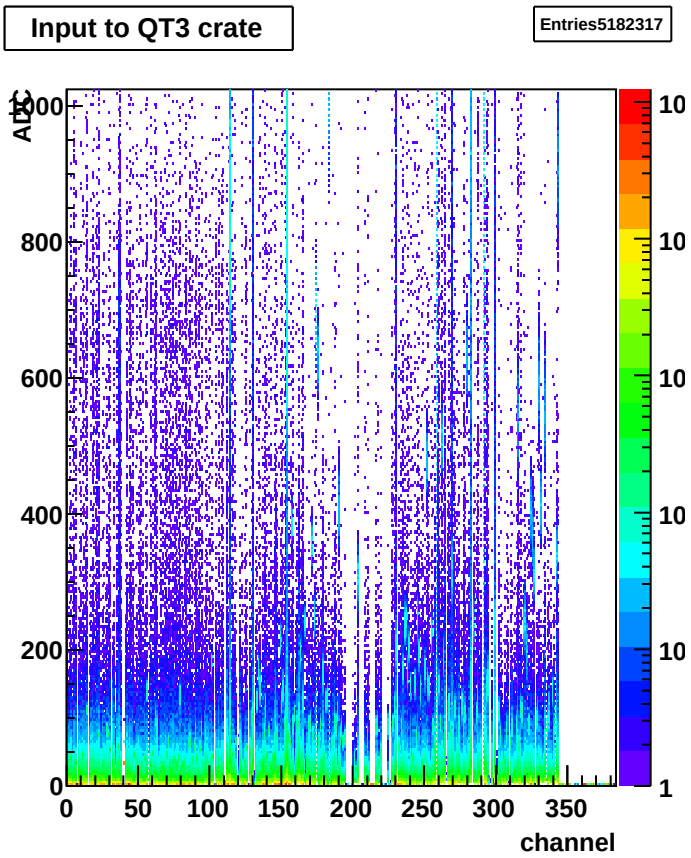

Entries 396778
BBC-S-East ADC Sum
BBC-S-West ADC Sum

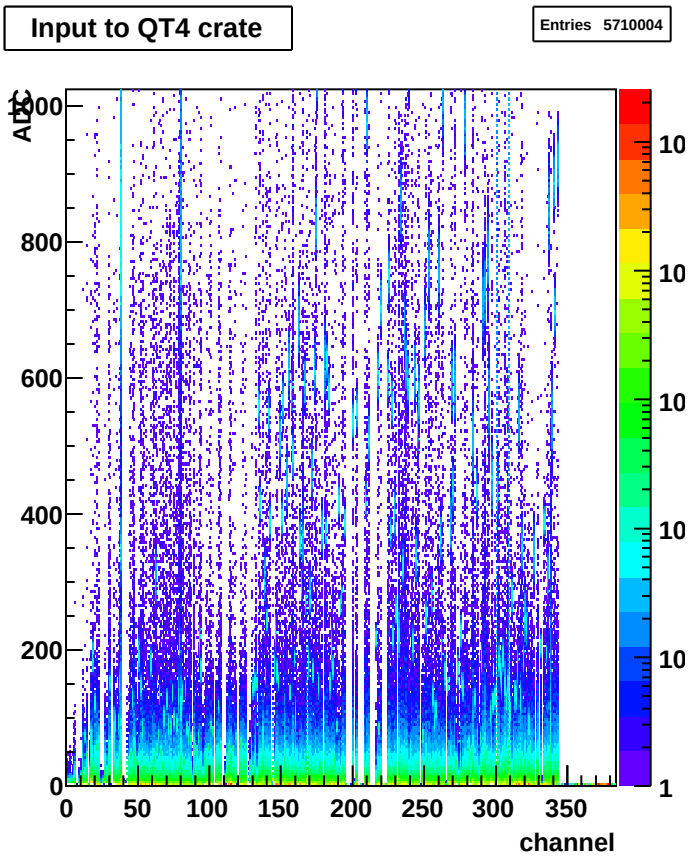

Entries 396778
ZDC East ADC Sum Att
ZDC West ADC Sum Att

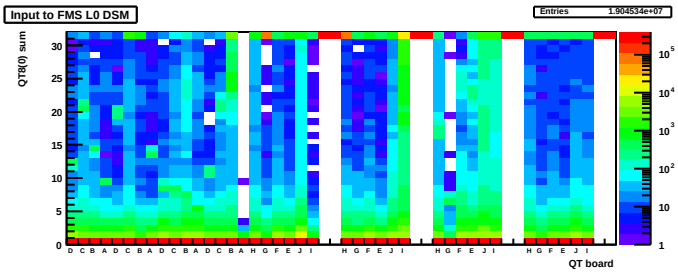

Entries 396778
BBC-L TAC Diff
BCC-S TAC Diff
Entries 396778
BBC-L TAC Diff
VPD TAC Diff
Entries 396778
BBC-L TAC Diff
ZDC TAC Diff
Entries 396778
BBC-S TAC Diff
VPD TAC Diff
Entries 396778
BBC-S TAC Diff
ZDC TAC Diff
Entries 396778
VPD TAC Diff
ZDC TAC Diff

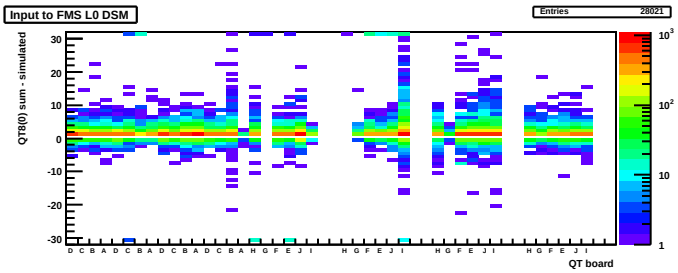

Input to QT1 crate
Entries7601350
ADC
channel

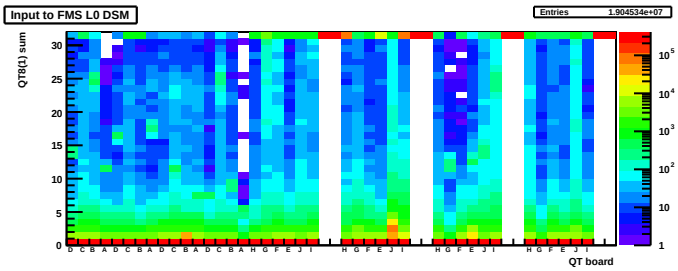

Input to QT2 crate
Entries8145508
ADC
channel

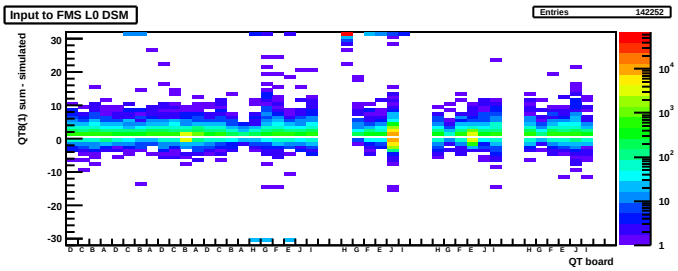

Input to QT3 crate
Entries5182317
ADC
channel

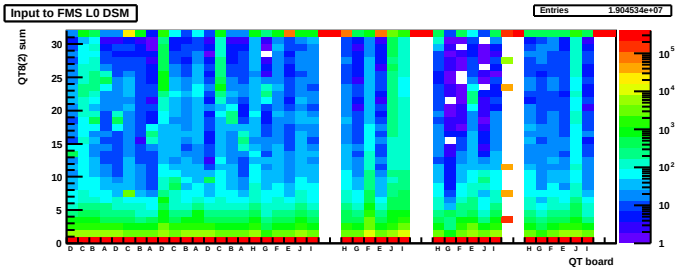

Input to QT4 crate
Entries 5710004
ADC
channel

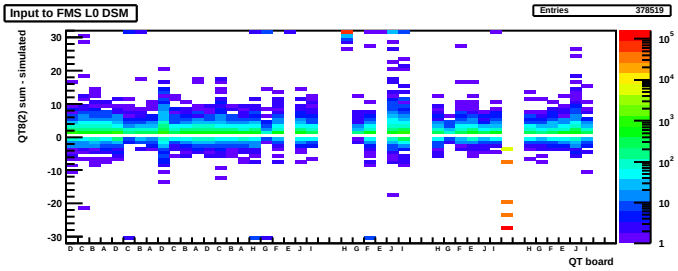

Input to FMS L0 DSM
Entries 1.904534e+07
QT8(0) sum
QT board

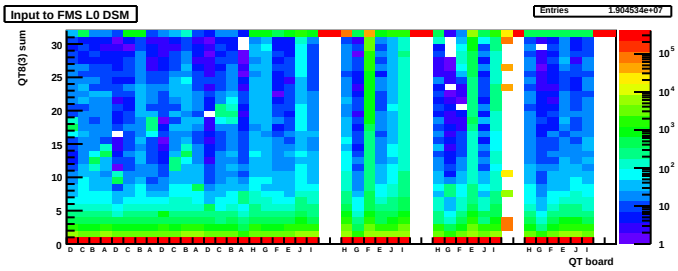

Input to FMS L0 DSM
Entries 28021
QT8(0) sum - simulated
QT board

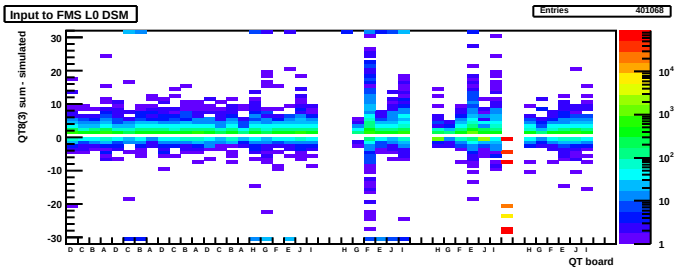

Input to FMS L0 DSM
Entries 1.904534e+07
QT8(1) sum
QT board

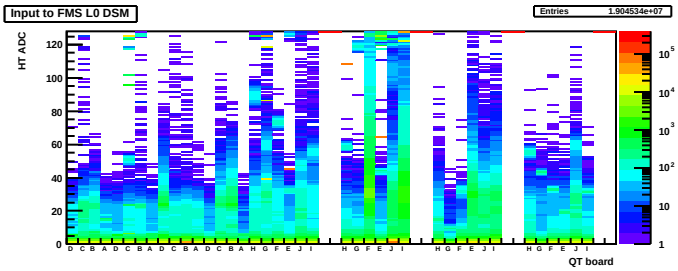

Input to FMS L0 DSM
Entries 142252
QT8(1) sum - simulated
QT board

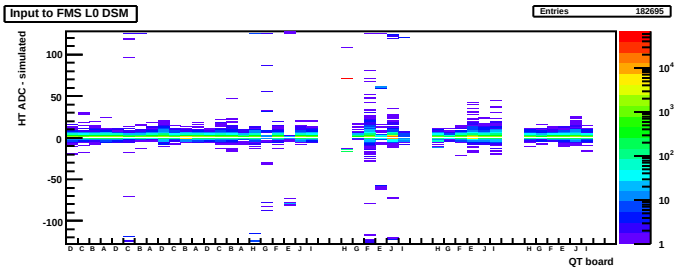

Input to FMS L0 DSM
Entries 1.904534e+07
QT8(2) sum
QT board

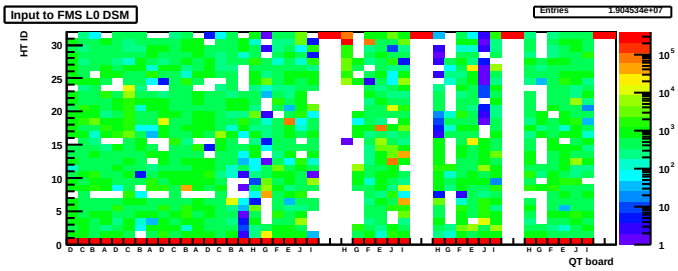

Input to FMS L0 DSM
Entries 378519
QT8(2) sum - simulated
QT board

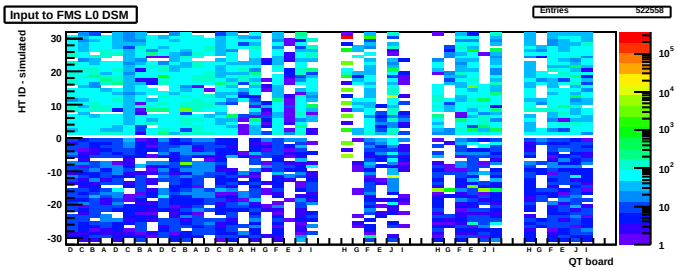

Input to FMS L0 DSM
Entries 1.904534e+07
QT8(3) sum
QT board

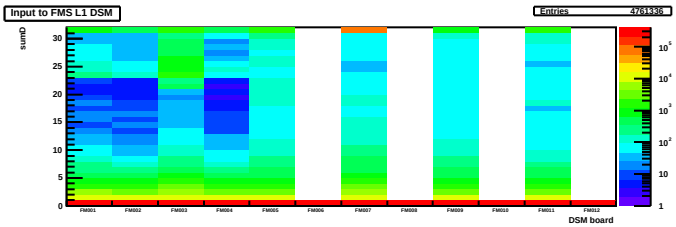

Input to FMS L0 DSM
Entries 401068
QT8(3) sum - simulated
QT board

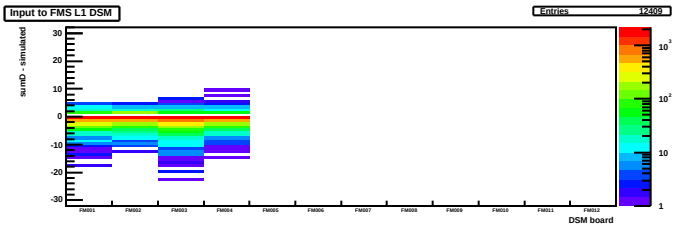

Input to FMS L0 DSM
Entries 1.904534e+07
HT ADC
QT board

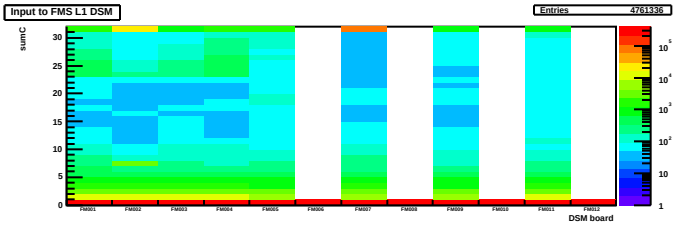

Input to FMS L0 DSM
Entries 182695
HT ADC - simulated
QT board

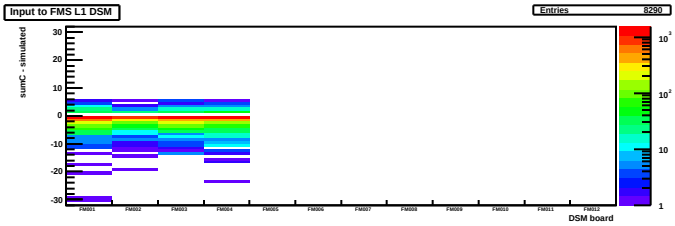

Input to FMS L0 DSM
Entries 1.904534e+07
HT ID
QT board

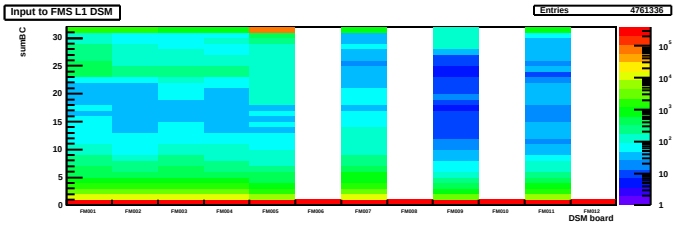

Input to FMS L0 DSM
Entries 522558
HT ID - simulated
QT board

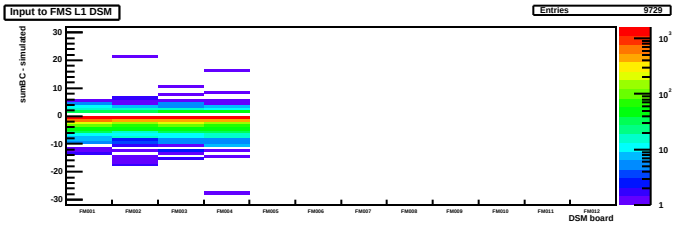

Input to FMS L1 DSM
Entries 4761336
sumD
DSM board

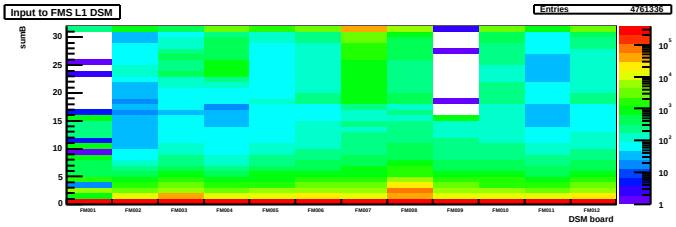

Input to FMS L1 DSM
Entries 12409
sumD - simulated
DSM board

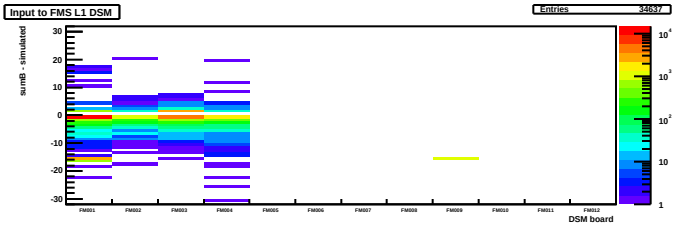

Input to FMS L1 DSM
Entries 4761336
sumC
DSM board

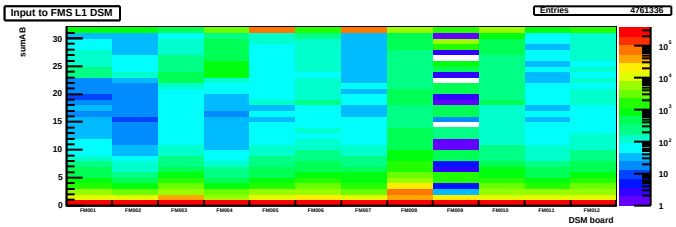

Input to FMS L1 DSM
Entries 8290
sumC - simulated
DSM board

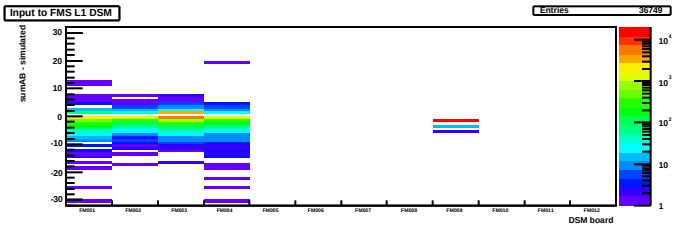

Input to FMS L1 DSM
Entries 4761336
sumBC
DSM board

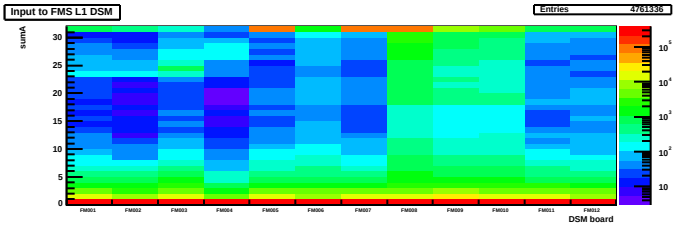

Input to FMS L1 DSM
Entries 9729
sumBC - simulated
DSM board

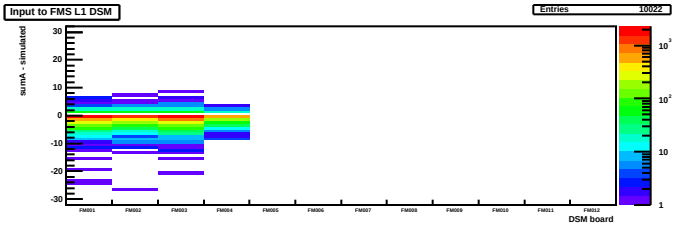

Input to FMS L1 DSM
Entries 4761336
sumB
DSM board

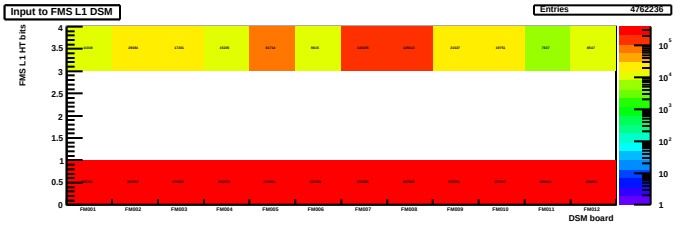

Input to FMS L1 DSM
Entries 34637
sumB - simulated
DSM board

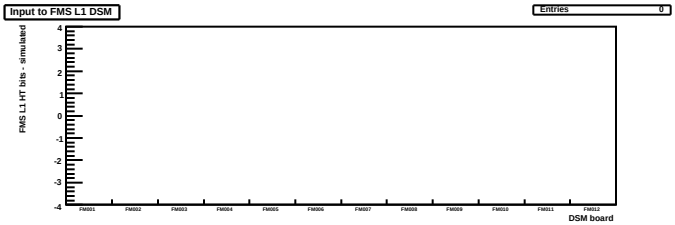

Input to FMS L1 DSM
Entries 4761336
sumAB
DSM board

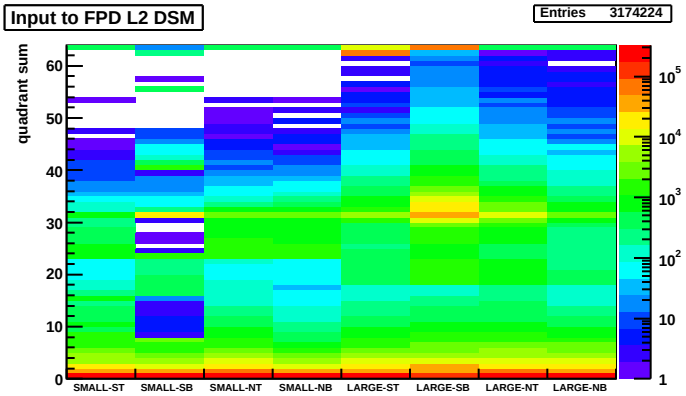

Input to FMS L1 DSM
Entries 36749
sumAB - simulated
DSM board

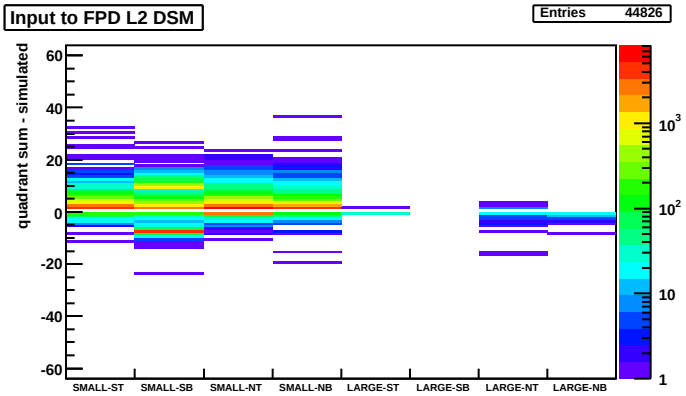

Input to FMS L1 DSM
Entries 4761336
sumA
DSM board

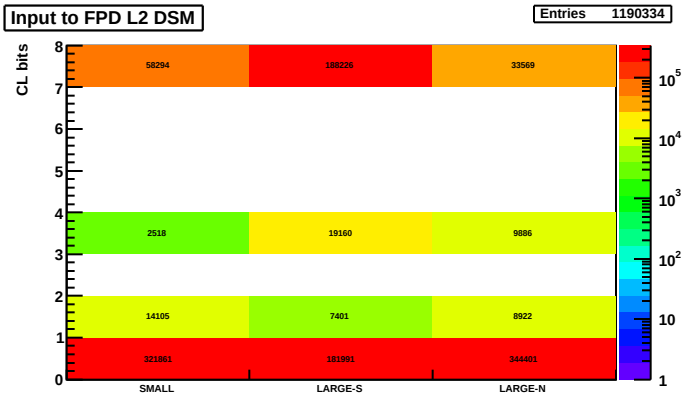

Input to FMS L1 DSM
Entries 10022
sumA - simulated
DSM board

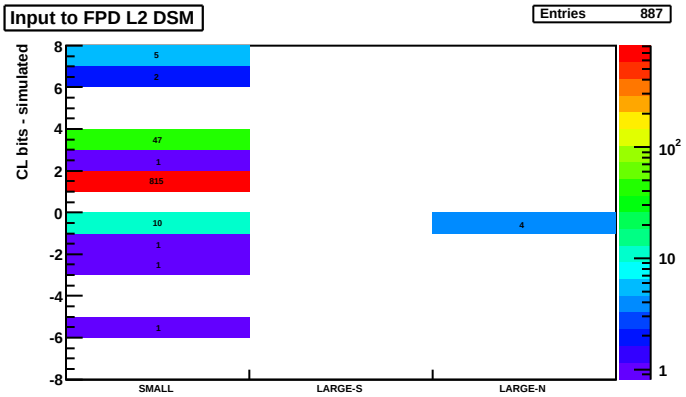

Input to FMS L1 DSM
Entries 4762236
FMS L1 HT bits
DSM board

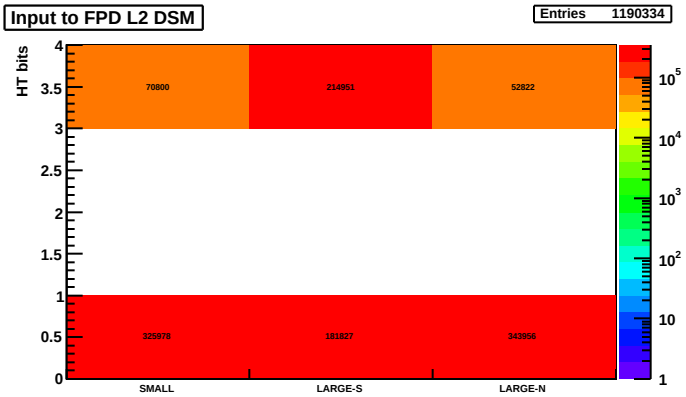

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

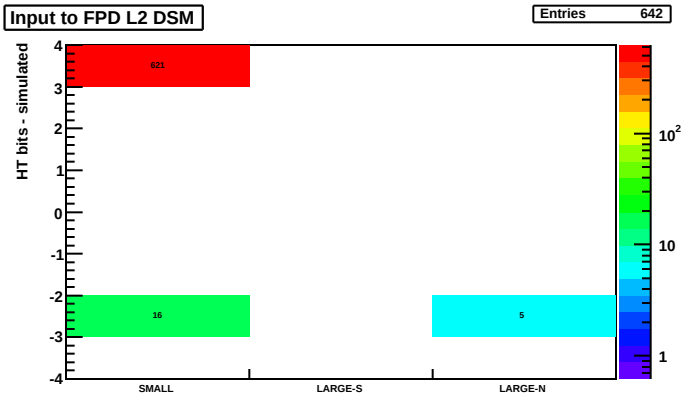

Input to FPD L2 DSM
Entries 3174224
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

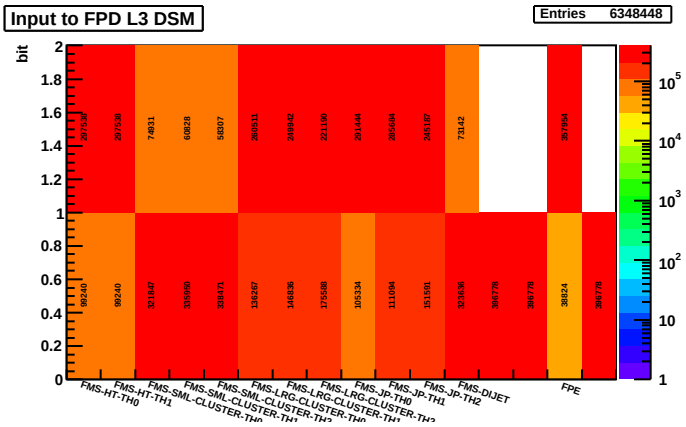

Input to FPD L2 DSM
Entries 44826
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

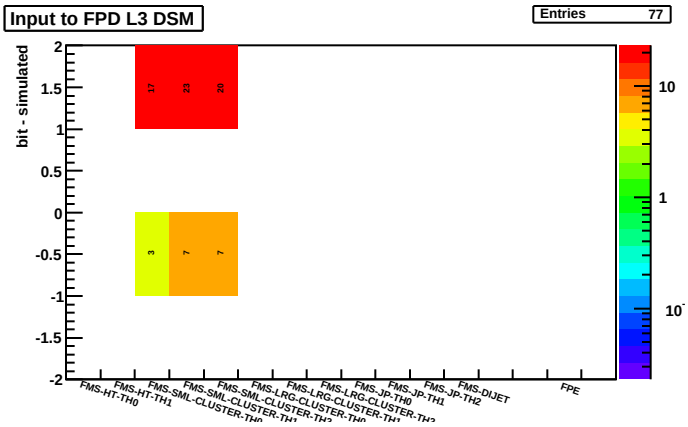

Input to FPD L2 DSM
Entries 1190334
CL bits
58294
188226
33569
2518
19160
9886
14105
7401
8922
321861
181991
344401
SMALL
LARGE-S
LARGE-N

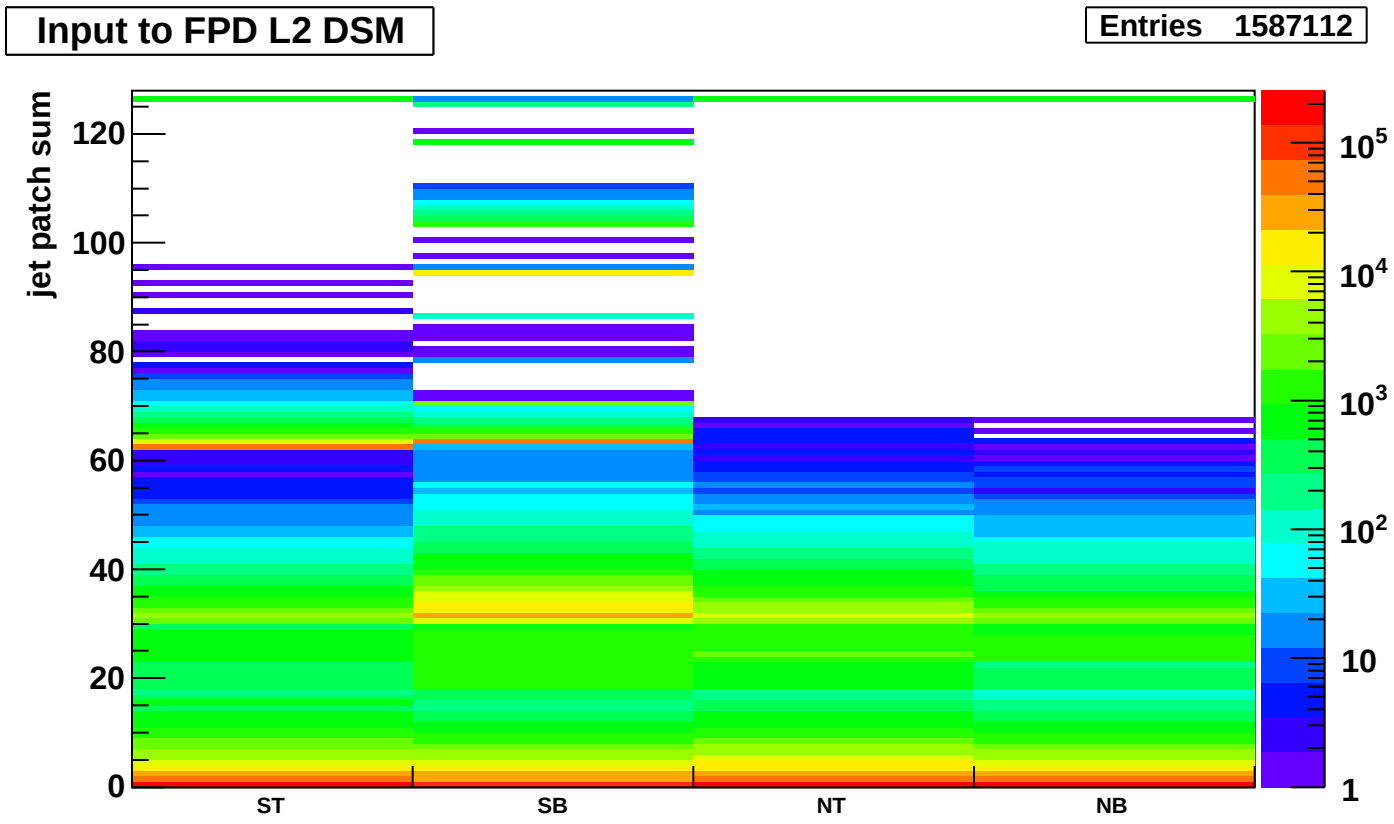

Input to FPD L2 DSM
Entries 887
CL bits - simulated
5
2
47
1
815
10
1
1
1
4
SMALL
LARGE-S
LARGE-N

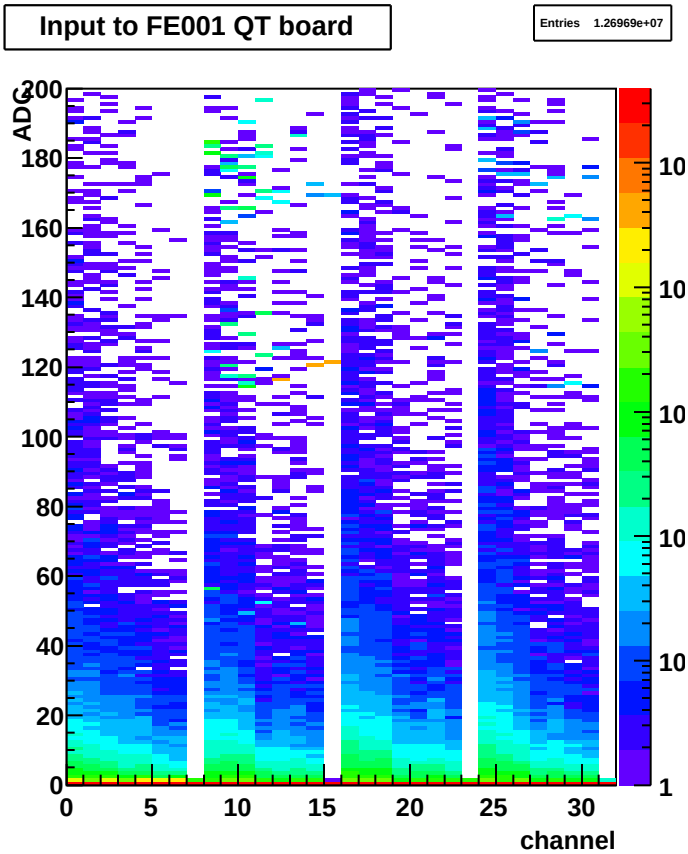

Input to FPD L2 DSM
Entries 1190334
HT bits
70800
214951
52822
325978
181827
343956
SMALL
LARGE-S
LARGE-N

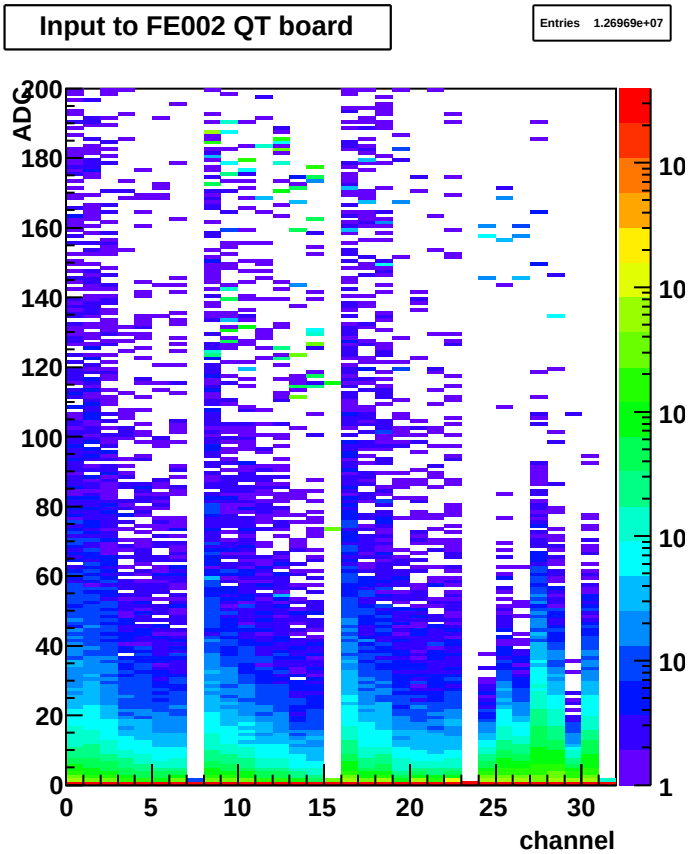

Input to FPD L2 DSM
Entries 642
HT bits - simulated
621
16
5
SMALL
LARGE-S
LARGE-N

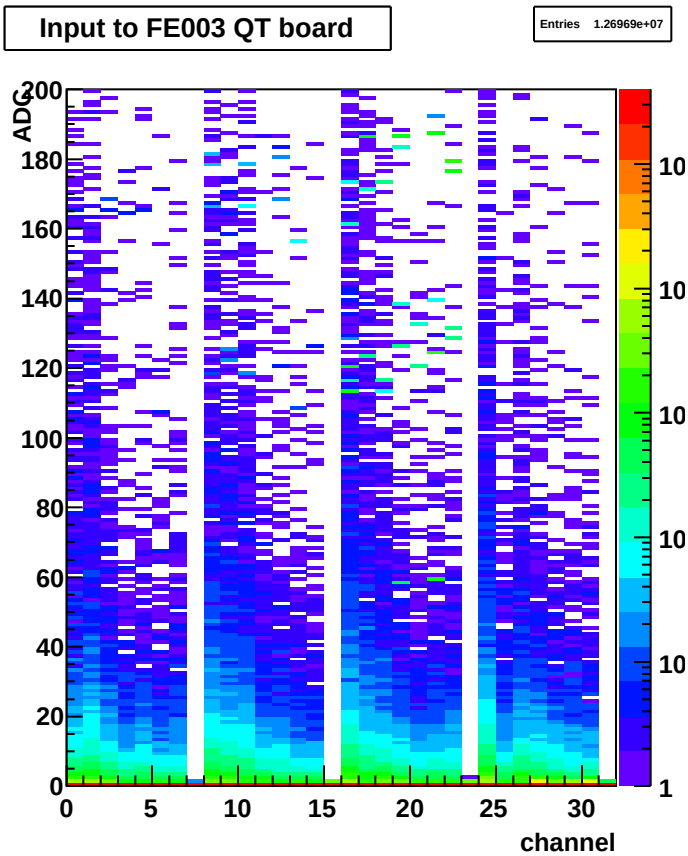

Input to FPD L3 DSM
Entries 6348448
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

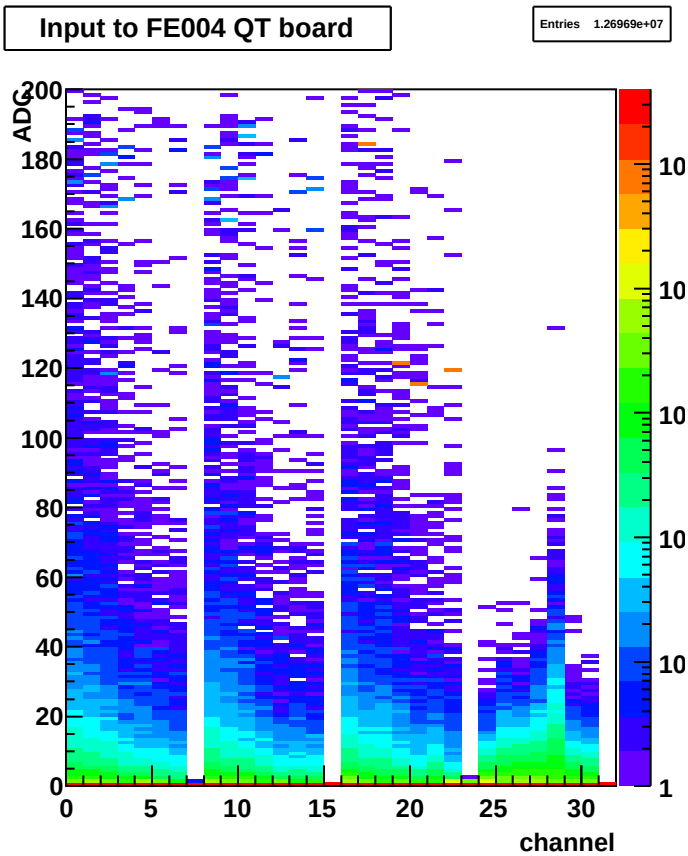

Input to FPD L3 DSM
Entries 77
bit - simulated
17
23
20
3
7
7
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

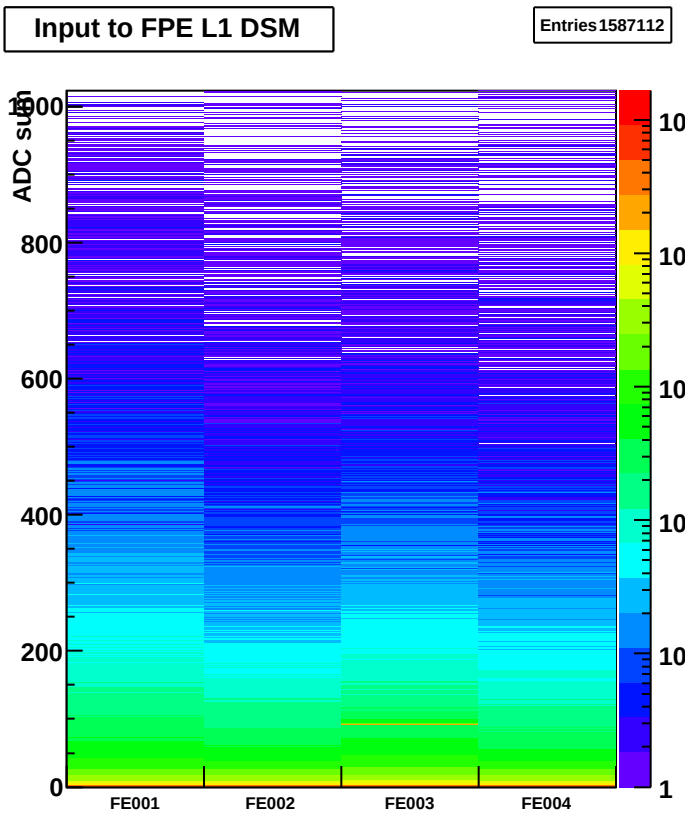

Input to FPD L2 DSM
Entries 1587112
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

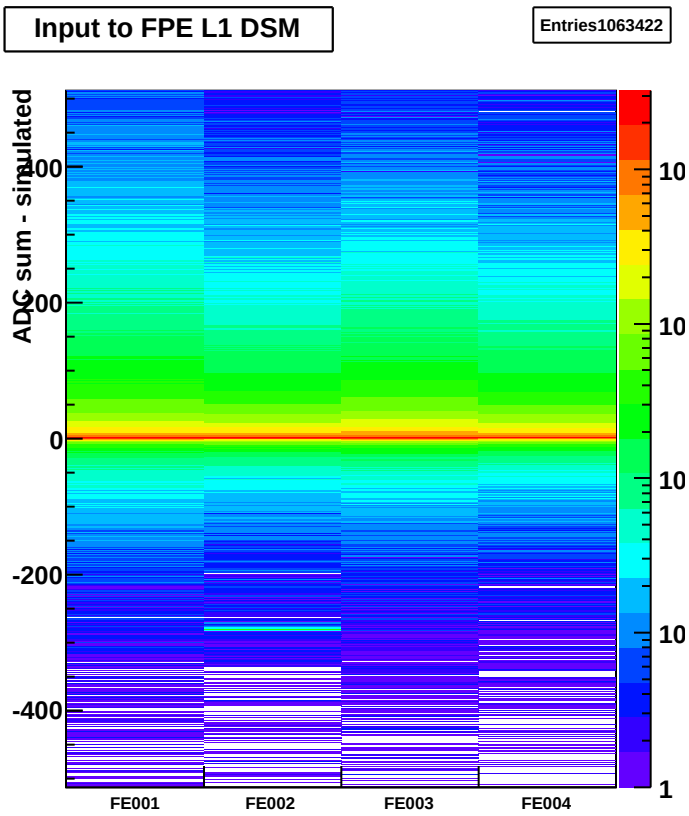

Input to FE001 QT board
Entries 1.26969e+07
ADC
channel

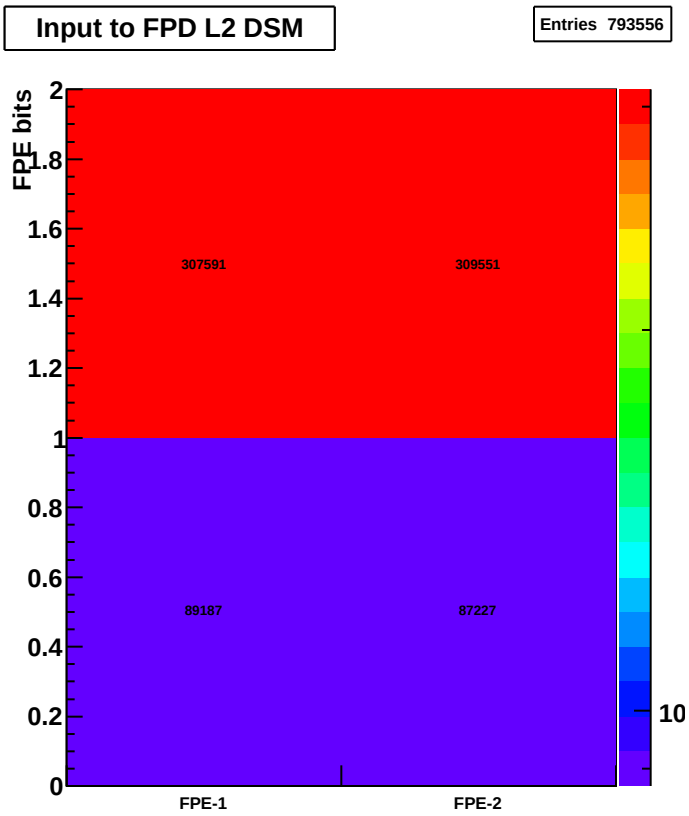

Input to FE002 QT board
Entries 1.26969e+07
ADC
channel

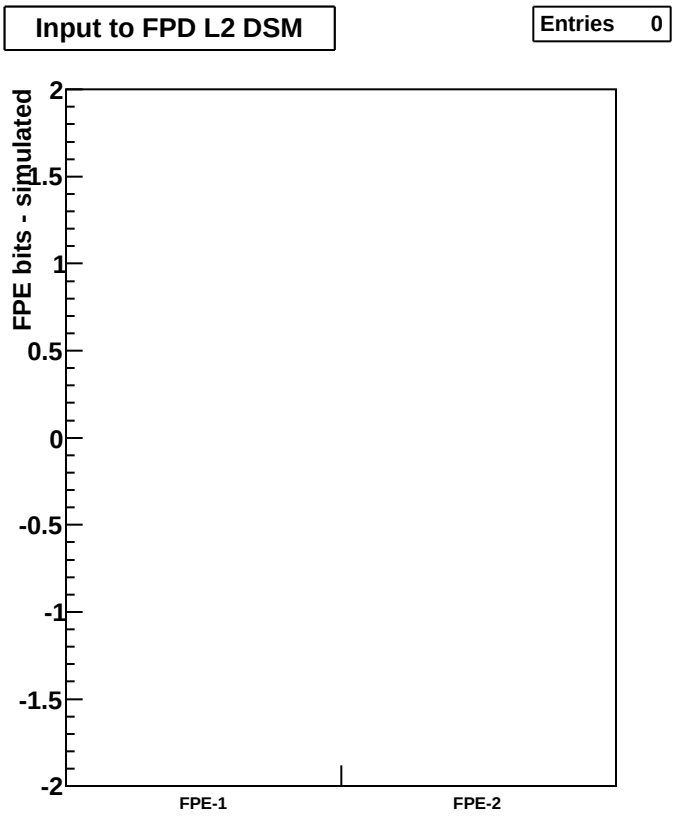

Input to FE003 QT board
Entries 1.26969e+07
ADC
channel


Input to FE004 QT board
Entries 1.26969e+07
ADC
channel


Input to FPE L1 DSM
Entries 1587112
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 1063422
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 793556
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
307591
309551
89187
87227
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2