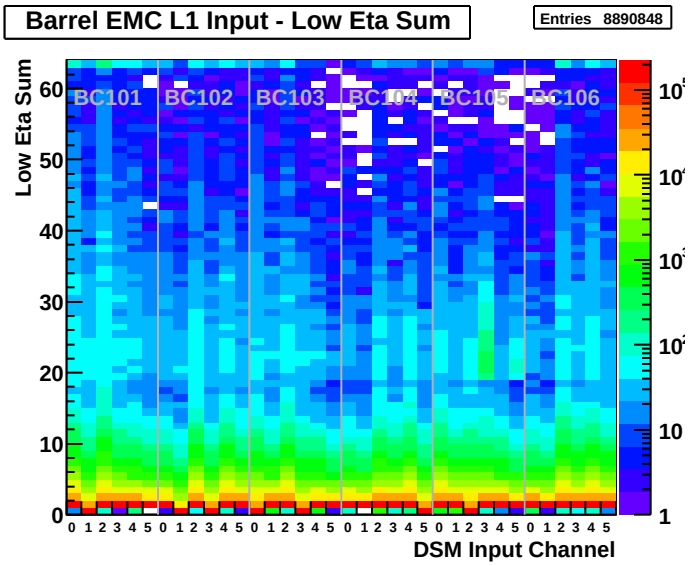
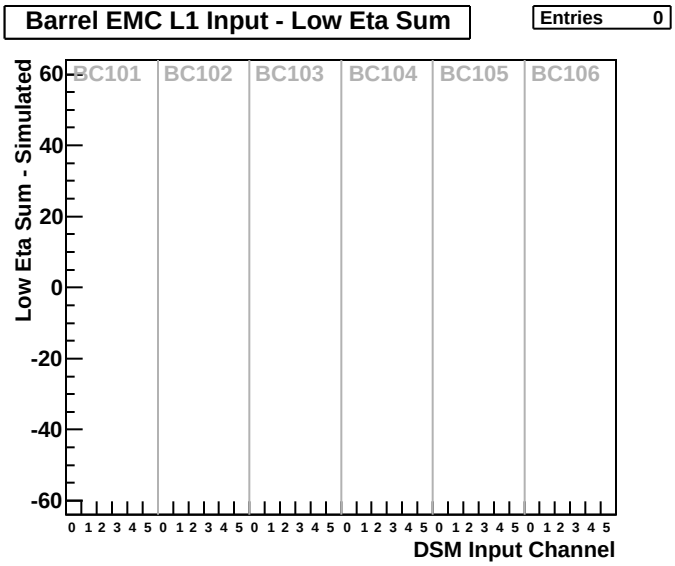
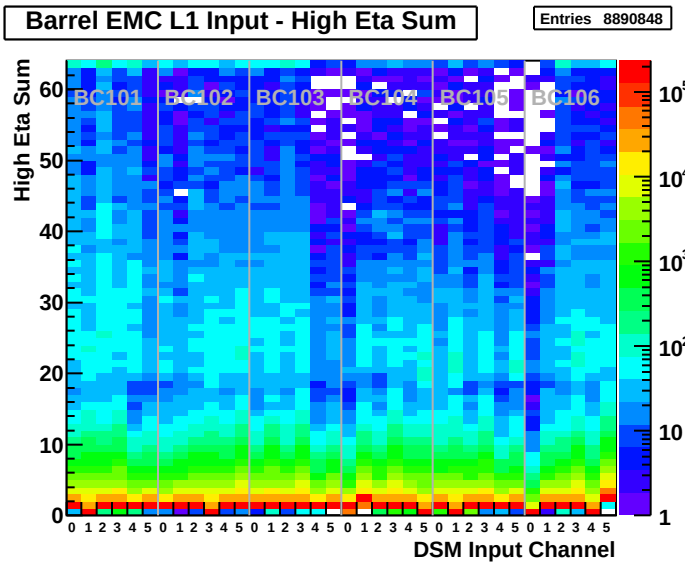
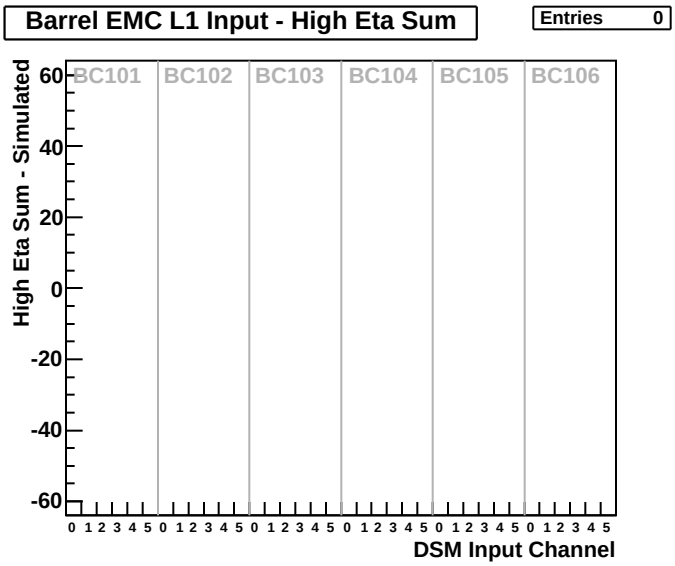
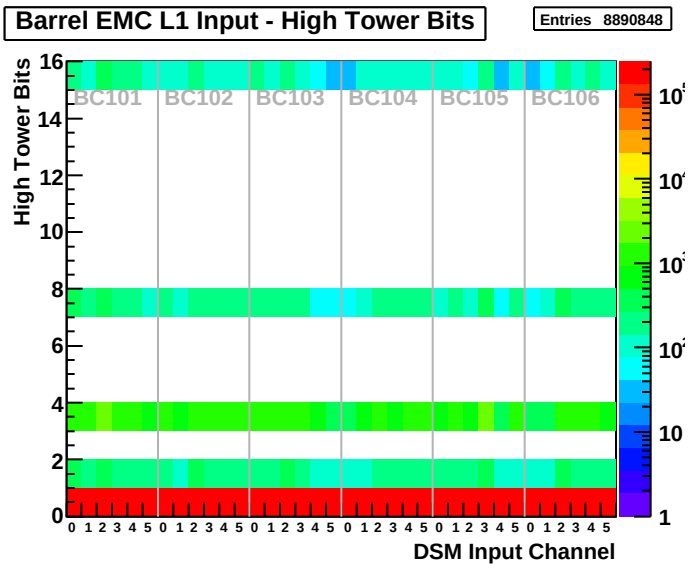
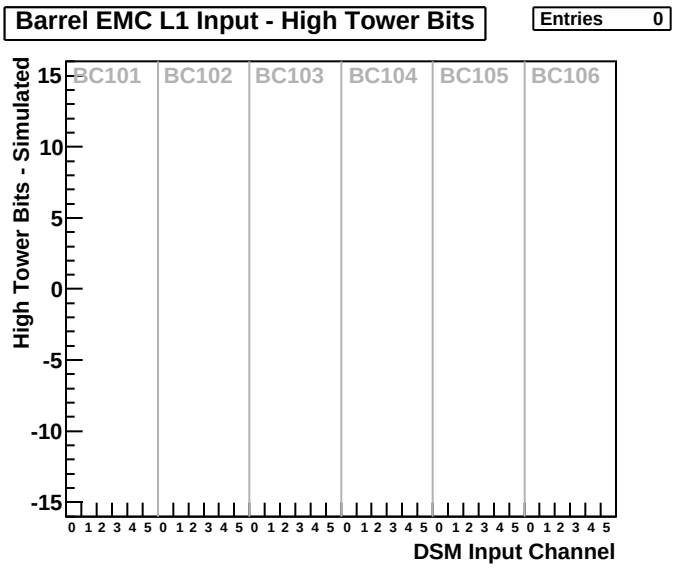

Barrel EMC L1 Input - Low Eta Sum
Entries 8890848
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

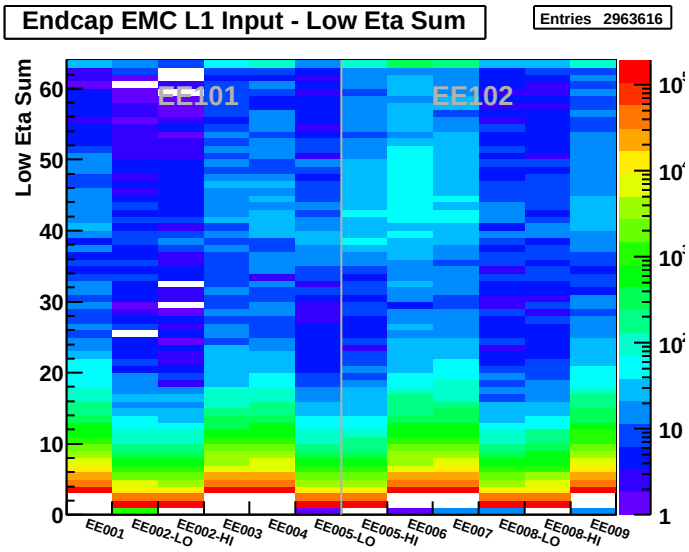

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

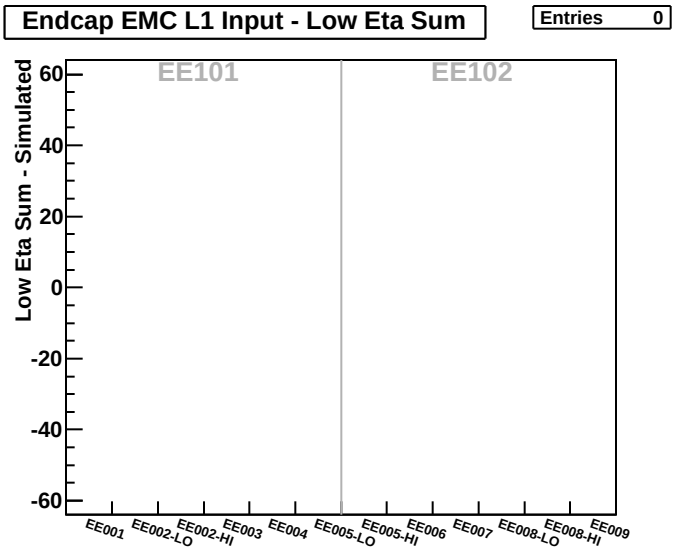

Barrel EMC L1 Input - High Eta Sum
Entries 8890848
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

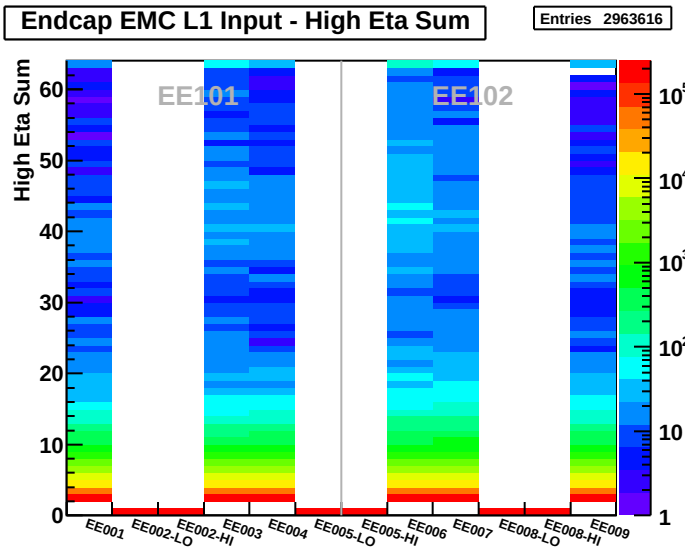

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

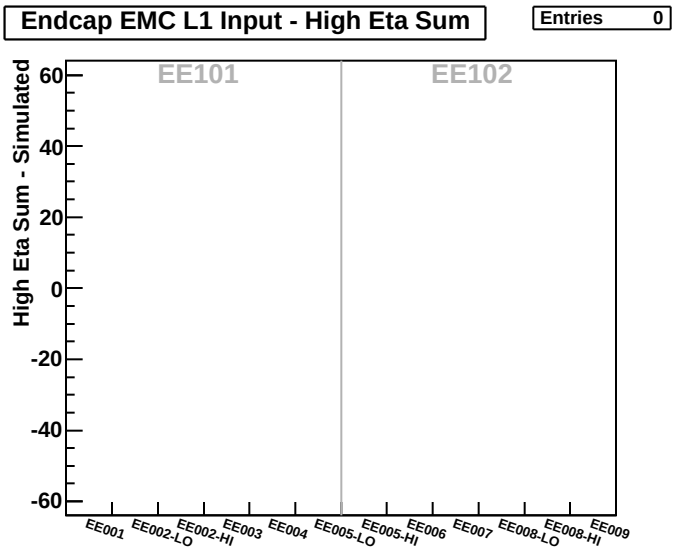

Barrel EMC L1 Input - High Tower Bits
Entries 8890848
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

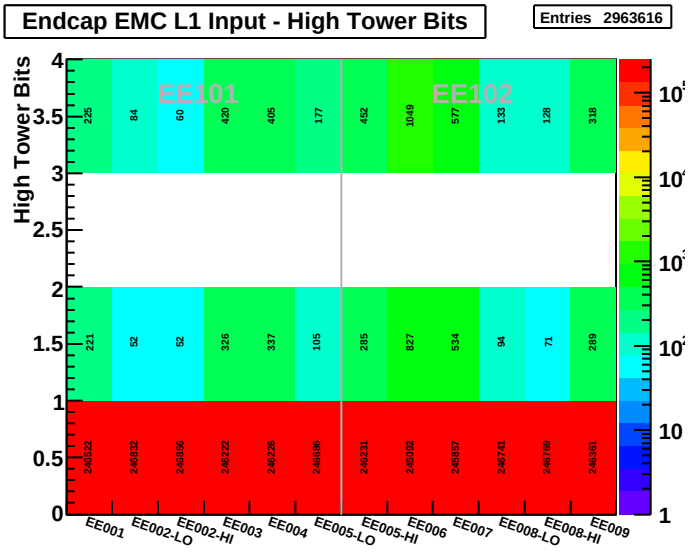

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

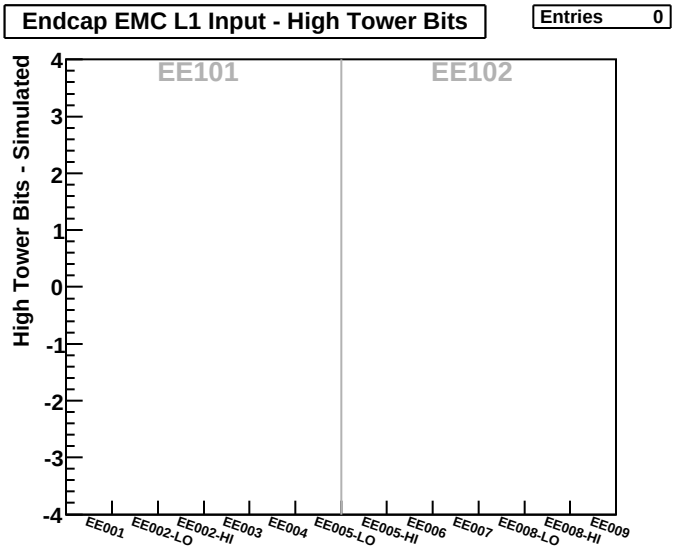

Endcap EMC L1 Input - Low Eta Sum
Entries 2963616
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

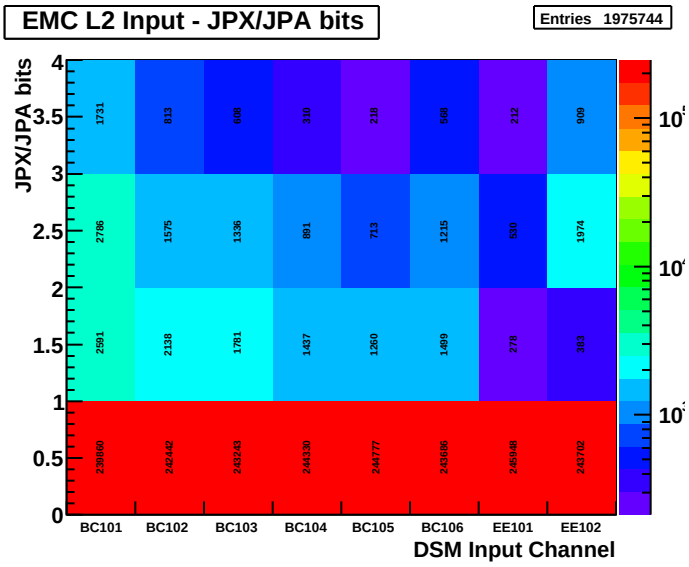

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

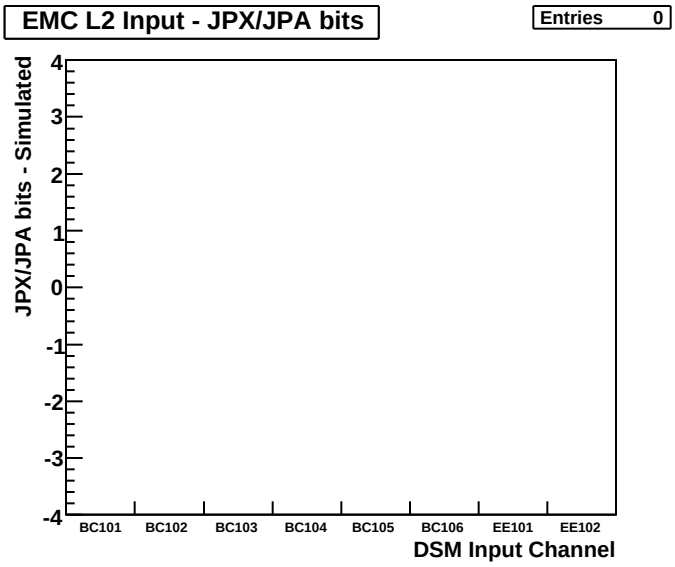

Endcap EMC L1 Input - High Eta Sum
Entries 2963616
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

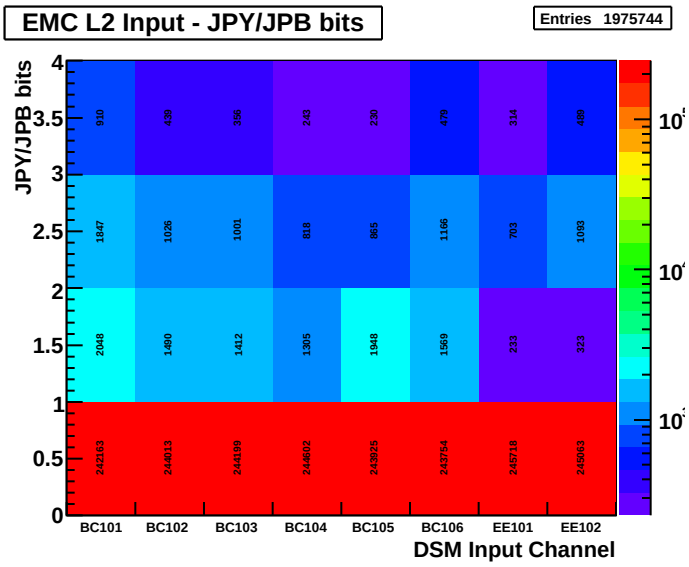

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

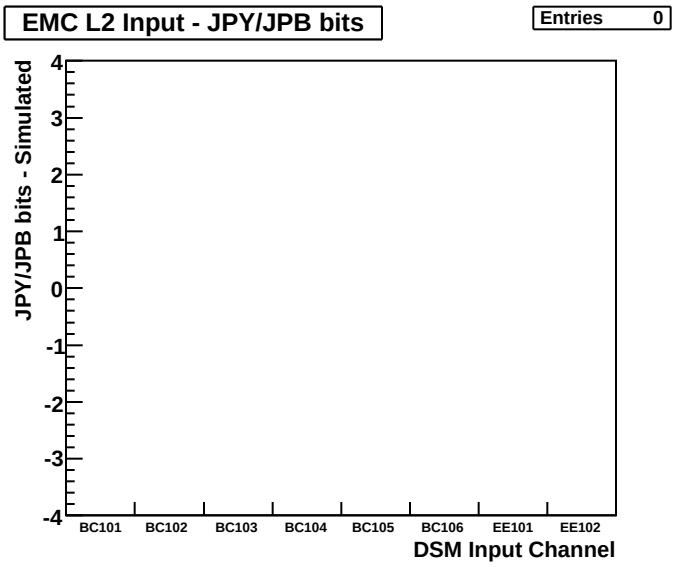

Endcap EMC L1 Input - High Tower Bits
Entries 2963616
High Tower Bits
EE101
EE102
225 84 60 420 405 177 452 1049 577 133 128 318
221 52 52 326 337 105 285 827 534 94 71 289
246522 246832 246856 246222 246226 246686 246231 245092 245657 246741 246769 246361
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

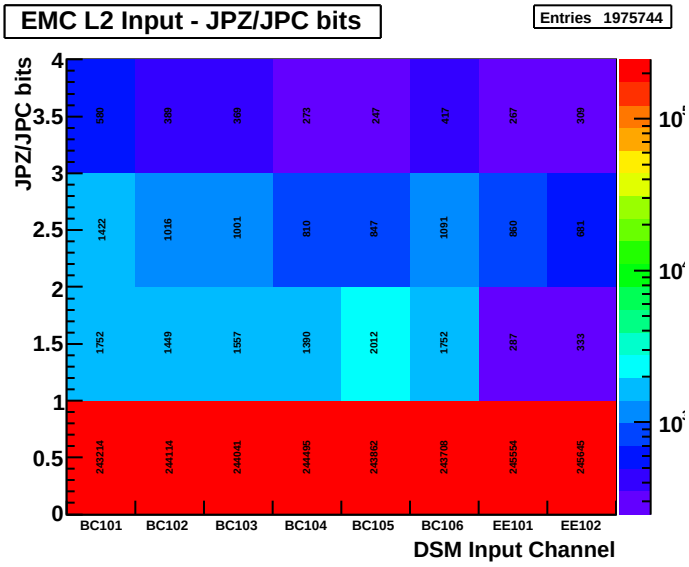

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

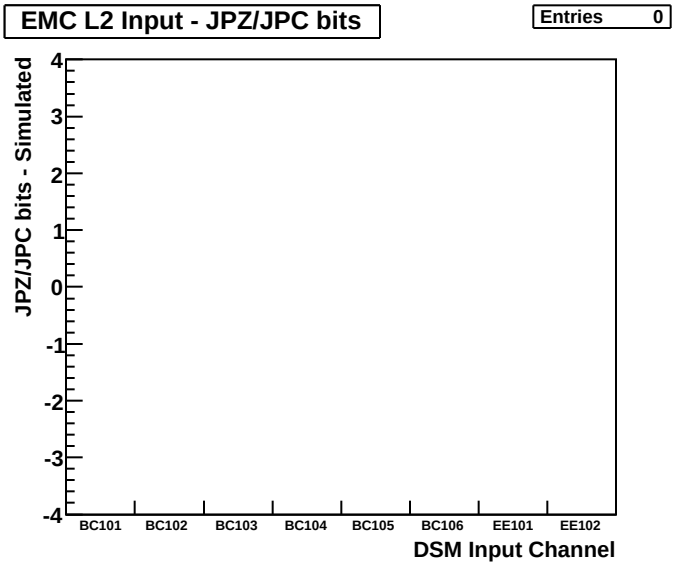

EMC L2 Input - JPX/JPA bits
Entries 1975744
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1731 813 698 310 218 568 212 909
2786 1575 1336 891 713 1215 530 1974
2591 2138 1781 1437 1260 1499 278 383
238860 242442 242243 244330 244777 245686 245948 243702

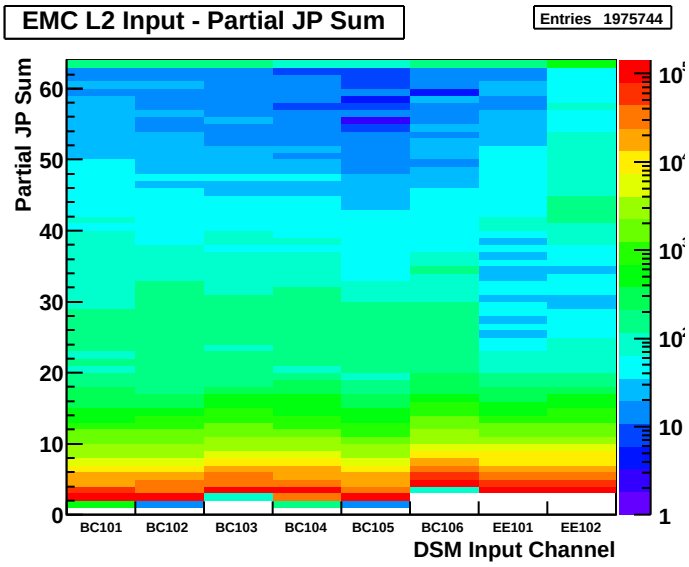

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

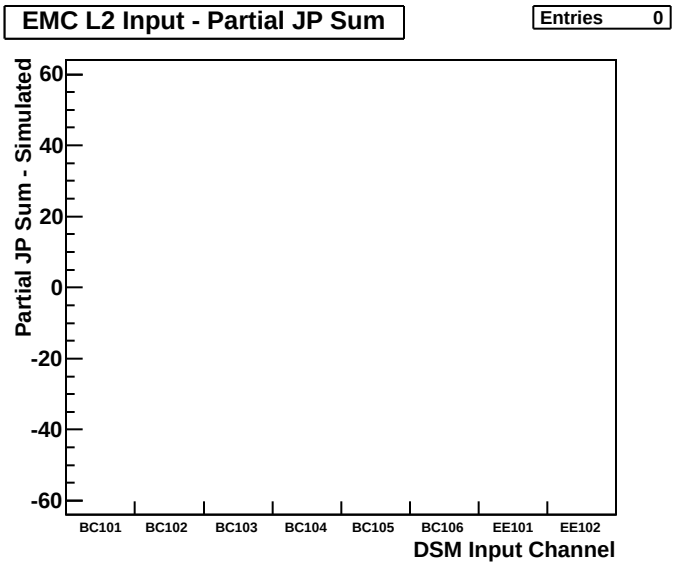

EMC L2 Input - JPY/JPB bits
Entries 1975744
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
910 439 356 243 230 479 314 489
1847 1026 1001 818 865 1166 703 1093
2048 1490 1412 1305 1948 1569 233 323
242163 244013 244199 244602 243925 243754 245718 245063

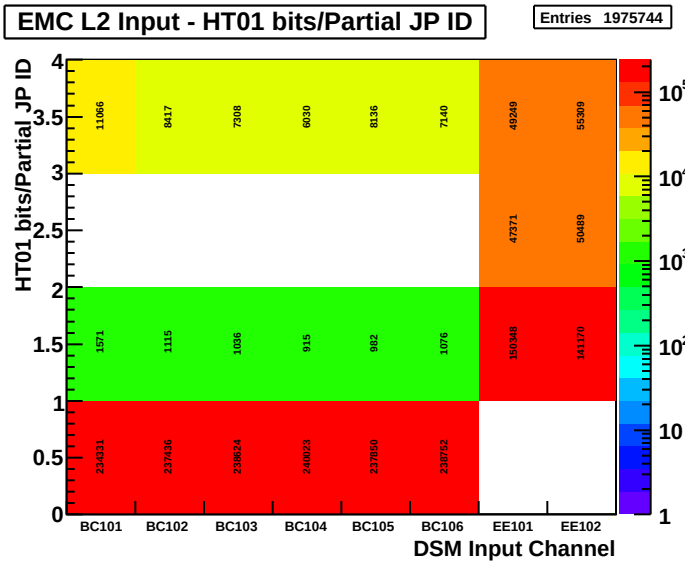

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

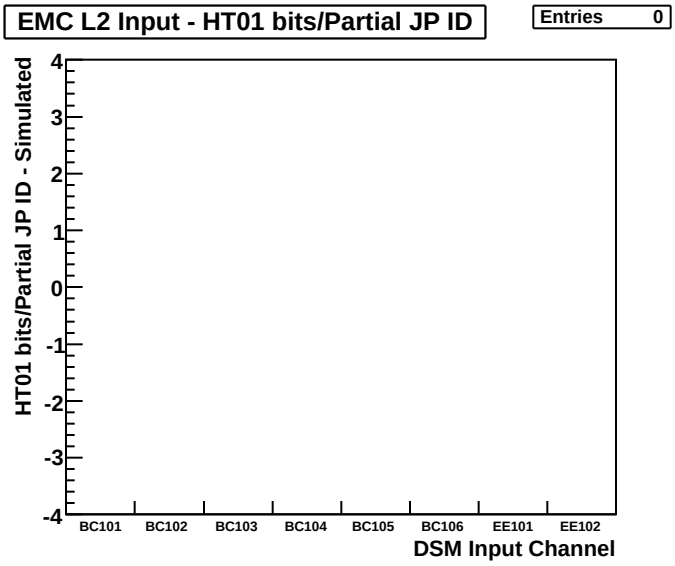

EMC L2 Input - JPZ/JPC bits
Entries 1975744
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
580 389 369 273 247 417 267 309
1422 1016 1001 810 847 1091 860 681
1752 1449 1567 1390 2012 1752 297 333
243214 244114 244041 244495 243862 243708 245554 245645

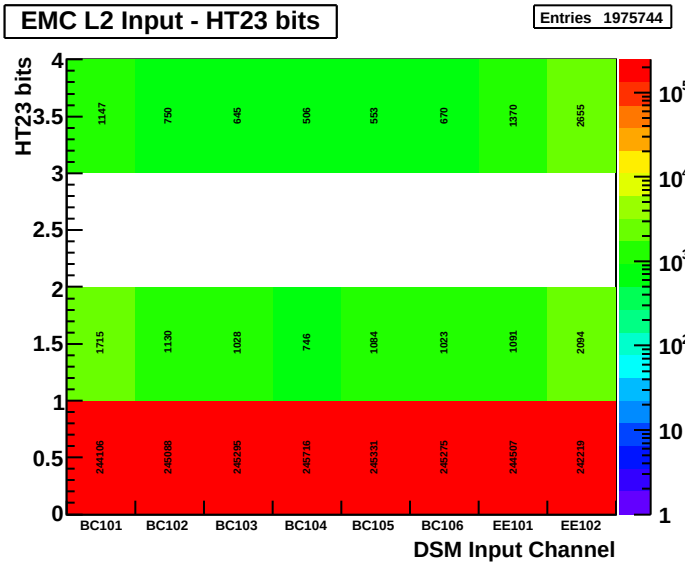

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

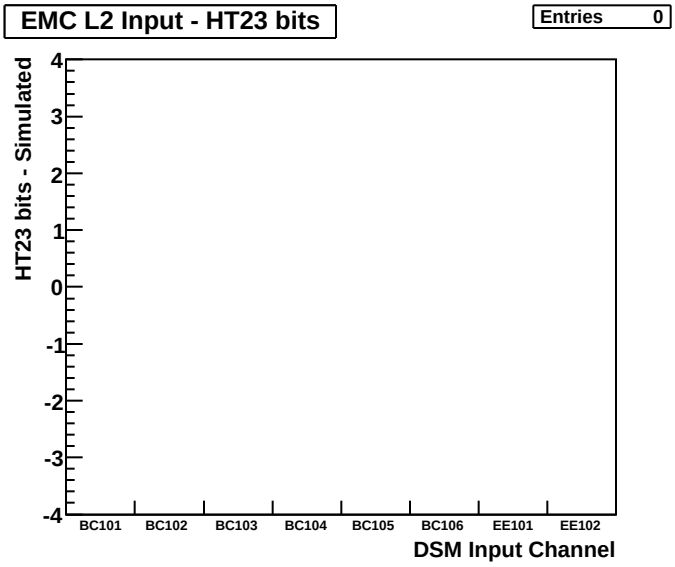

EMC L2 Input - Partial JP Sum
Entries 1975744
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

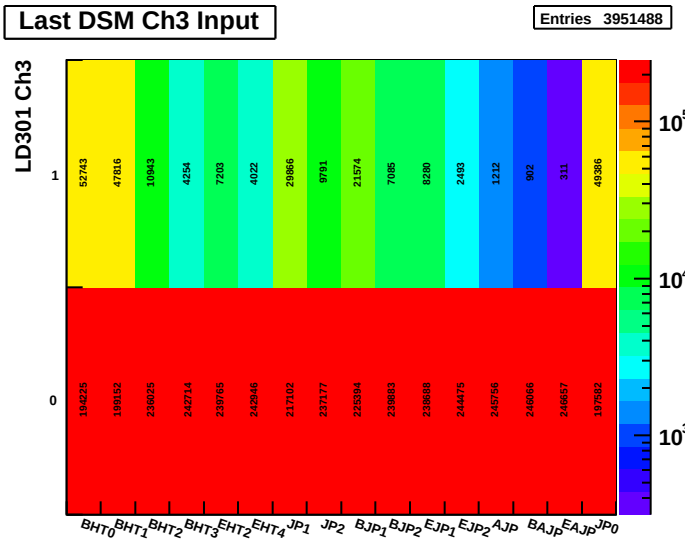

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

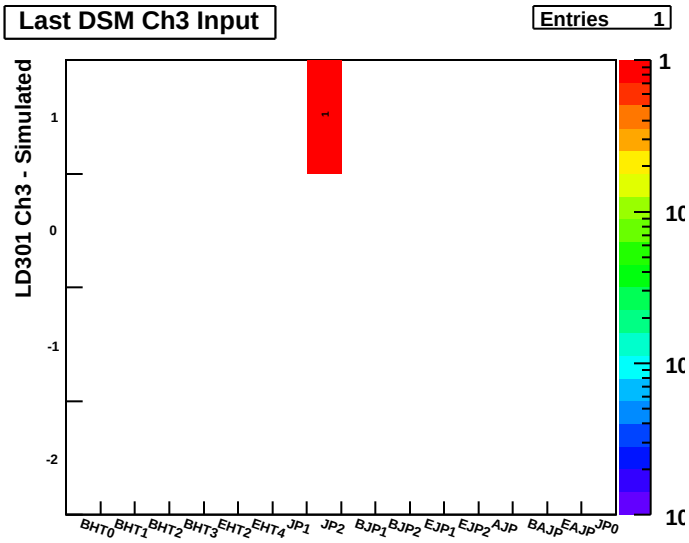

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1975744
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

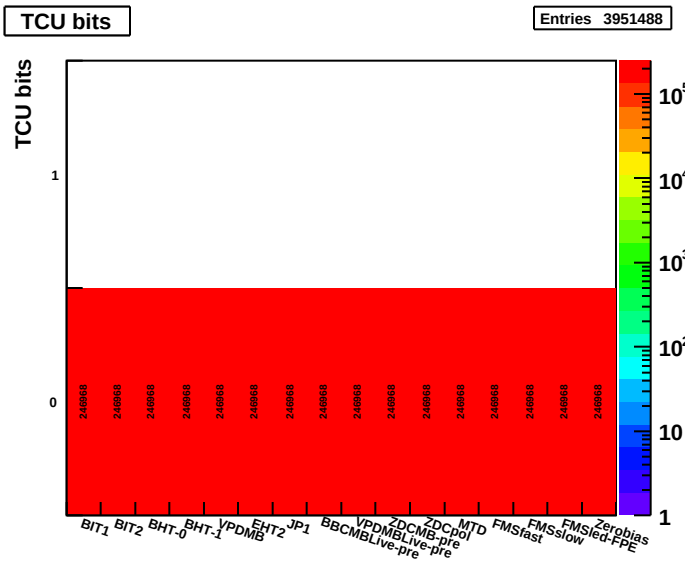

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

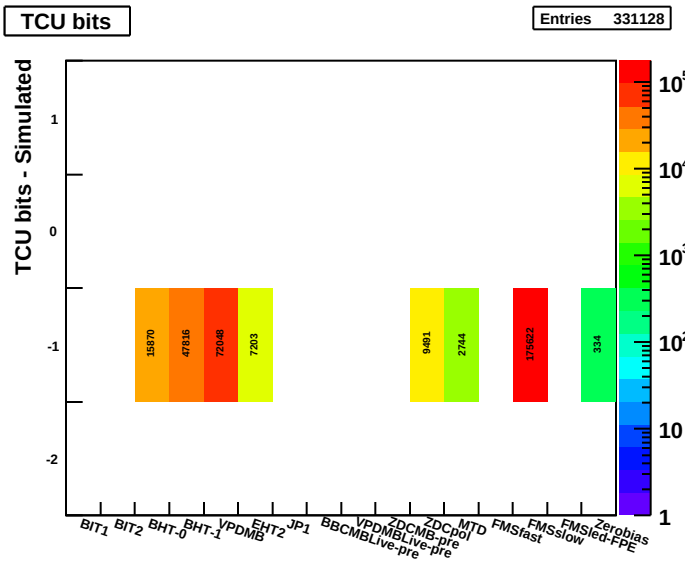

EMC L2 Input - HT23 bits
Entries 1975744
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

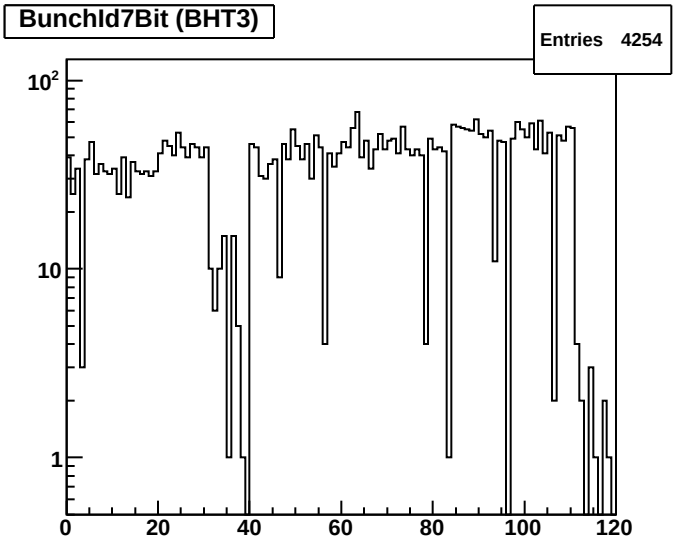

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

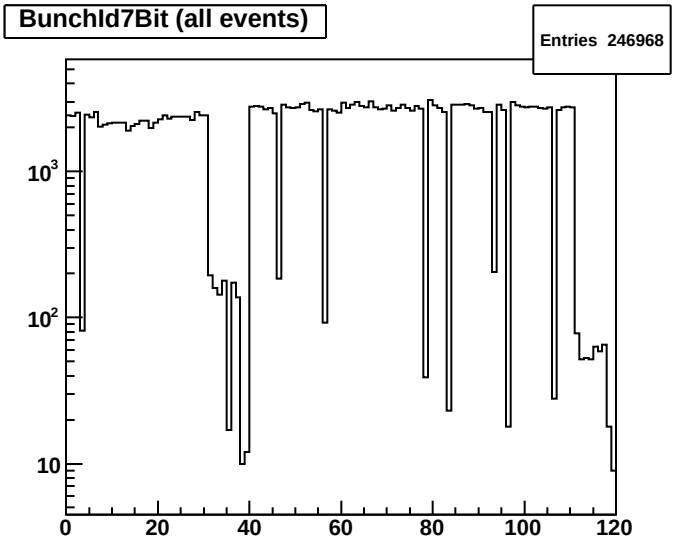

Last DSM Ch3 Input
Entries 3951488
LD301 Ch3
52743 47816 10943 4254 7203 4022 29866 9791 21574 7085 8280 2493 1212 902 311 49386
194225 199152 236025 242714 239765 242946 217102 237177 225394 239883 238688 244475 245756 246066 246657 197582
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

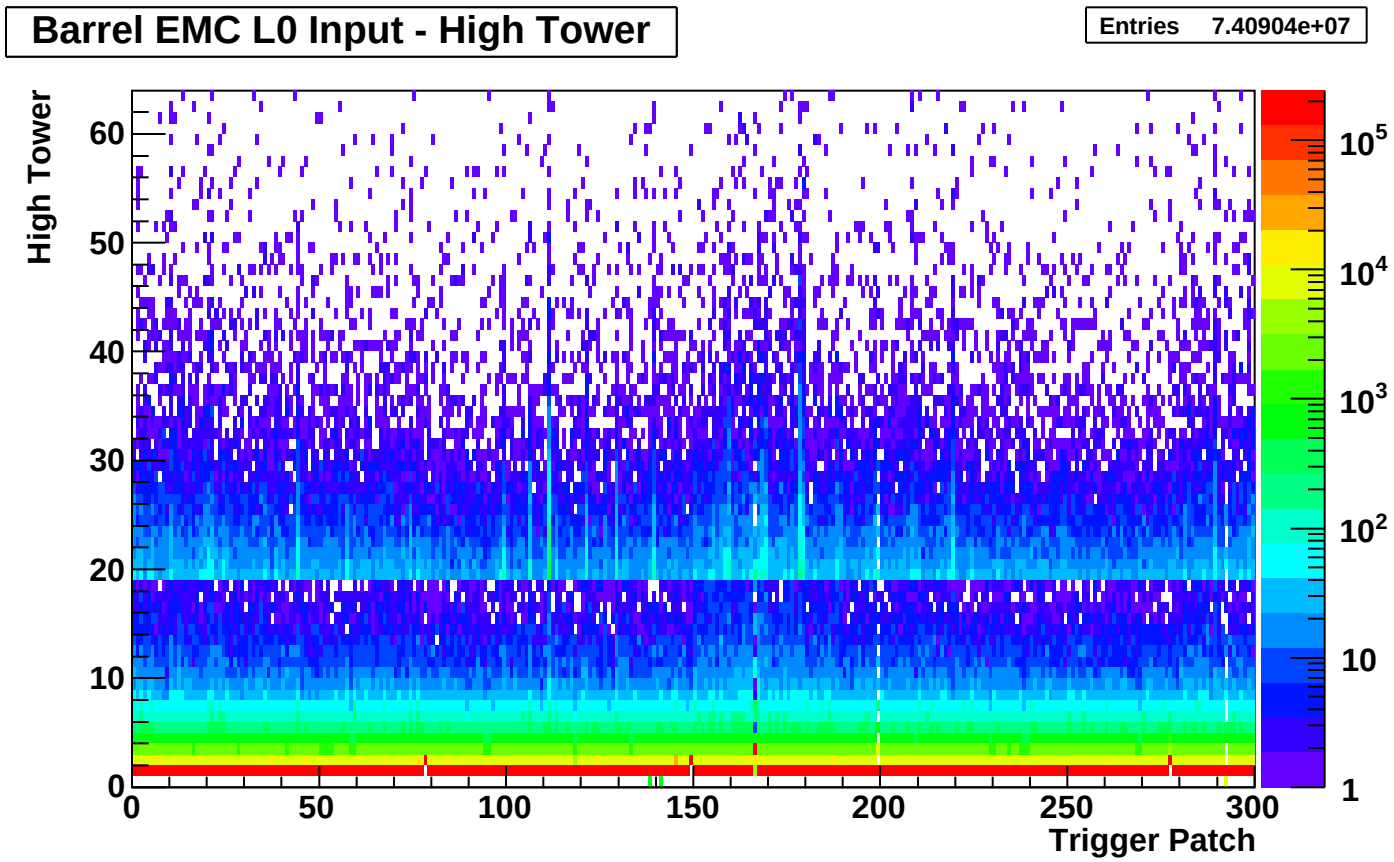

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

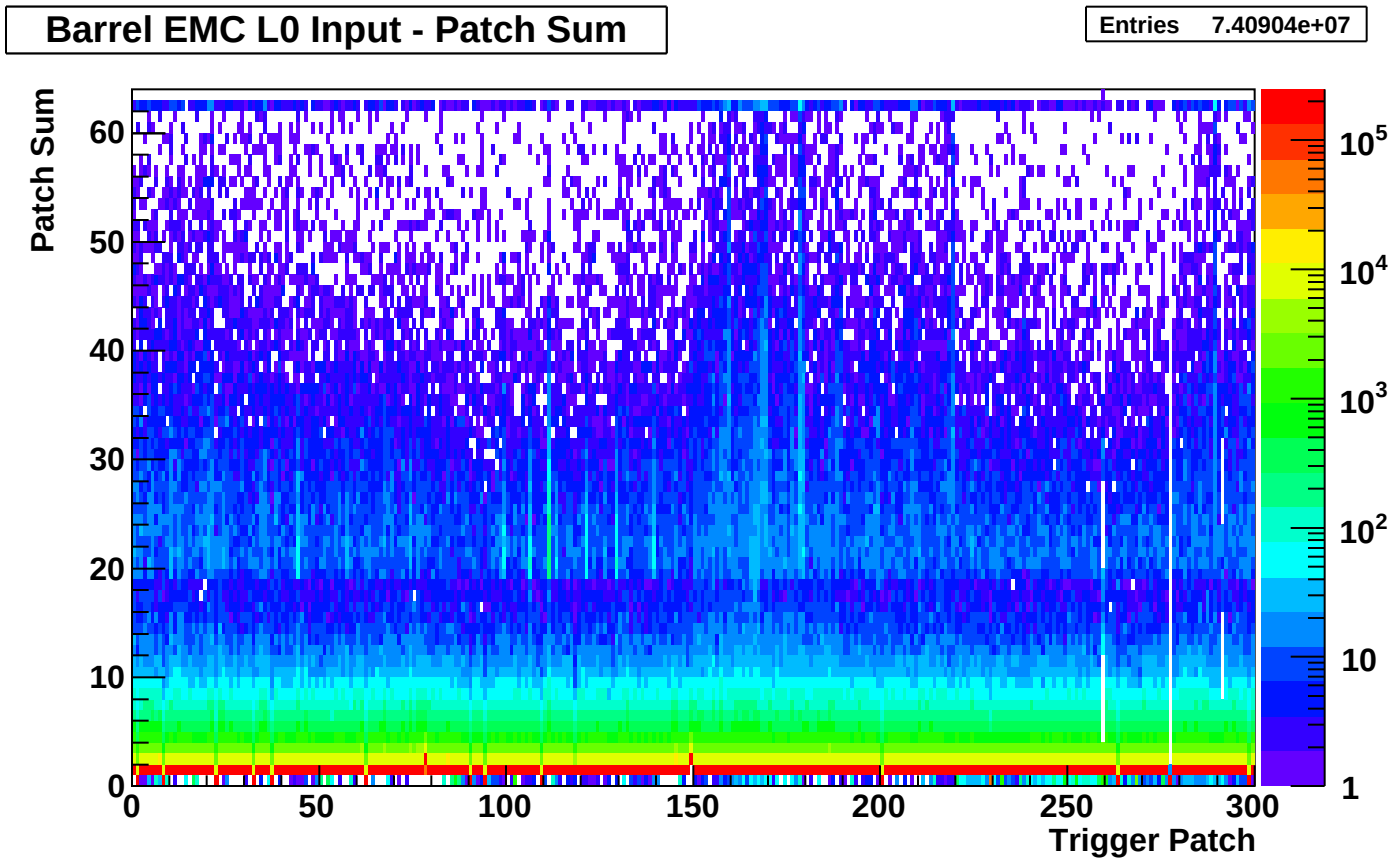

TCU bits
Entries 3951488
TCU bits
246968
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

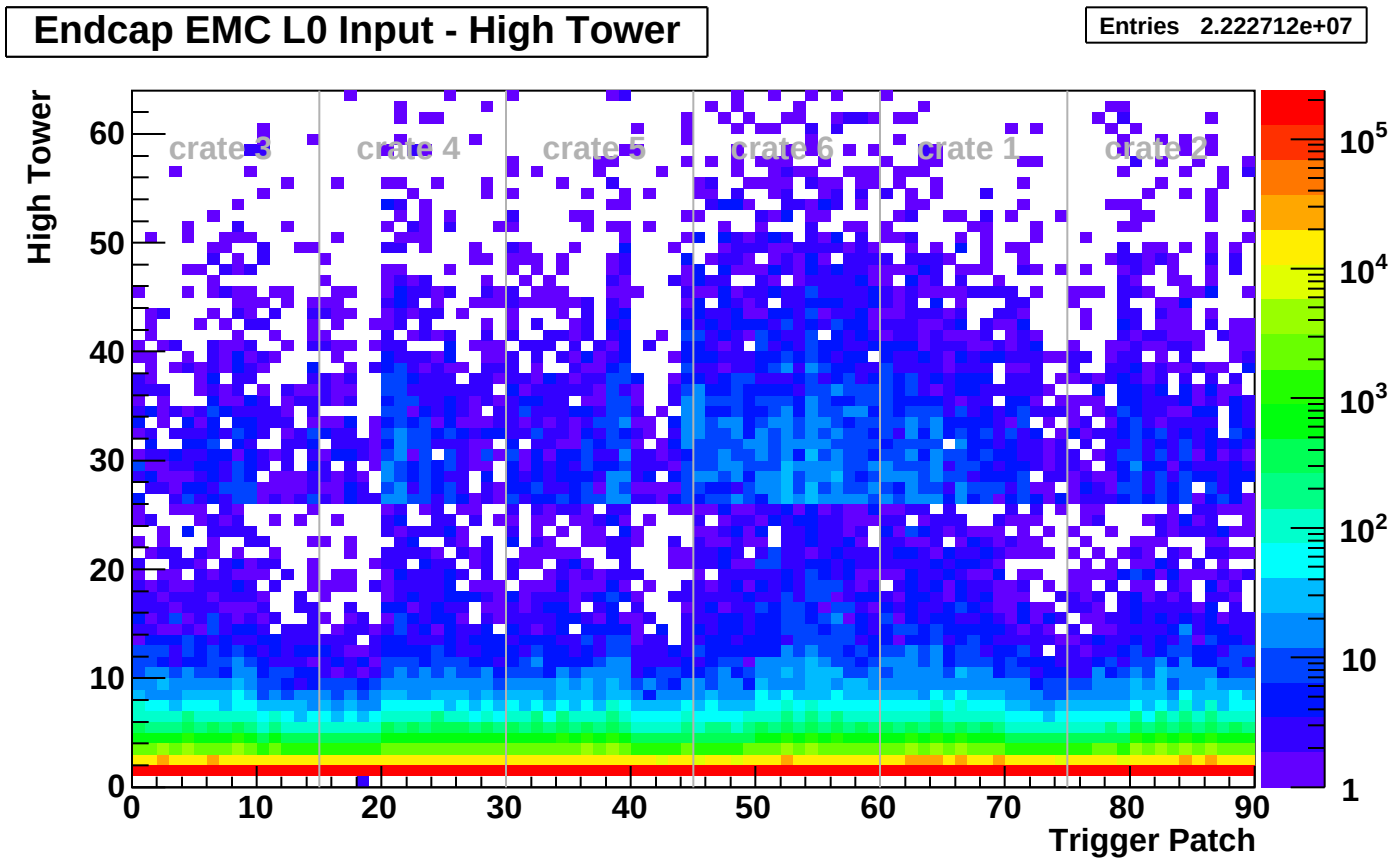

TCU bits
Entries 331128
TCU bits - Simulated
15870 47816 72048 7203 9491 2744 175622 334
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

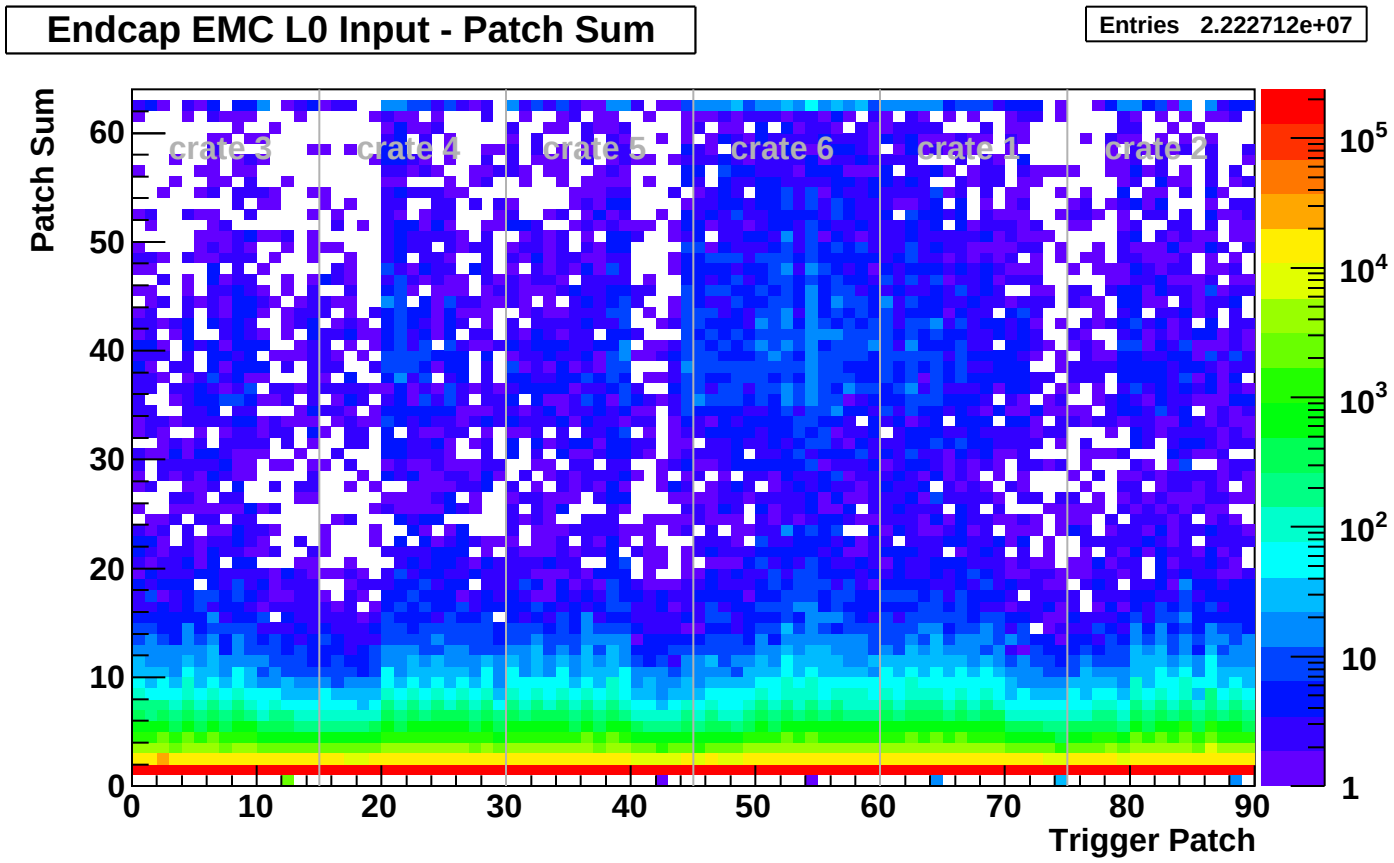

BunchId7Bit (BHT3)
Entries 4254

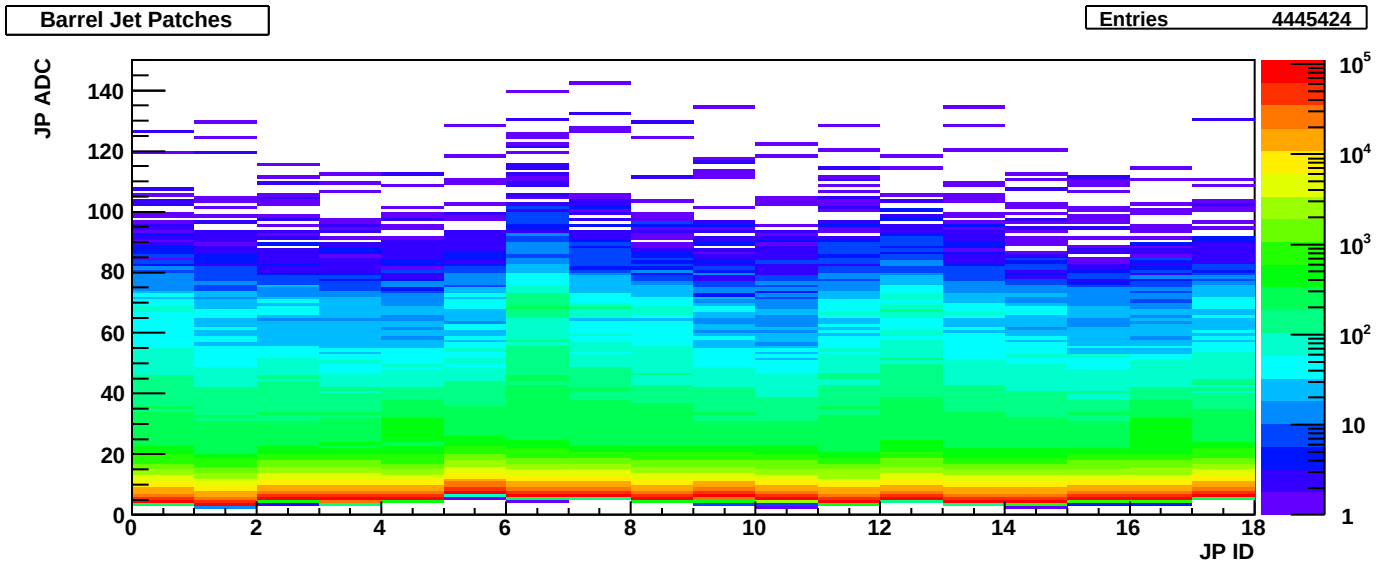

BunchId7Bit (all events)
Entries 246968

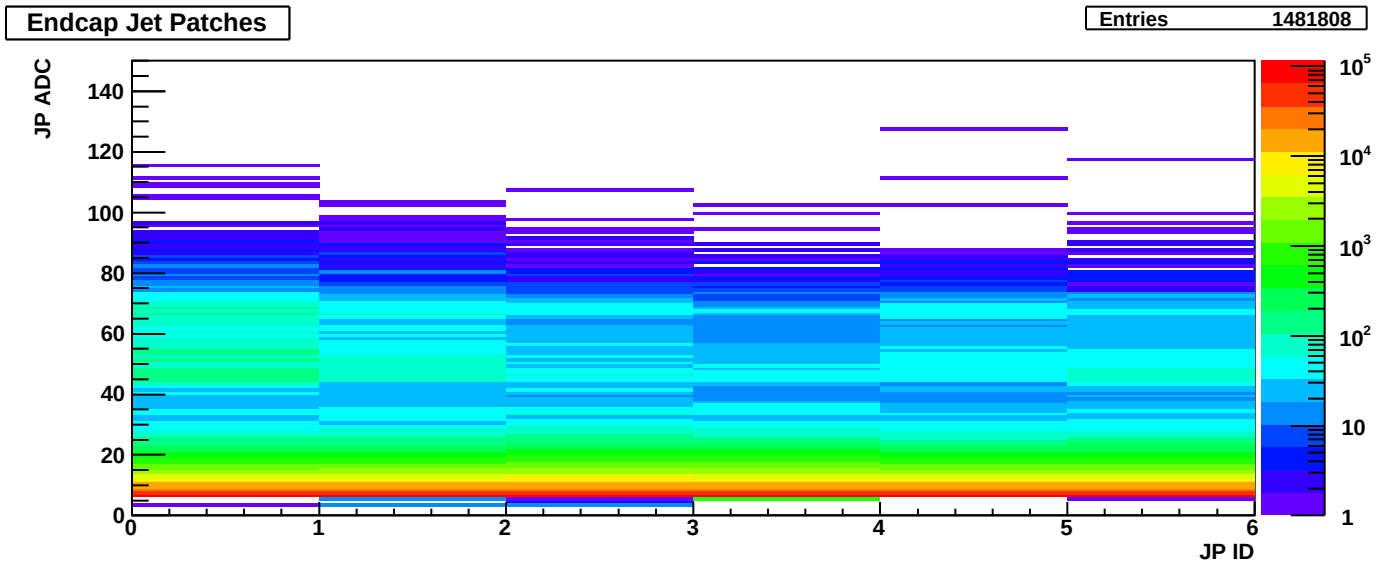

Barrel EMC L0 Input - High Tower
Entries 7.40904e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

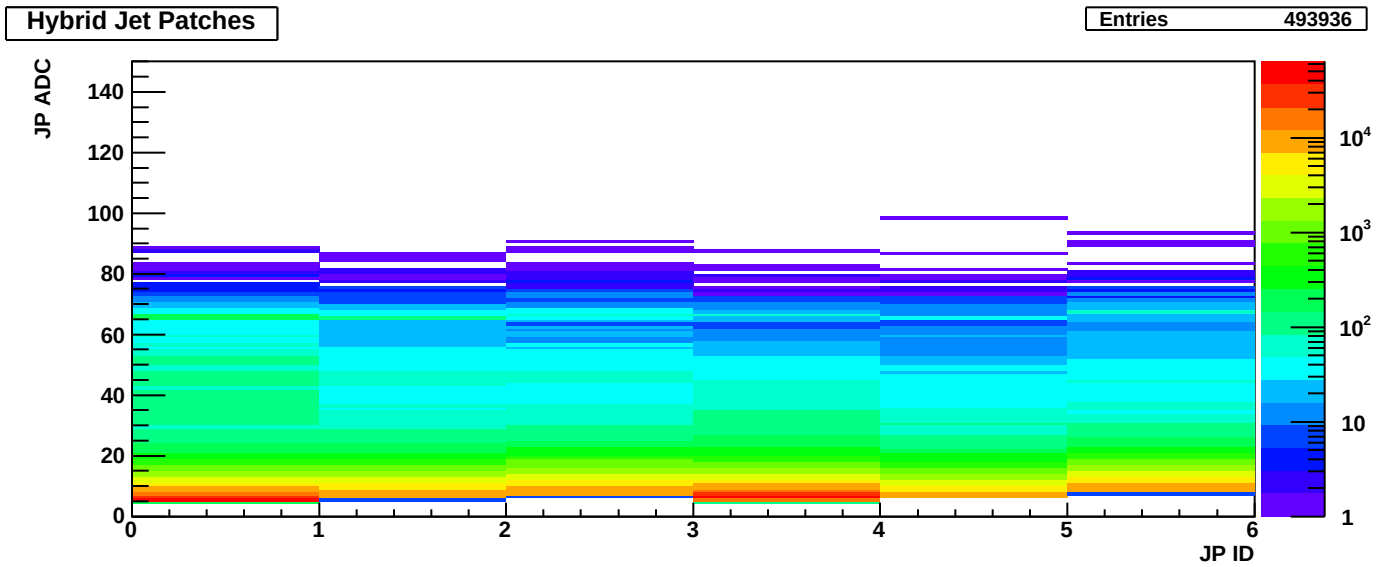

Barrel EMC L0 Input - Patch Sum
Entries 7.40904e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

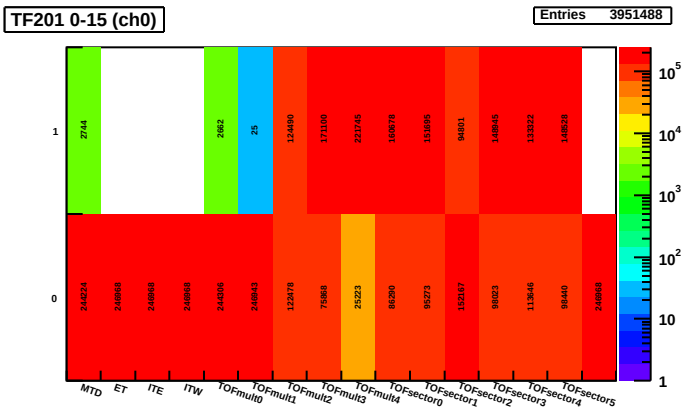

Endcap EMC L0 Input - High Tower
Entries 2.222712e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

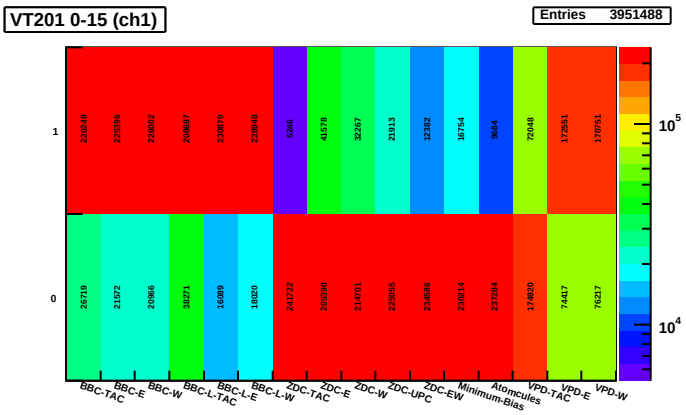

Endcap EMC L0 Input - Patch Sum
Entries 2.222712e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

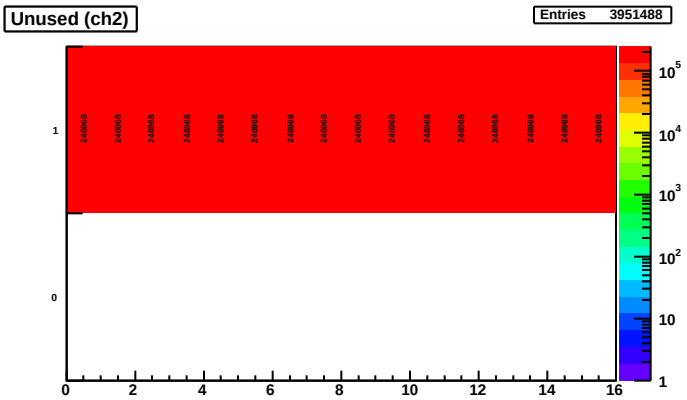

Barrel Jet Patches
Entries 4445424
JP ADC
JP ID

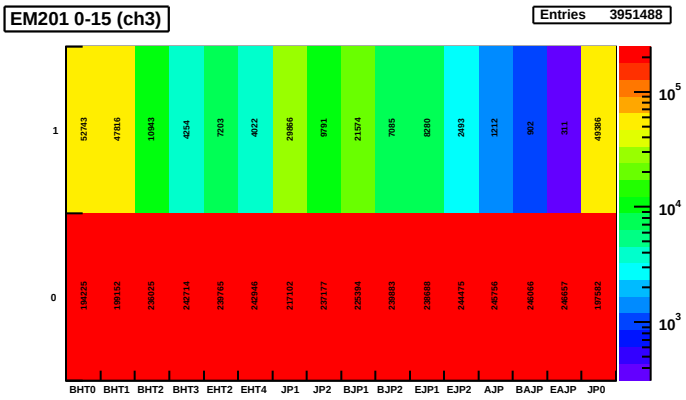

Endcap Jet Patches
Entries 1481808
JP ADC
JP ID

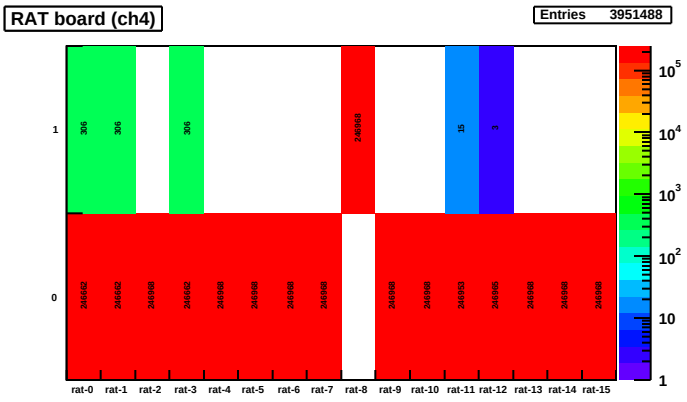

Hybrid Jet Patches
Entries 493936
JP ADC
JP ID

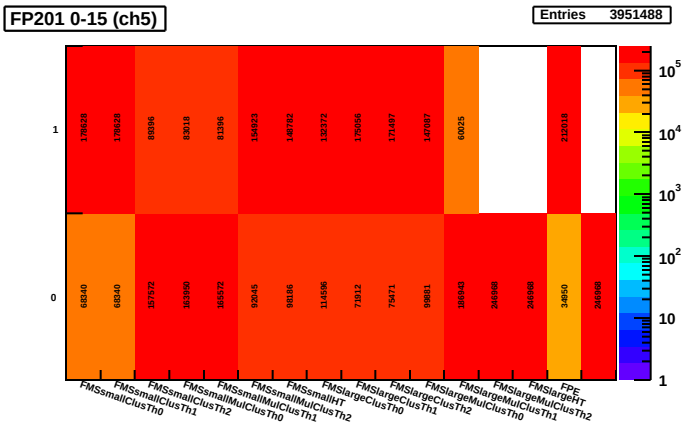

TF201 0-15 (ch0)
Entries 3951488
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

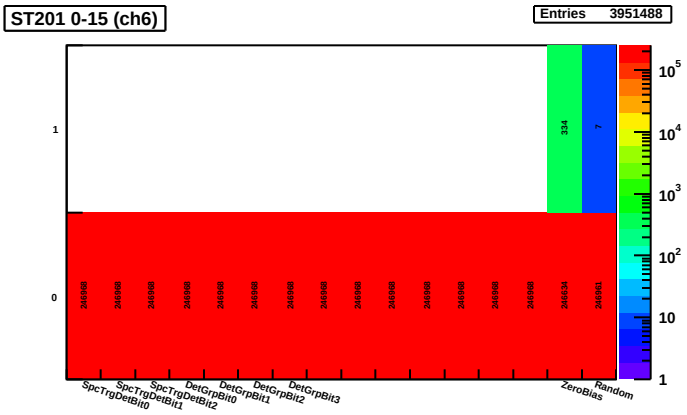

VT201 0-15 (ch1)
Entries 3951488
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

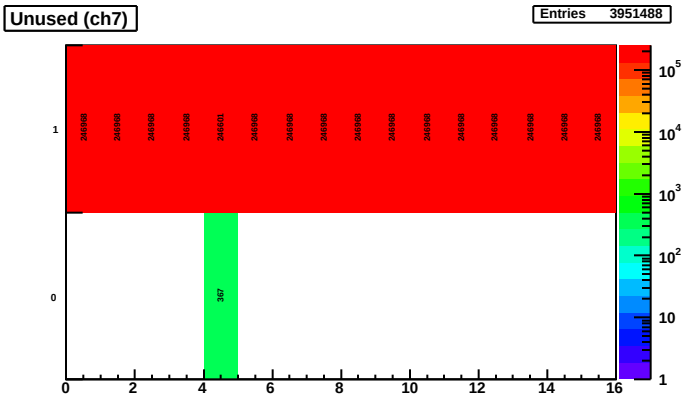

Unused (ch2)
Entries 3951488

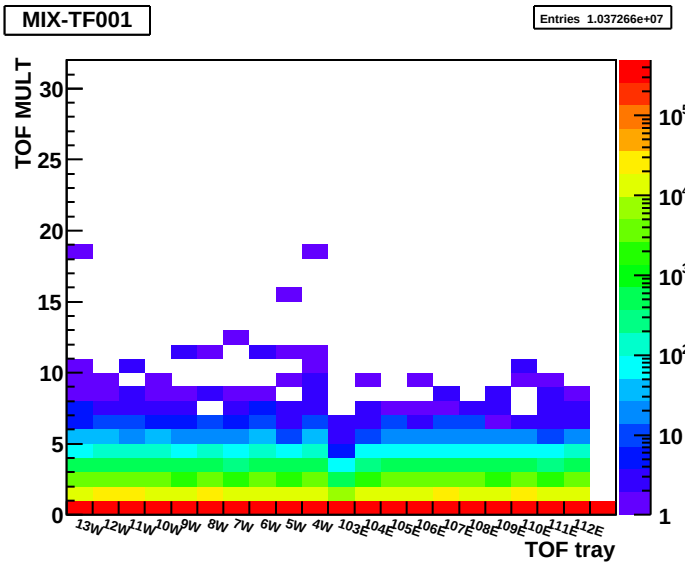

EM201 0-15 (ch3)
Entries 3951488
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

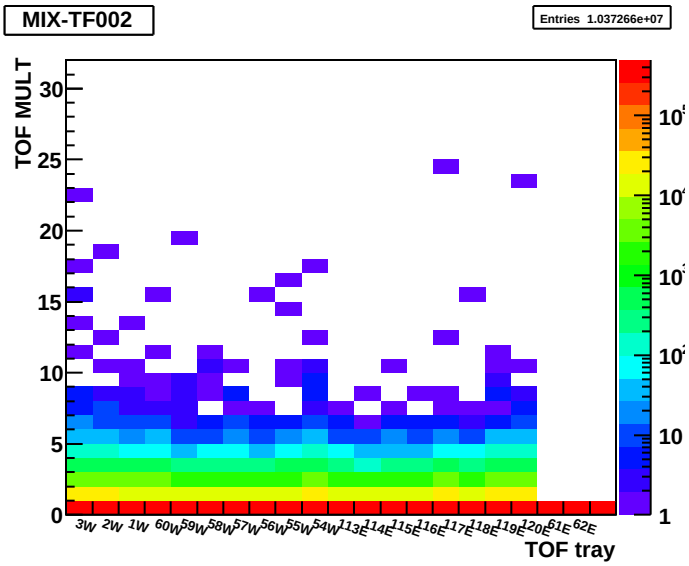

RAT board (ch4)
Entries 3951488
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

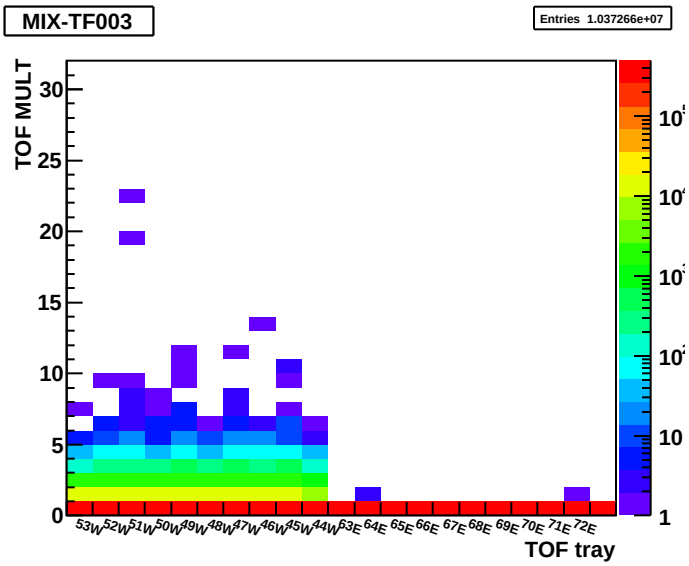

FP201 0-15 (ch5)
Entries 3951488
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

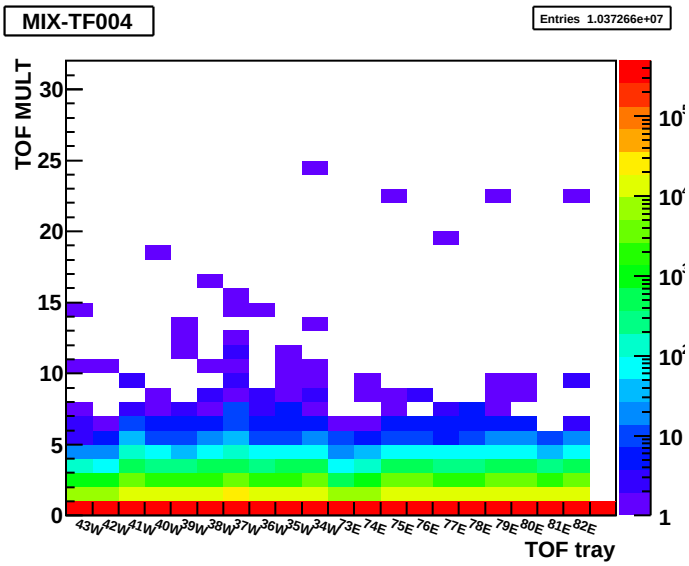

ST201 0-15 (ch6)
Entries 3951488
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

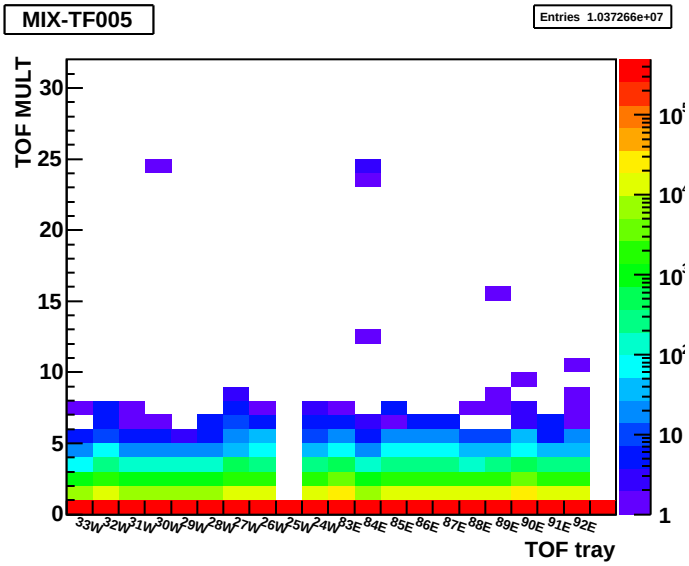

Unused (ch7)
Entries 3951488

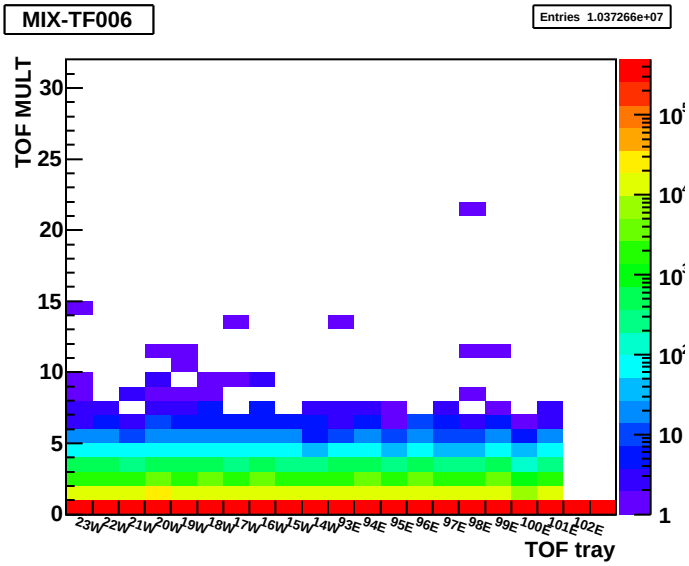

MIX-TF001
Entries 1.037266e+07
TOF MULT
TOF tray

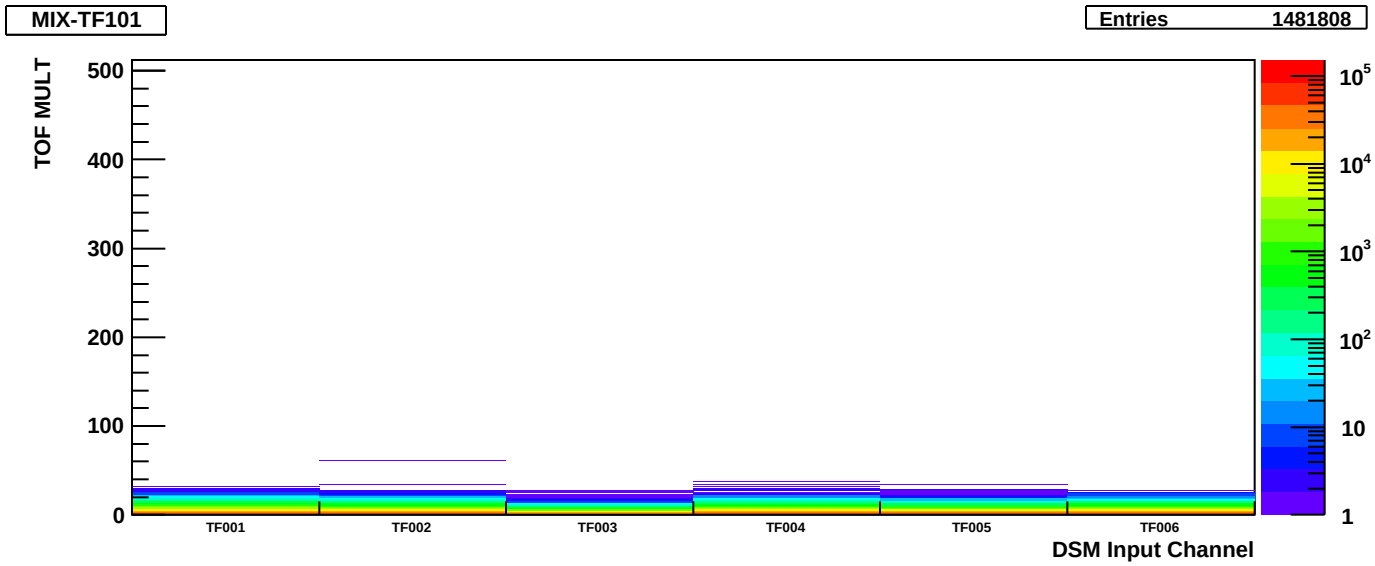

MIX-TF002
Entries 1.037266e+07
TOF MULT
TOF tray

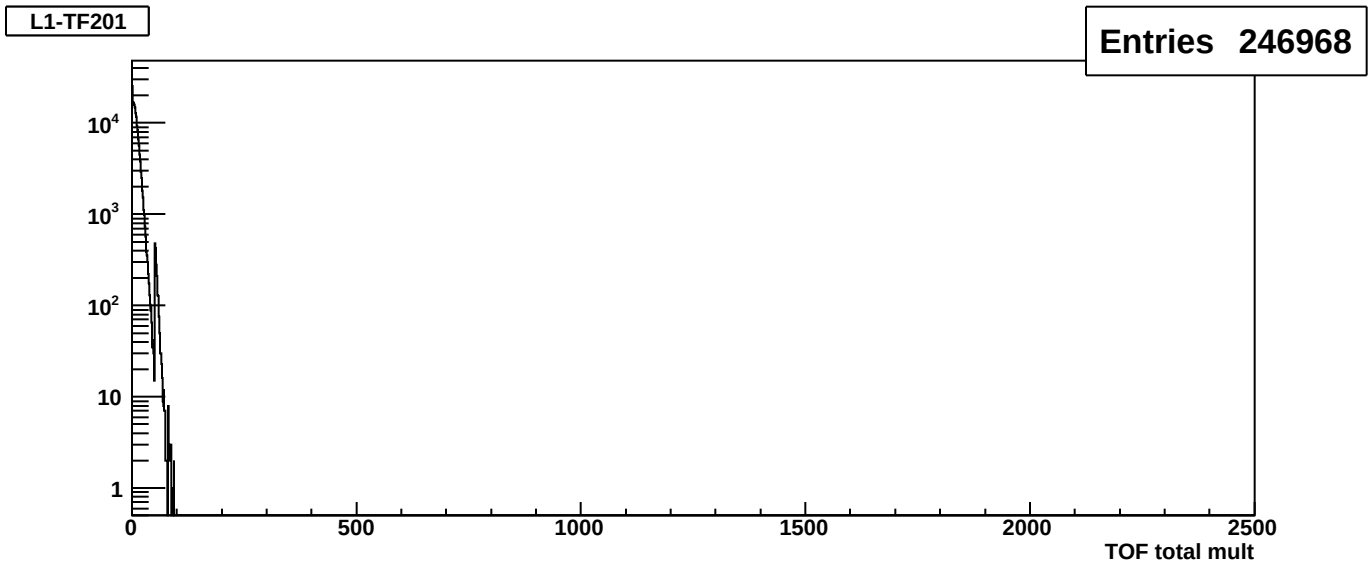

MIX-TF003
Entries 1.037266e+07
TOF MULT
TOF tray

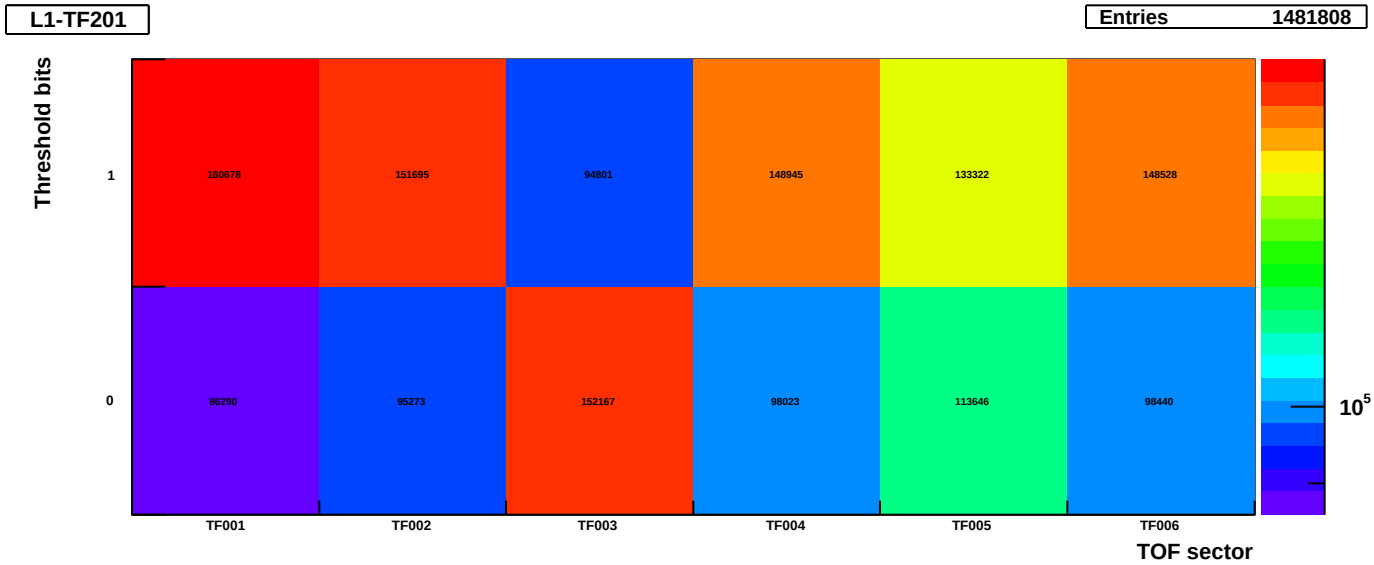

MIX-TF004
Entries 1.037266e+07
TOF MULT
TOF tray

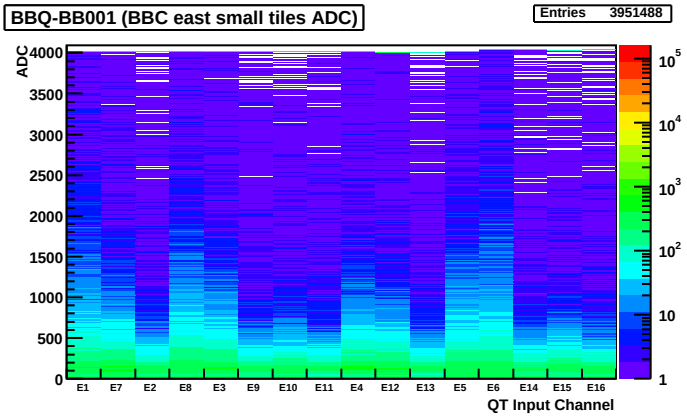

MIX-TF005
Entries 1.037266e+07
TOF MULT
TOF tray

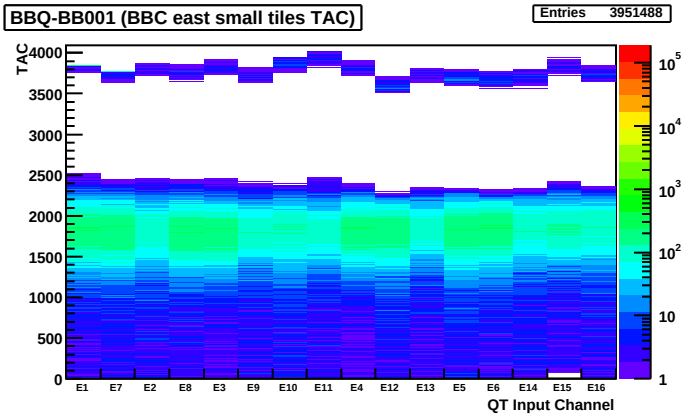

MIX-TF006
Entries 1.037266e+07
TOF MULT
TOF tray

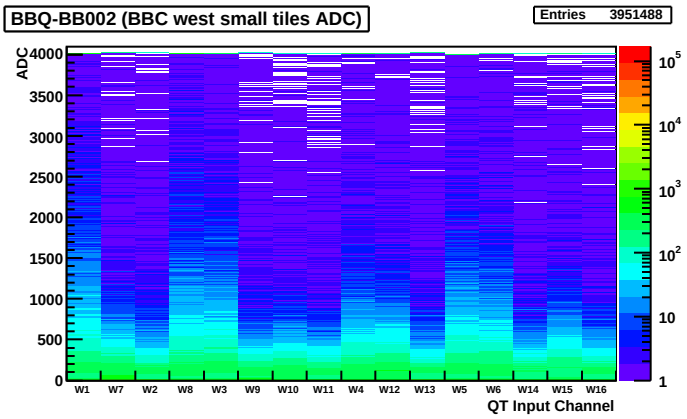

MIX-TF101
Entries 1481808
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

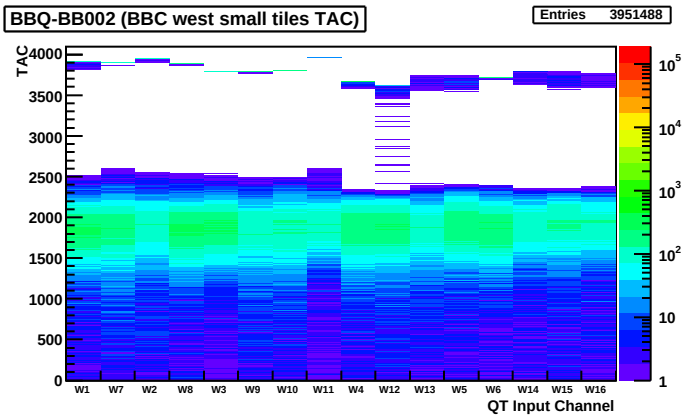

L1-TF201
Entries 246968
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

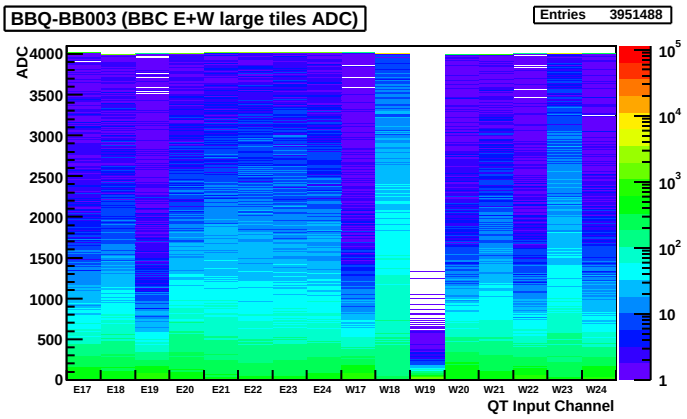

L1-TF201
Entries 1481808
Threshold bits
1
0
160678
151695
94801
148945
133322
148528
86290
95273
152167
98023
113646
98440
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

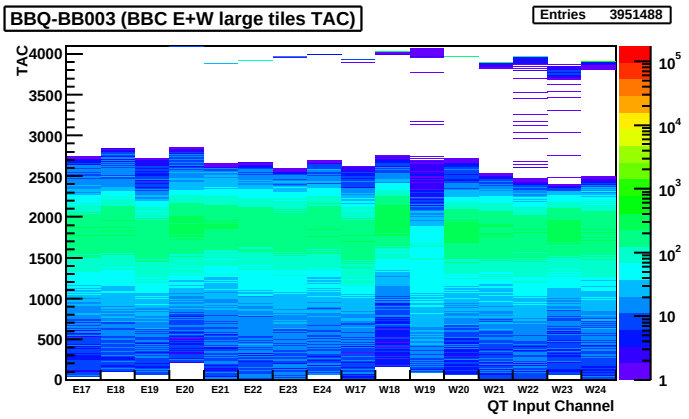

BBQ-BB001 (BBC east small tiles ADC)
Entries 3951488
ADC
QT Input Channel

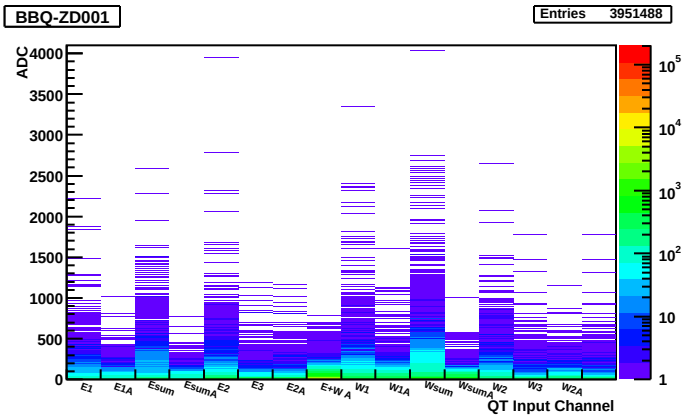

BBQ-BB001 (BBC east small tiles TAC)
Entries 3951488
TAC
QT Input Channel

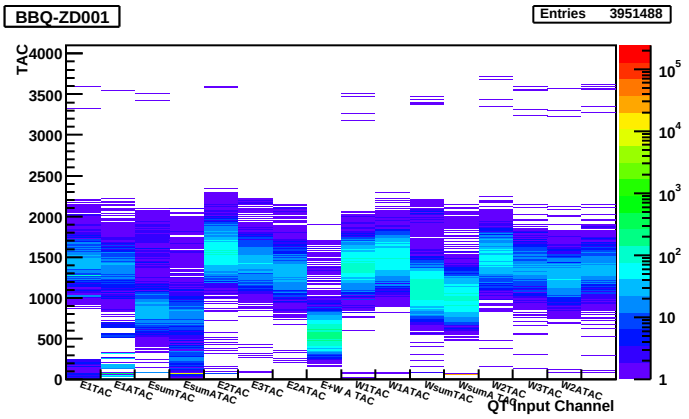

BBQ-BB002 (BBC west small tiles ADC)
Entries 3951488
ADC
QT Input Channel

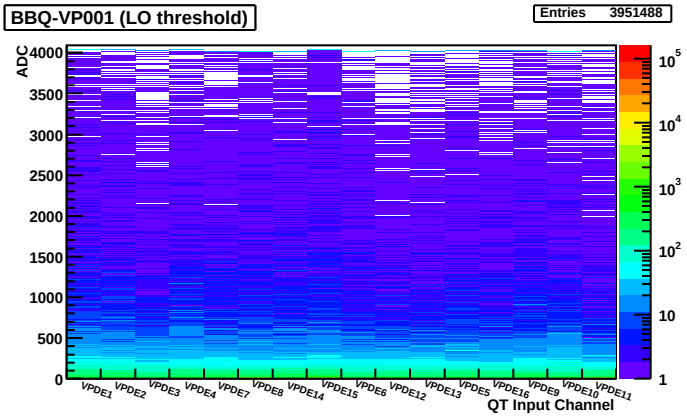

BBQ-BB002 (BBC west small tiles TAC)
Entries 3951488
TAC
QT Input Channel

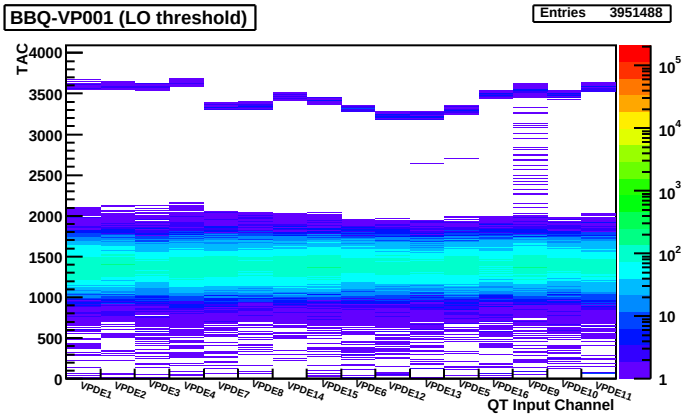

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3951488
ADC
QT Input Channel

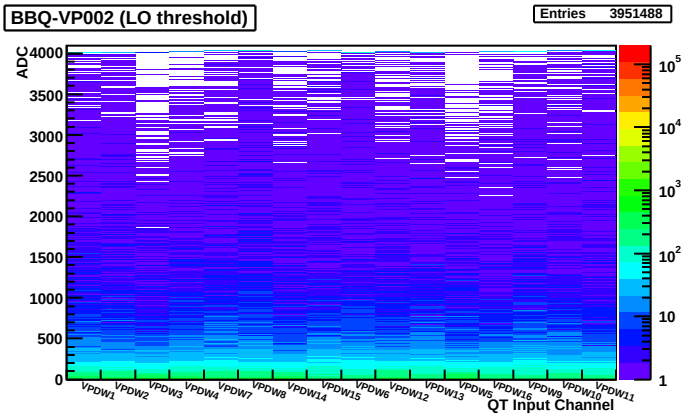

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3951488
TAC
QT Input Channel

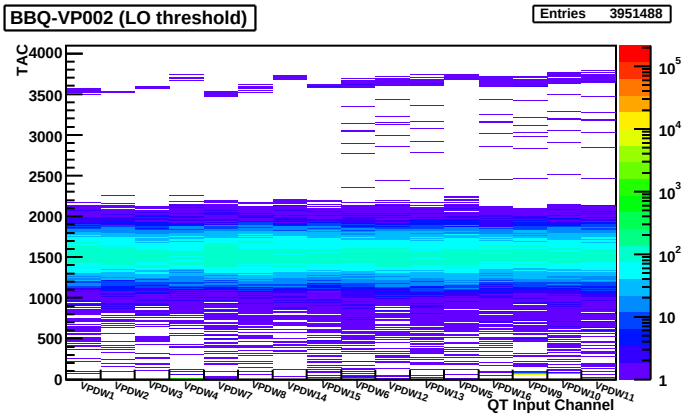

BBQ-ZD001
Entries 3951488
ADC
QT Input Channel

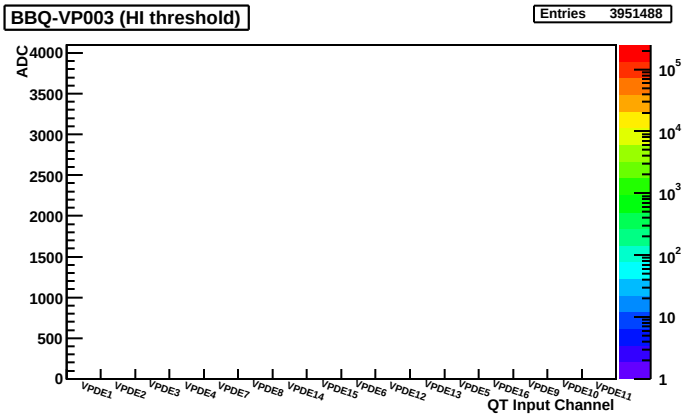

BBQ-ZD001
Entries 3951488
TAC
QT Input Channel

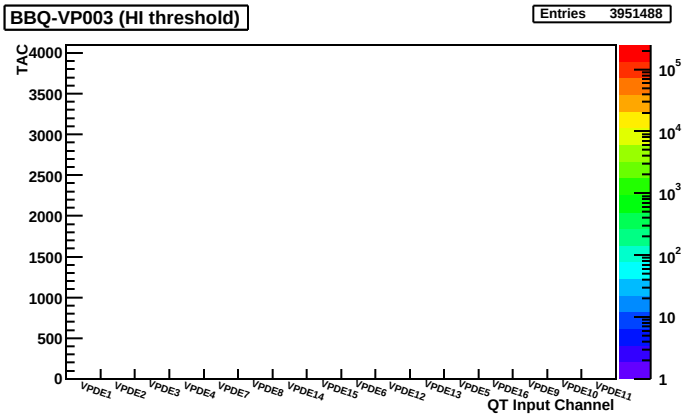

BBQ-VP001 (LO threshold)
Entries 3951488
ADC
QT Input Channel

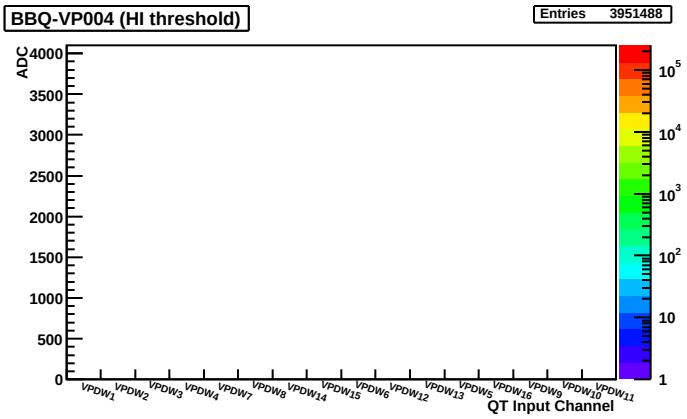

BBQ-VP001 (LO threshold)
Entries 3951488
TAC
QT Input Channel

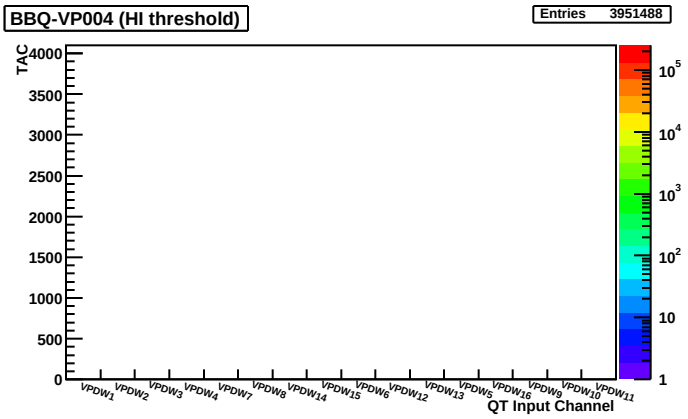

BBQ-VP002 (LO threshold)
Entries 3951488
ADC
QT Input Channel

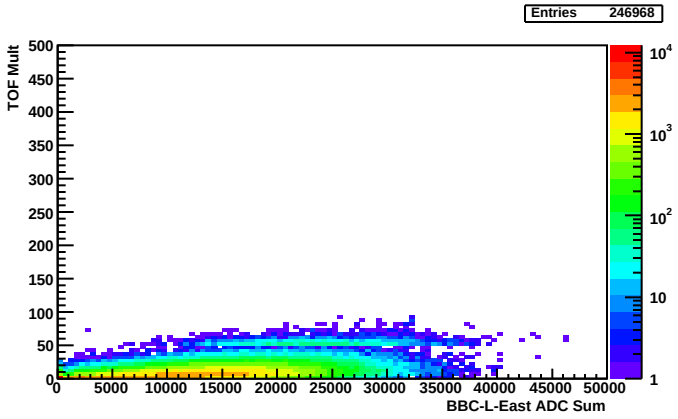

BBQ-VP002 (LO threshold)
Entries 3951488
TAC
QT Input Channel

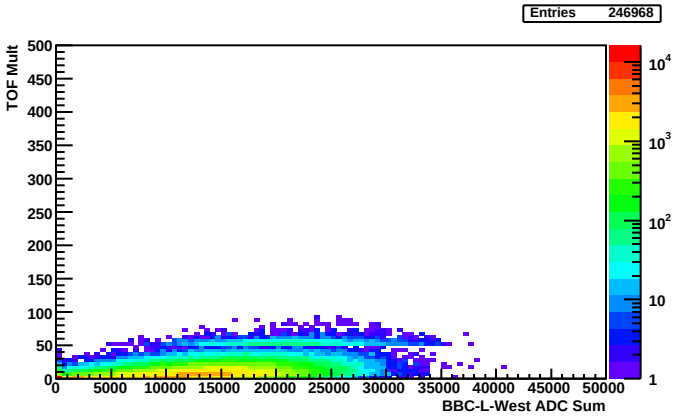

BBQ-VP003 (HI threshold)
Entries 3951488
ADC
QT Input Channel

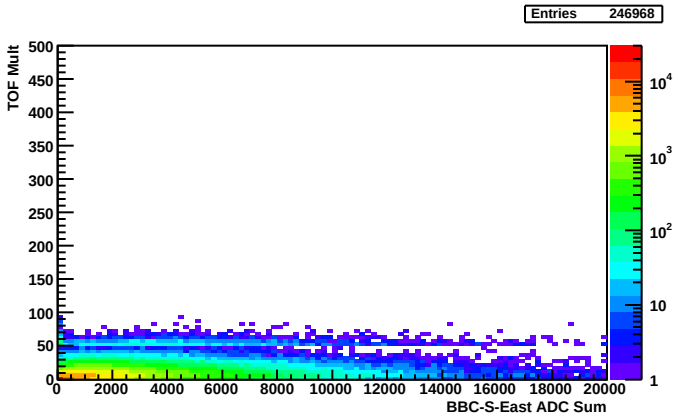

BBQ-VP003 (HI threshold)
Entries 3951488
TAC
QT Input Channel

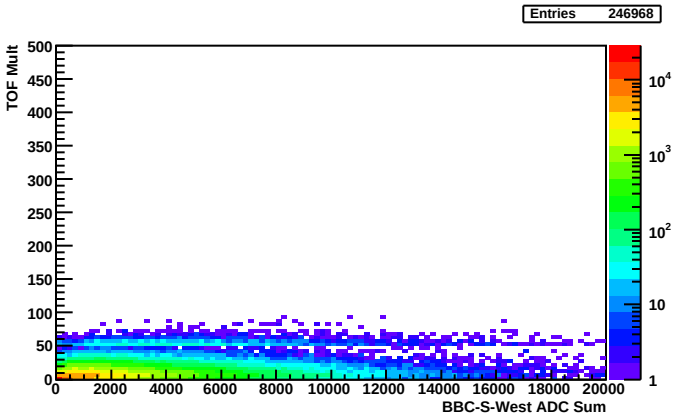

BBQ-VP004 (HI threshold)
Entries 3951488
ADC
QT Input Channel

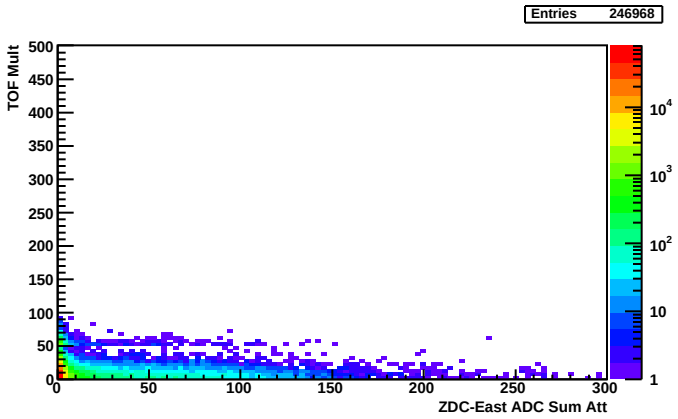

BBQ-VP004 (HI threshold)
Entries 3951488
TAC
QT Input Channel

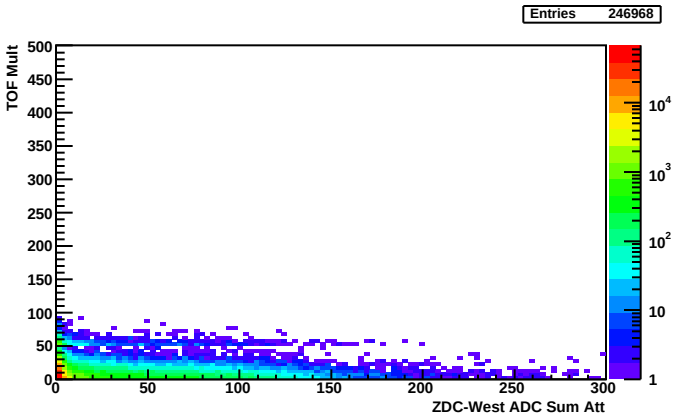

Entries 246968
TOF Mult
BBC-L-East ADC Sum

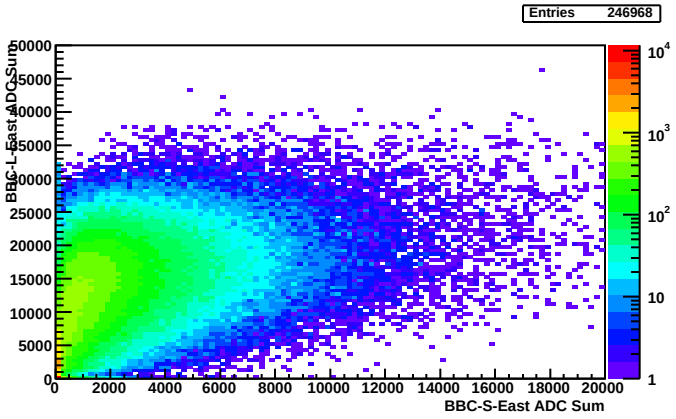

Entries 246968
TOF Mult
BBC-L-West ADC Sum

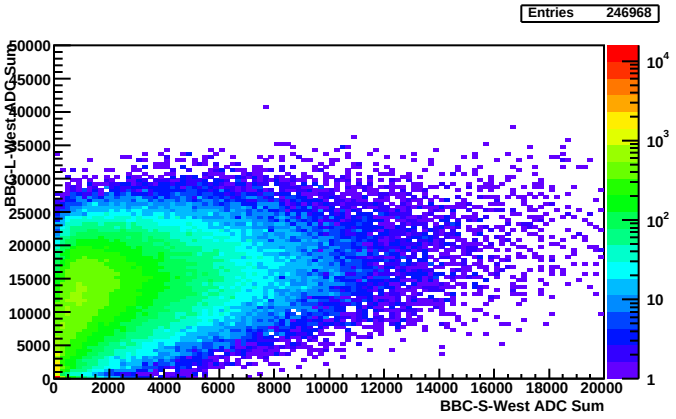

Entries 246968
TOF Mult
BBC-S-East ADC Sum

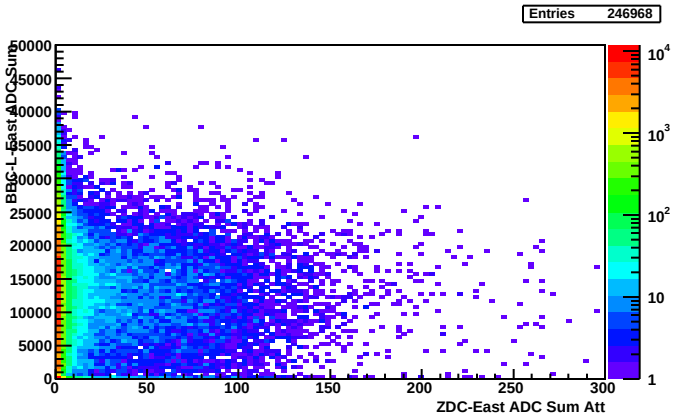

Entries 246968
TOF Mult
BBC-S-West ADC Sum

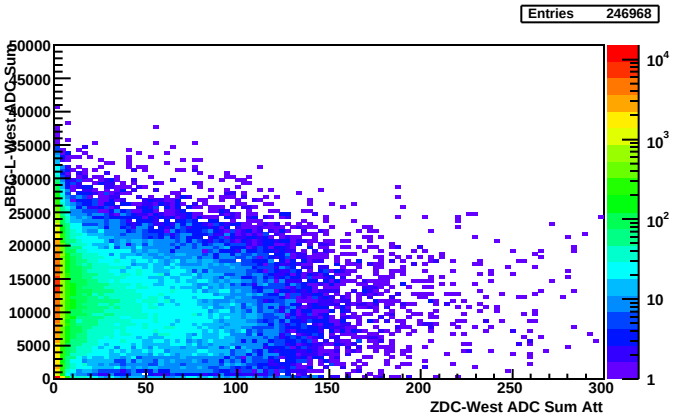

Entries 246968
TOF Mult
ZDC-East ADC Sum Att

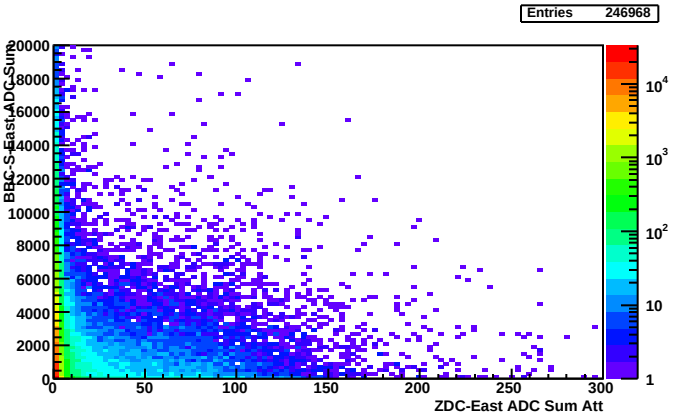

Entries 246968
TOF Mult
ZDC-West ADC Sum Att

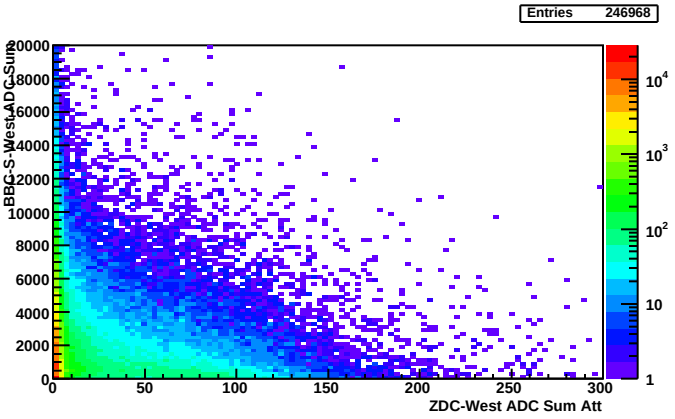

Entries 246968
BBC-L-East ADC Sum
BBC-S-East ADC Sum

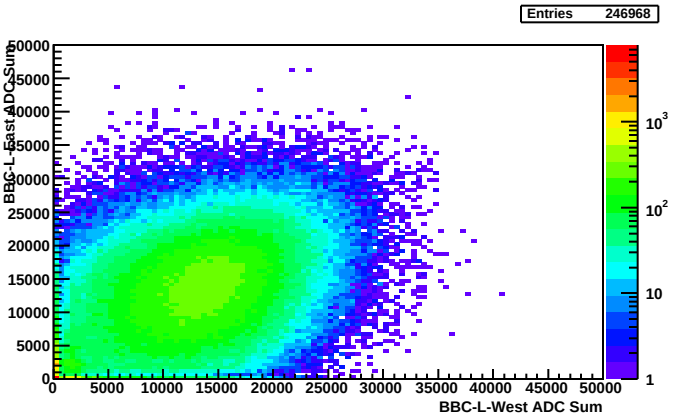

Entries 246968
BBC-L-West ADC Sum
BBC-S-West ADC Sum

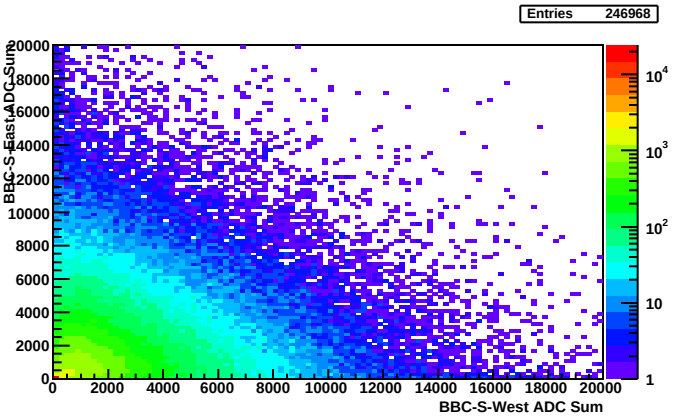

Entries 246968
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

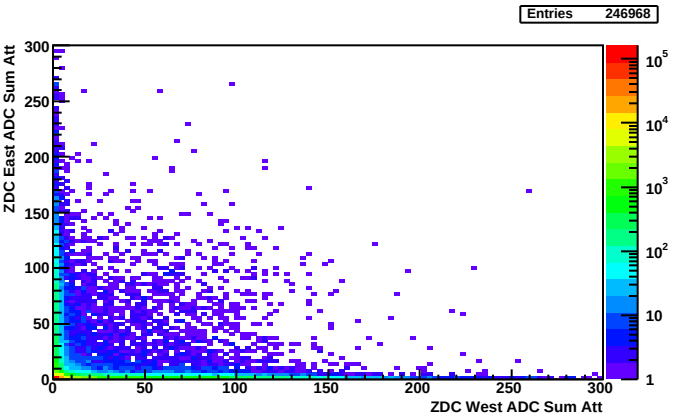

Entries 246968
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

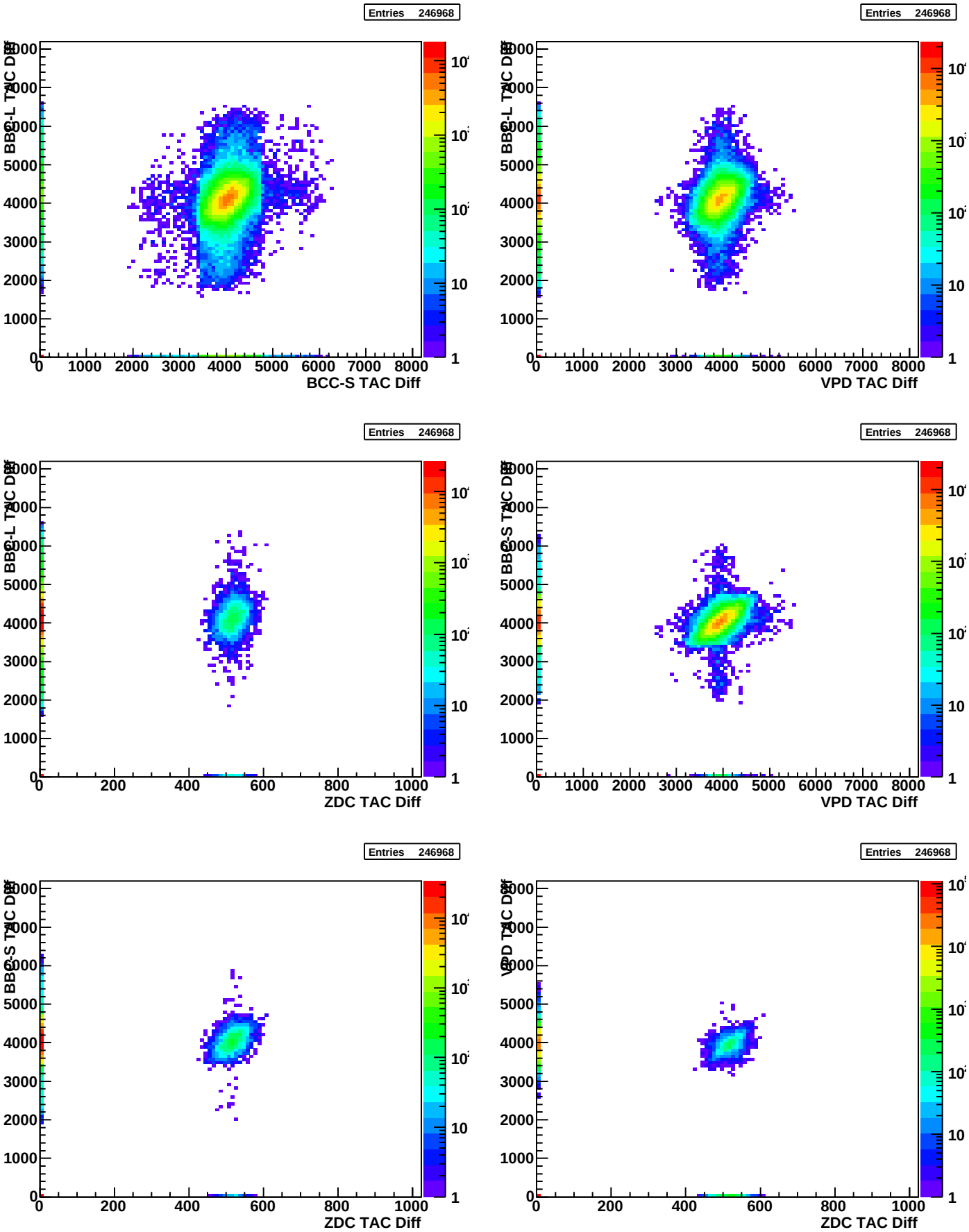

Entries 246968
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

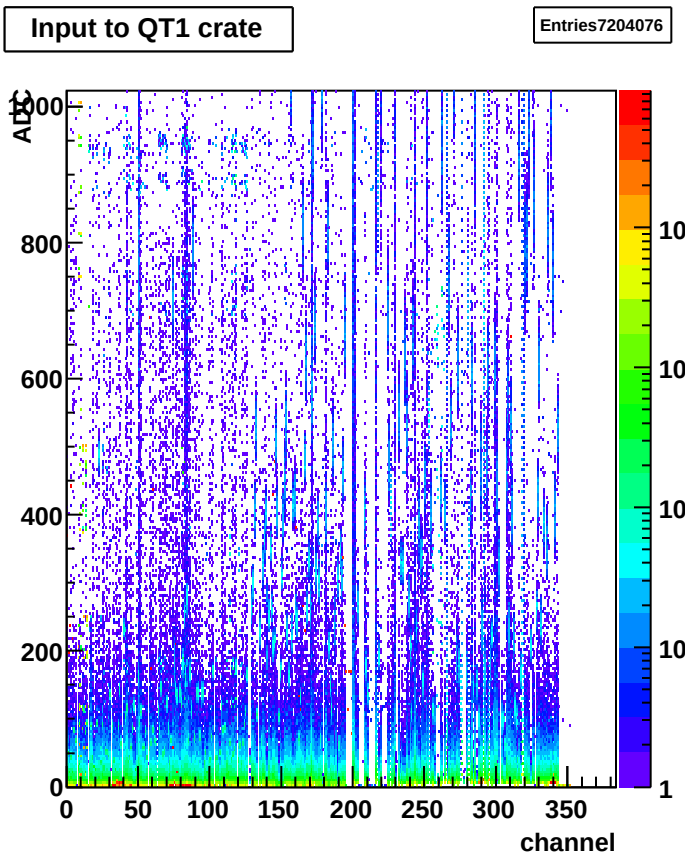

Entries 246968
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

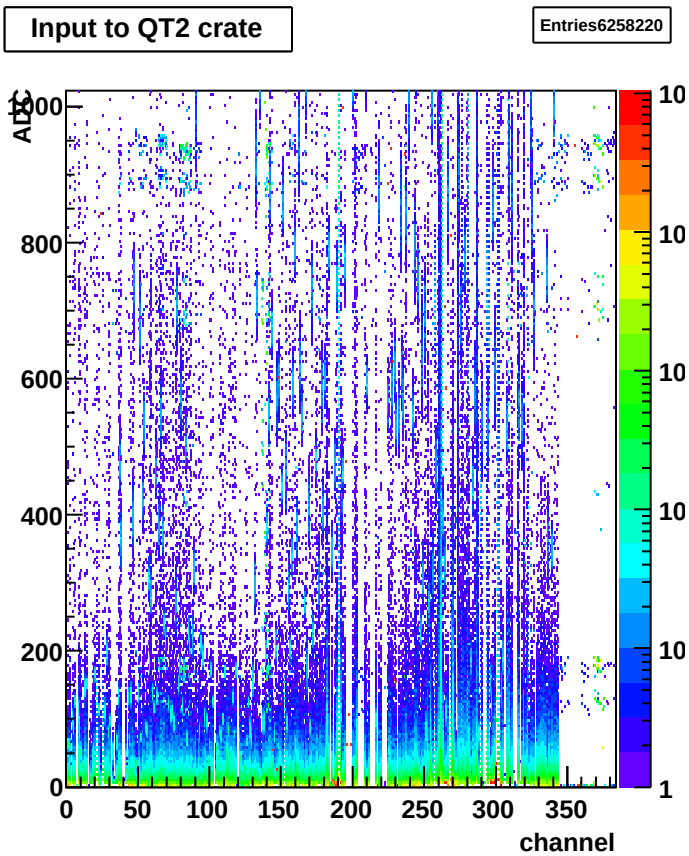

Entries 246968
BBC-L-East ADC Sum
BBC-L-West ADC Sum

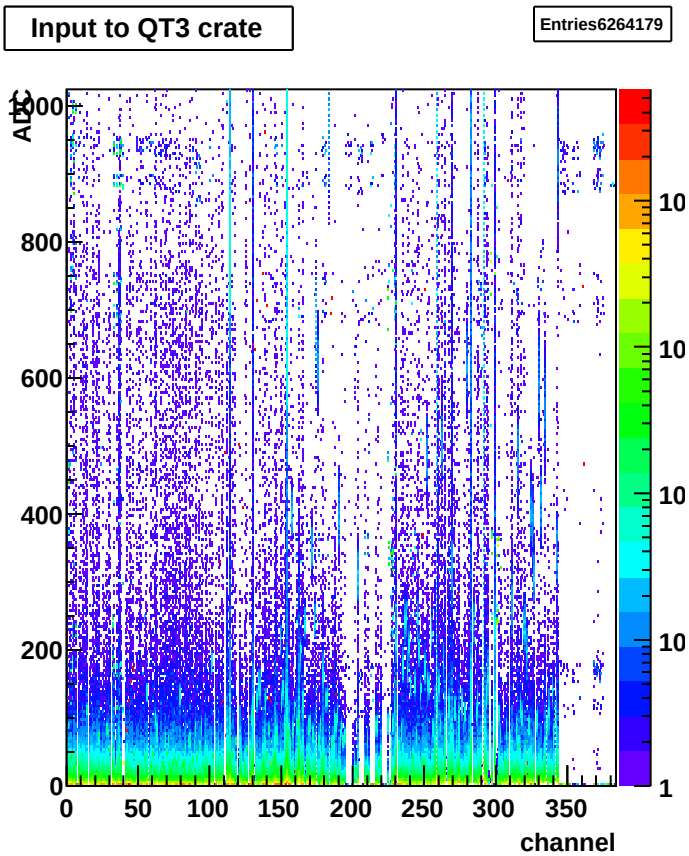

Entries 246968
BBC-S-East ADC Sum
BBC-S-West ADC Sum

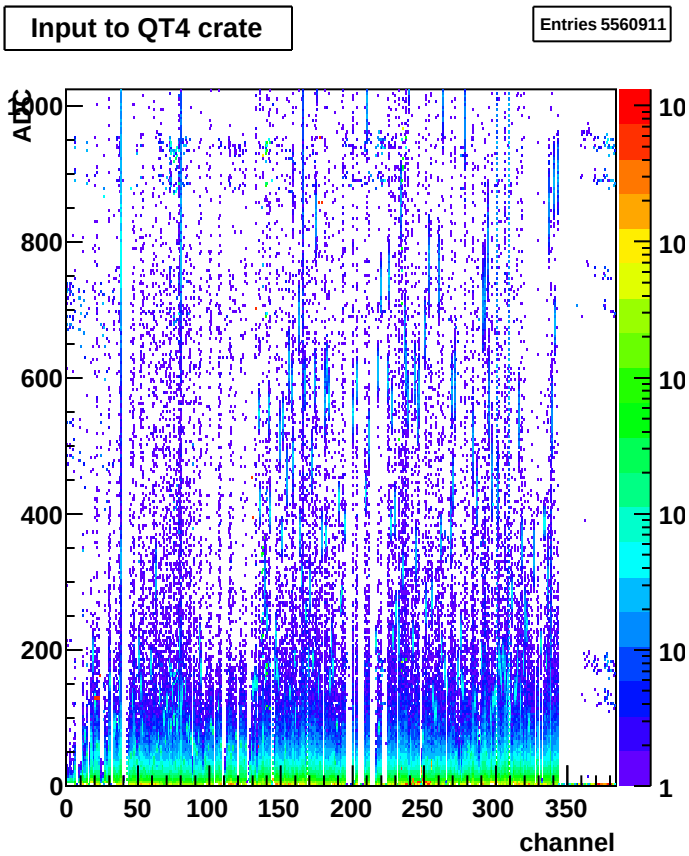

Entries 246968
ZDC East ADC Sum Att
ZDC West ADC Sum Att

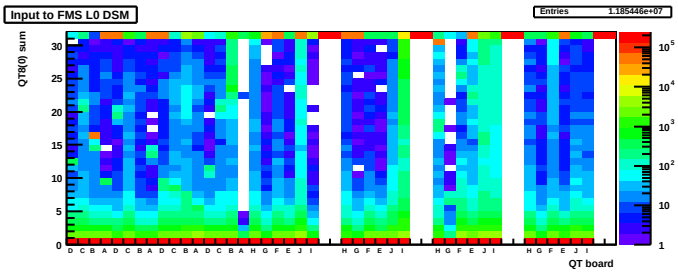

Entries 246968
BBC-L TAC Diff
BCC-S TAC Diff
Entries 246968
BBC-L TAC Diff
VPD TAC Diff
Entries 246968
BBC-L TAC Diff
ZDC TAC Diff
Entries 246968
BBC-S TAC Diff
VPD TAC Diff
Entries 246968
BBC-S TAC Diff
ZDC TAC Diff
Entries 246968
VPD TAC Diff
ZDC TAC Diff

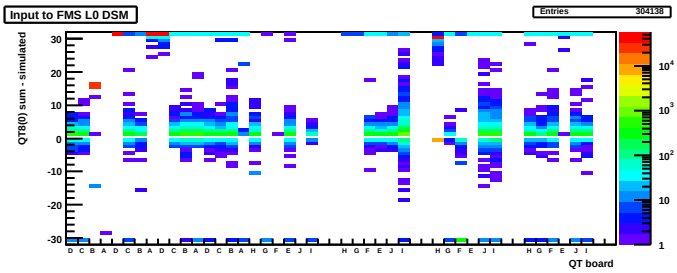

Input to QT1 crate
Entries7204076
ADC
channel

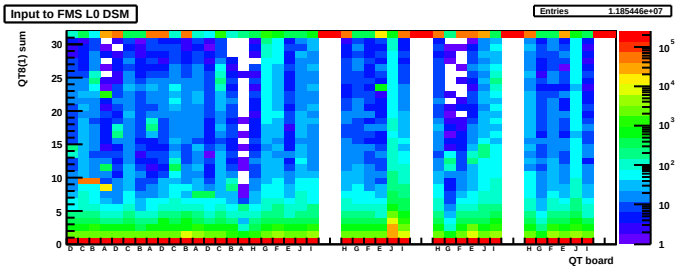

Input to QT2 crate
Entries6258220
ADC
channel

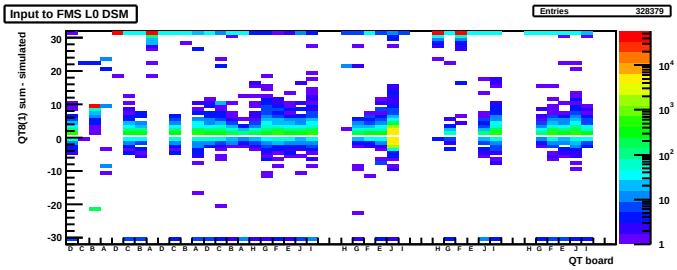

Input to QT3 crate
Entries6264179
ADC
channel

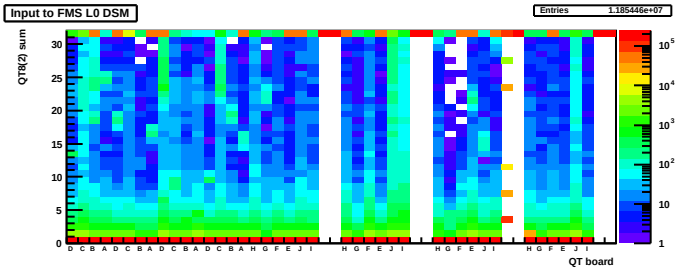

Input to QT4 crate
Entries 5560911
ADC
channel

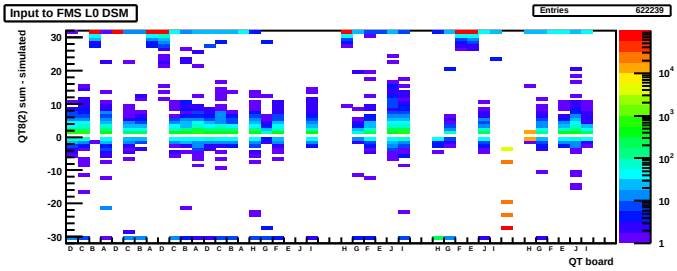

Input to FMS L0 DSM
Entries 1.185446e+07
QT8(0) sum
QT board

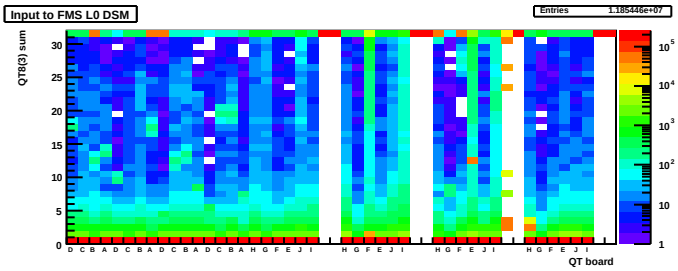

Input to FMS L0 DSM
Entries 304138
QT8(0) sum - simulated
QT board

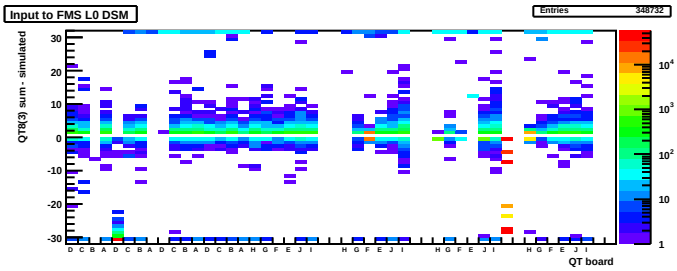

Input to FMS L0 DSM
Entries 1.185446e+07
QT8(1) sum
QT board

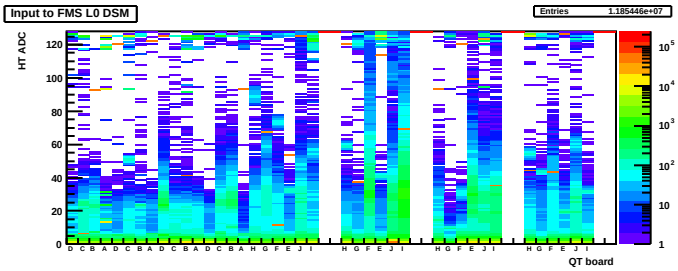

Input to FMS L0 DSM
Entries 328379
QT8(1) sum - simulated
QT board

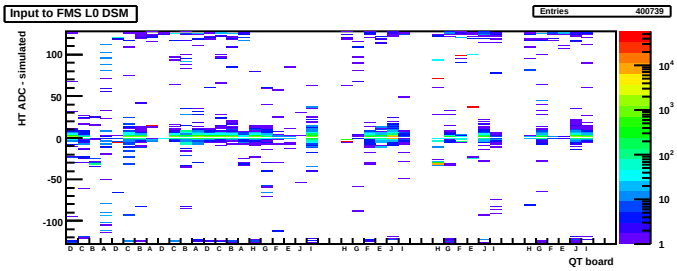

Input to FMS L0 DSM
Entries 1.185446e+07
QT8(2) sum
QT board

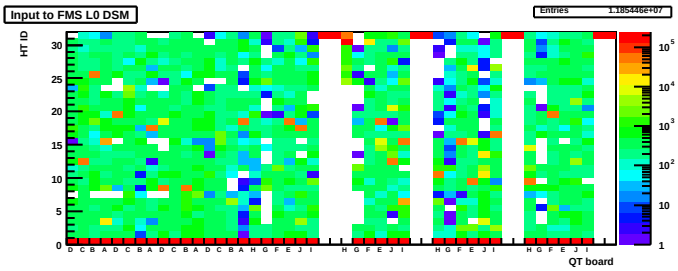

Input to FMS L0 DSM
Entries 622239
QT8(2) sum - simulated
QT board

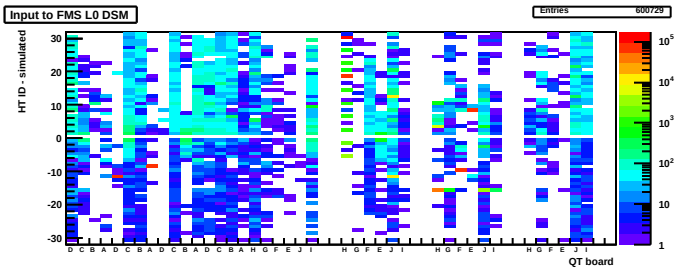

Input to FMS L0 DSM
Entries 1.185446e+07
QT8(3) sum
QT board

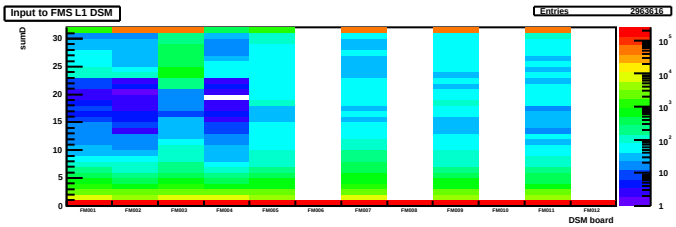

Input to FMS L0 DSM
Entries 348732
QT8(3) sum - simulated
QT board

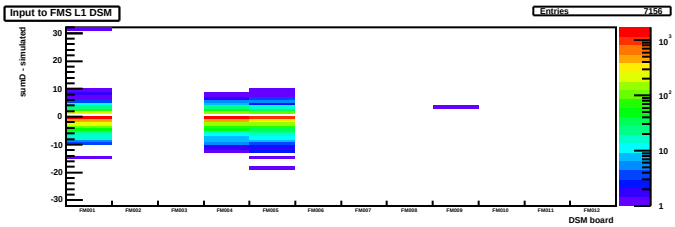

Input to FMS L0 DSM
Entries 1.185446e+07
HT ADC
QT board

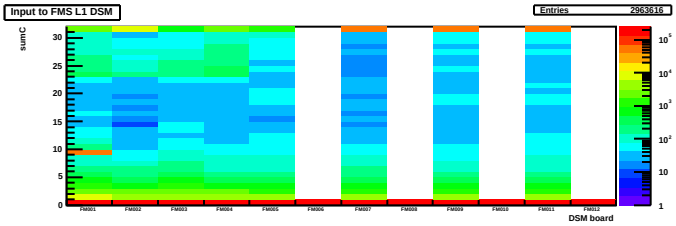

Input to FMS L0 DSM
Entries 400739
HT ADC - simulated
QT board

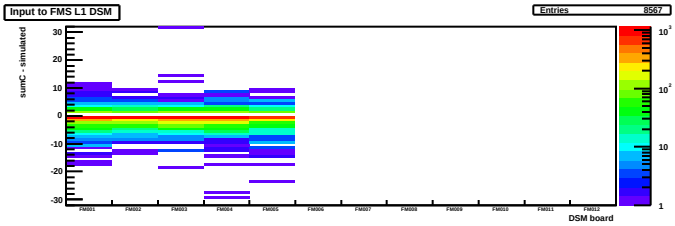

Input to FMS L0 DSM
Entries 1.185446e+07
HT ID
QT board

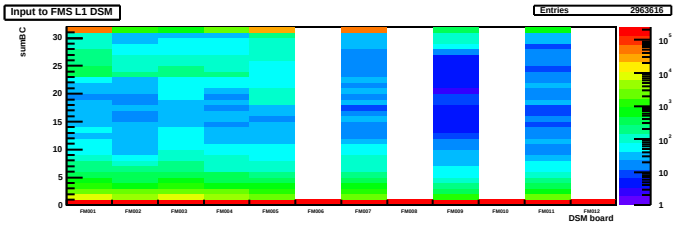

Input to FMS L0 DSM
Entries 600729
HT ID - simulated
QT board

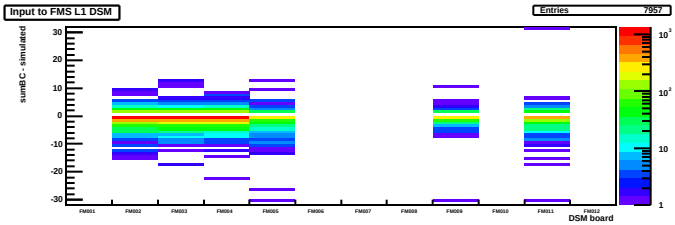

Input to FMS L1 DSM
Entries 2963616
sumD
DSM board

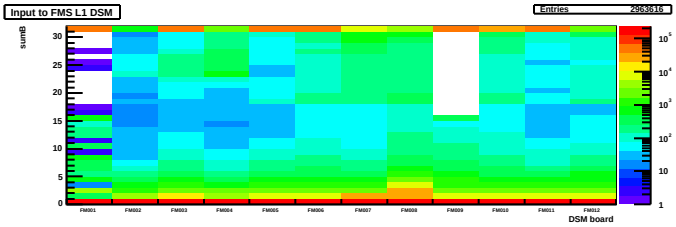

Input to FMS L1 DSM
Entries 7156
sumD - simulated
DSM board

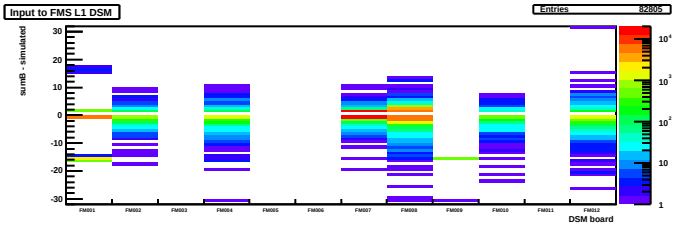

Input to FMS L1 DSM
Entries 2963616
sumC
DSM board

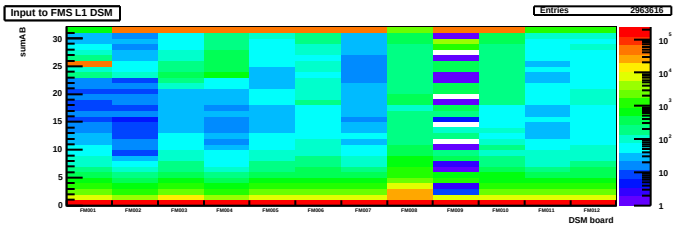

Input to FMS L1 DSM
Entries 8567
sumC - simulated
DSM board

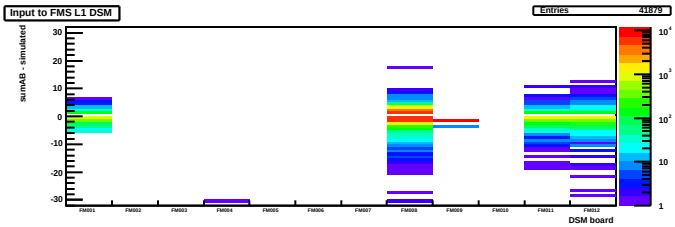

Input to FMS L1 DSM
Entries 2963616
sumBC
DSM board

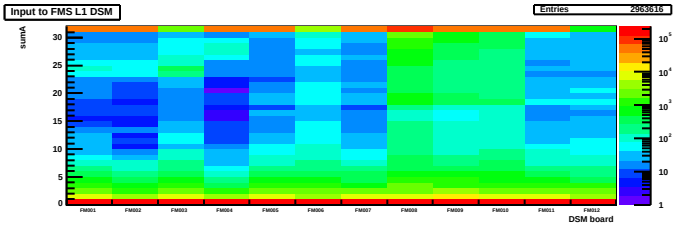

Input to FMS L1 DSM
Entries 7957
sumBC - simulated
DSM board

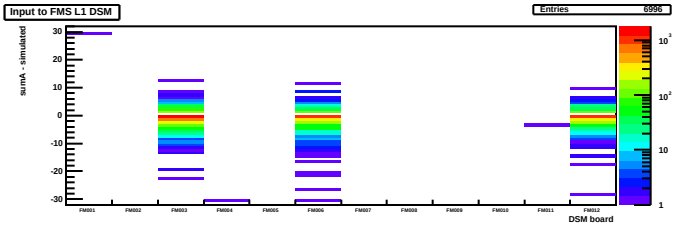

Input to FMS L1 DSM
Entries 2963616
sumB
DSM board

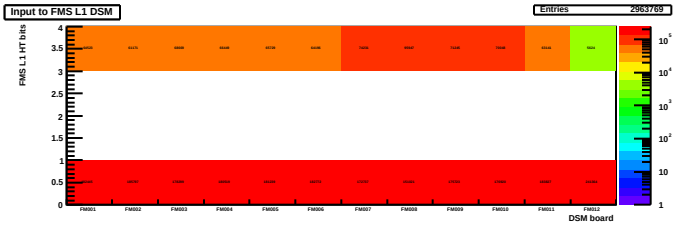

Input to FMS L1 DSM
Entries 82805
sumB - simulated
DSM board

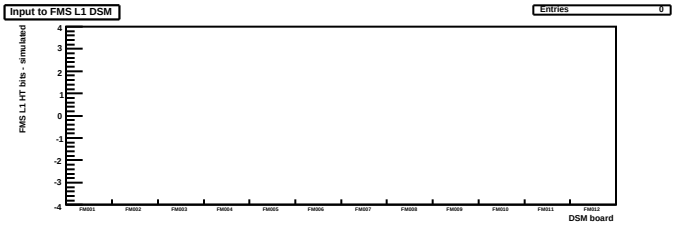

Input to FMS L1 DSM
Entries 2963616
sumAB
DSM board

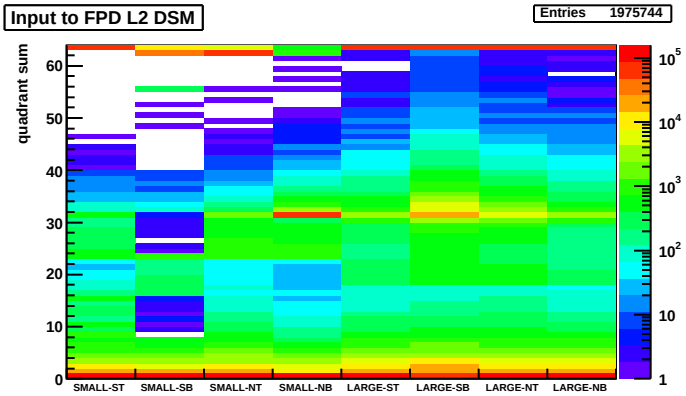

Input to FMS L1 DSM
Entries 41879
sumAB - simulated
DSM board

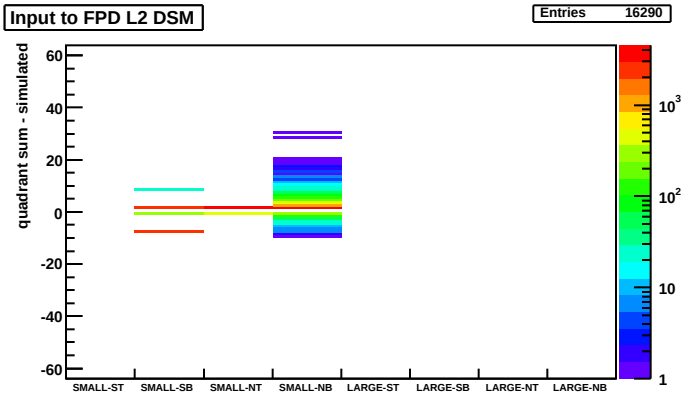

Input to FMS L1 DSM
Entries 2963616
sumA
DSM board

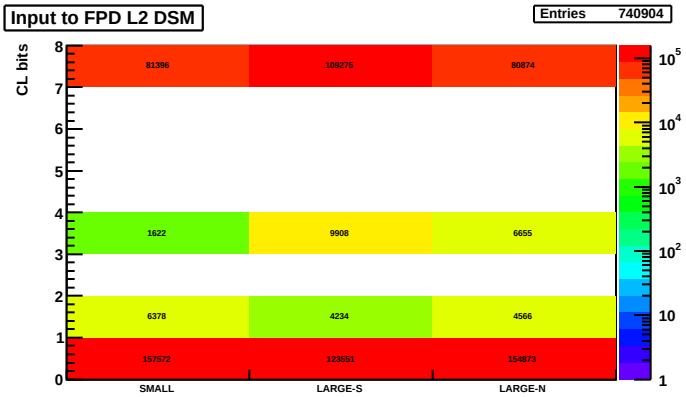

Input to FMS L1 DSM
Entries 6996
sumA - simulated
DSM board

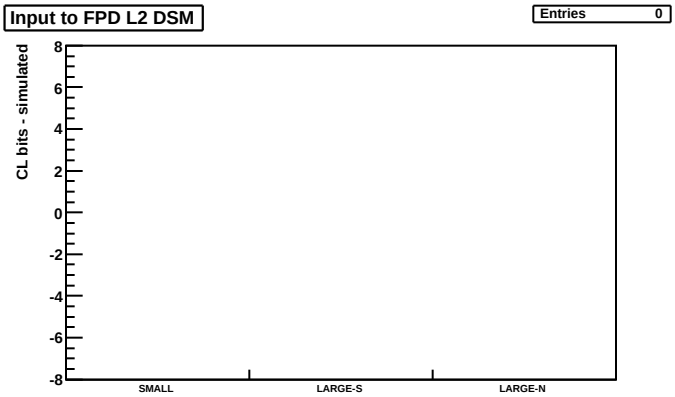

Input to FMS L1 DSM
Entries 2963769
FMS L1 HT bits
DSM board

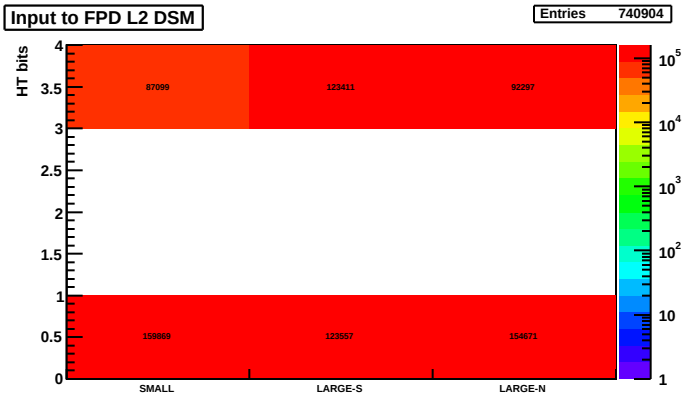

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

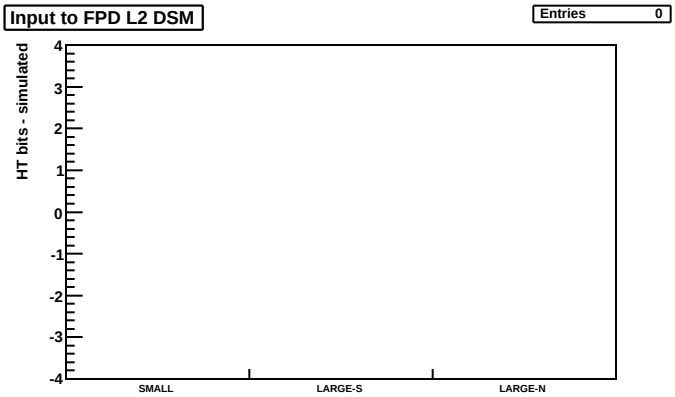

Input to FPD L2 DSM
Entries 1975744
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

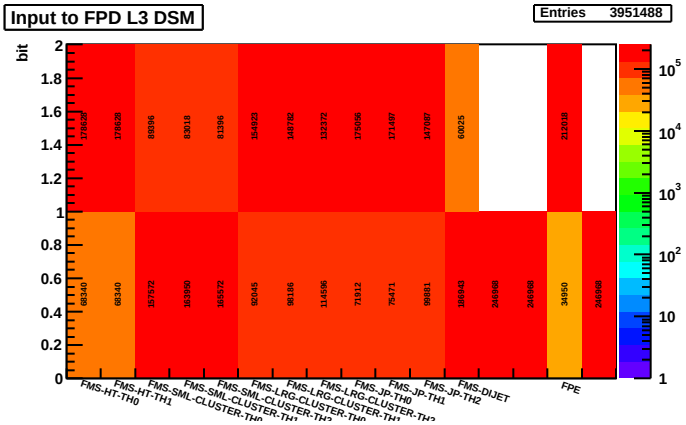

Input to FPD L2 DSM
Entries 16290
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

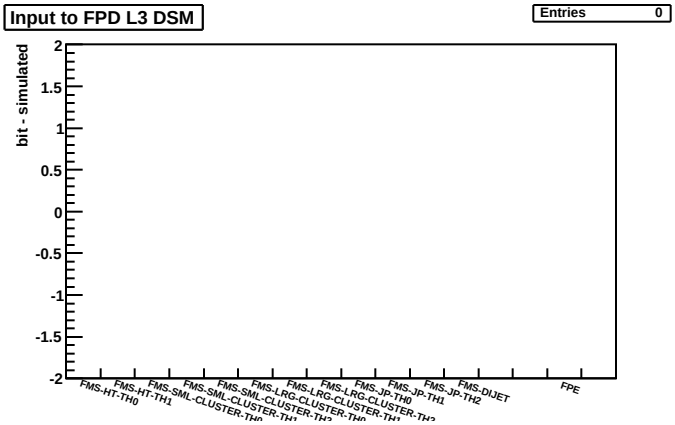

Input to FPD L2 DSM
Entries 740904
CL bits
81396
109275
80874
1622
9908
6655
6378
4234
4566
157572
123551
154873
SMALL
LARGE-S
LARGE-N

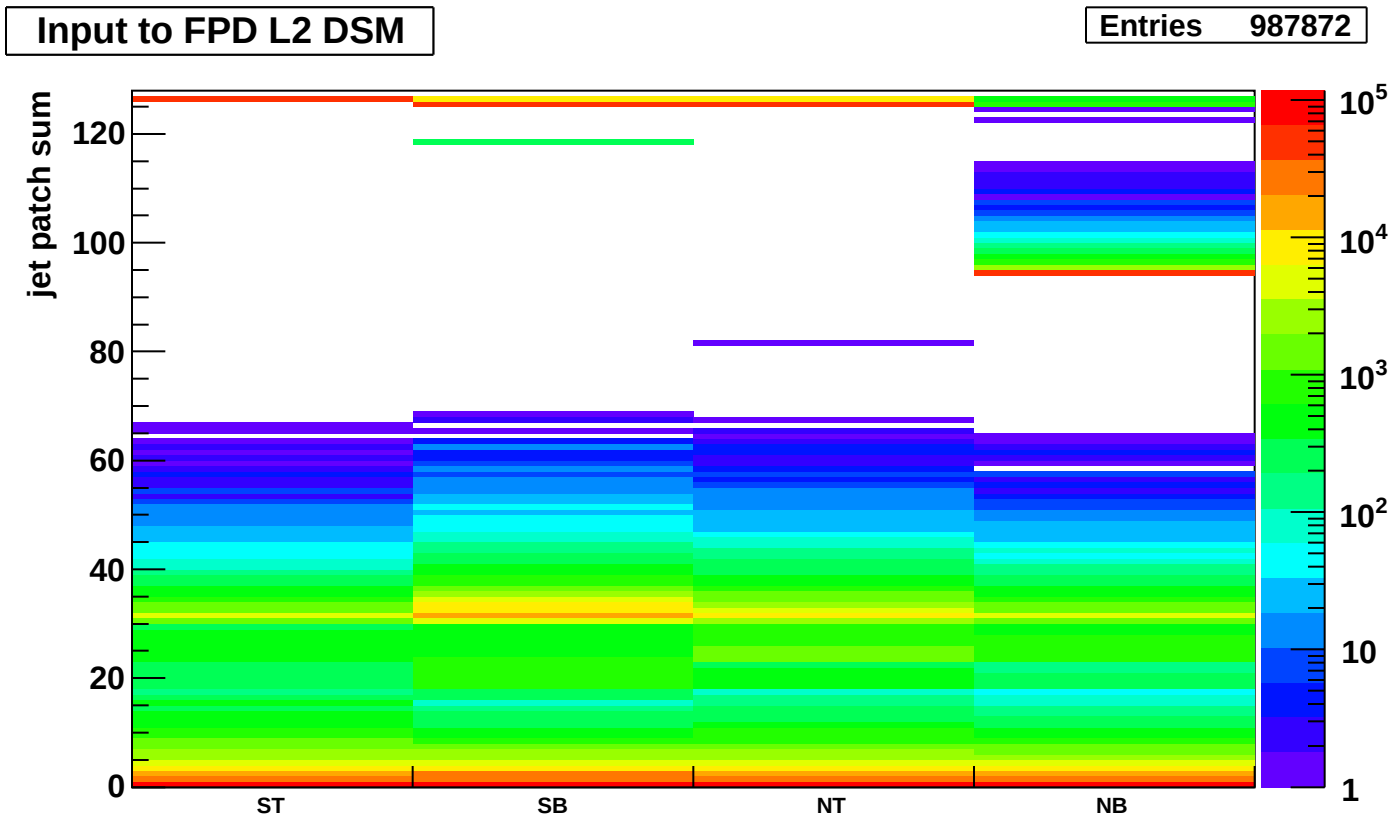

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

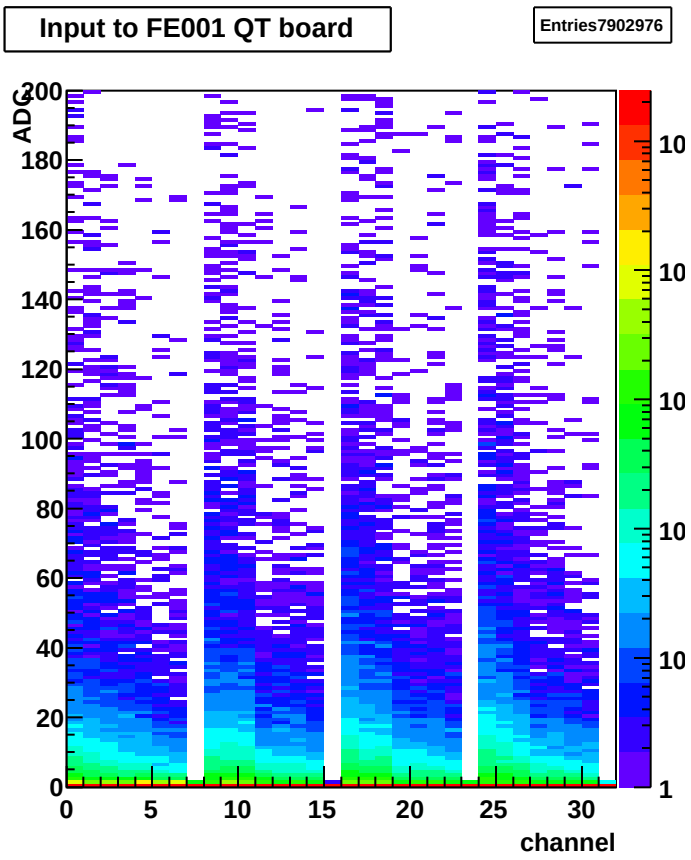

Input to FPD L2 DSM
Entries 740904
HT bits
87099
123411
92297
159869
123557
154671
SMALL
LARGE-S
LARGE-N

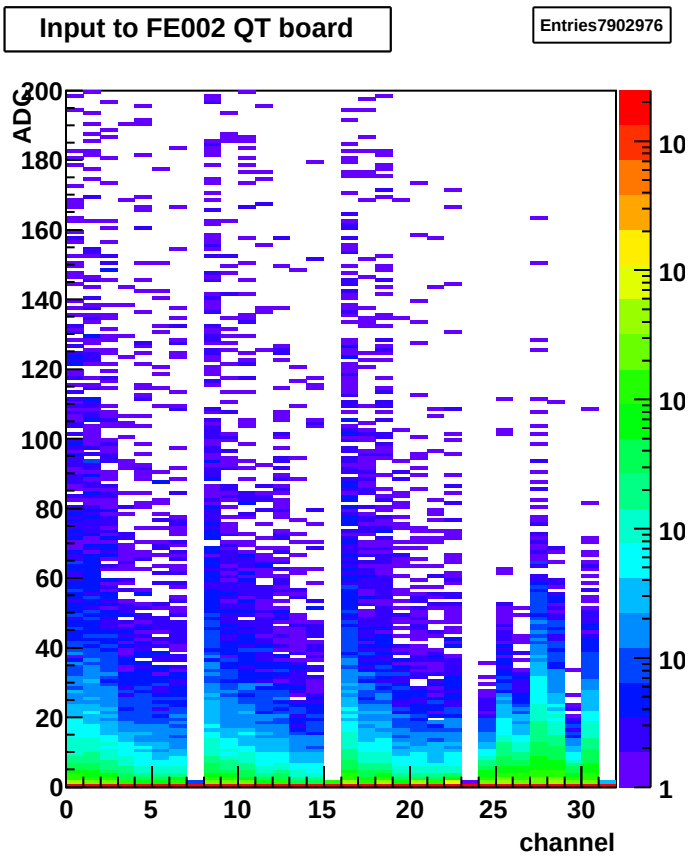

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

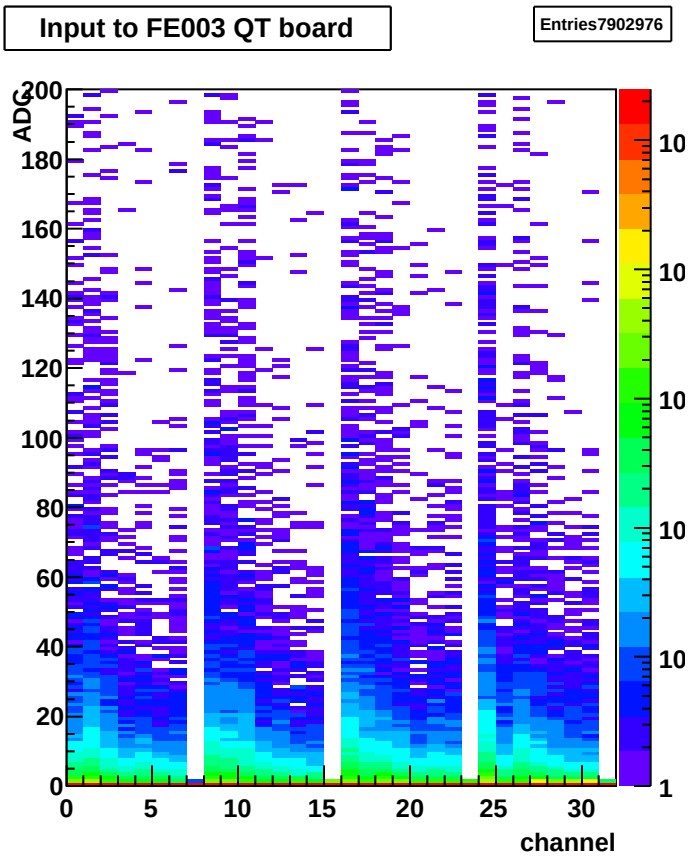

Input to FPD L3 DSM
Entries 3951488
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

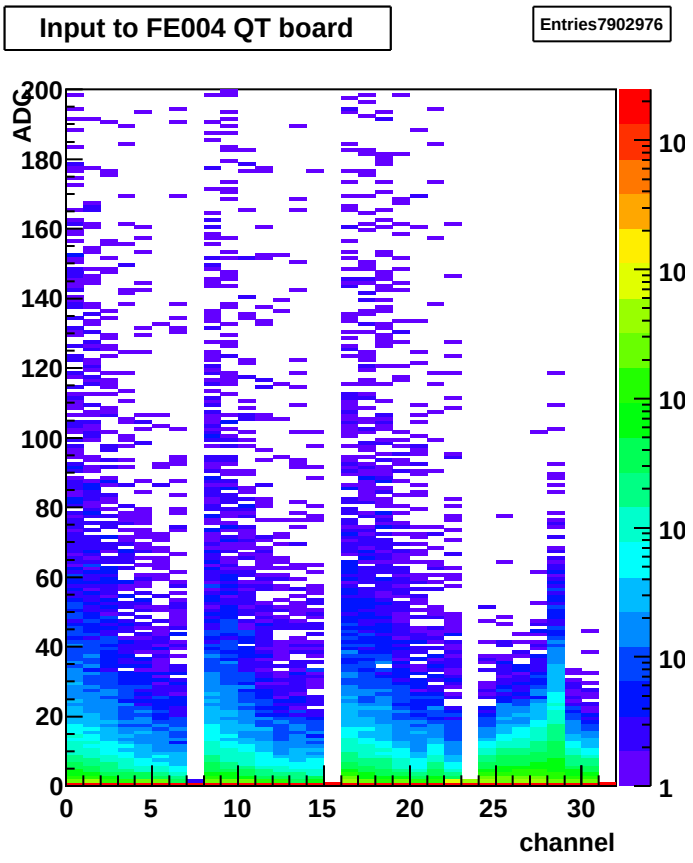

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

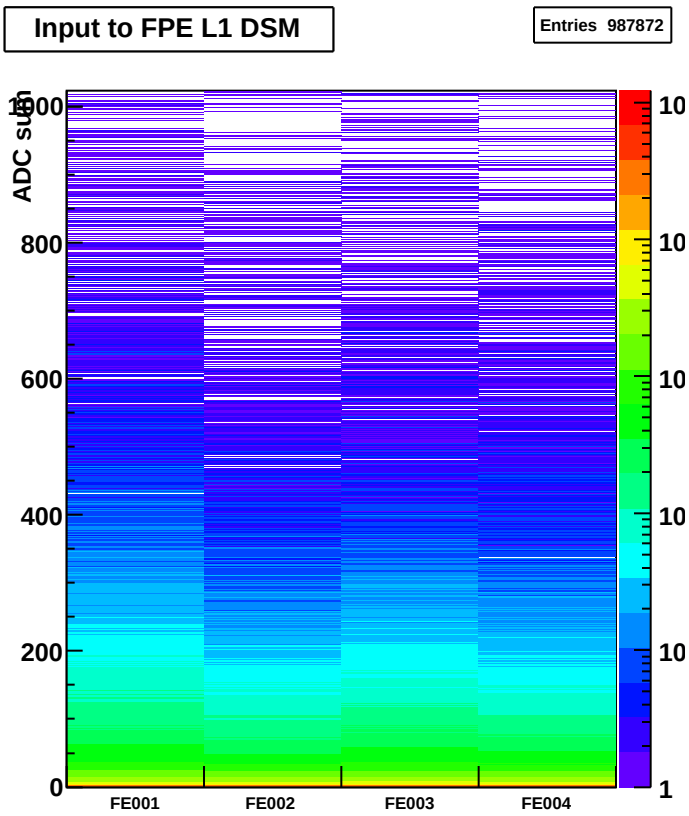

Input to FPD L2 DSM
Entries 987872
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

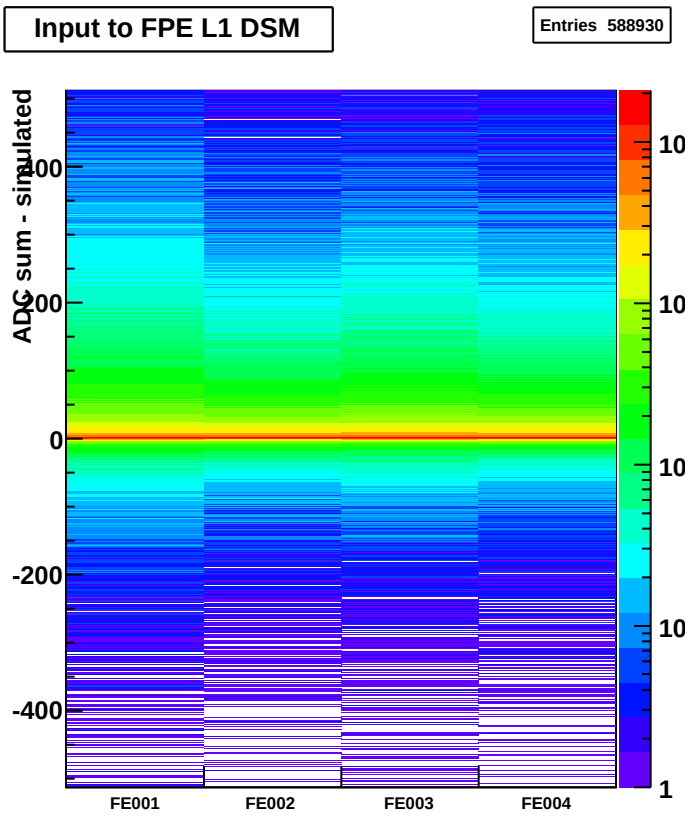

Input to FE001 QT board
Entries7902976
ADC
channel

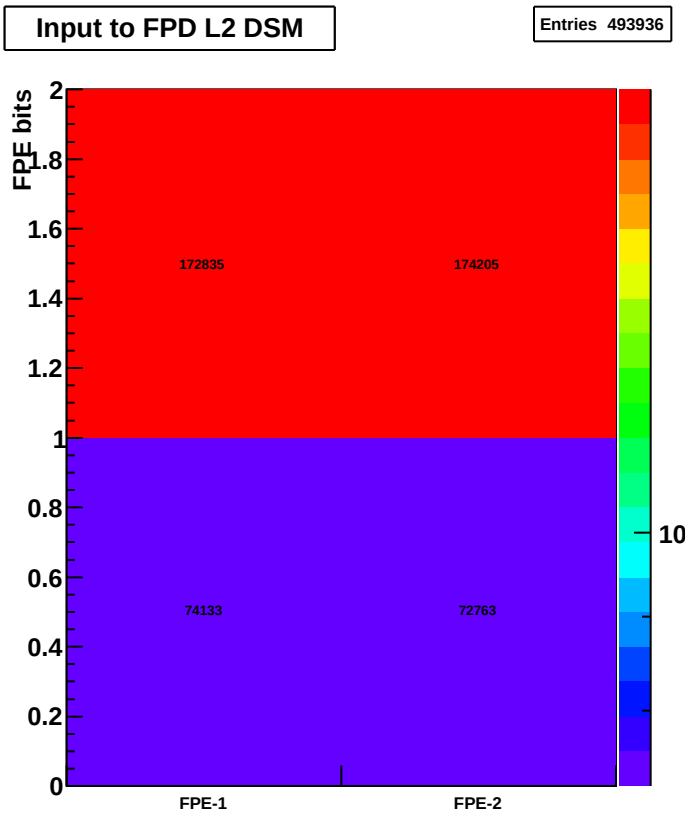

Input to FE002 QT board
Entries7902976
ADC
channel

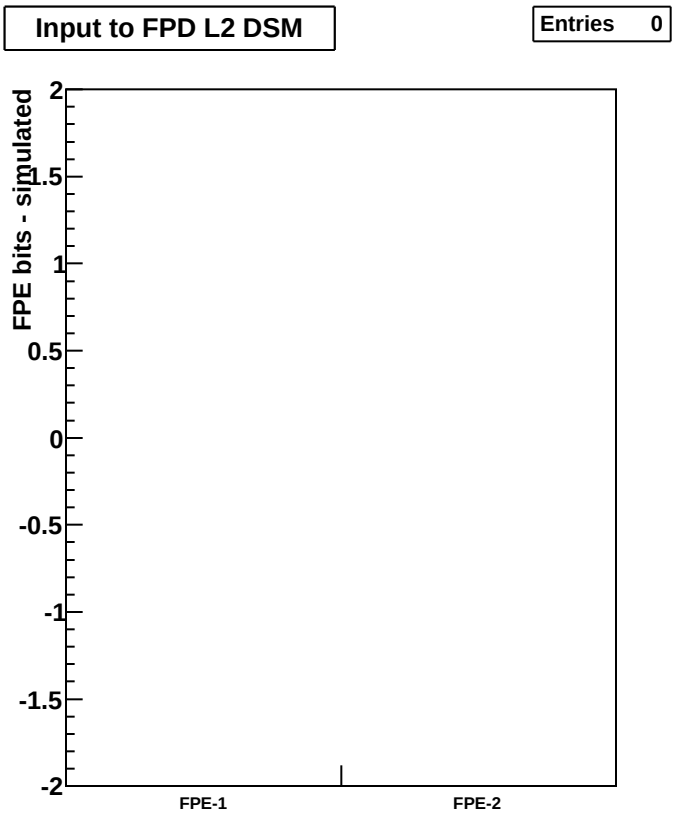

Input to FE003 QT board
Entries7902976
ADC
channel


Input to FE004 QT board
Entries7902976
ADC
channel


Input to FPE L1 DSM
Entries 987872
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 588930
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 493936
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
172835
174205
74133
72763
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2