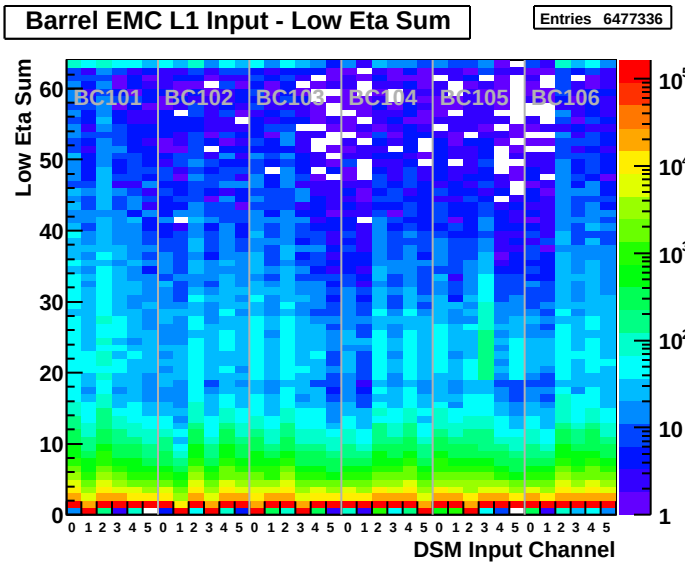
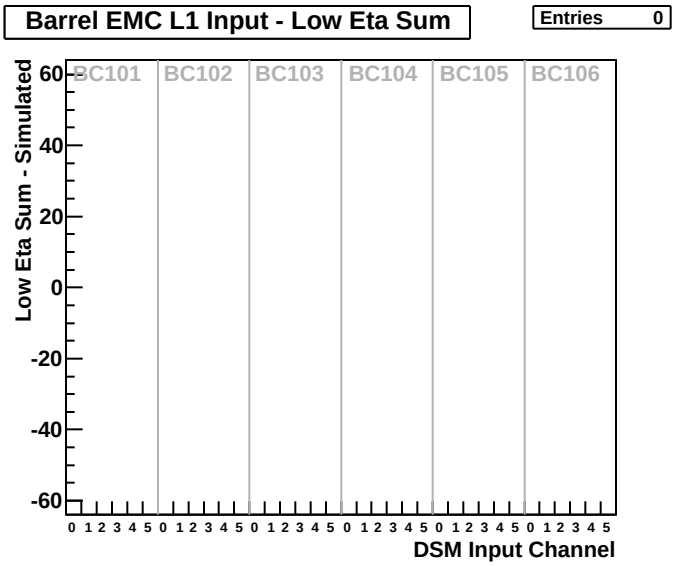
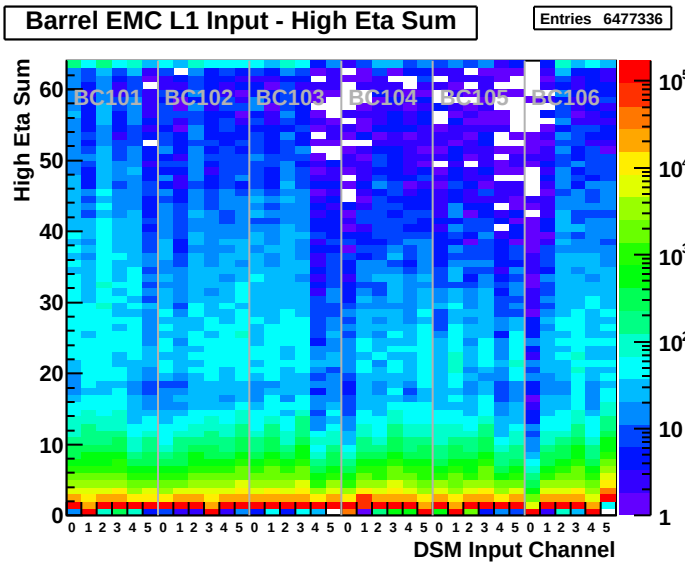
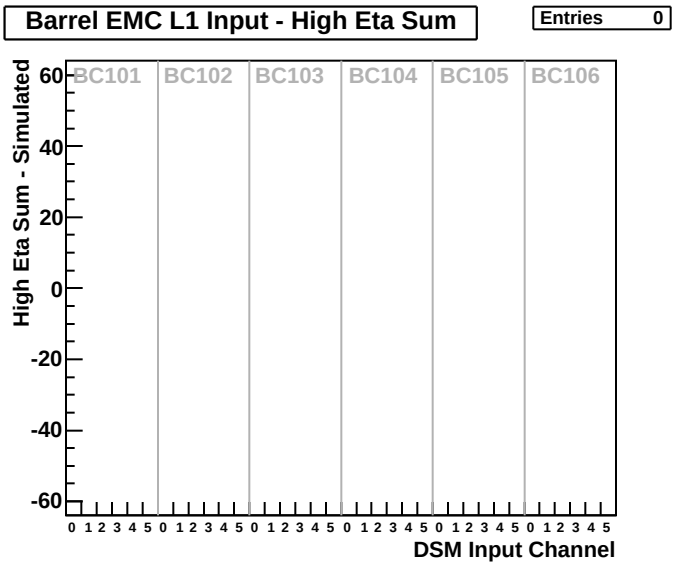
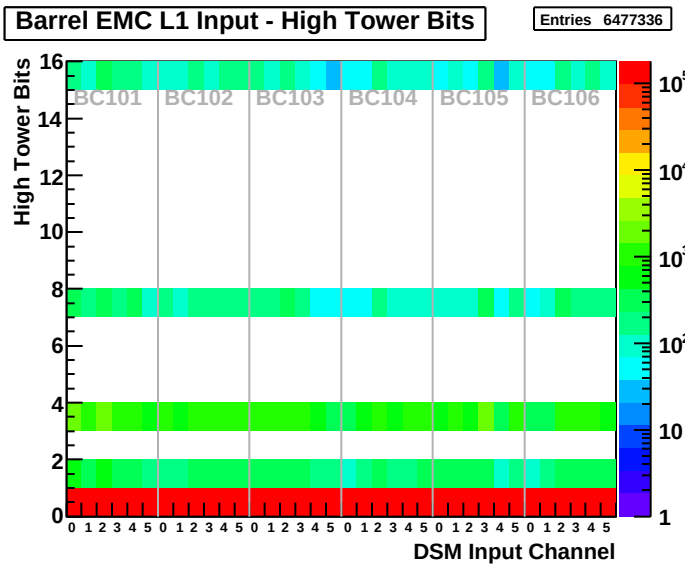
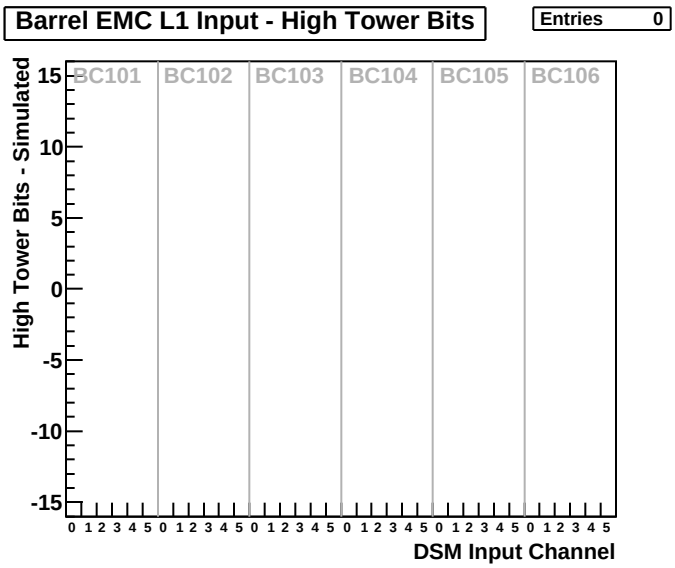

Barrel EMC L1 Input - Low Eta Sum
Entries 6477336
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

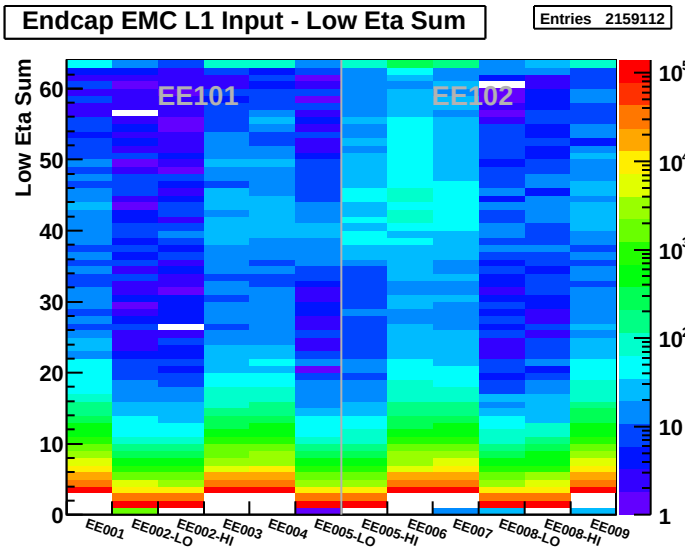

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

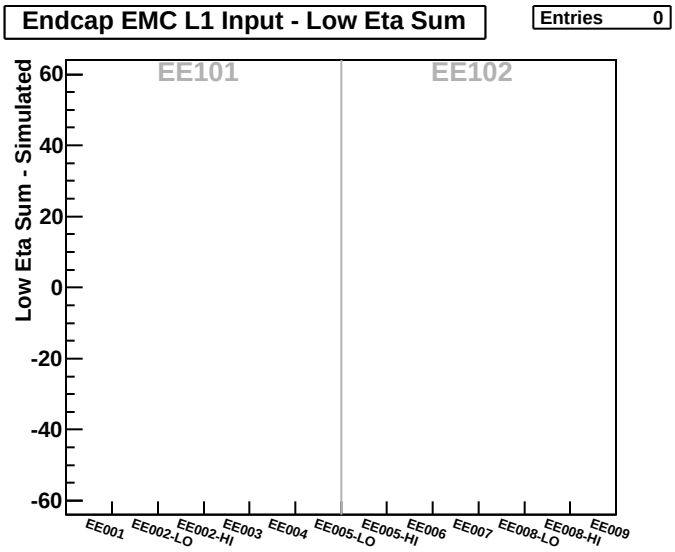

Barrel EMC L1 Input - High Eta Sum
Entries 6477336
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

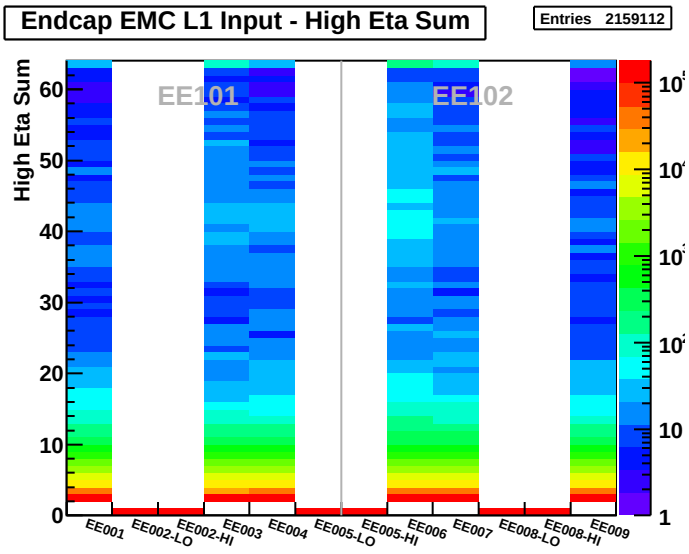

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

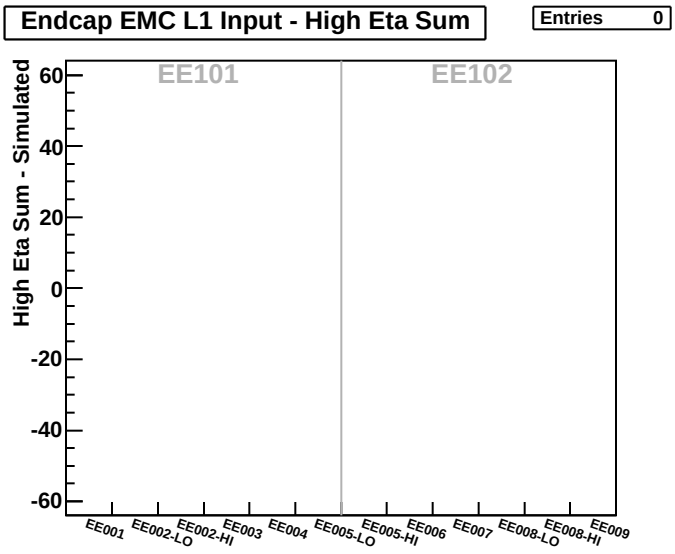

Barrel EMC L1 Input - High Tower Bits
Entries 6477336
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

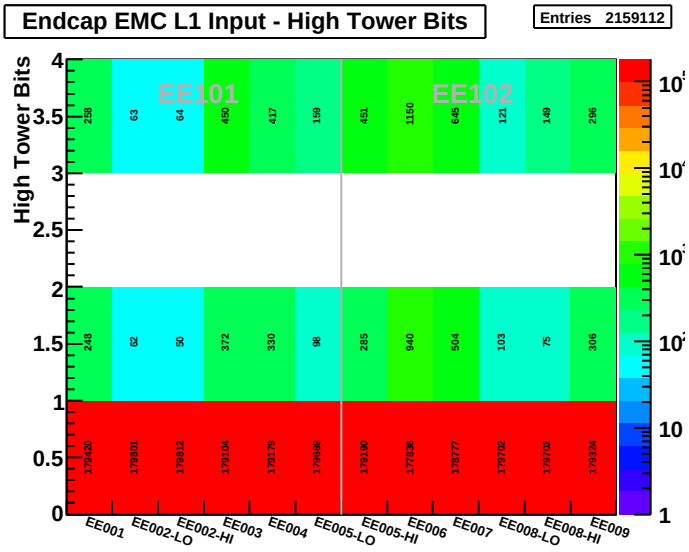

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

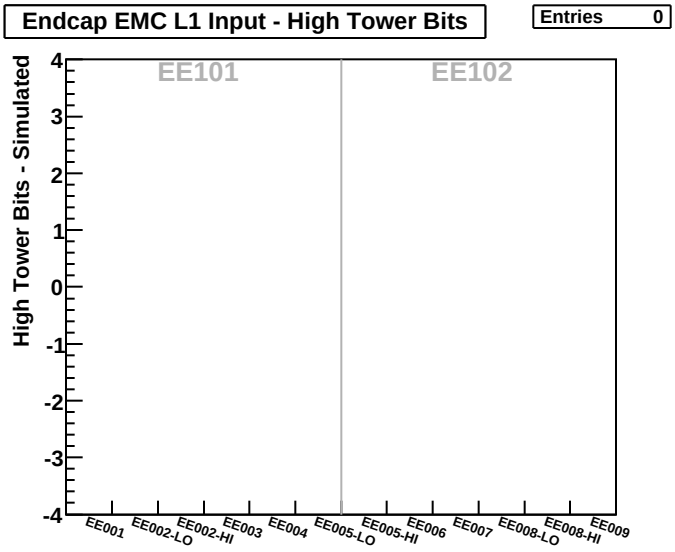

Endcap EMC L1 Input - Low Eta Sum
Entries 2159112
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

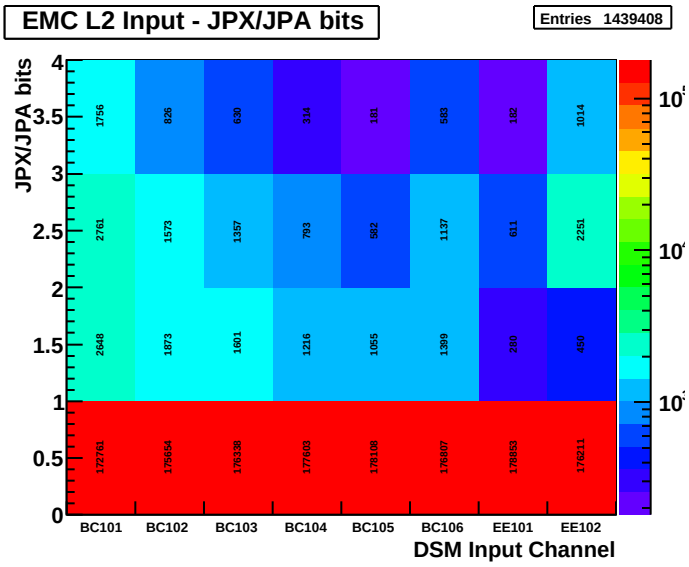

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

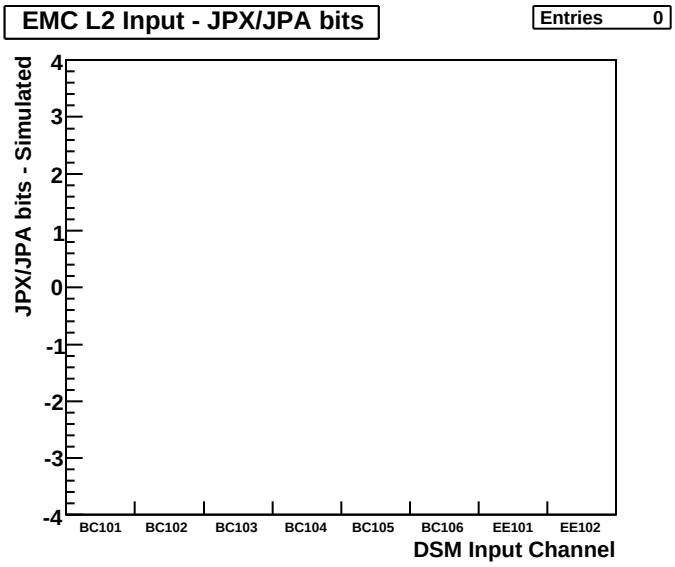

Endcap EMC L1 Input - High Eta Sum
Entries 2159112
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

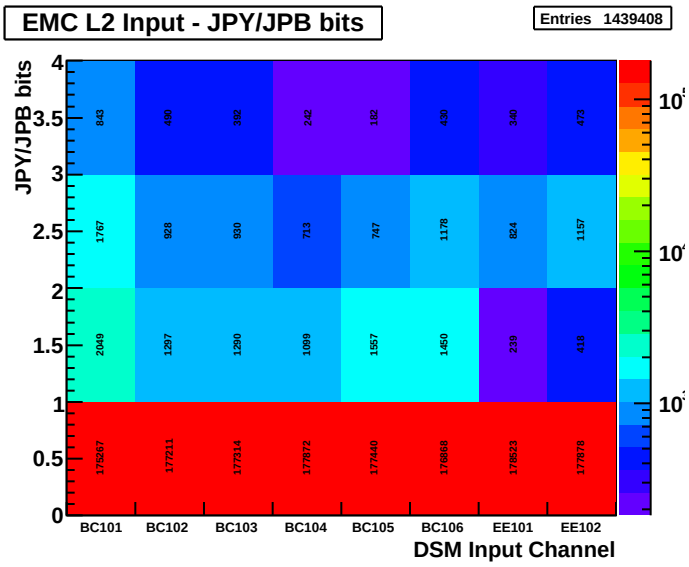

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

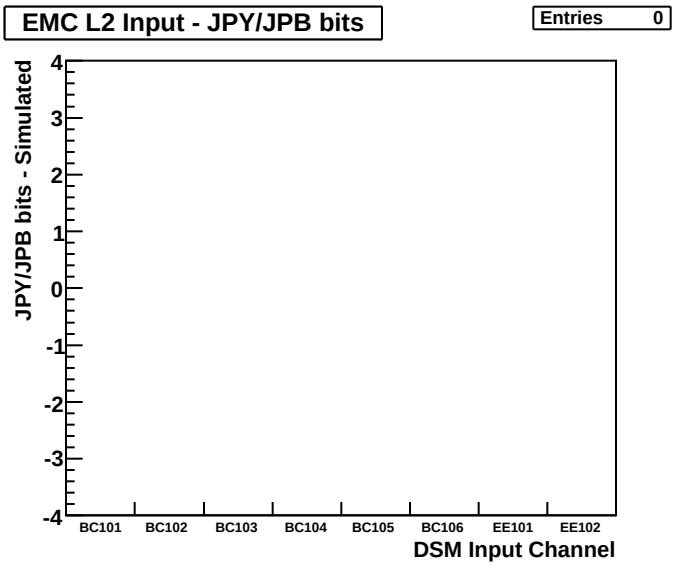

Endcap EMC L1 Input - High Tower Bits
Entries 2159112
High Tower Bits
EE101
EE102
258 63 64 450 417 159 451 1150 645 121 149 296
248 62 50 372 330 98 285 940 504 103 75 306
179420 179801 179812 179104 179179 179669 179190 177635 178777 179702 179702 179324
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

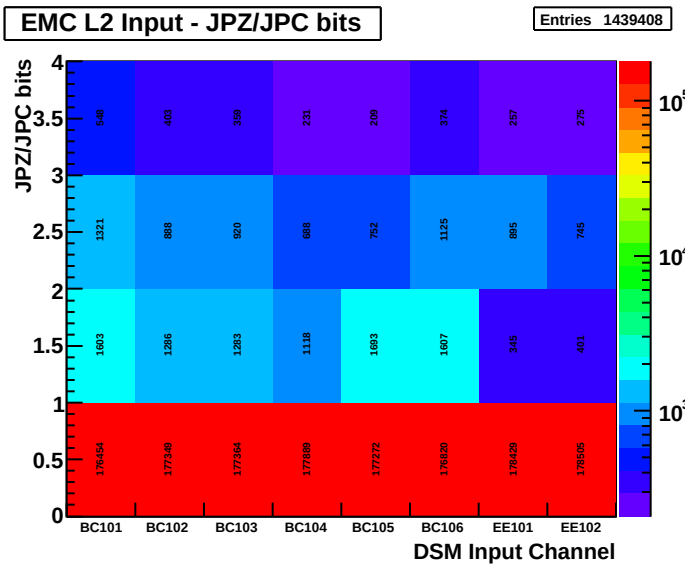

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

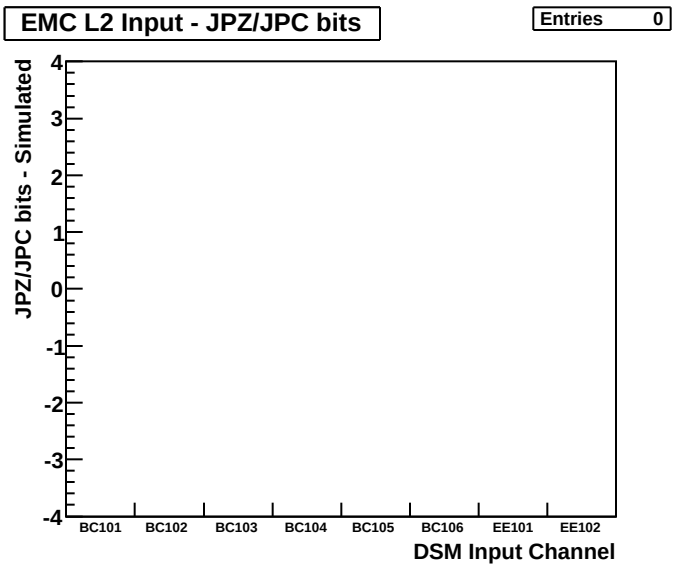

EMC L2 Input - JPX/JPA bits
Entries 1439408
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1756 826 630 314 181 583 182 1014
2761 1573 1357 793 582 1137 611 2251
2648 1873 1601 1216 1055 1399 280 450
172761 175654 176338 177603 178108 176807 178853 176211

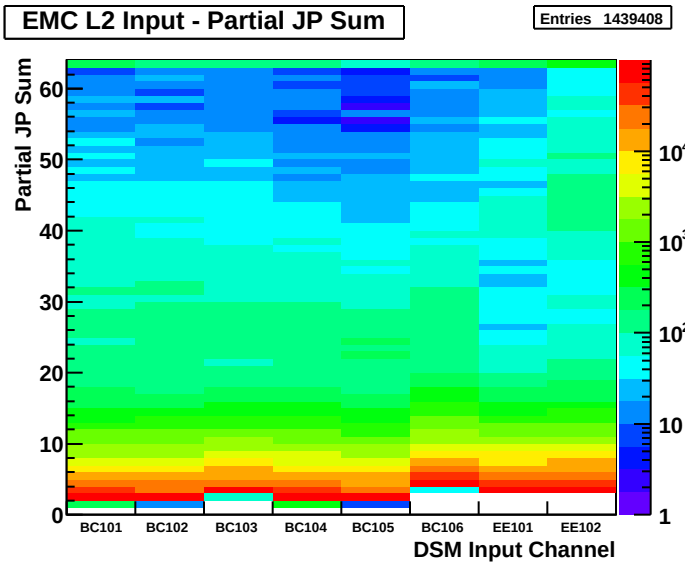

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

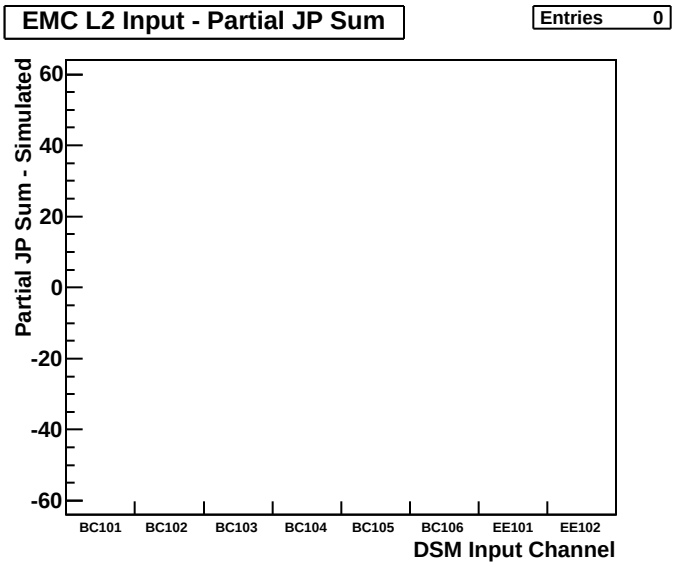

EMC L2 Input - JPY/JPB bits
Entries 1439408
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
843 490 392 242 182 430 340 473
1767 928 930 713 747 1178 824 1157
2049 1297 1290 1009 1557 1450 239 418
175267 177211 177314 177872 177440 176868 178523 177878

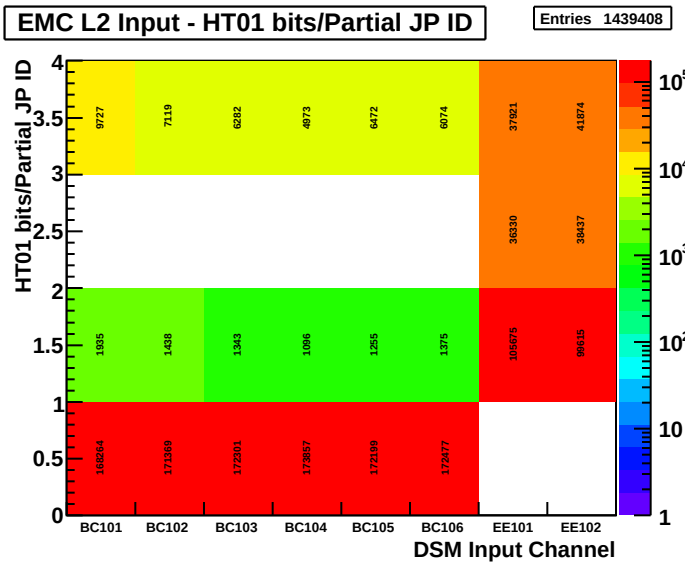

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

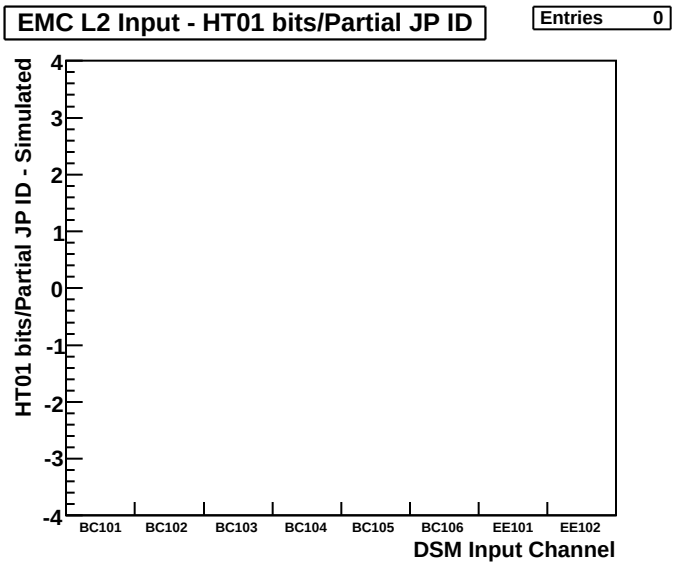

EMC L2 Input - JPZ/JPC bits
Entries 1439408
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
548 403 359 231 209 374 257 275
1321 888 920 688 752 1125 895 745
1603 1286 1283 1118 1693 1607 345 401
176454 177349 177364 177889 177272 176820 178429 178505

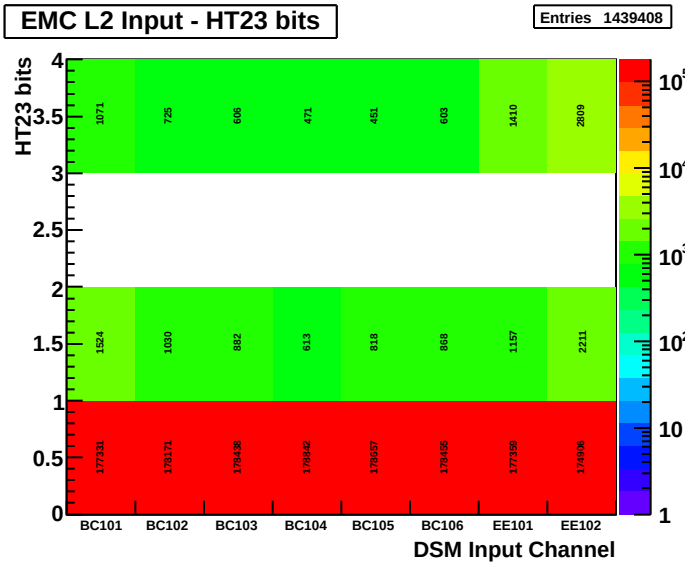

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

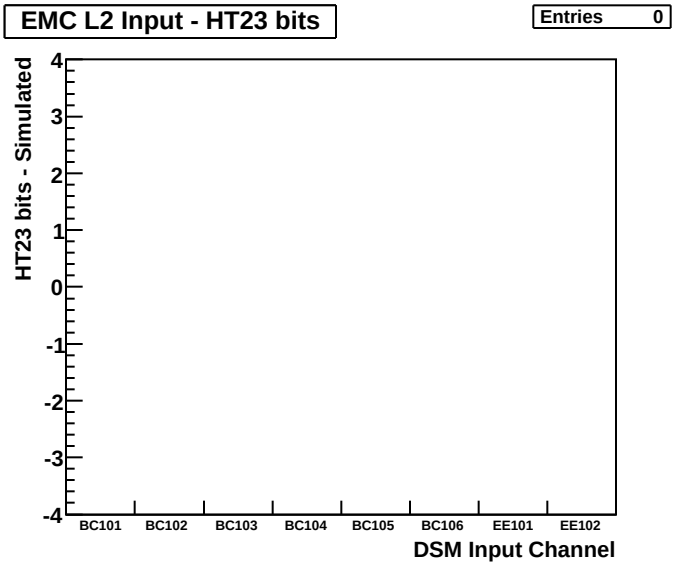

EMC L2 Input - Partial JP Sum
Entries 1439408
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

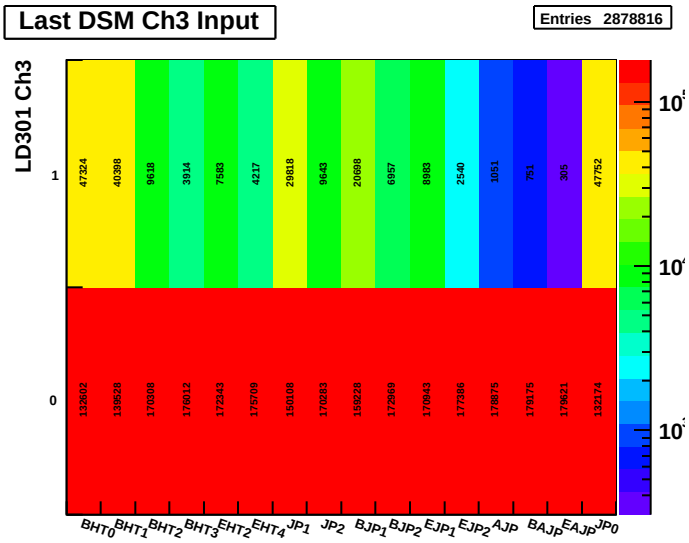

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

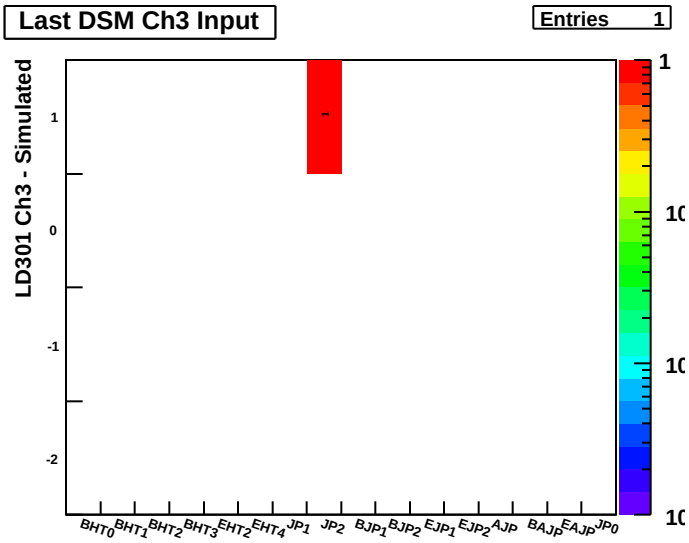

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1439408
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

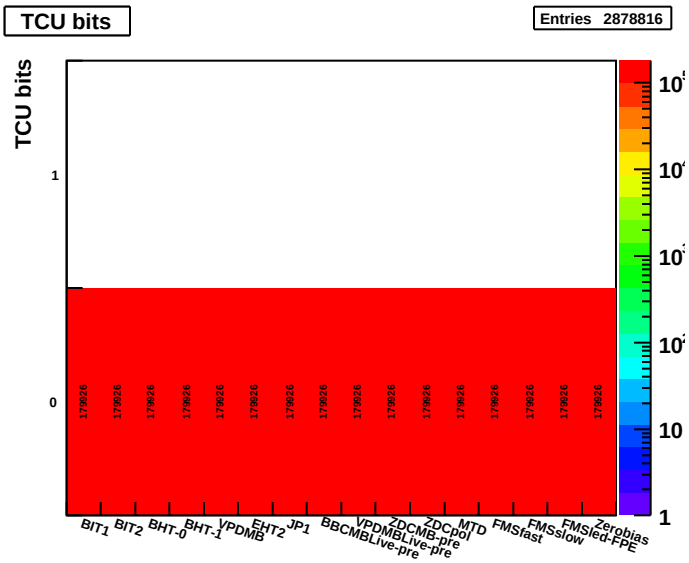

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

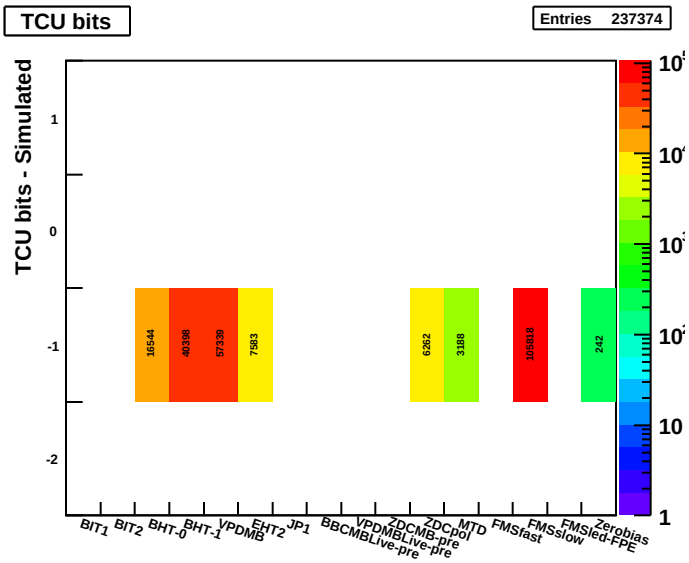

EMC L2 Input - HT23 bits
Entries 1439408
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

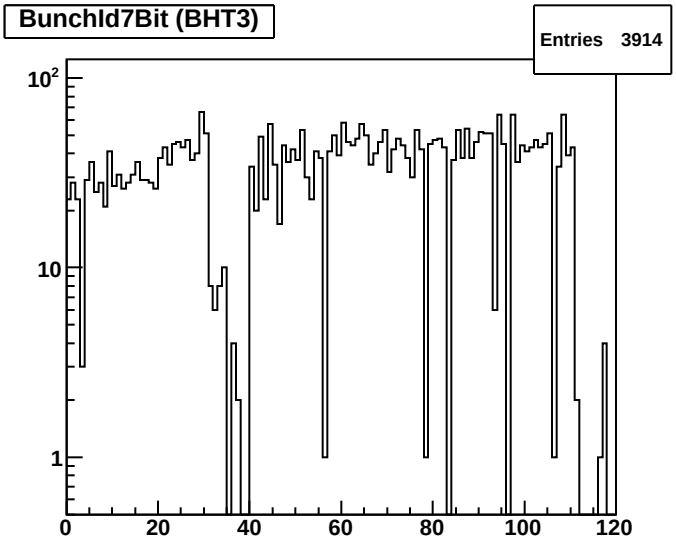

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

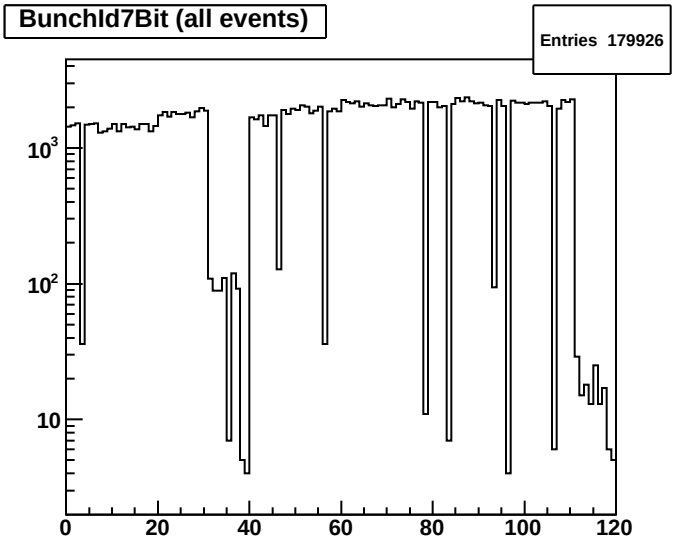

Last DSM Ch3 Input
Entries 2878816
LD301 Ch3
47324 40398 9618 3914 7583 4217 29818 9643 20698 6957 8983 2540 1051 751 305 47752
132602 139528 170308 176012 172343 175709 150108 170283 159228 172869 170943 177386 178875 179175 179621 132174
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

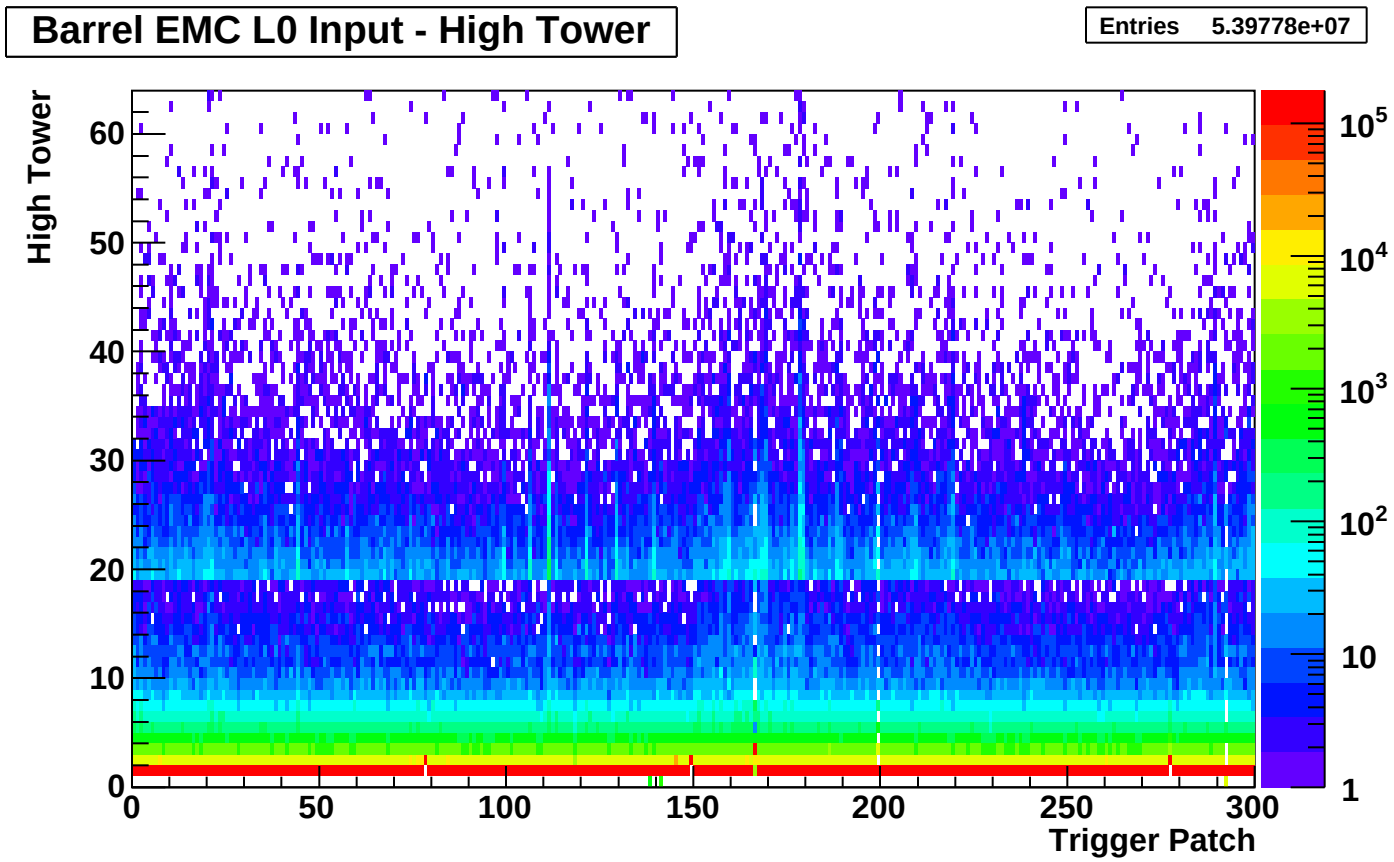

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

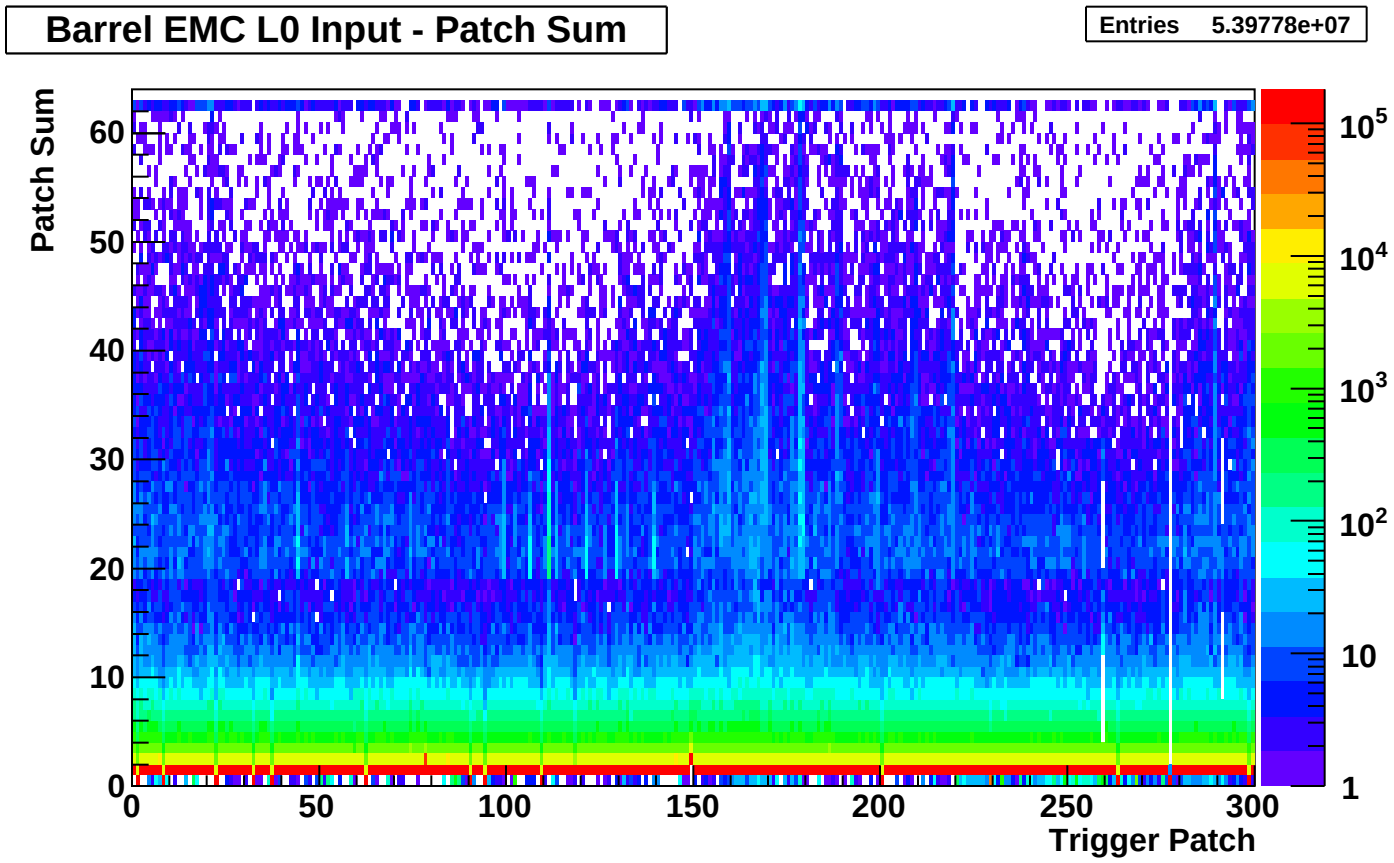

TCU bits
Entries 2878816
TCU bits
179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926 179926
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

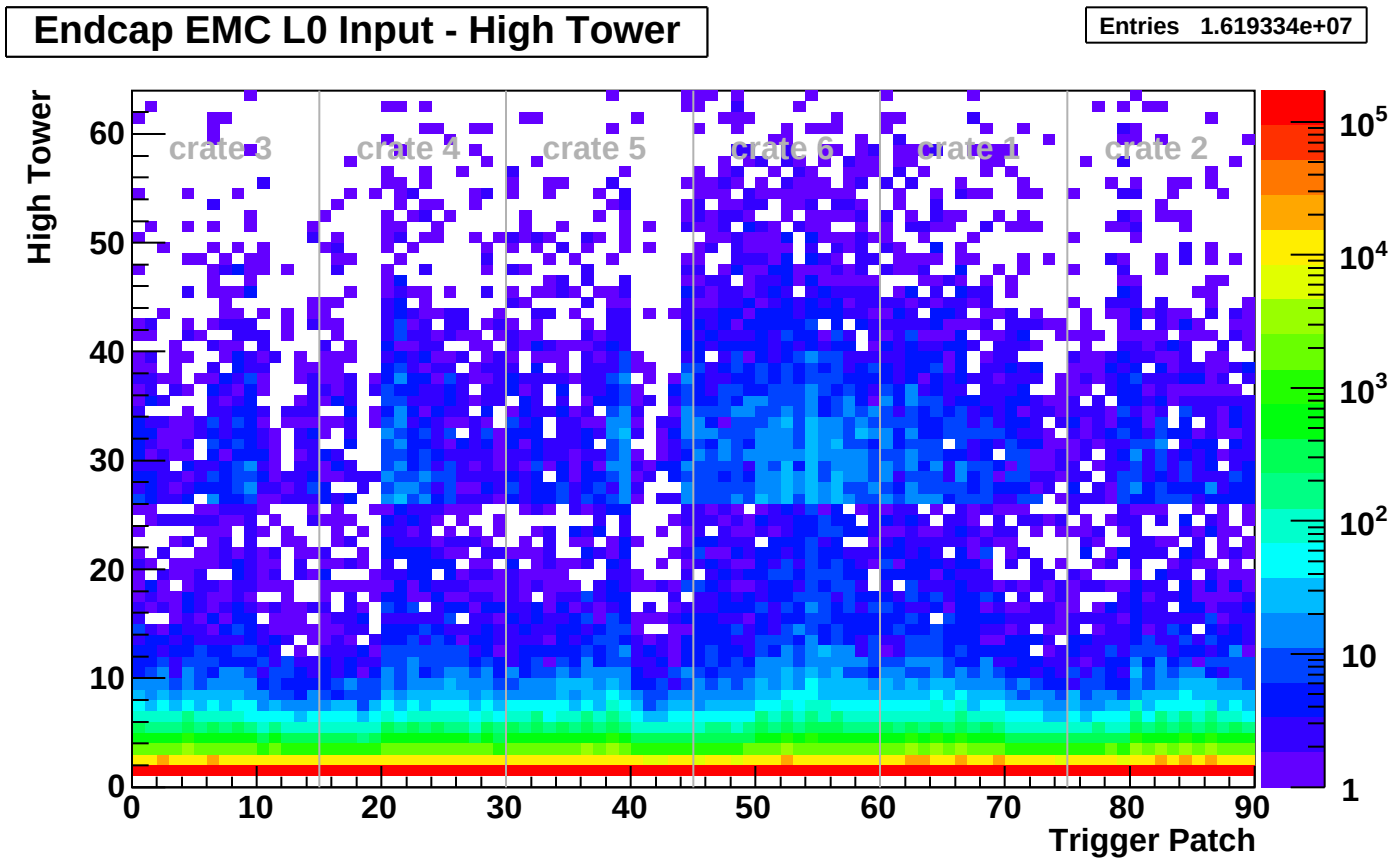

TCU bits
Entries 237374
TCU bits - Simulated
16544 40398 57339 7583 6262 3188 105818 242
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

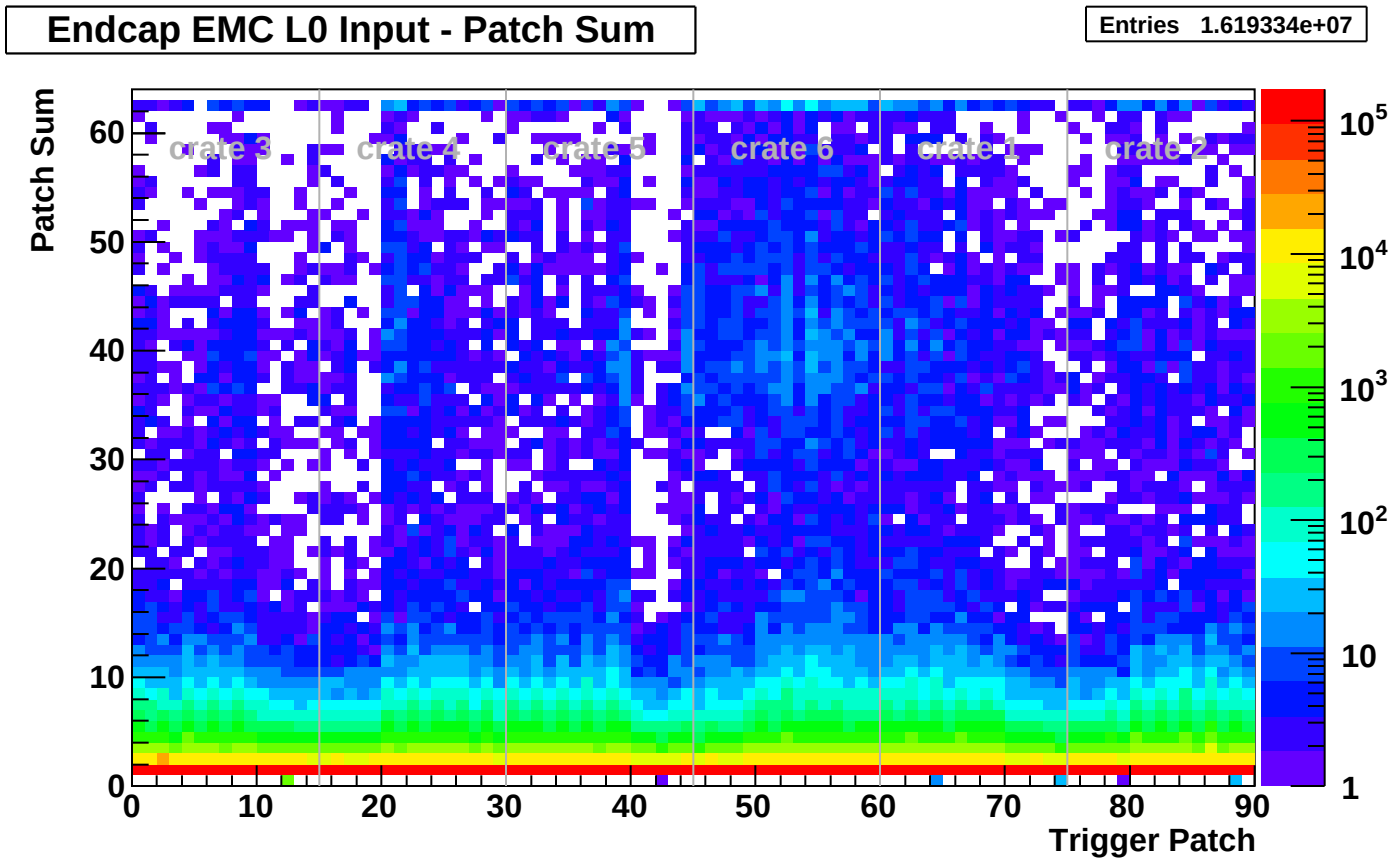

BunchId7Bit (BHT3)
Entries 3914

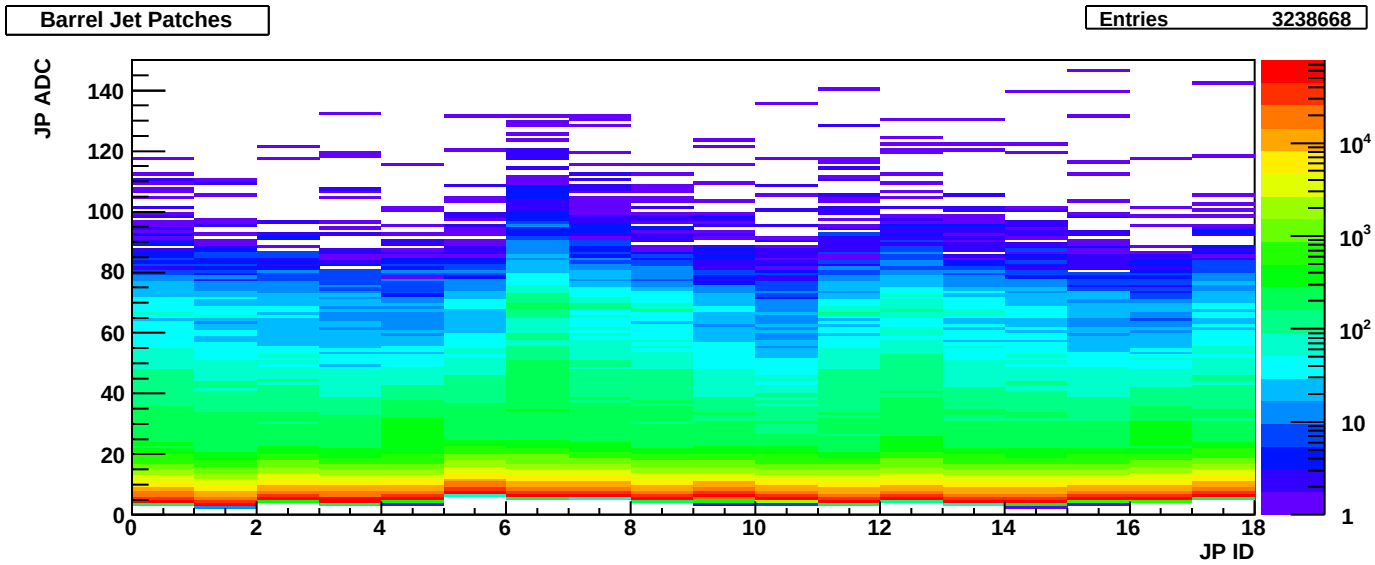

BunchId7Bit (all events)
Entries 179926

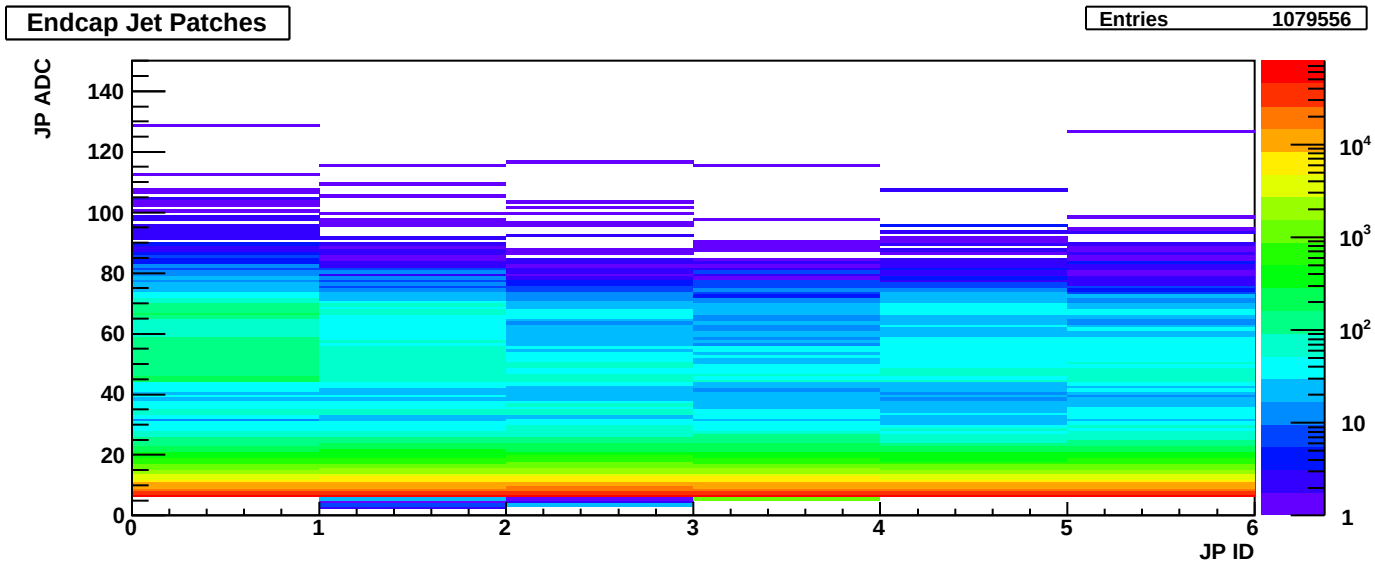

Barrel EMC L0 Input - High Tower
Entries 5.39778e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

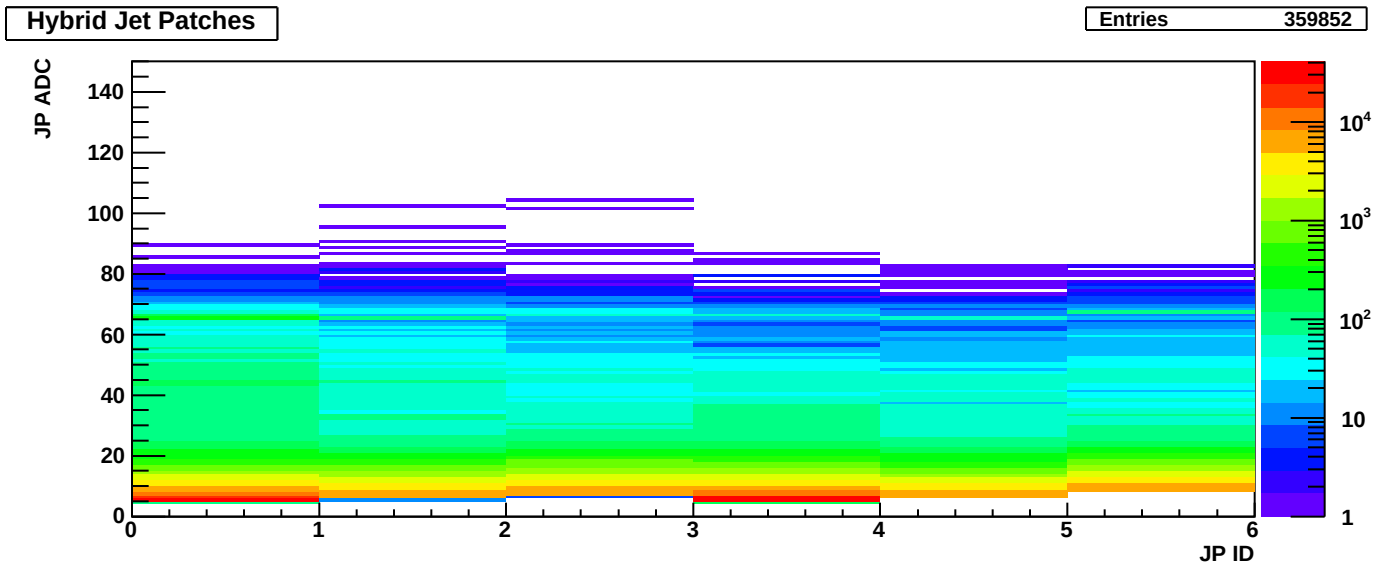

Barrel EMC L0 Input - Patch Sum
Entries 5.39778e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

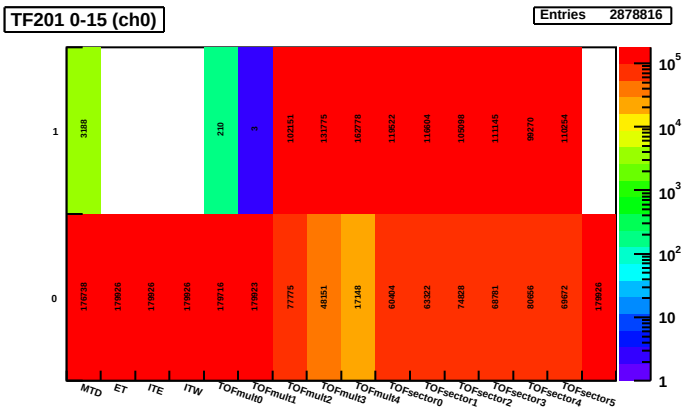

Endcap EMC L0 Input - High Tower
Entries 1.619334e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

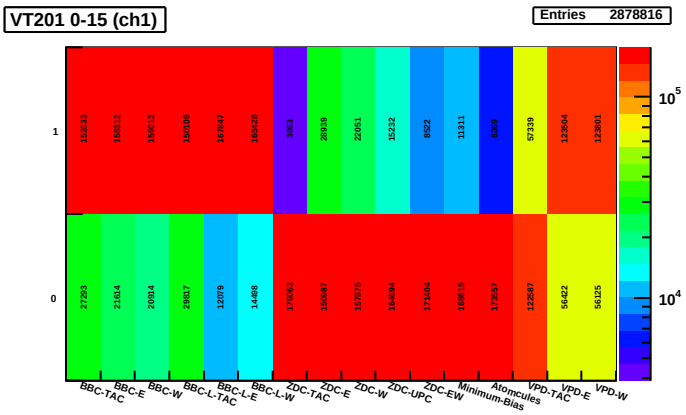

Endcap EMC L0 Input - Patch Sum
Entries 1.619334e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

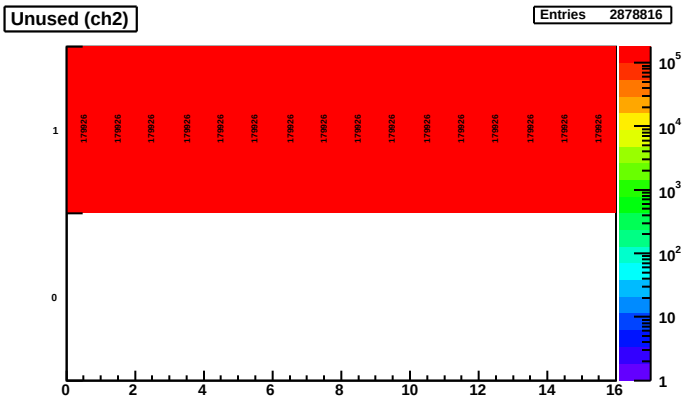

Barrel Jet Patches
Entries 3238668
JP ADC
JP ID

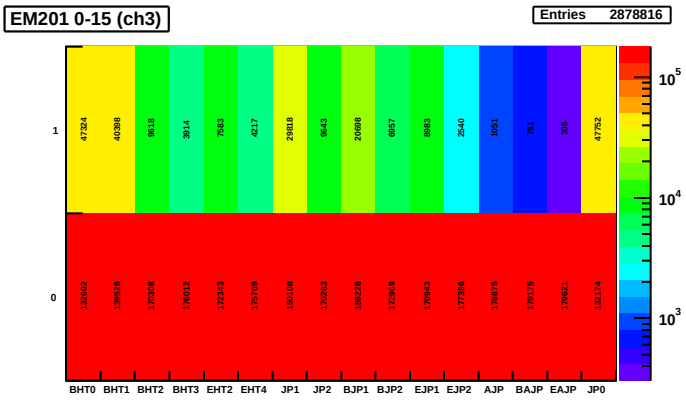

Endcap Jet Patches
Entries 1079556
JP ADC
JP ID

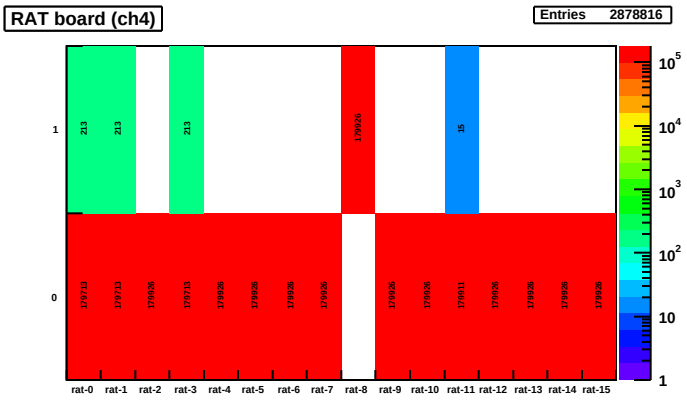

Hybrid Jet Patches
Entries 359852
JP ADC
JP ID

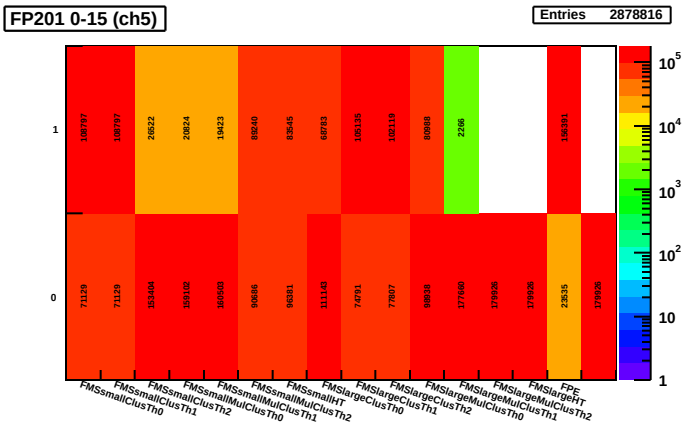

TF201 0-15 (ch0)
Entries 2878816
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

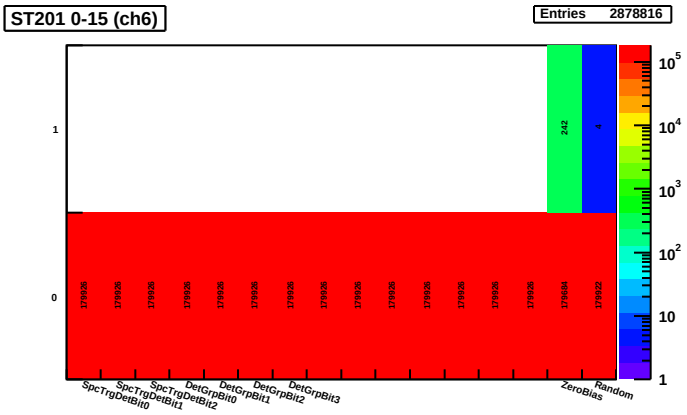

VT201 0-15 (ch1)
Entries 2878816
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

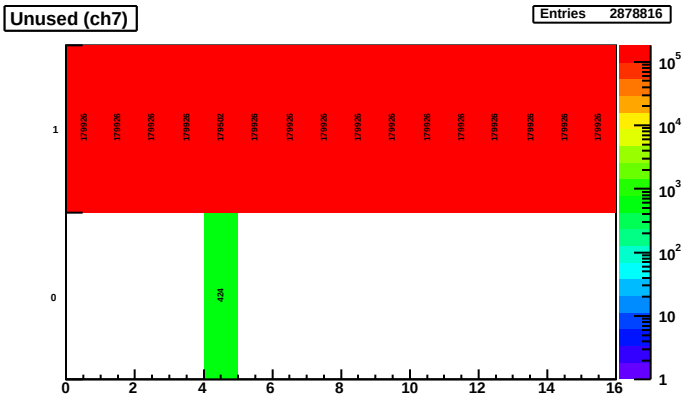

Unused (ch2)
Entries 2878816

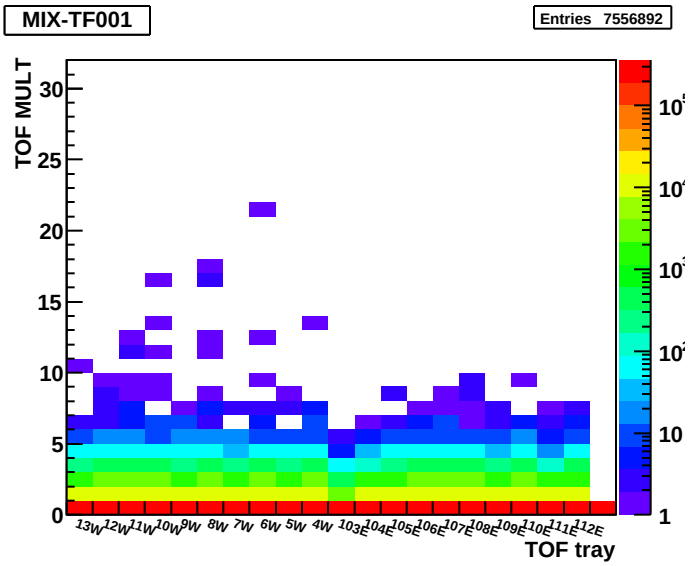

EM201 0-15 (ch3)
Entries 2878816
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

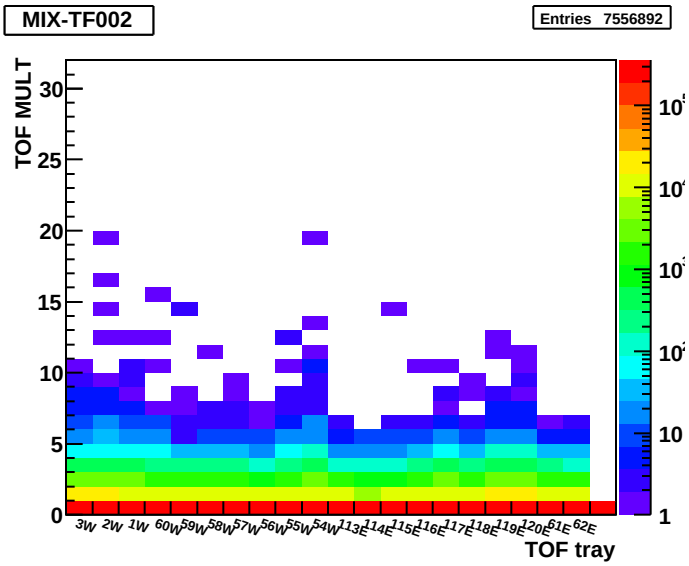

RAT board (ch4)
Entries 2878816
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

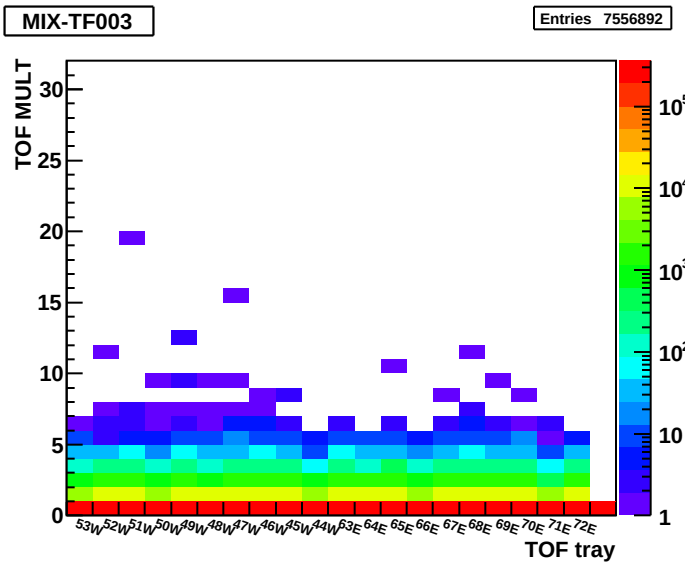

FP201 0-15 (ch5)
Entries 2878816
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

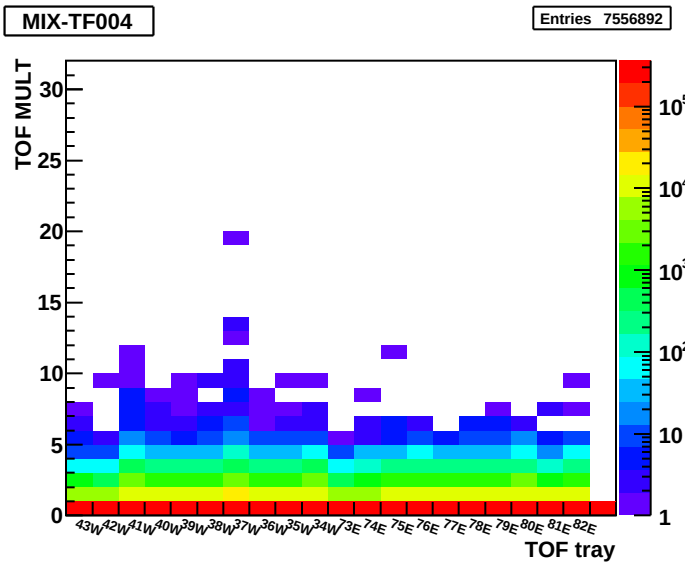

ST201 0-15 (ch6)
Entries 2878816
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

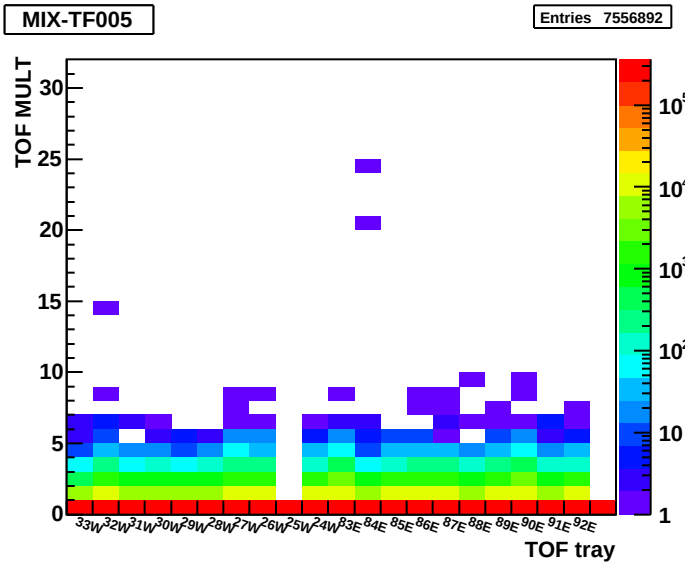

Unused (ch7)
Entries 2878816

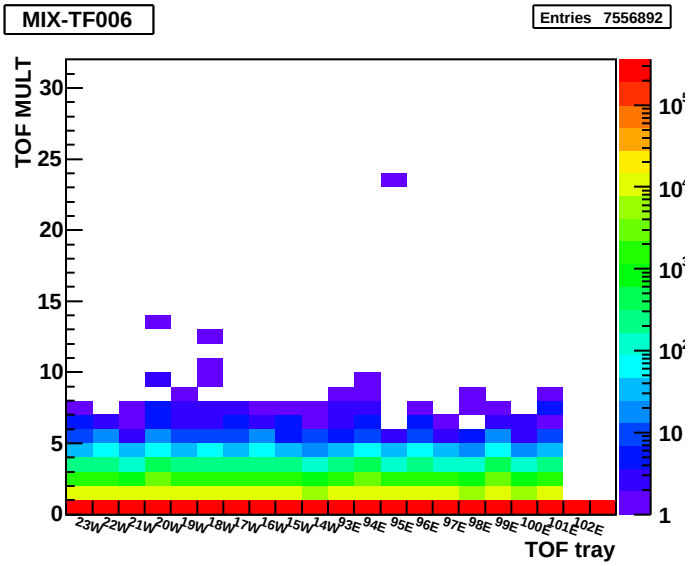

MIX-TF001
Entries 7556892
TOF MULT
TOF tray

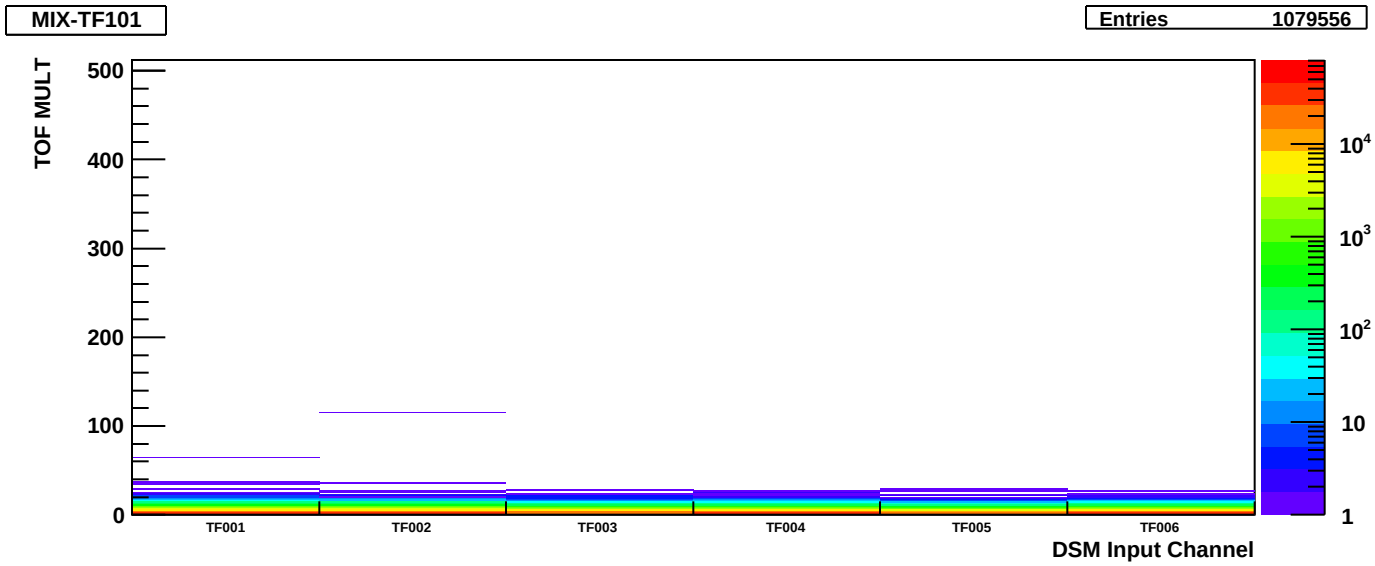

MIX-TF002
Entries 7556892
TOF MULT
TOF tray

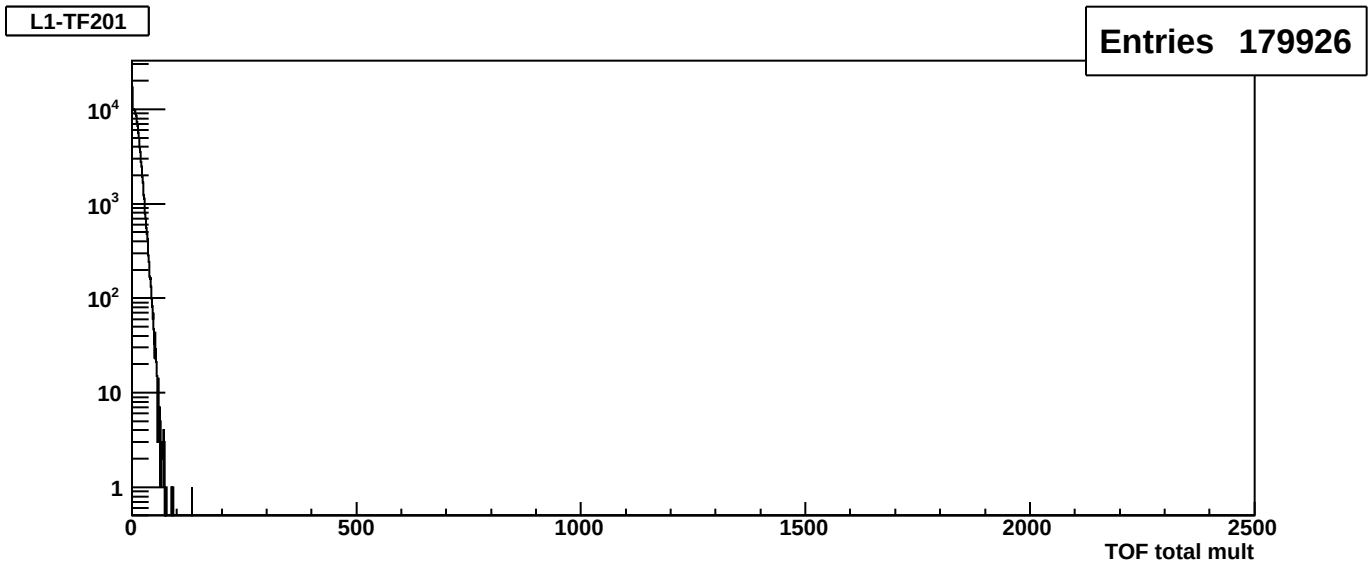

MIX-TF003
Entries 7556892
TOF MULT
TOF tray

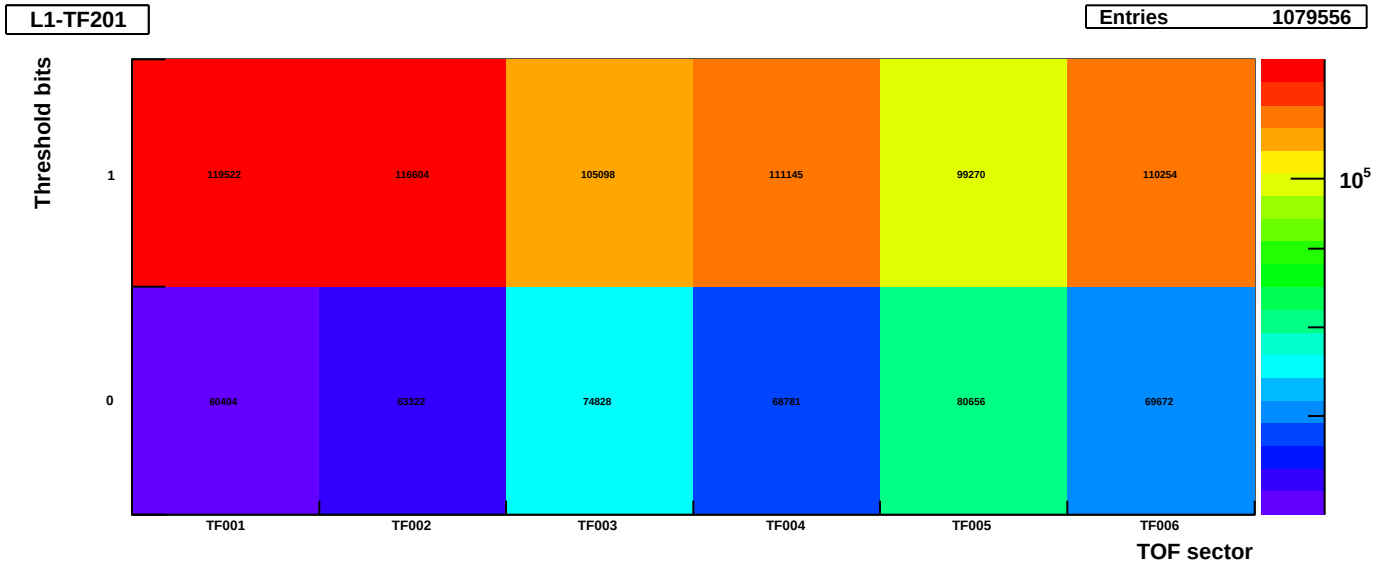

MIX-TF004
Entries 7556892
TOF MULT
TOF tray

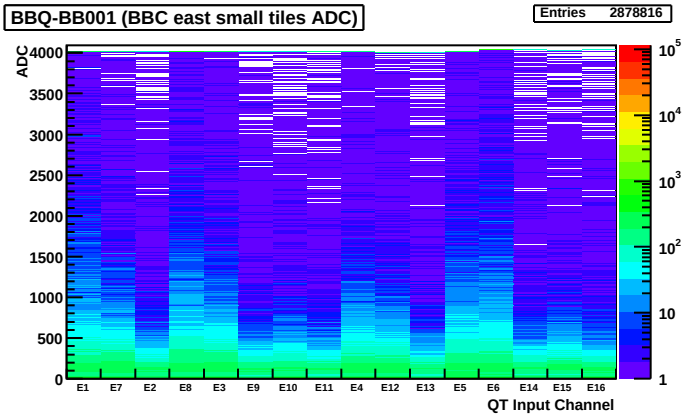

MIX-TF005
Entries 7556892
TOF MULT
TOF tray

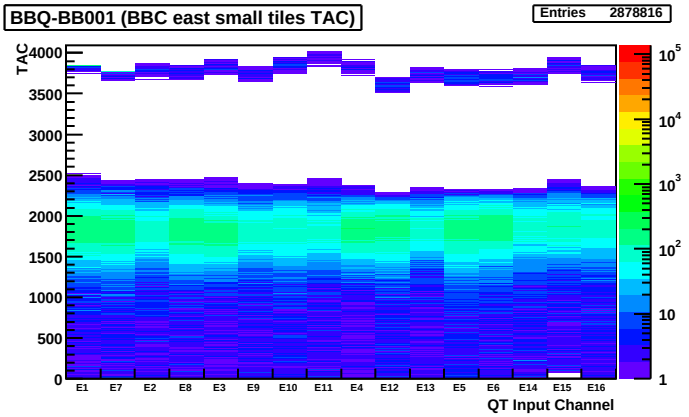

MIX-TF006
Entries 7556892
TOF MULT
TOF tray

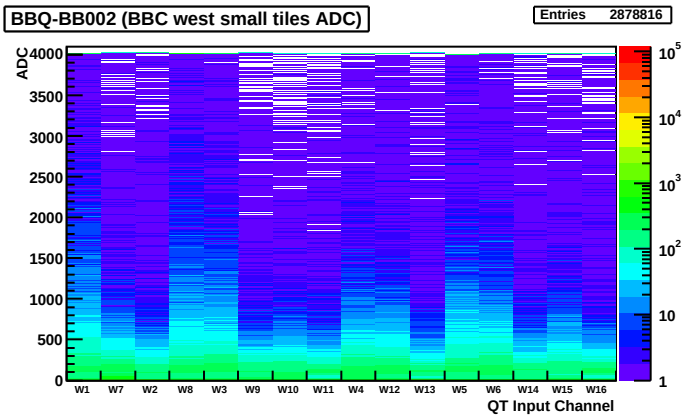

MIX-TF101
Entries 1079556
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

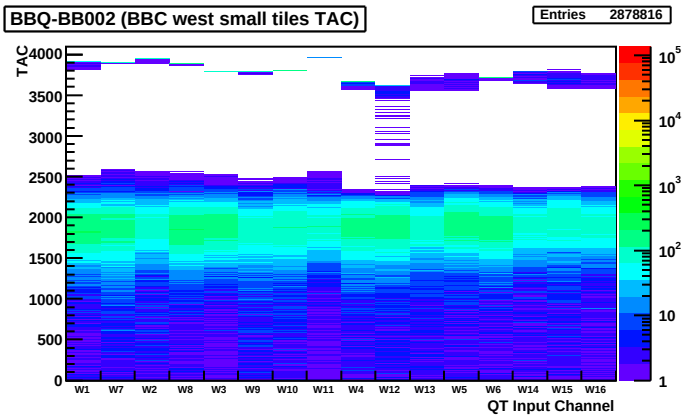

L1-TF201
Entries 179926
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

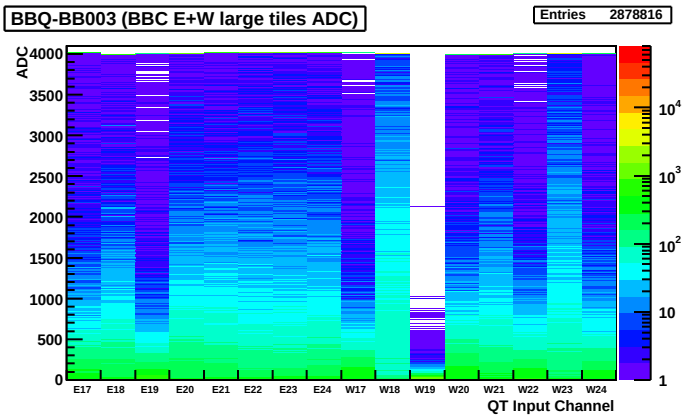

L1-TF201
Entries 1079556
Threshold bits
1
0
119522
116604
105098
111145
99270
110254
60404
63322
74828
68781
80656
69672
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

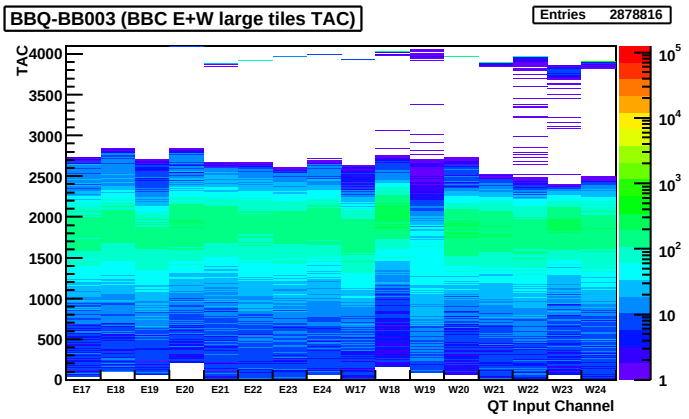

BBQ-BB001 (BBC east small tiles ADC)
Entries 2878816
ADC
QT Input Channel

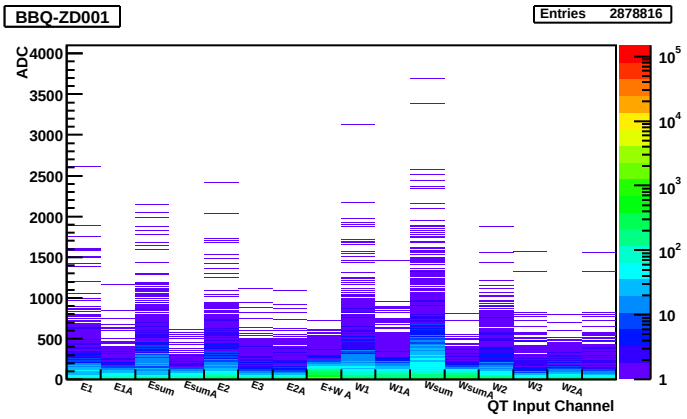

BBQ-BB001 (BBC east small tiles TAC)
Entries 2878816
TAC
QT Input Channel

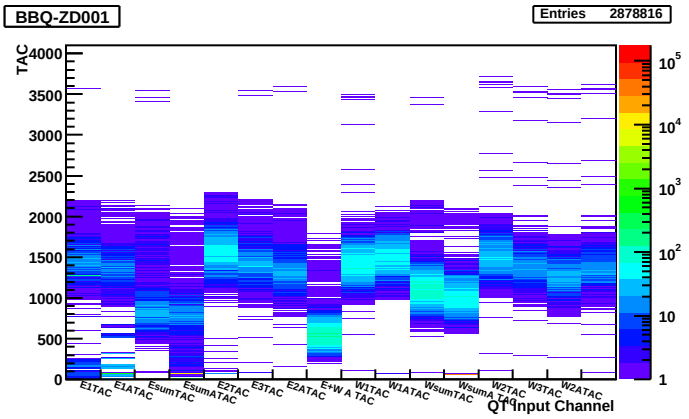

BBQ-BB002 (BBC west small tiles ADC)
Entries 2878816
ADC
QT Input Channel

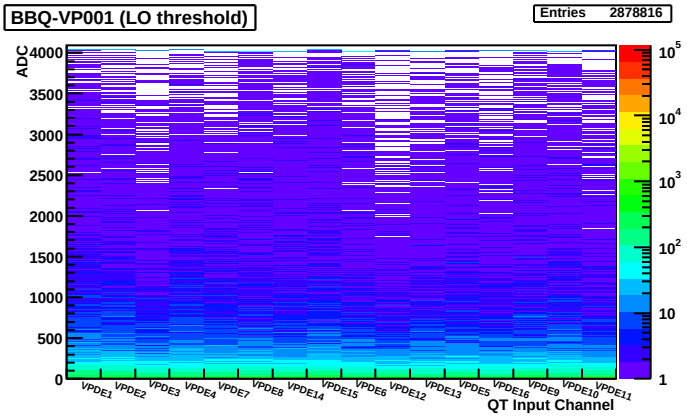

BBQ-BB002 (BBC west small tiles TAC)
Entries 2878816
TAC
QT Input Channel

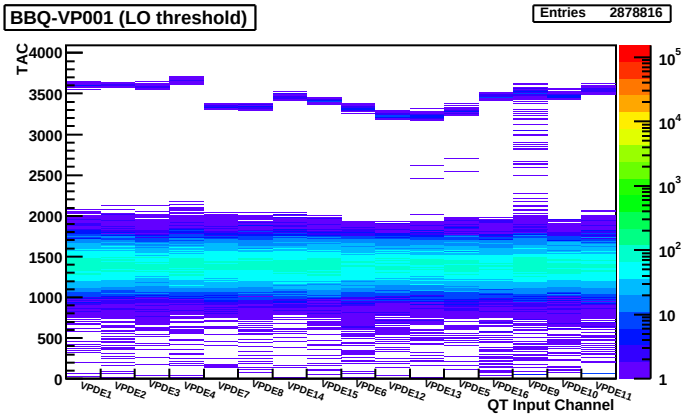

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2878816
ADC
QT Input Channel

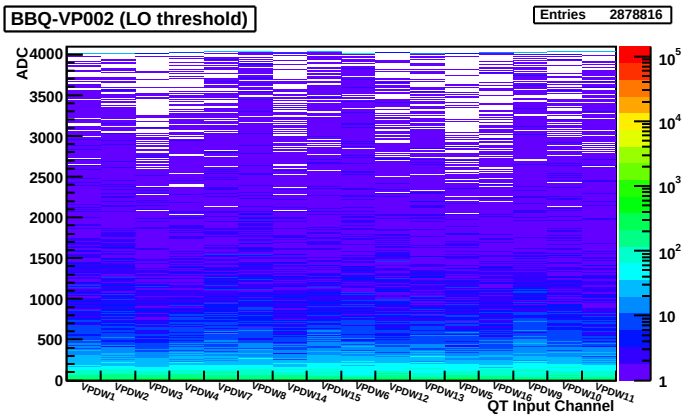

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2878816
TAC
QT Input Channel

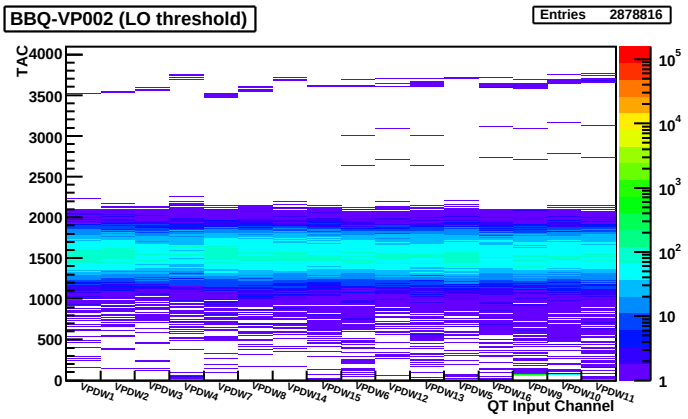

BBQ-ZD001
Entries 2878816
ADC
QT Input Channel

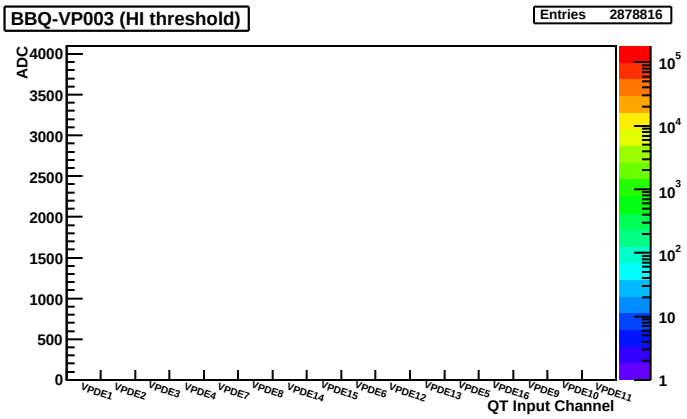

BBQ-ZD001
Entries 2878816
TAC
QT Input Channel

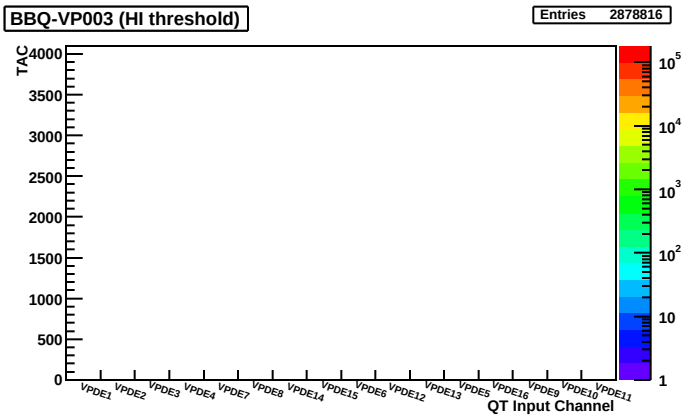

BBQ-VP001 (LO threshold)
Entries 2878816
ADC
QT Input Channel

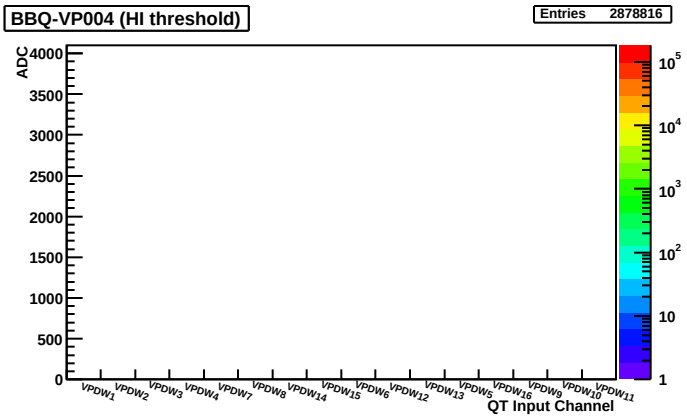

BBQ-VP001 (LO threshold)
Entries 2878816
TAC
QT Input Channel

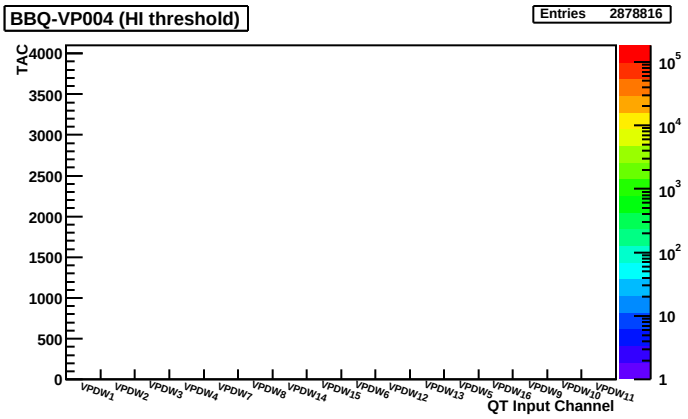

BBQ-VP002 (LO threshold)
Entries 2878816
ADC
QT Input Channel

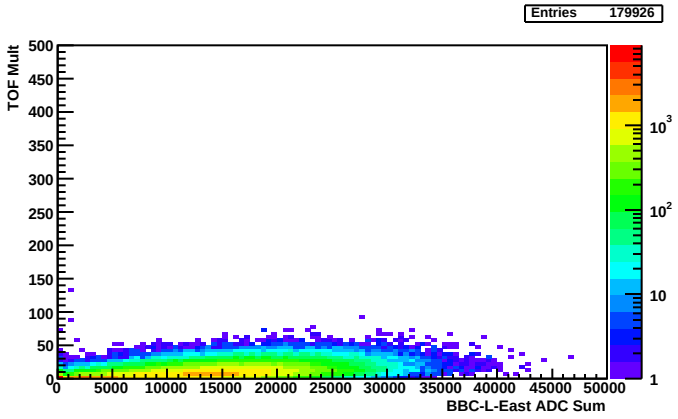

BBQ-VP002 (LO threshold)
Entries 2878816
TAC
QT Input Channel

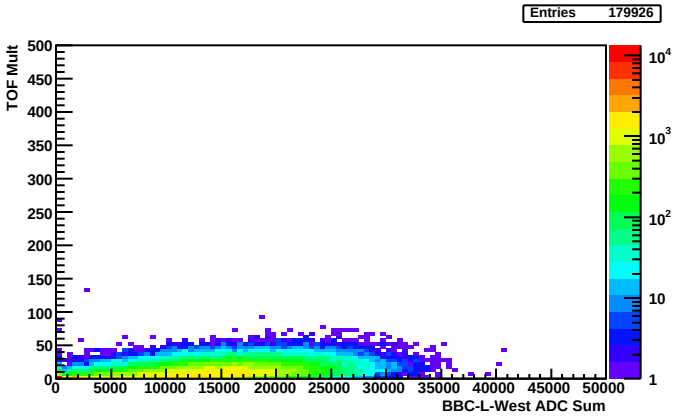

BBQ-VP003 (HI threshold)
Entries 2878816
ADC
QT Input Channel

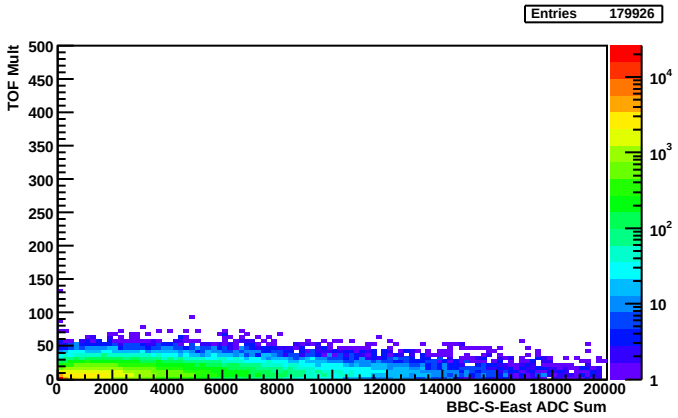

BBQ-VP003 (HI threshold)
Entries 2878816
TAC
QT Input Channel

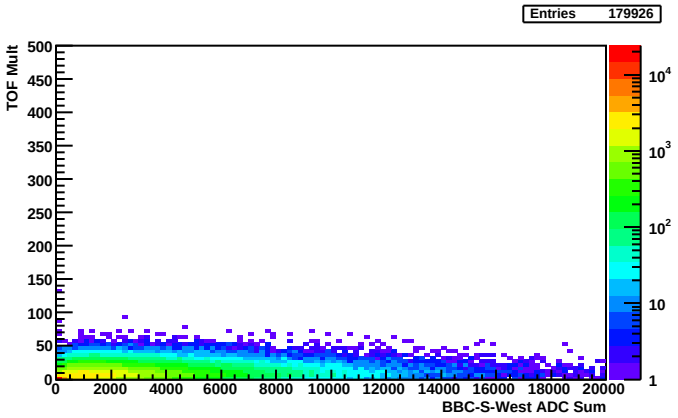

BBQ-VP004 (HI threshold)
Entries 2878816
ADC
QT Input Channel

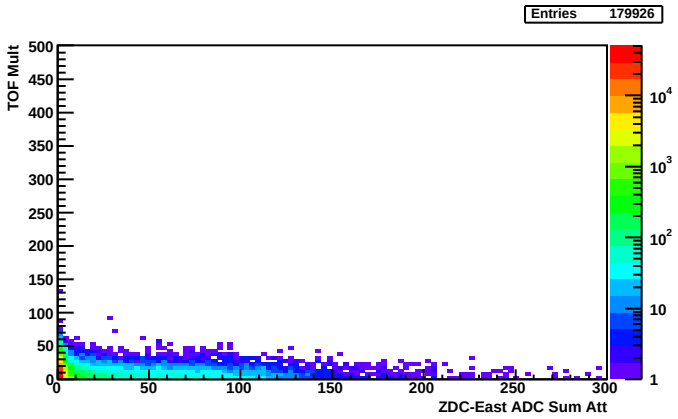

BBQ-VP004 (HI threshold)
Entries 2878816
TAC
QT Input Channel

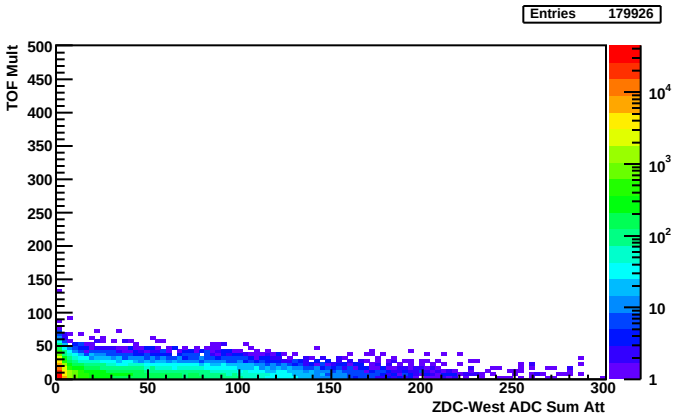

Entries 179926
TOF Mult
BBC-L-East ADC Sum

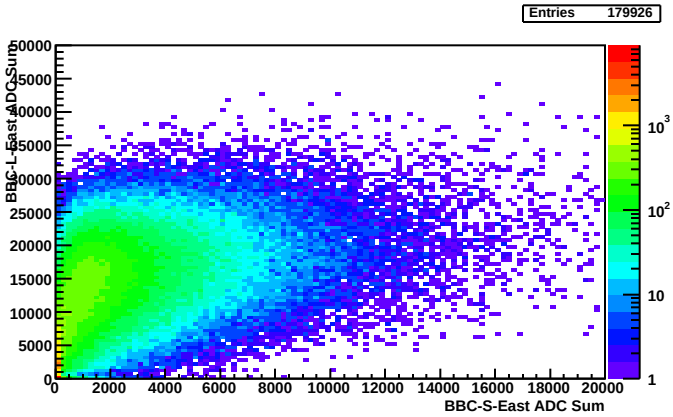

Entries 179926
TOF Mult
BBC-L-West ADC Sum

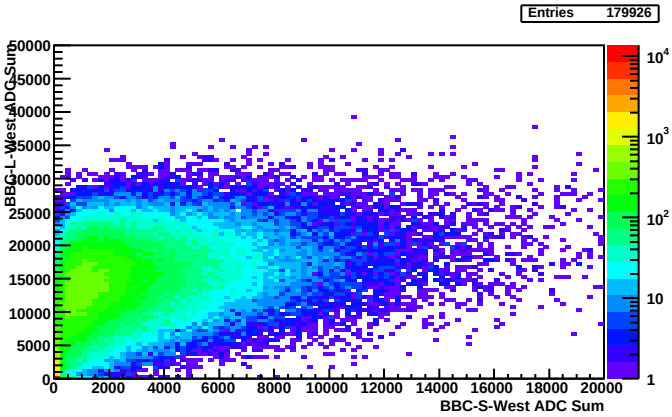

Entries 179926
TOF Mult
BBC-S-East ADC Sum

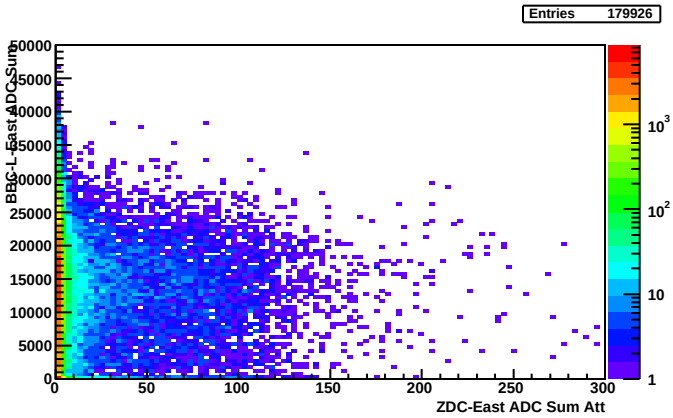

Entries 179926
TOF Mult
BBC-S-West ADC Sum

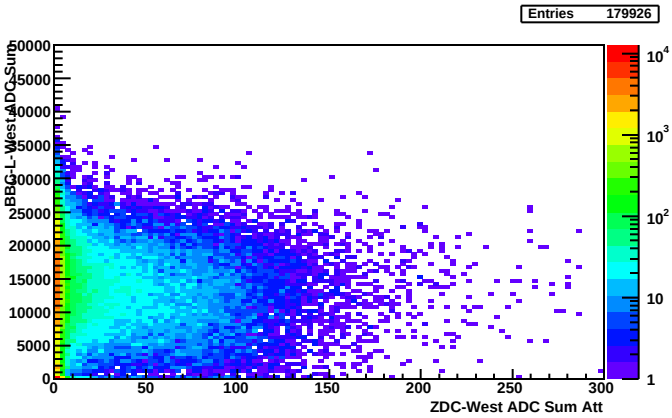

Entries 179926
TOF Mult
ZDC-East ADC Sum Att

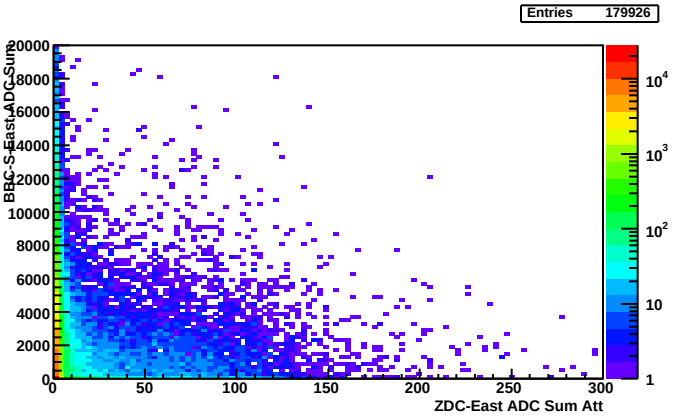

Entries 179926
TOF Mult
ZDC-West ADC Sum Att

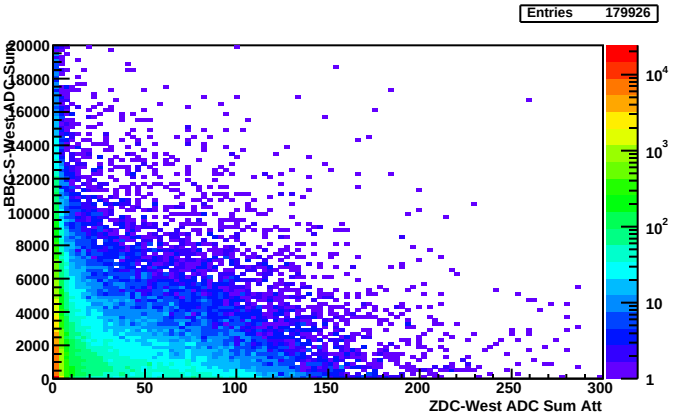

Entries 179926
BBC-L-East ADC Sum
BBC-S-East ADC Sum

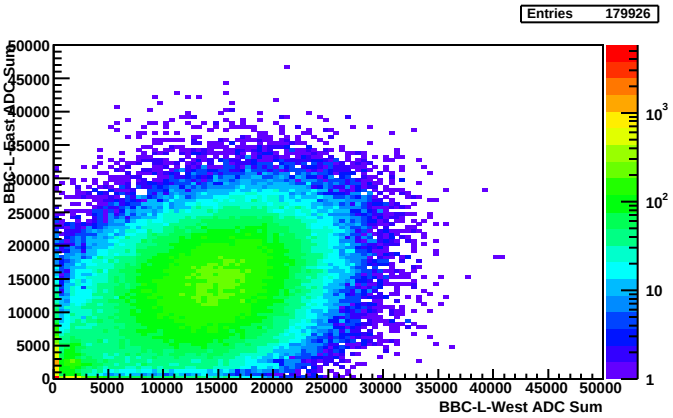

Entries 179926
BBC-L-West ADC Sum
BBC-S-West ADC Sum

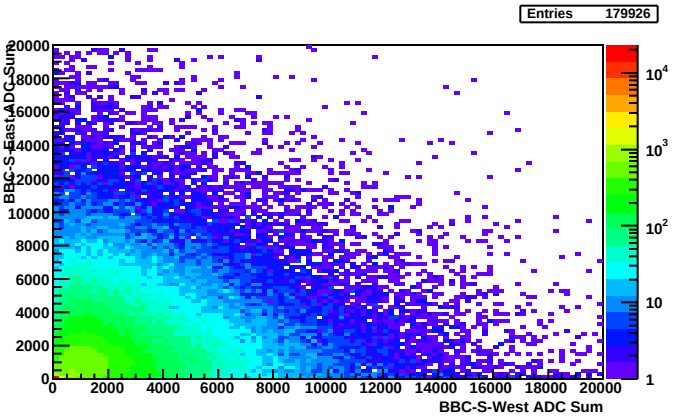

Entries 179926
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

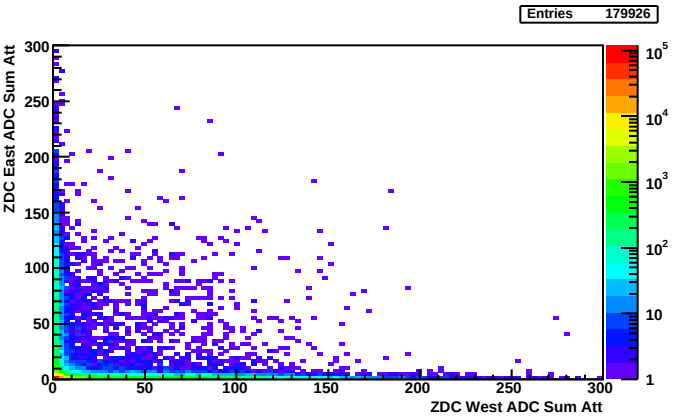

Entries 179926
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

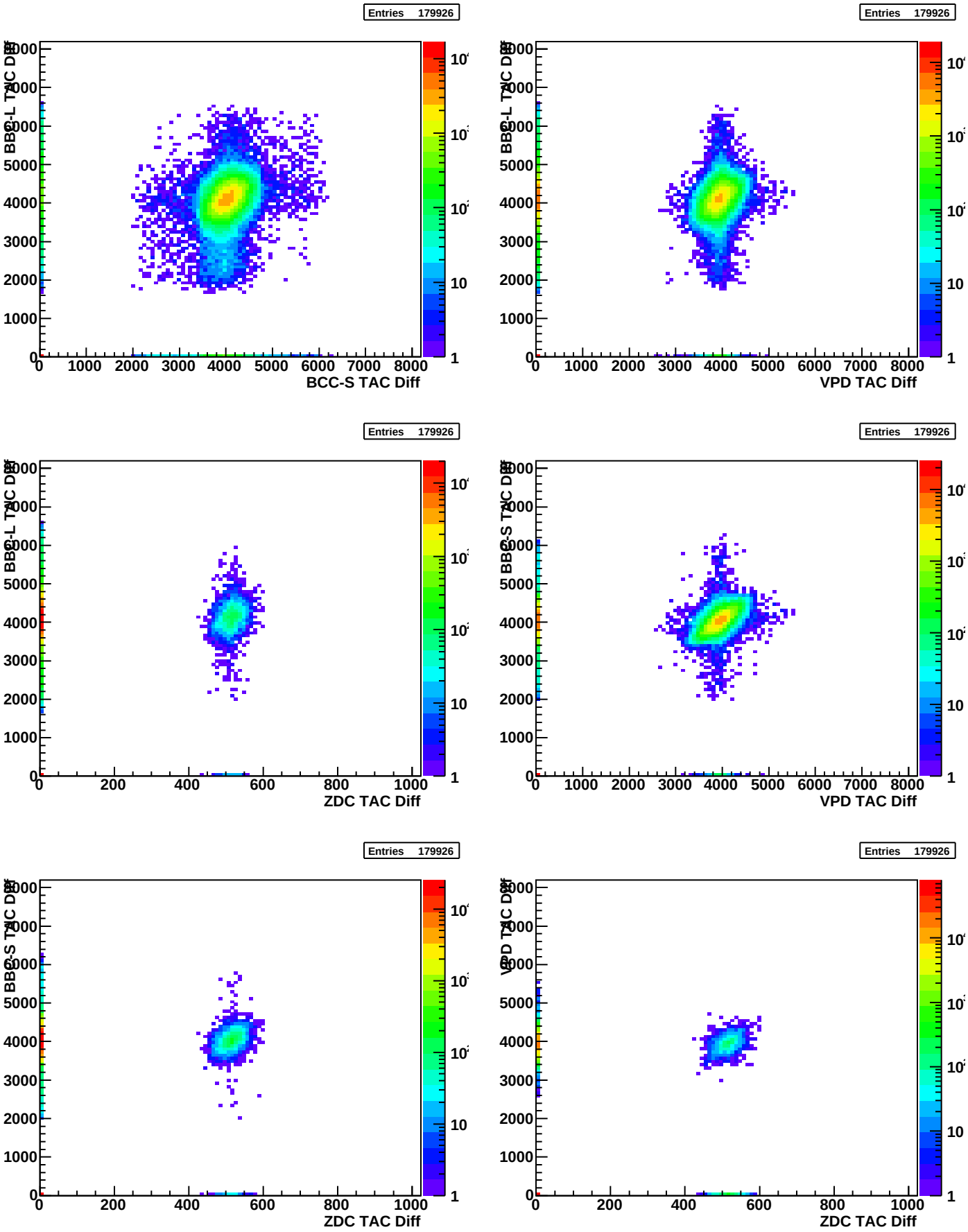

Entries 179926
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

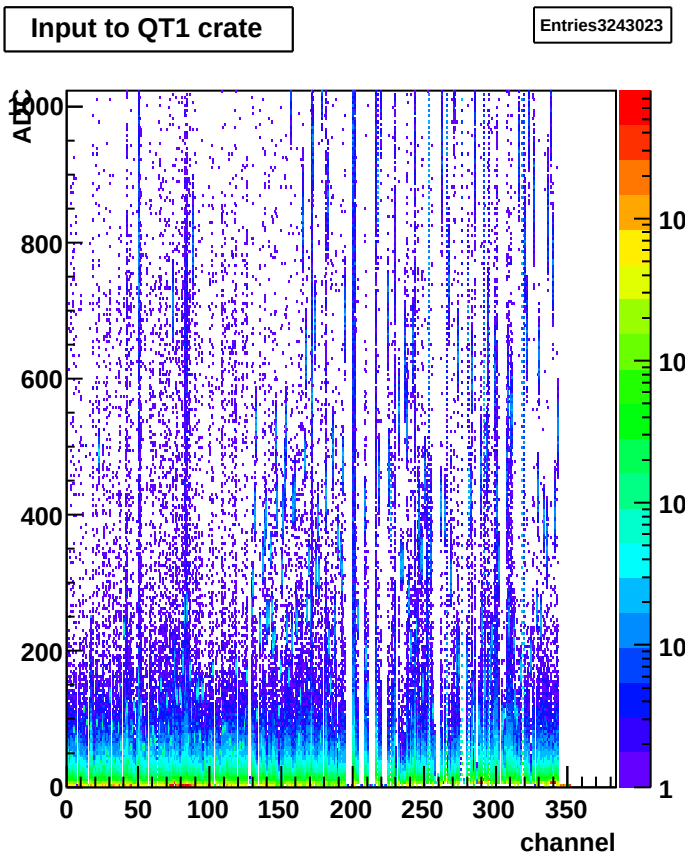

Entries 179926
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

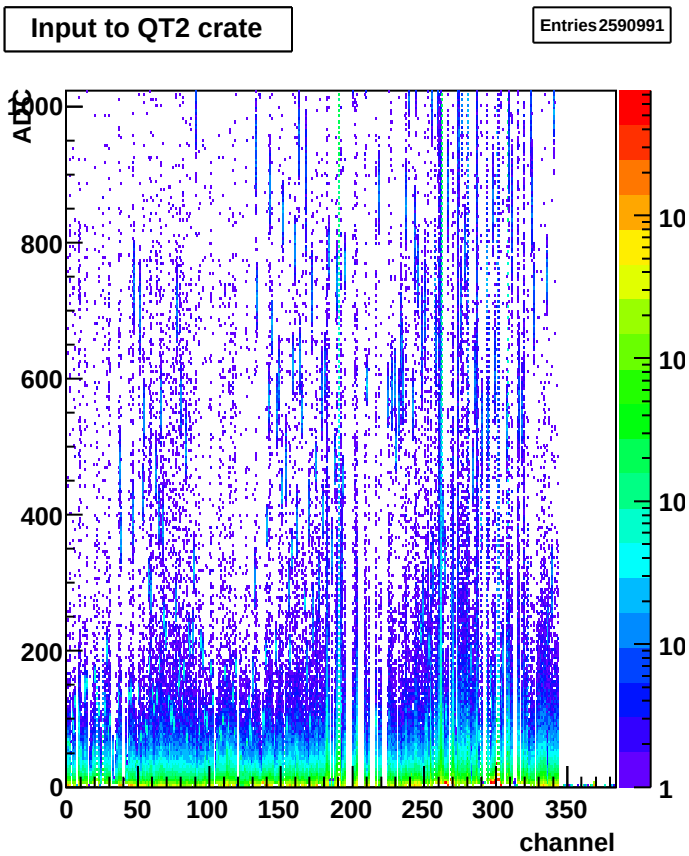

Entries 179926
BBC-L-East ADC Sum
BBC-L-West ADC Sum

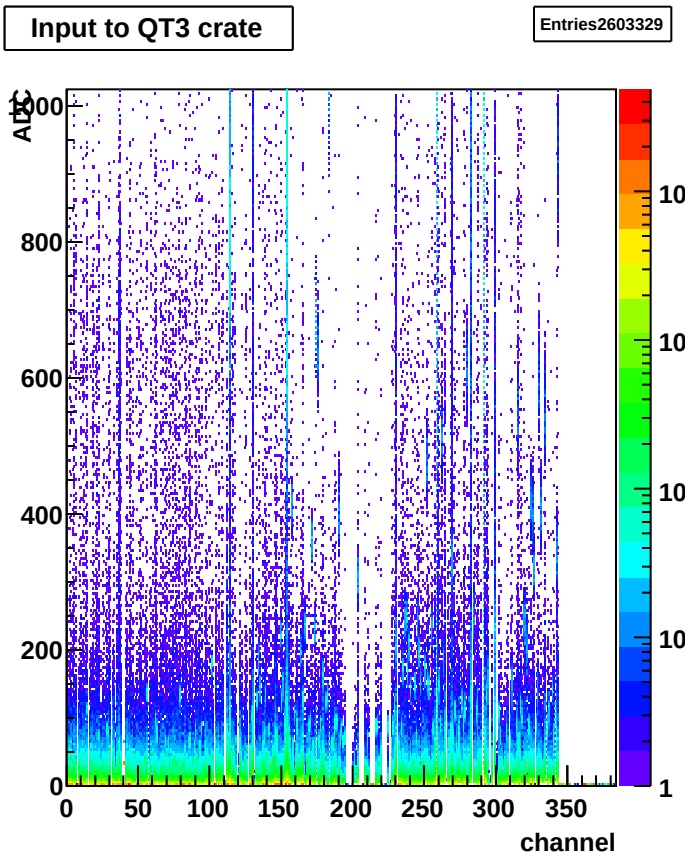

Entries 179926
BBC-S-East ADC Sum
BBC-S-West ADC Sum

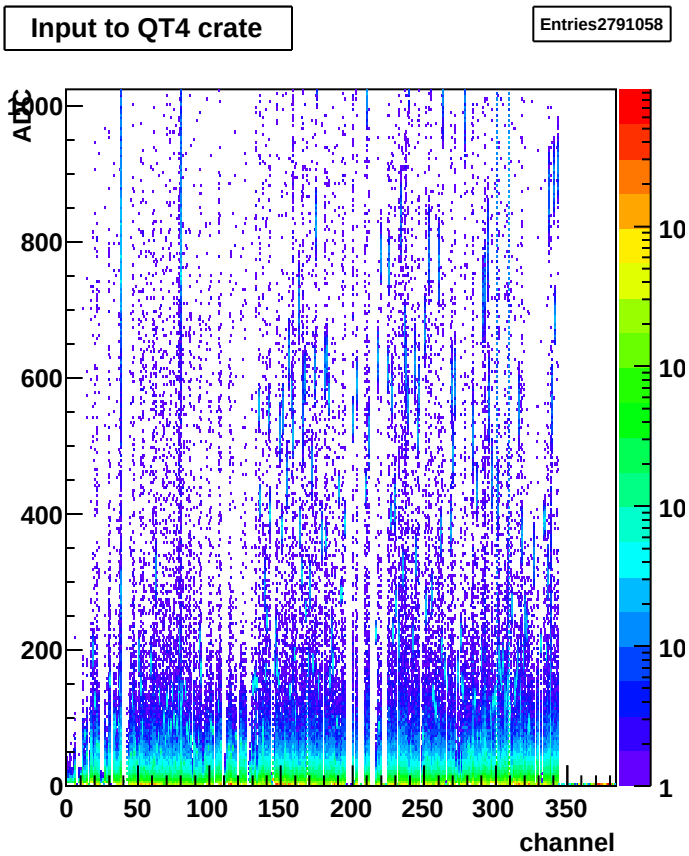

Entries 179926
ZDC East ADC Sum Att
ZDC West ADC Sum Att

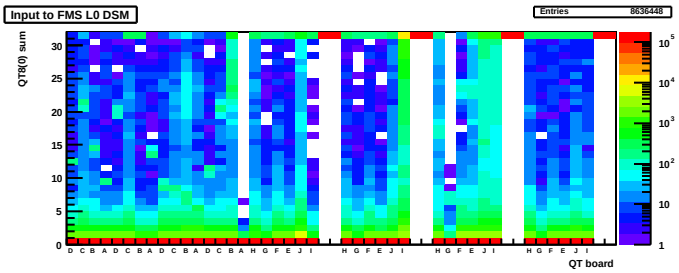

Entries 179926
BBC-L TAC Diff
BCC-S TAC Diff
Entries 179926
BBC-L TAC Diff
VPD TAC Diff
Entries 179926
BBC-L TAC Diff
ZDC TAC Diff
Entries 179926
BBC-S TAC Diff
VPD TAC Diff
Entries 179926
BBC-S TAC Diff
ZDC TAC Diff
Entries 179926
VPD TAC Diff
ZDC TAC Diff

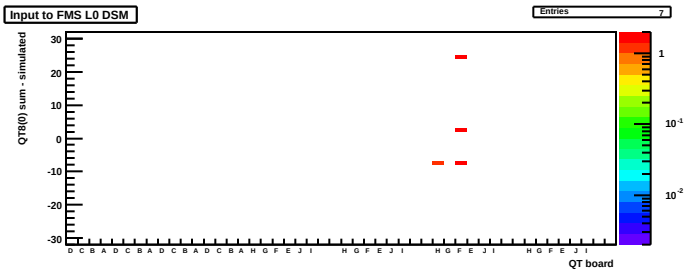

Input to QT1 crate
Entries3243023
ADC
channel

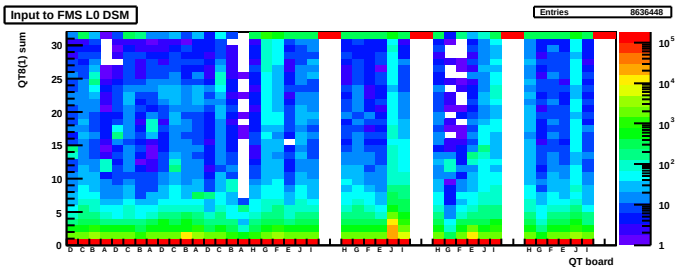

Input to QT2 crate
Entries2590991
ADC
channel

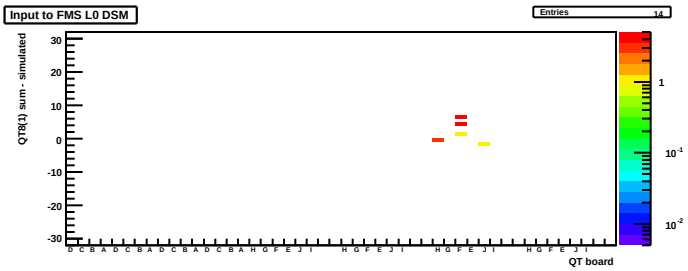

Input to QT3 crate
Entries2603329
ADC
channel

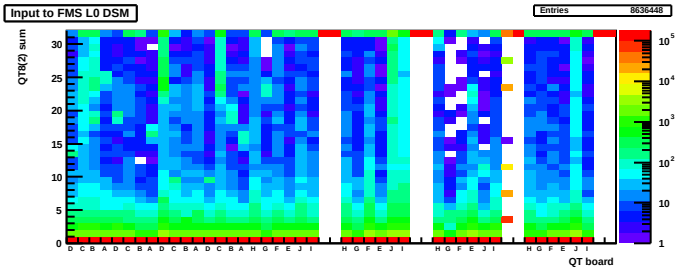

Input to QT4 crate
Entries2791058
ADC
channel

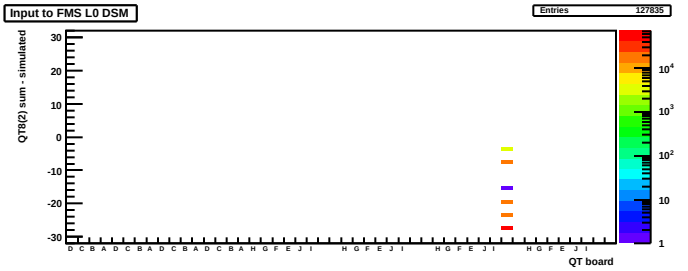

Input to FMS L0 DSM
Entries 8636448
QT8(0) sum
QT board

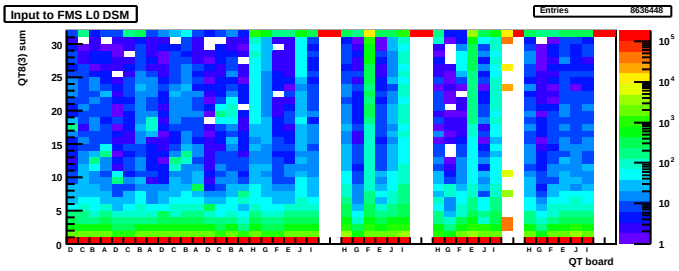

Input to FMS L0 DSM
Entries 7
QT8(0) sum - simulated
QT board

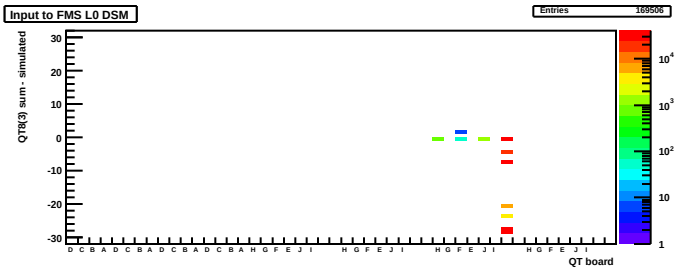

Input to FMS L0 DSM
Entries 8636448
QT8(1) sum
QT board

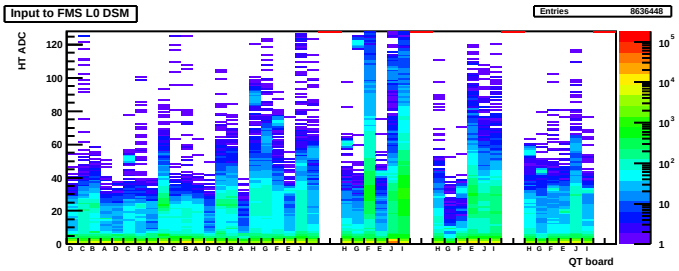

Input to FMS L0 DSM
Entries 14
QT8(1) sum - simulated
QT board

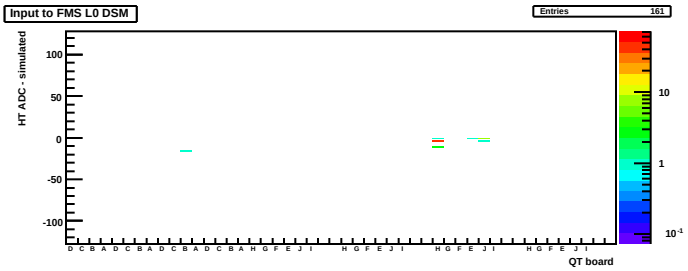

Input to FMS L0 DSM
Entries 8636448
QT8(2) sum
QT board

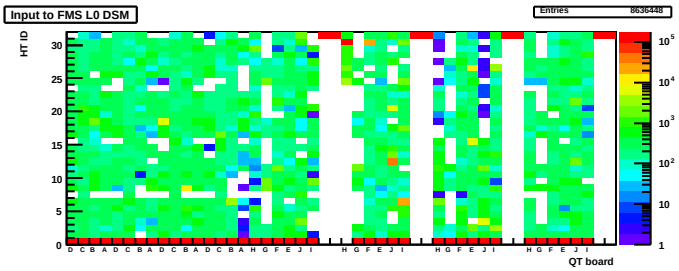

Input to FMS L0 DSM
Entries 127835
QT8(2) sum - simulated
QT board

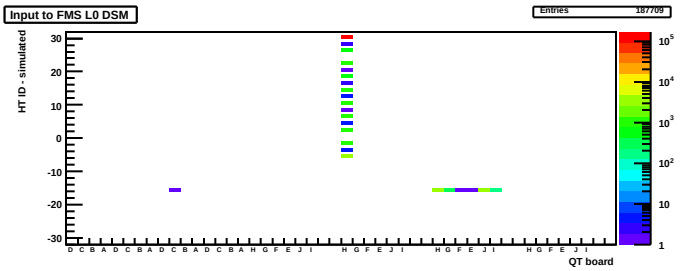

Input to FMS L0 DSM
Entries 8636448
QT8(3) sum
QT board

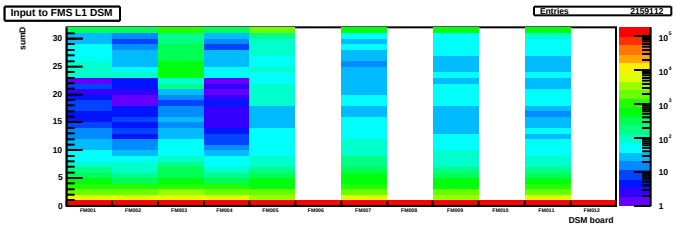

Input to FMS L0 DSM
Entries 169506
QT8(3) sum - simulated
QT board

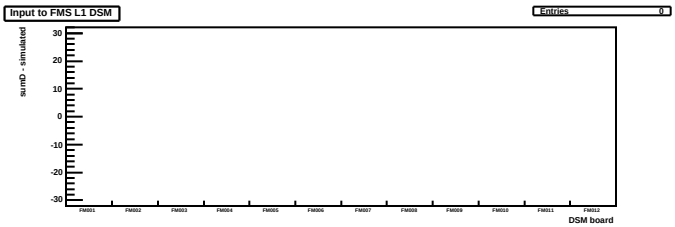

Input to FMS L0 DSM
Entries 8636448
HT ADC
QT board

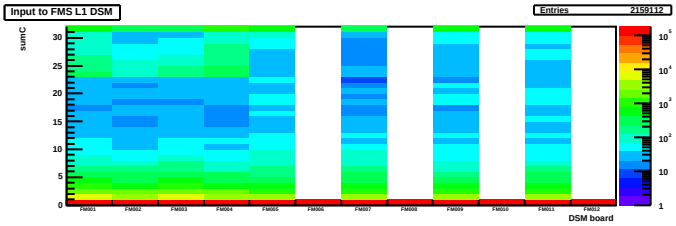

Input to FMS L0 DSM
Entries 161
HT ADC - simulated
QT board

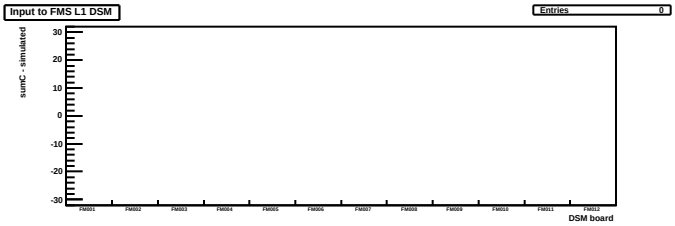

Input to FMS L0 DSM
Entries 8636448
HT ID
QT board

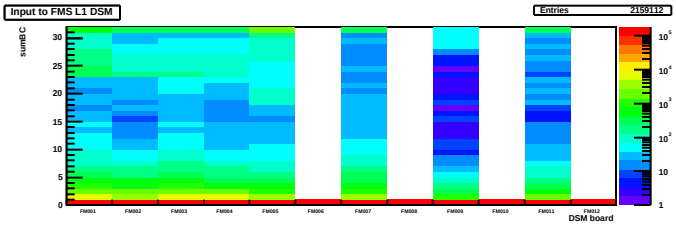

Input to FMS L0 DSM
Entries 187709
HT ID - simulated
QT board

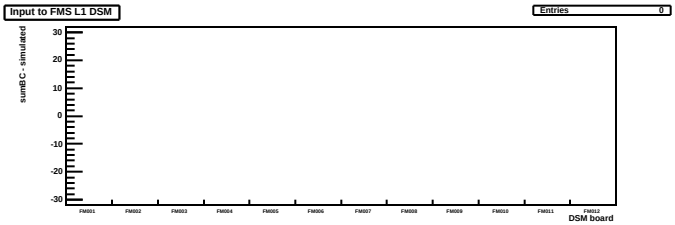

Input to FMS L1 DSM
Entries 2159112
sumD
DSM board

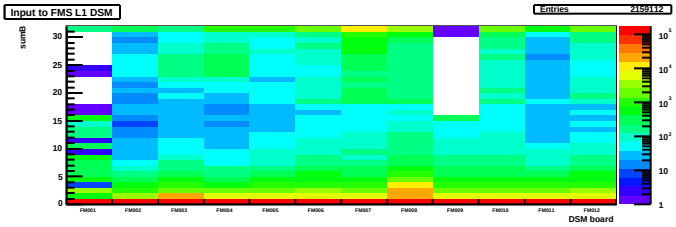

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

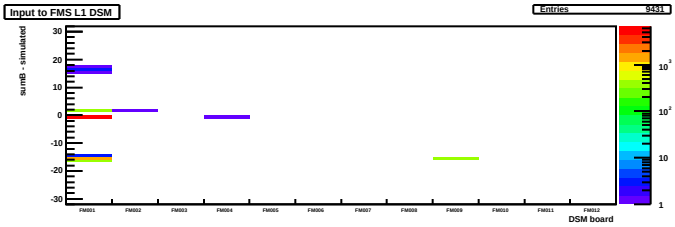

Input to FMS L1 DSM
Entries 2159112
sumC
DSM board

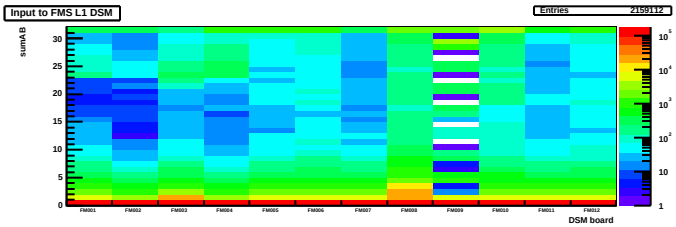

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

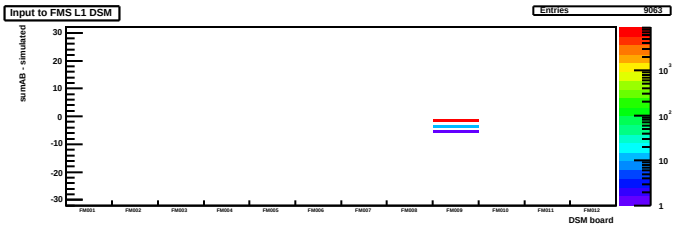

Input to FMS L1 DSM
Entries 2159112
sumBC
DSM board

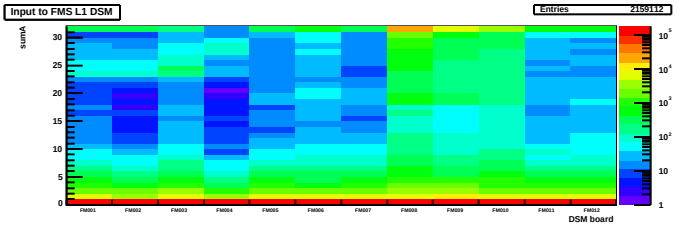

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

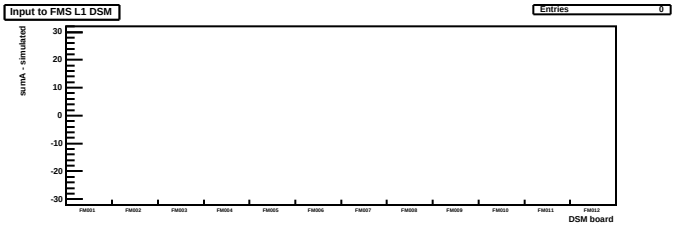

Input to FMS L1 DSM
Entries 2159112
sumB
DSM board

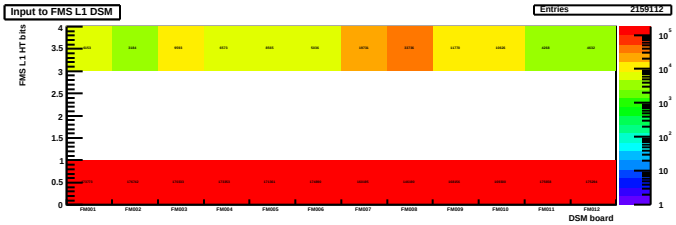

Input to FMS L1 DSM
Entries 9431
sumB - simulated
DSM board

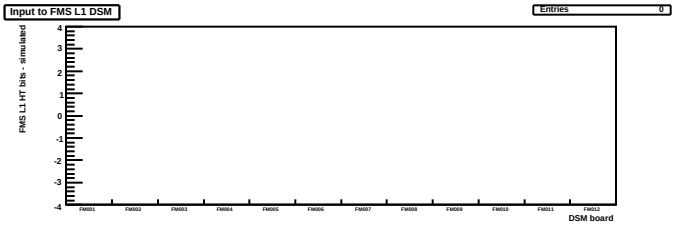

Input to FMS L1 DSM
Entries 2159112
sumAB
DSM board

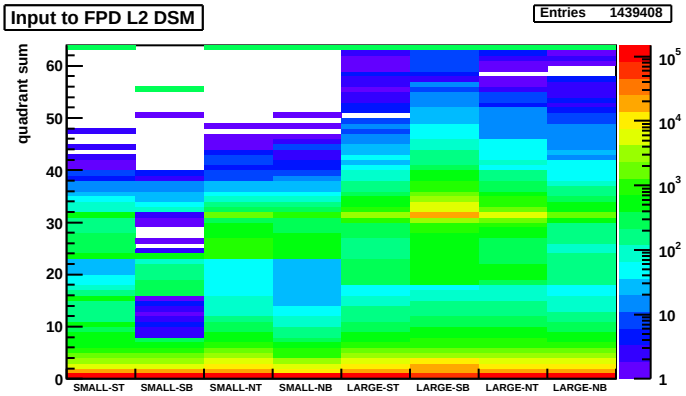

Input to FMS L1 DSM
Entries 9063
sumAB - simulated
DSM board

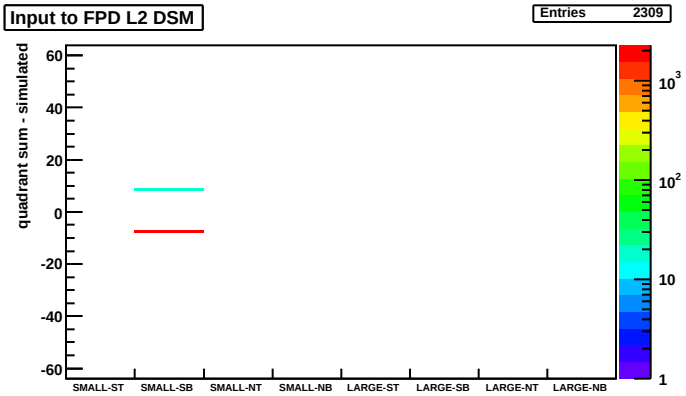

Input to FMS L1 DSM
Entries 2159112
sumA
DSM board

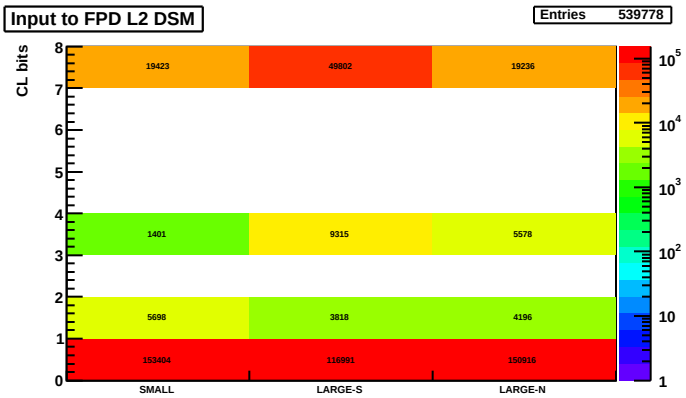

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

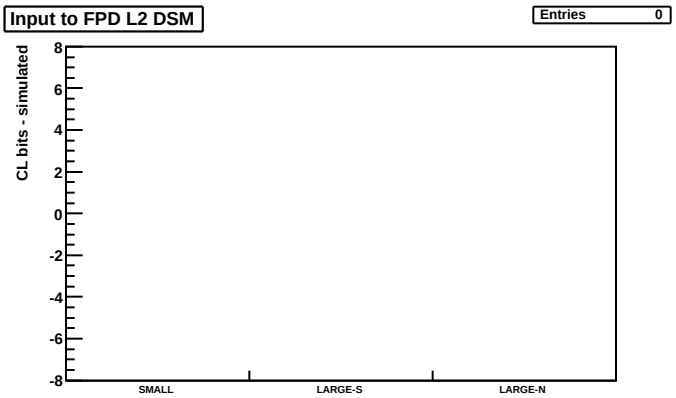

Input to FMS L1 DSM
Entries 2159112
FMS L1 HT bits
DSM board

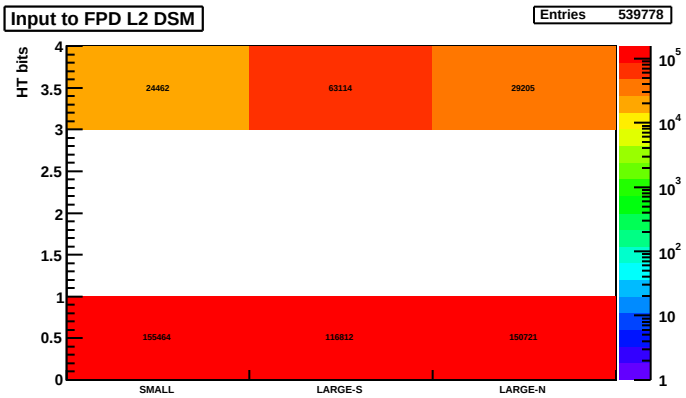

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

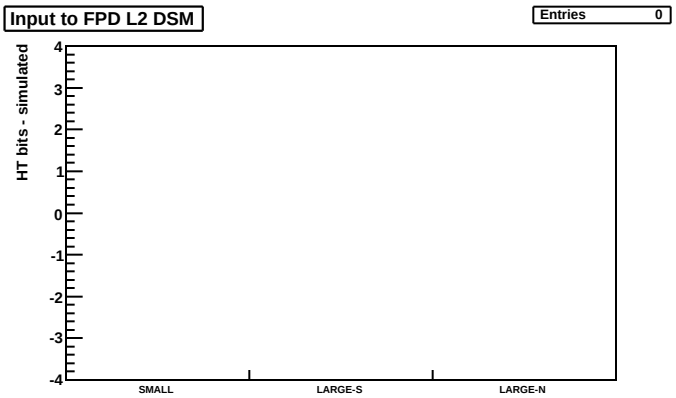

Input to FPD L2 DSM
Entries 1439408
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

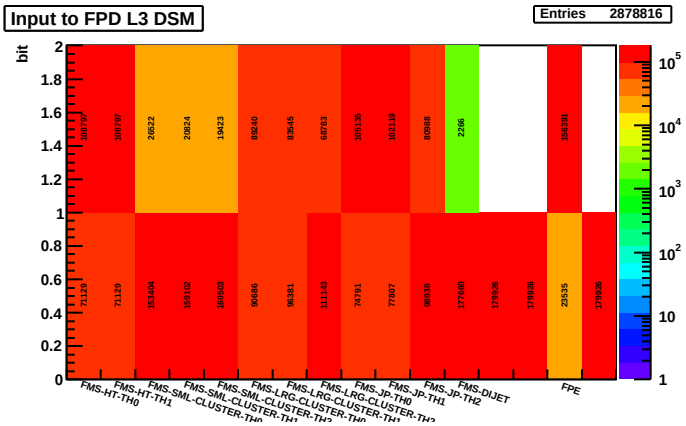

Input to FPD L2 DSM
Entries 2309
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

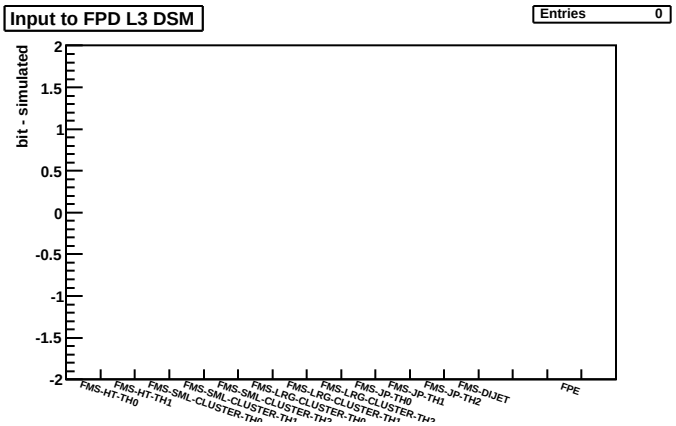

Input to FPD L2 DSM
Entries 539778
CL bits
19423
49802
19236
1401
9315
5578
5698
3818
4196
153404
116991
150916
SMALL
LARGE-S
LARGE-N

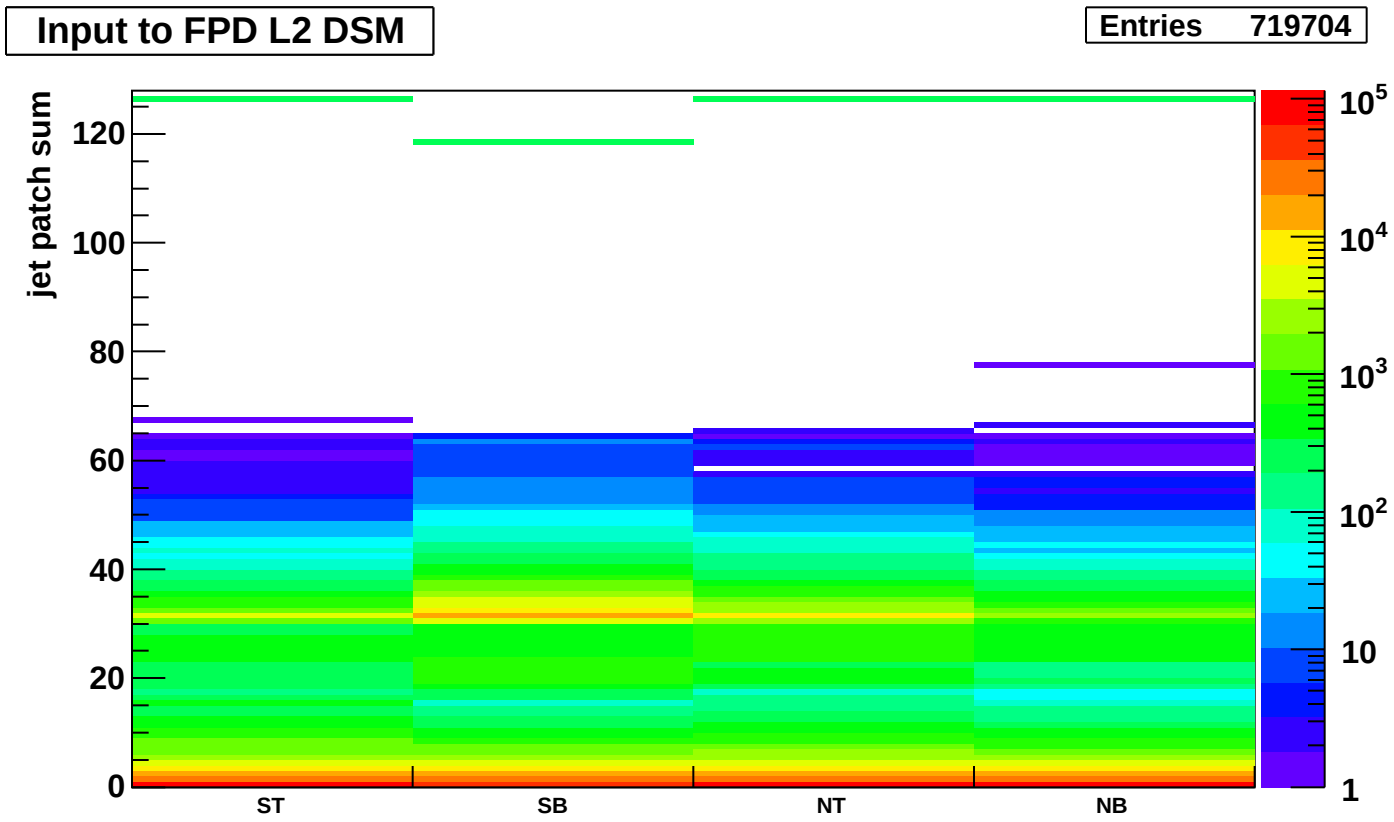

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

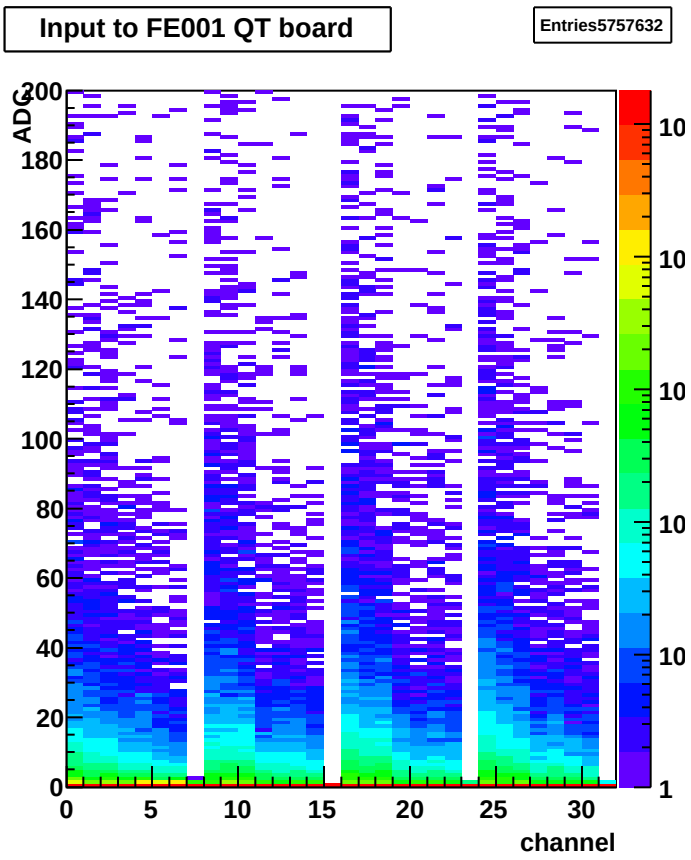

Input to FPD L2 DSM
Entries 539778
HT bits
24462
63114
29205
155464
116812
150721
SMALL
LARGE-S
LARGE-N

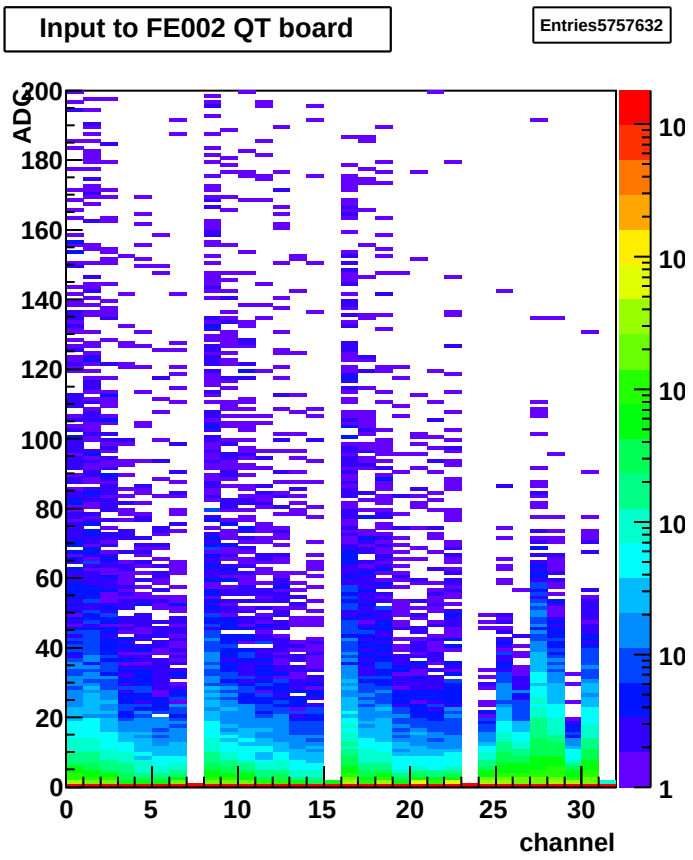

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

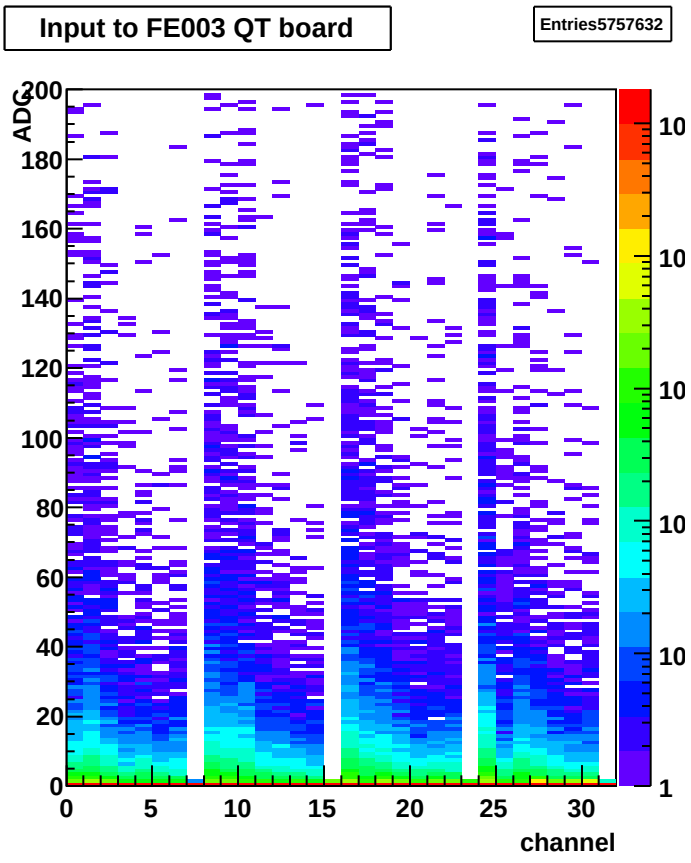

Input to FPD L3 DSM
Entries 2878816
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

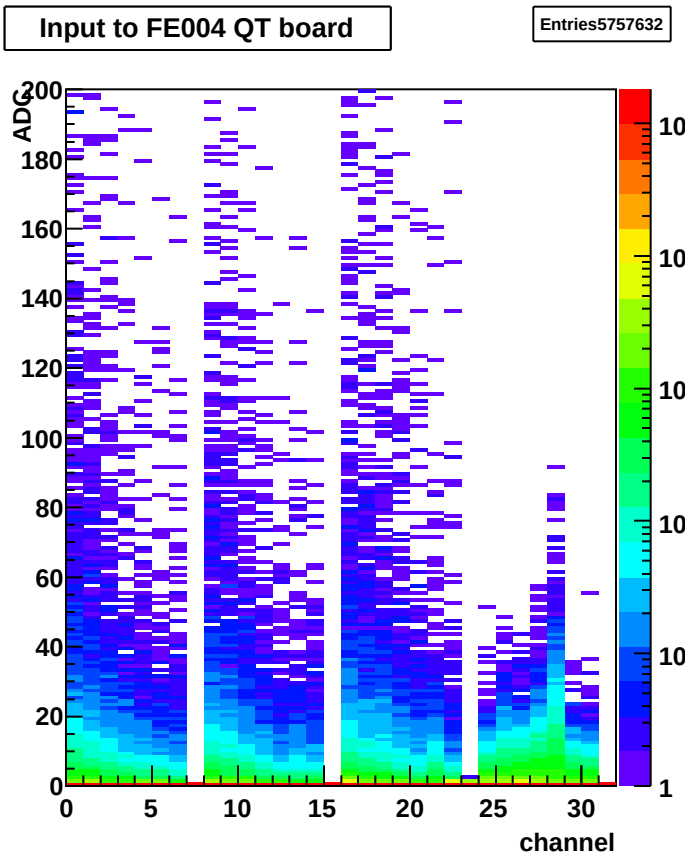

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

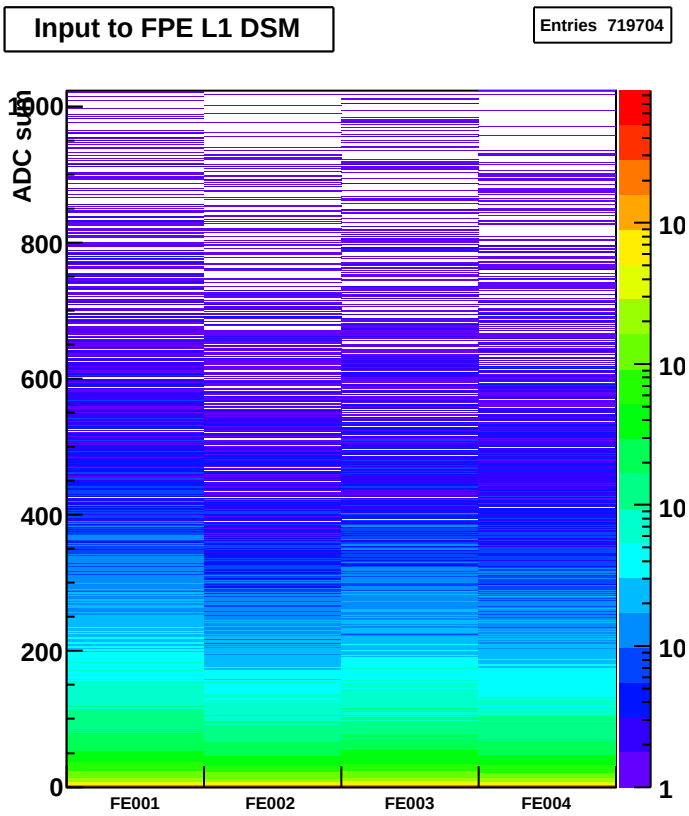

Input to FPD L2 DSM
Entries 719704
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

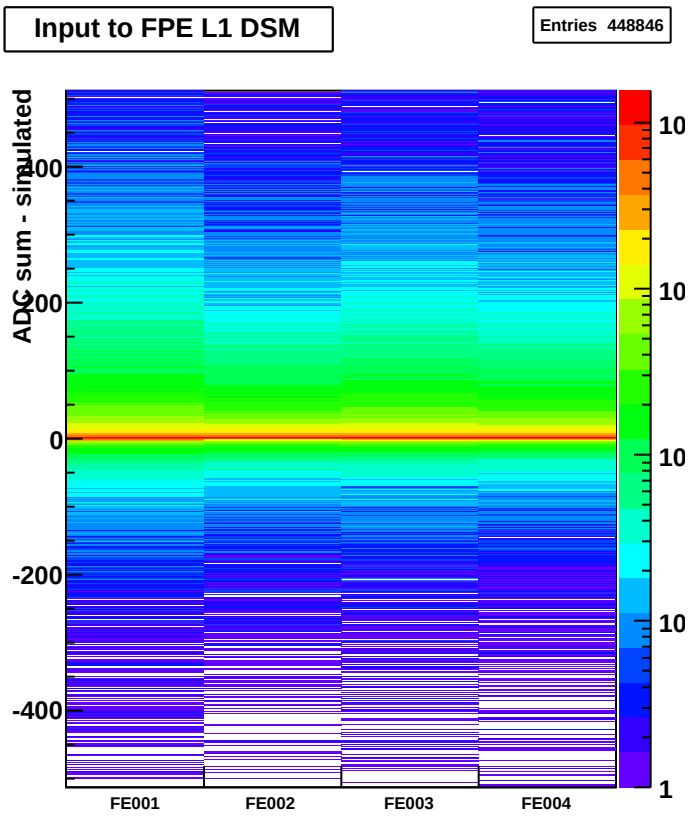

Input to FE001 QT board
Entries5757632
ADC
channel

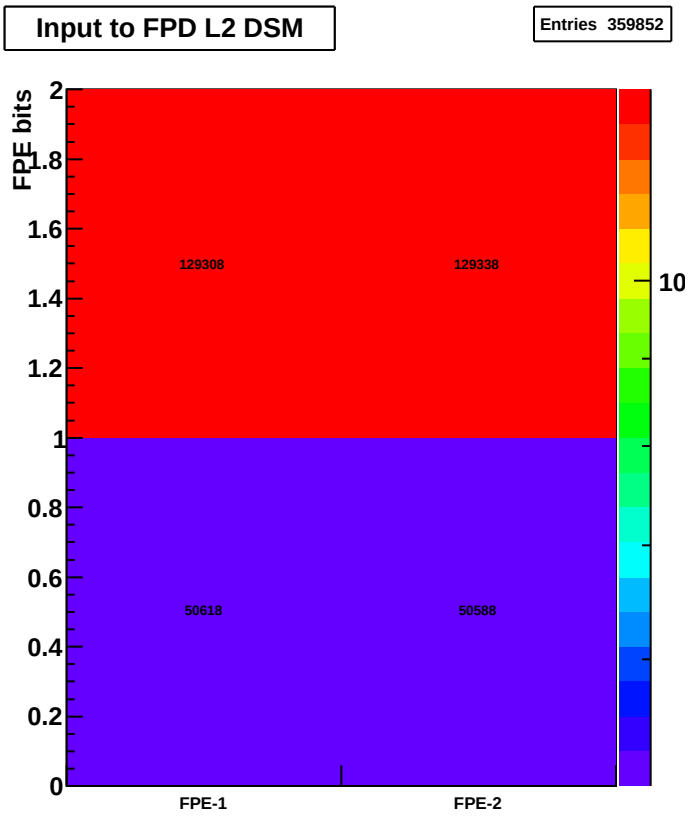

Input to FE002 QT board
Entries5757632
ADC
channel

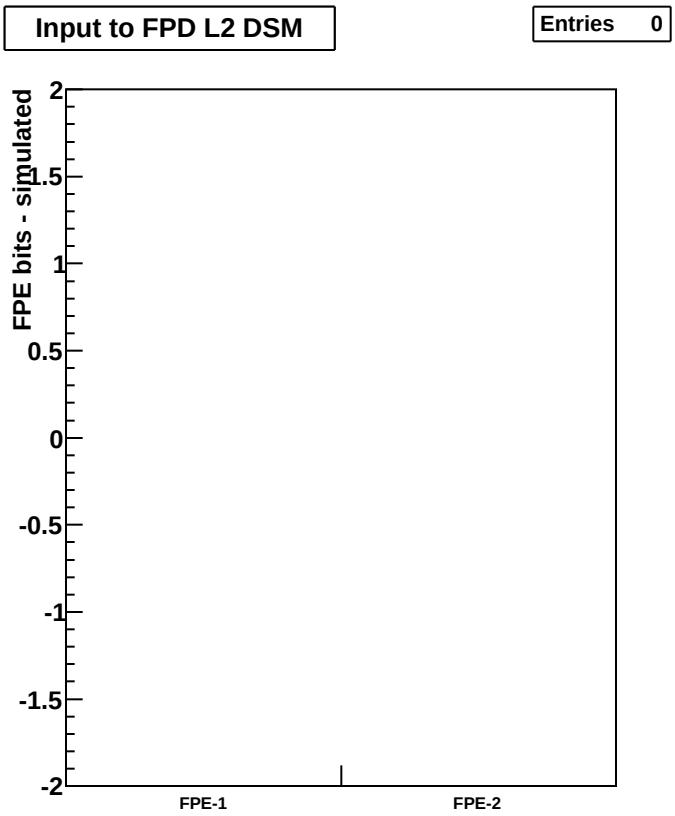

Input to FE003 QT board
Entries5757632
ADC
channel


Input to FE004 QT board
Entries5757632
ADC
channel


Input to FPE L1 DSM
Entries 719704
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 448846
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 359852
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
129308
129338
50618
50588
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2