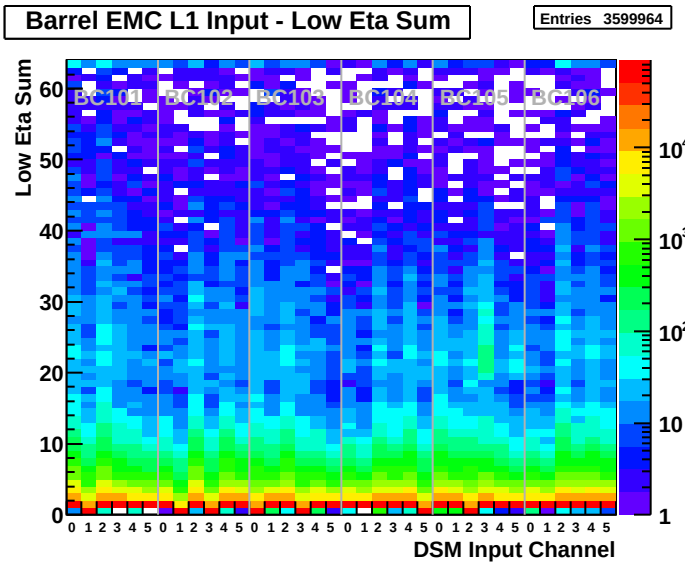
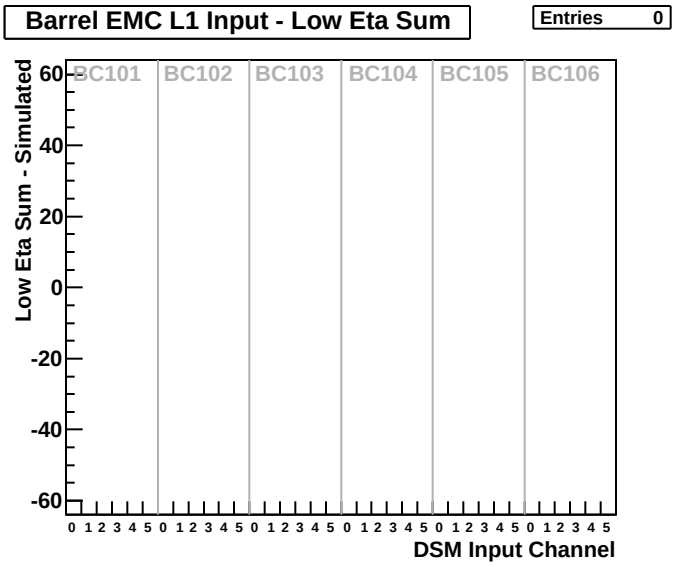
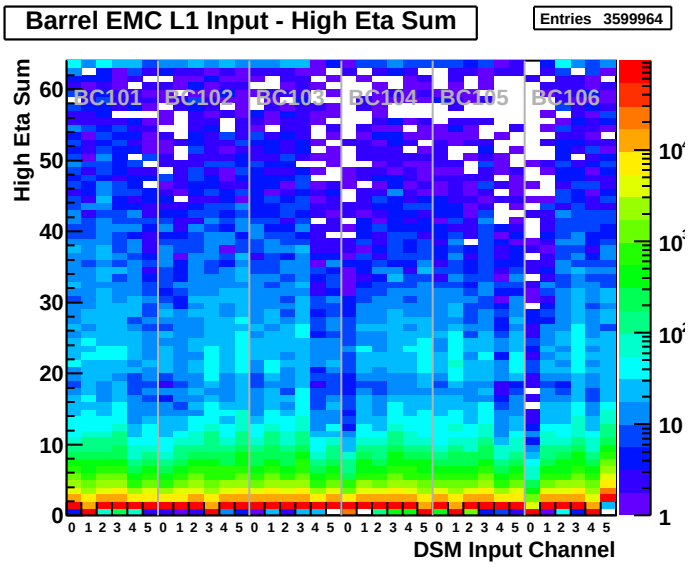
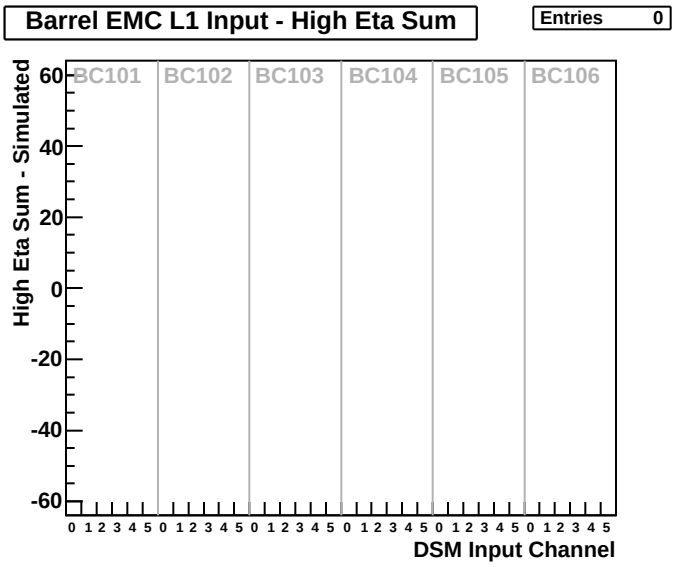
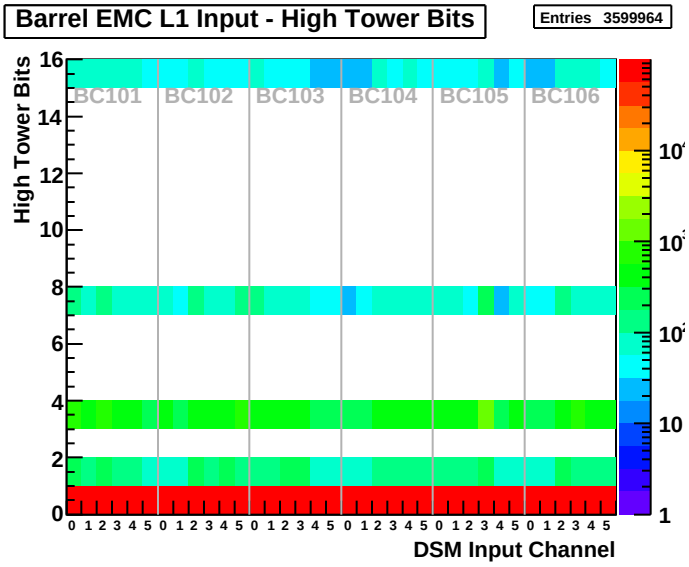
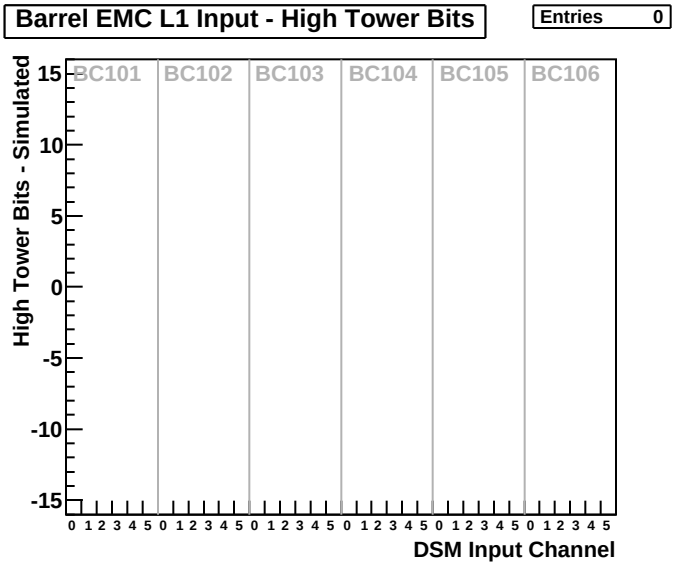

Barrel EMC L1 Input - Low Eta Sum
Entries 3599964
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

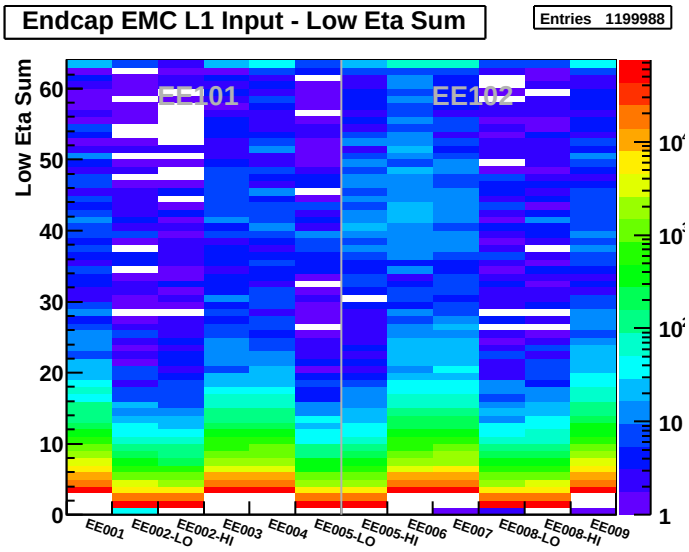

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

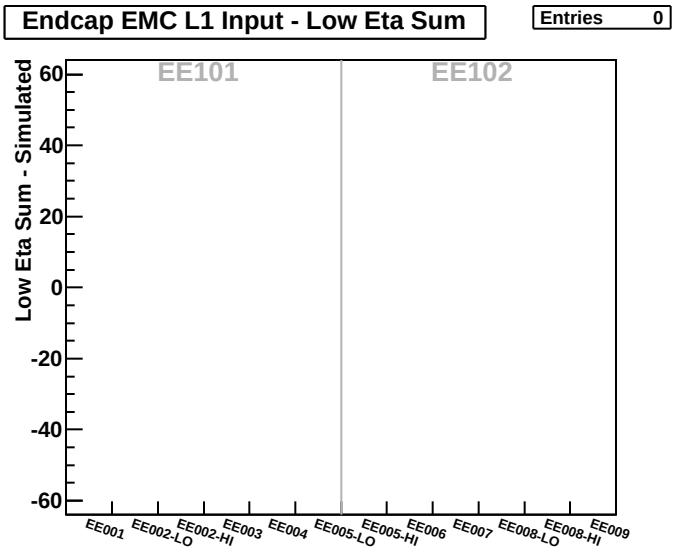

Barrel EMC L1 Input - High Eta Sum
Entries 3599964
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

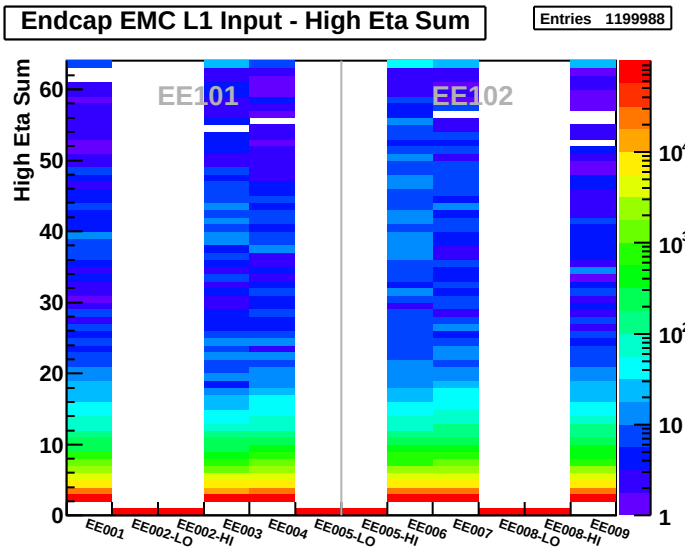

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

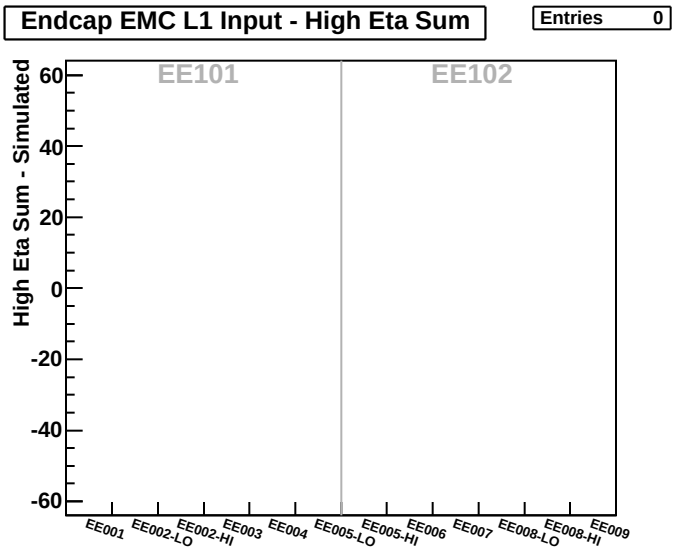

Barrel EMC L1 Input - High Tower Bits
Entries 3599964
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

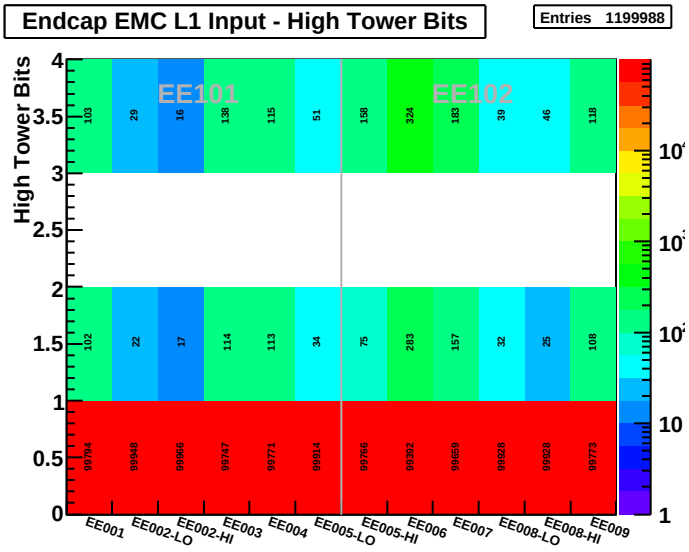

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

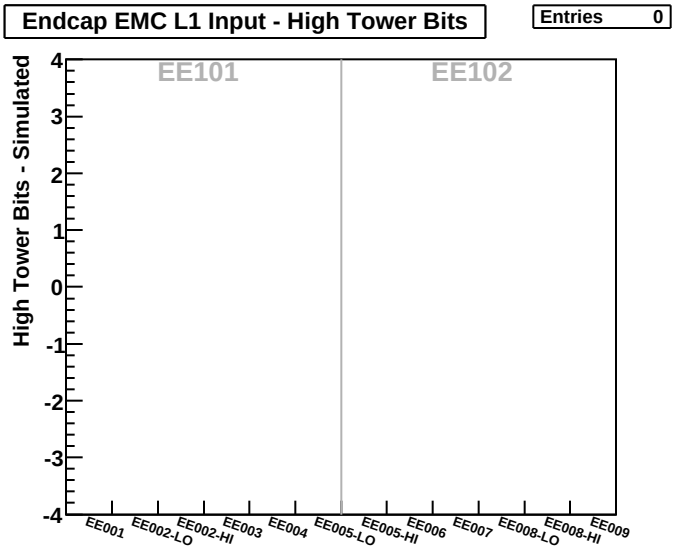

Endcap EMC L1 Input - Low Eta Sum
Entries 1199988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

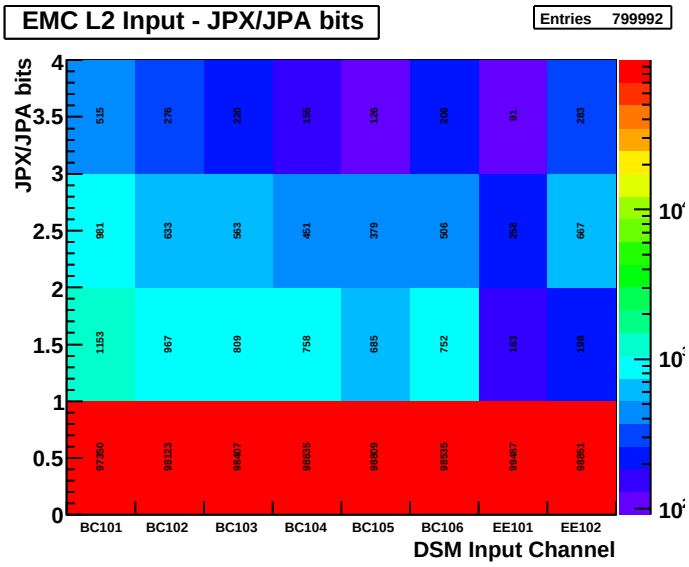

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

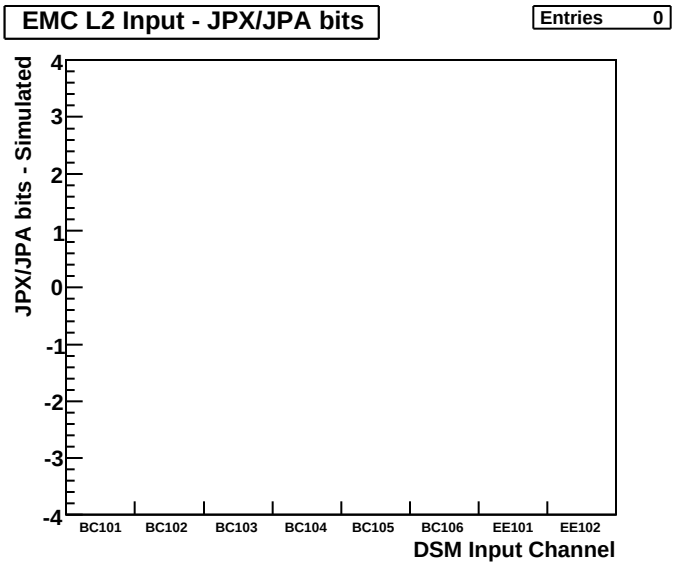

Endcap EMC L1 Input - High Eta Sum
Entries 1199988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

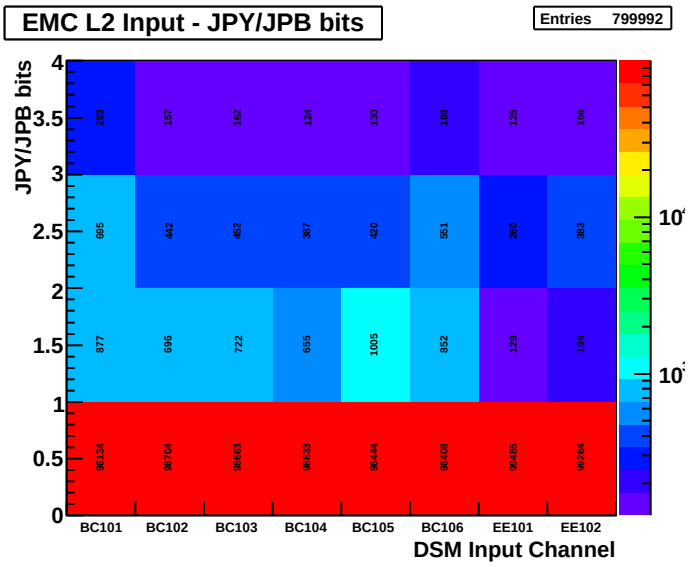

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

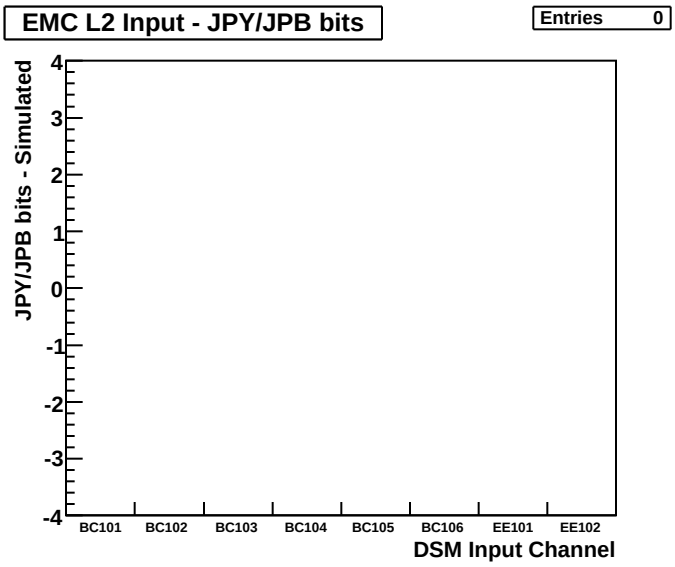

Endcap EMC L1 Input - High Tower Bits
Entries 1199988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

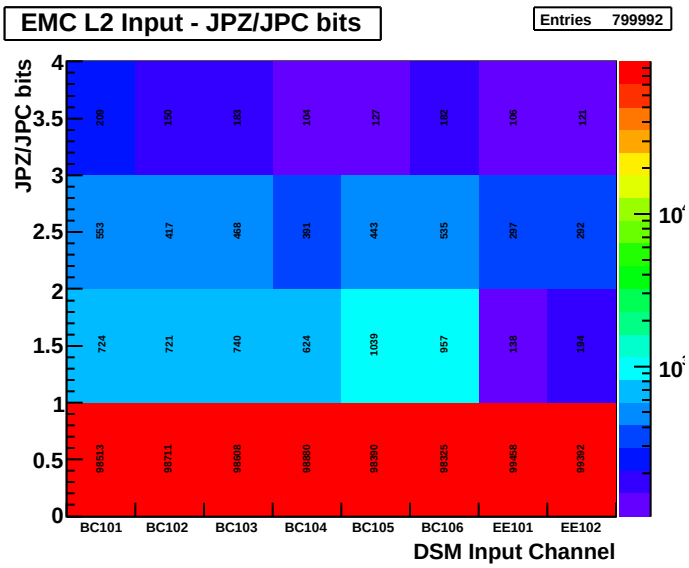

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

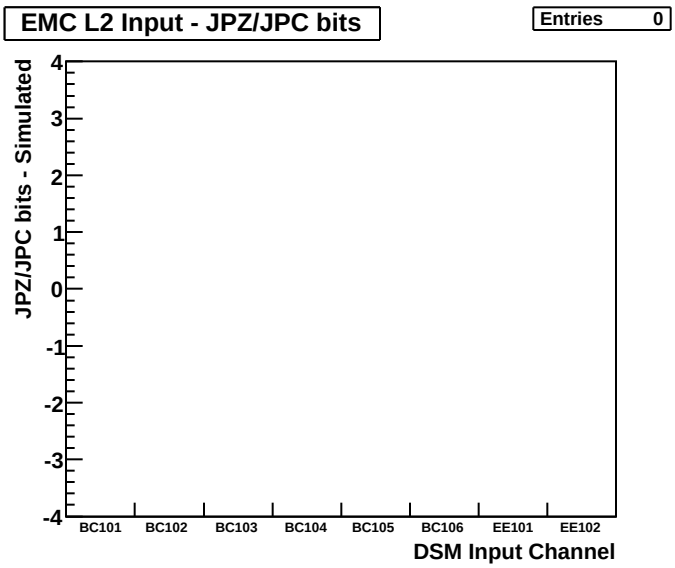

EMC L2 Input - JPX/JPA bits
Entries 799992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
515 276 220 155 126 206 91 283
981 633 563 451 379 506 258 667
1153 967 809 758 685 752 163 198
97350 98123 98407 98635 98809 98535 99487 98851

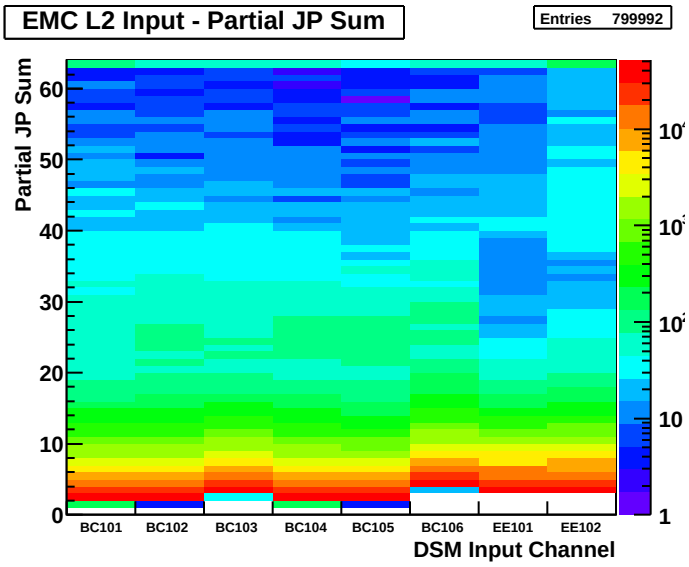

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

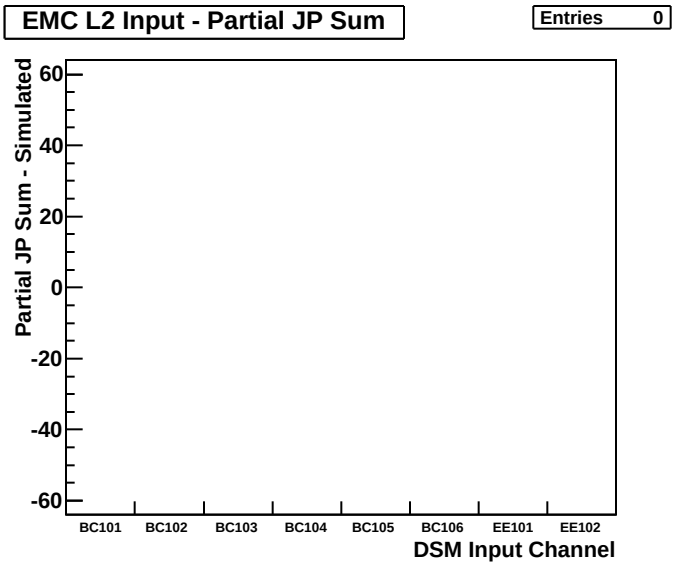

EMC L2 Input - JPY/JPB bits
Entries 799992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
236 157 162 124 130 188 125 156
695 442 452 387 420 551 260 383
877 696 722 655 1005 852 129 196
98134 98704 98663 98833 98444 98408 99485 99264

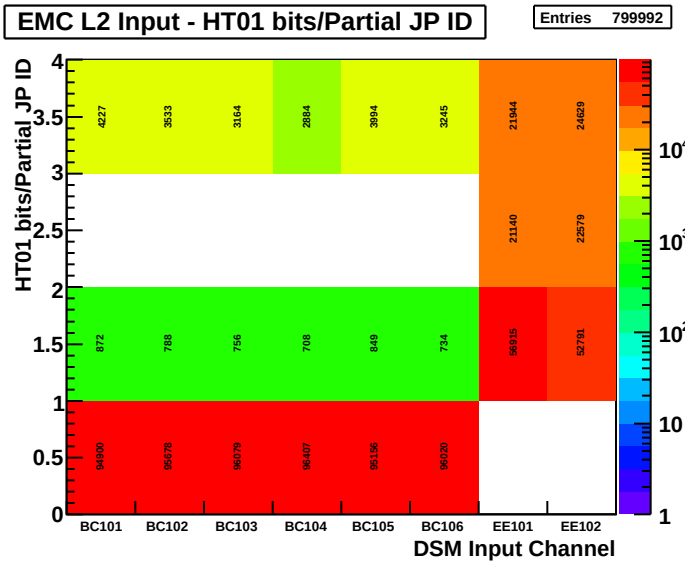

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

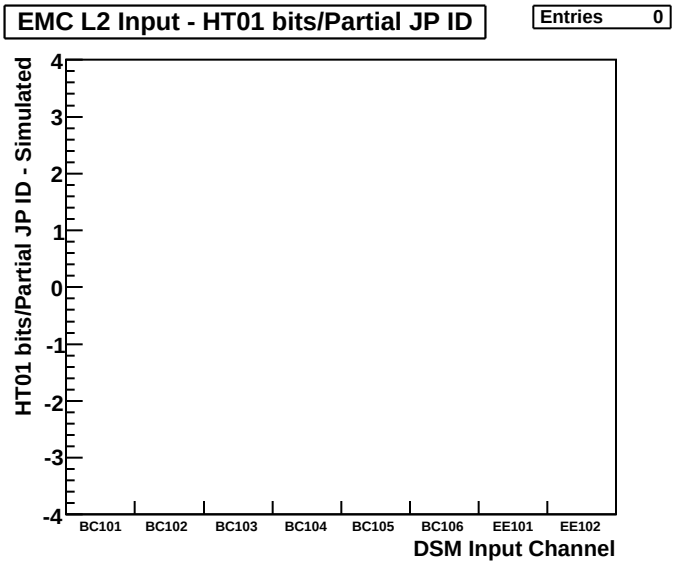

EMC L2 Input - JPZ/JPC bits
Entries 799992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
209 150 183 104 127 182 106 121
553 417 468 391 443 535 297 292
724 721 740 624 1039 957 138 194
98513 98711 98608 98880 98390 98325 99458 99392

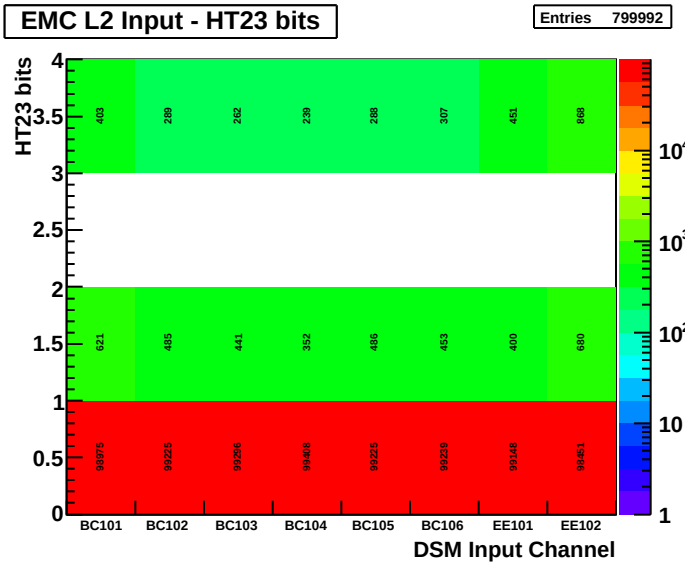

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

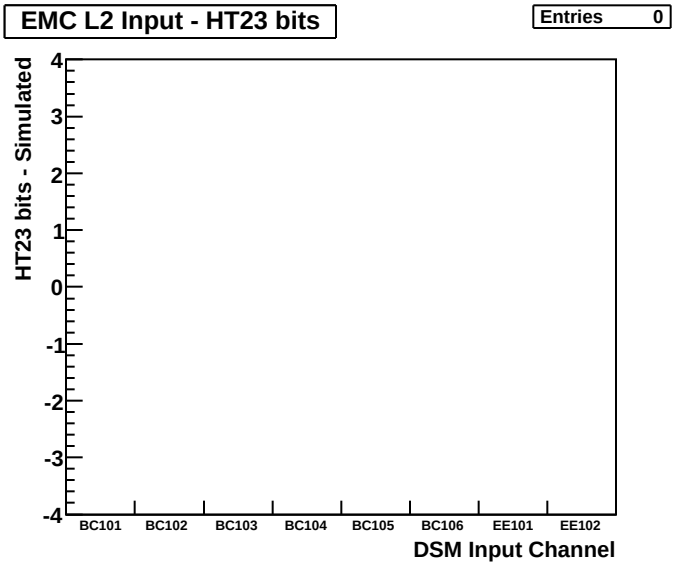

EMC L2 Input - Partial JP Sum
Entries 799992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

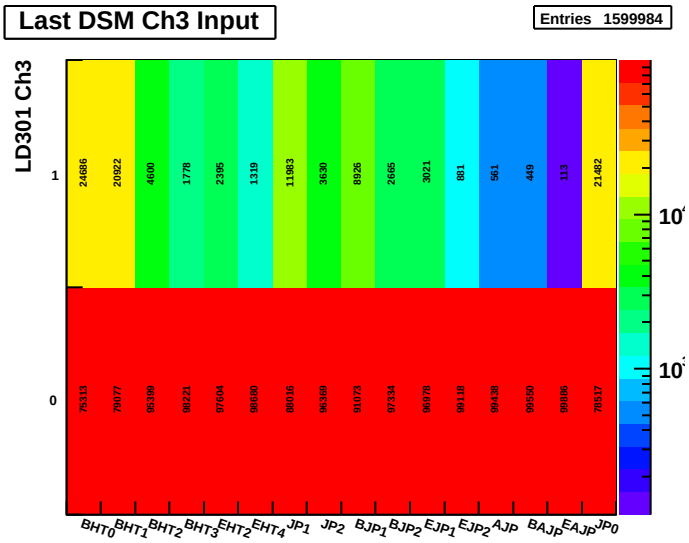

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

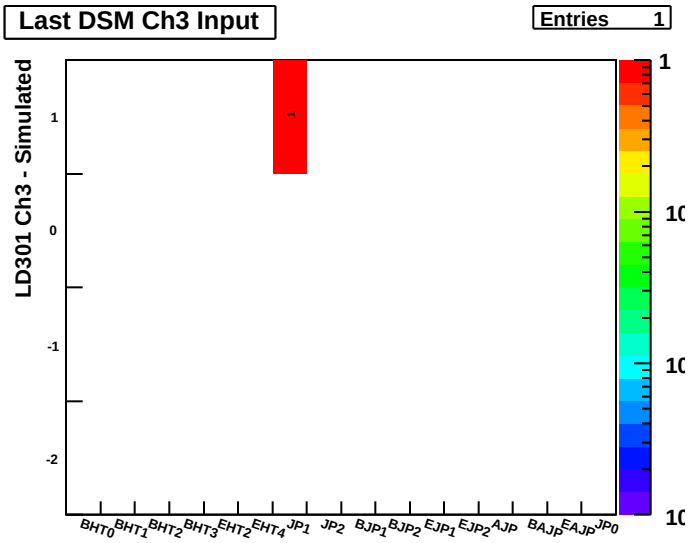

EMC L2 Input - HT01 bits/Partial JP ID
Entries 799992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

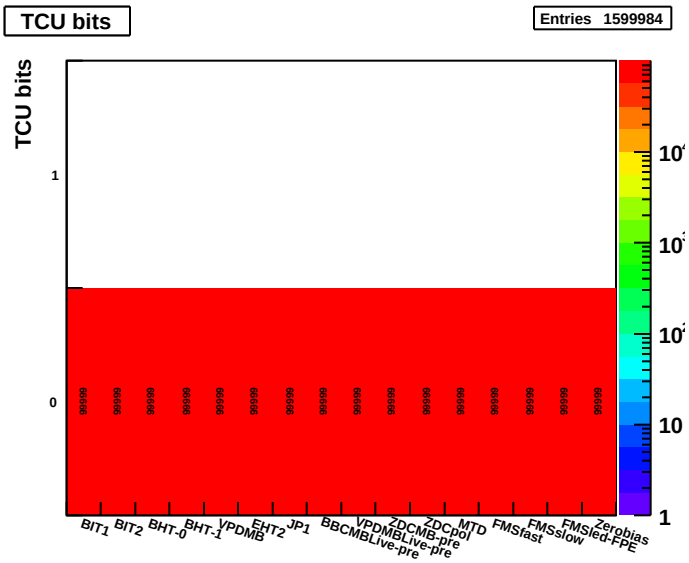

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

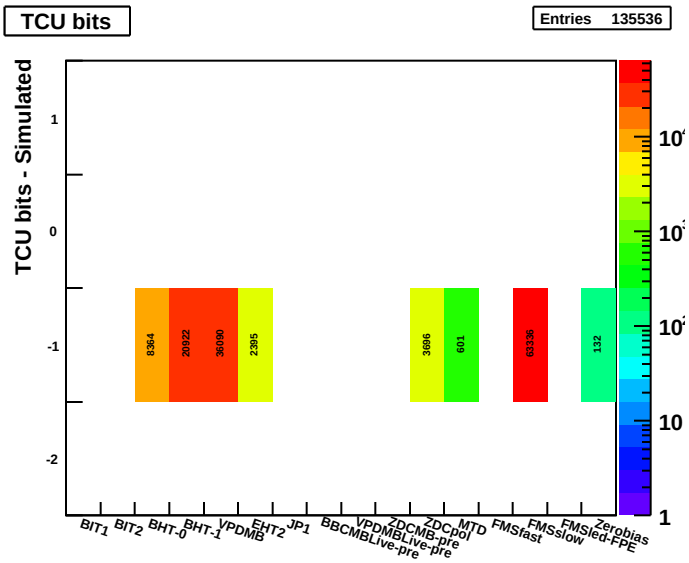

EMC L2 Input - HT23 bits
Entries 799992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

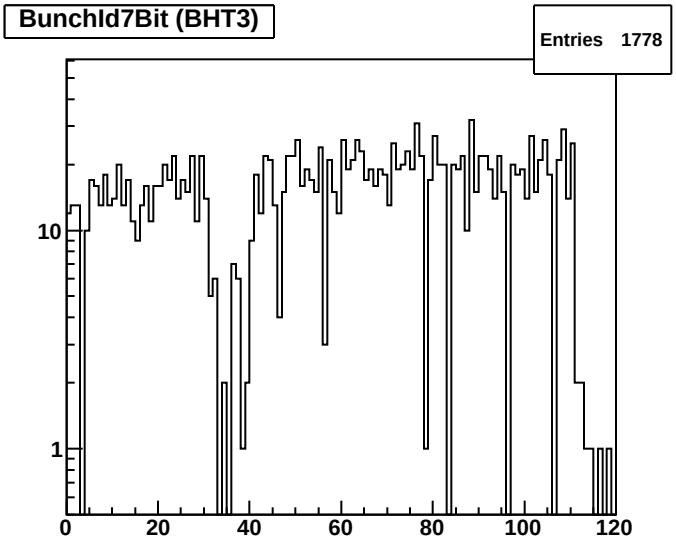

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

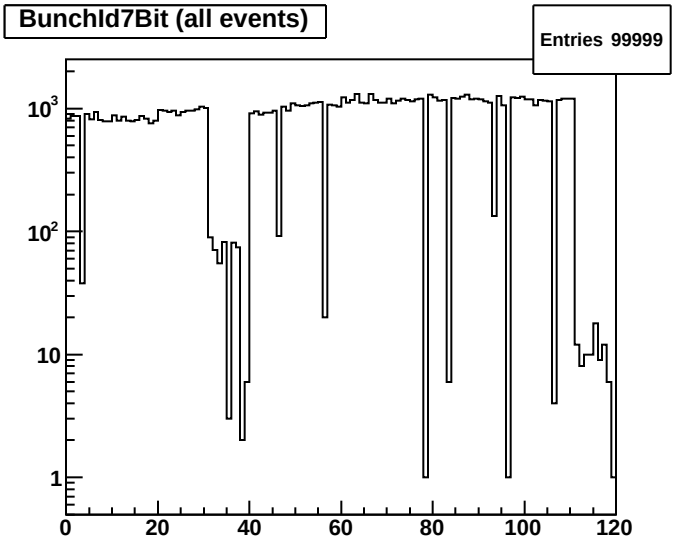

Last DSM Ch3 Input
Entries 1599984
LD301 Ch3
24686 20922 4600 1778 2395 1319 11983 3630 8926 2665 3021 881 561 449 113 21482
75313 79077 95399 98221 97604 96660 88016 96369 91073 97334 96978 98118 99438 99550 99886 78517
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

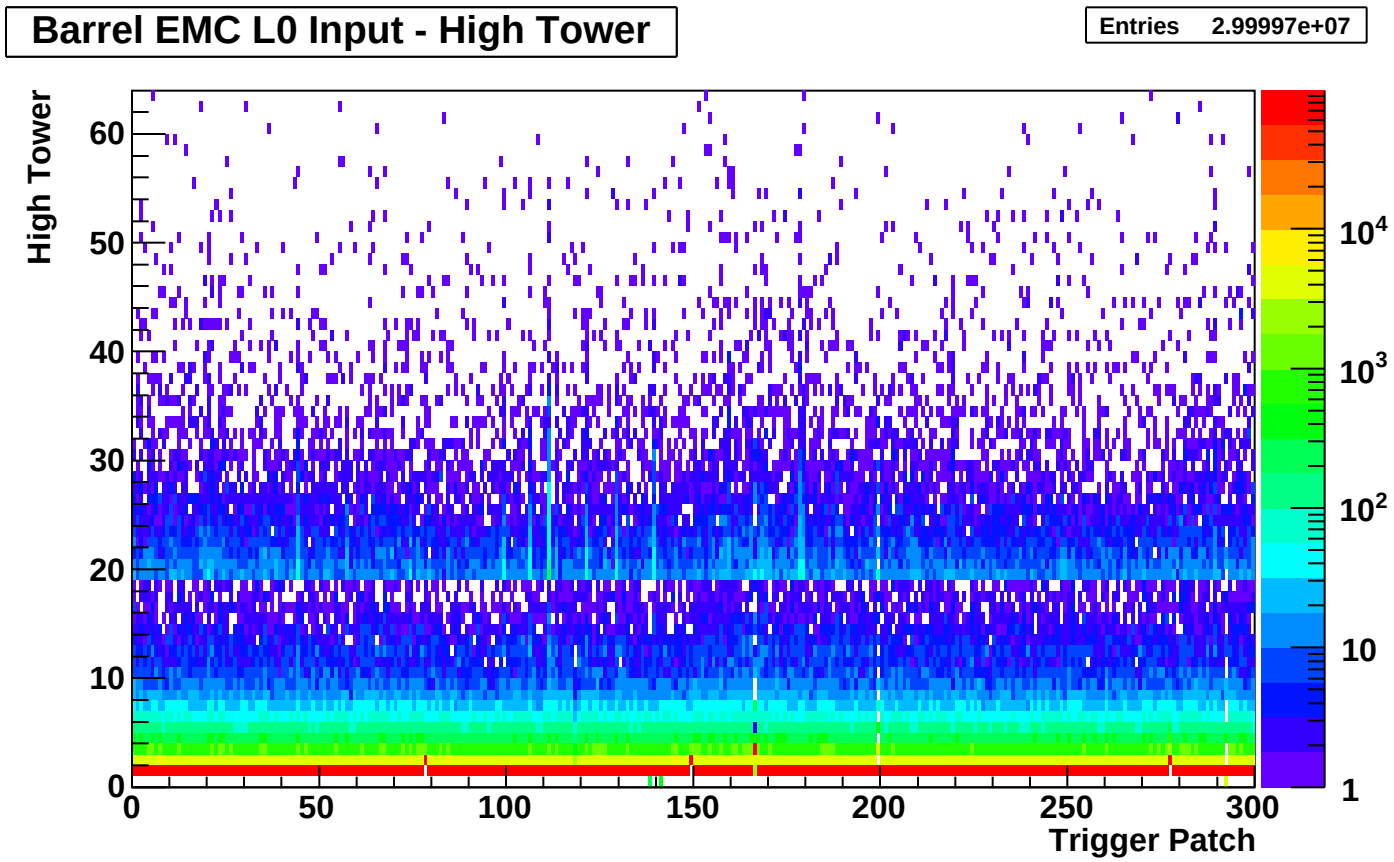

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

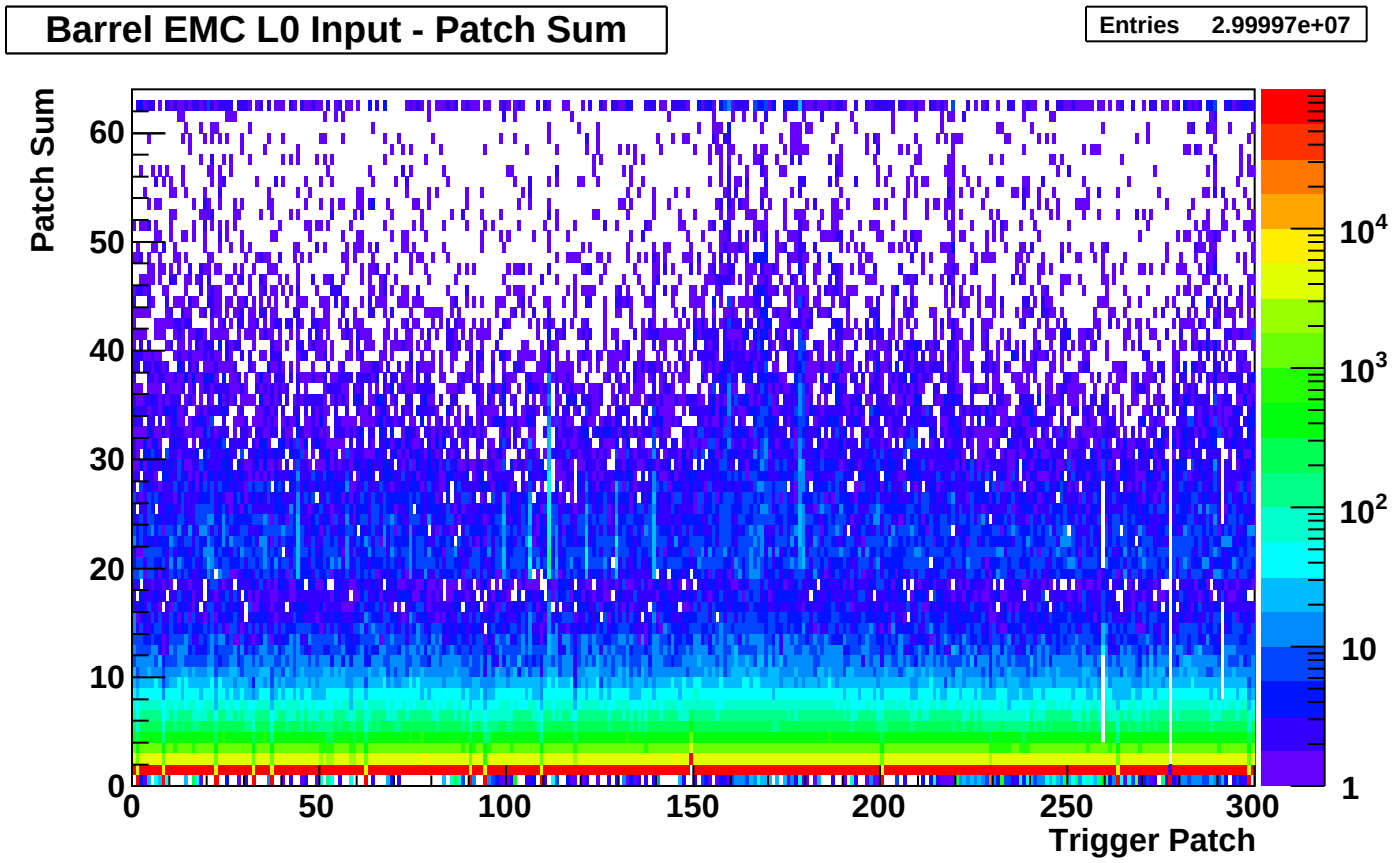

TCU bits
Entries 1599984
TCU bits
99999
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

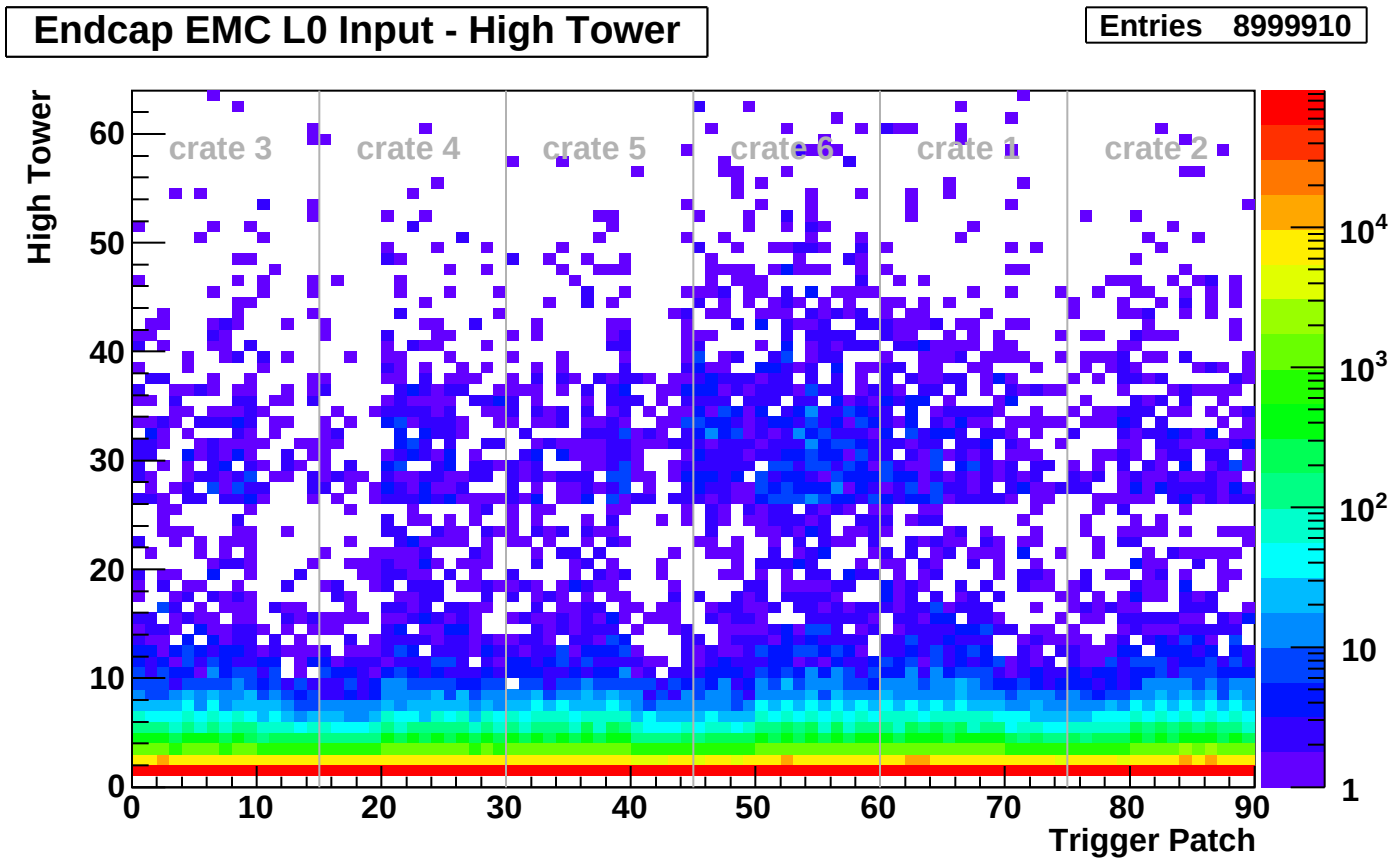

TCU bits
Entries 135536
TCU bits - Simulated
8364 20922 36090 2395 3666 601 63336 132
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

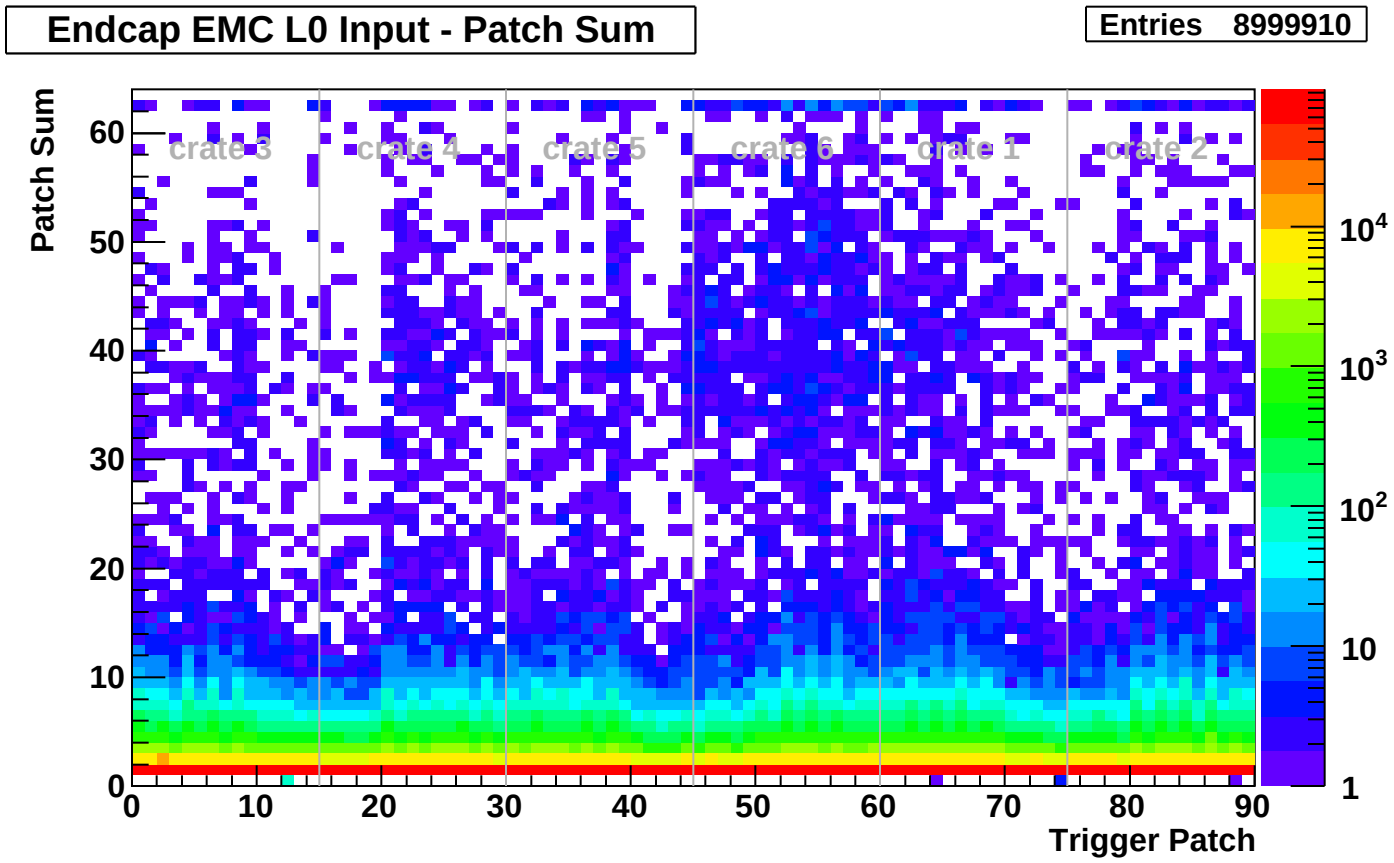

BunchId7Bit (BHT3)
Entries 1778

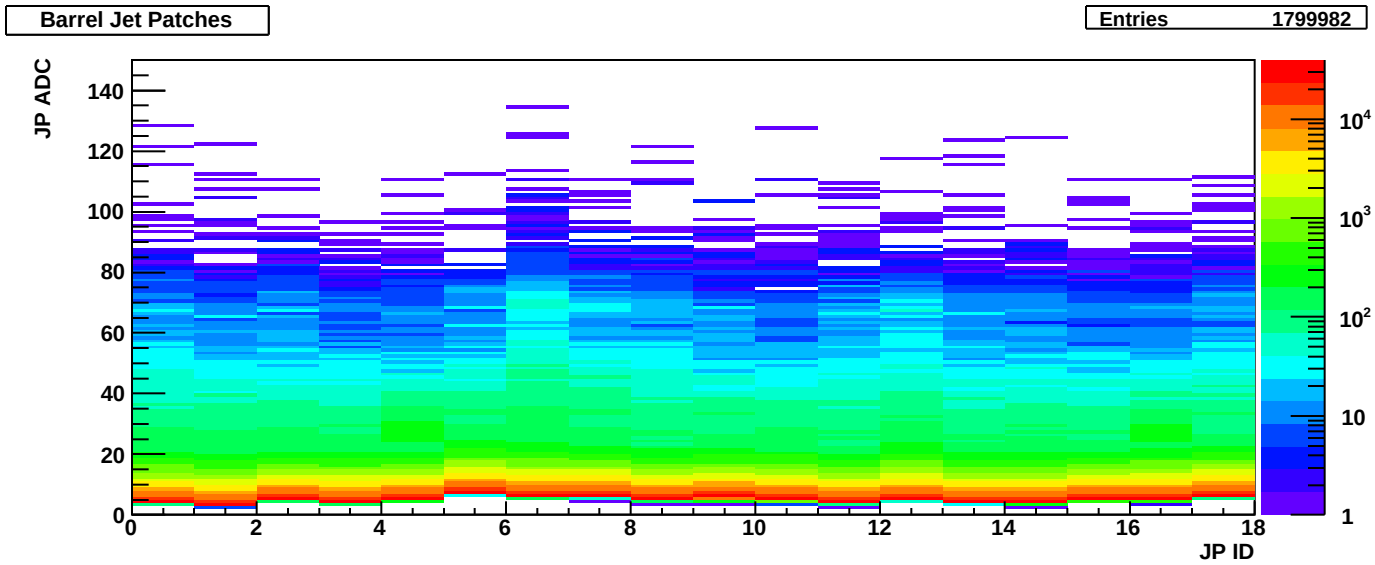

BunchId7Bit (all events)
Entries 99999

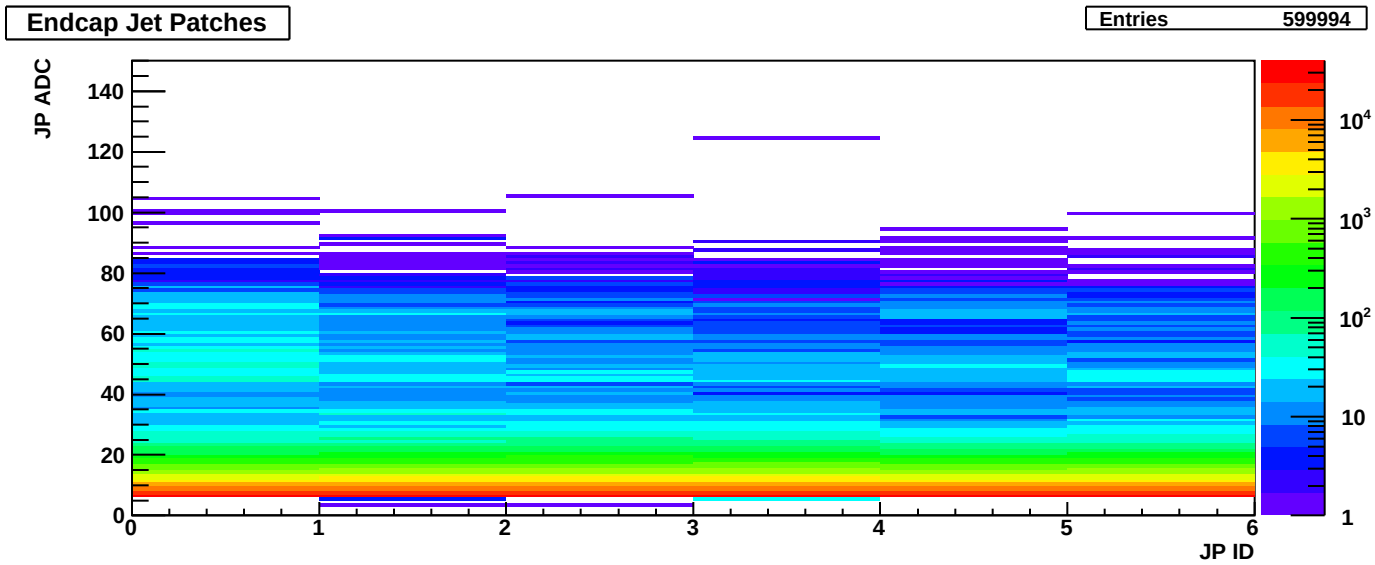

Barrel EMC L0 Input - High Tower
Entries 2.99997e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

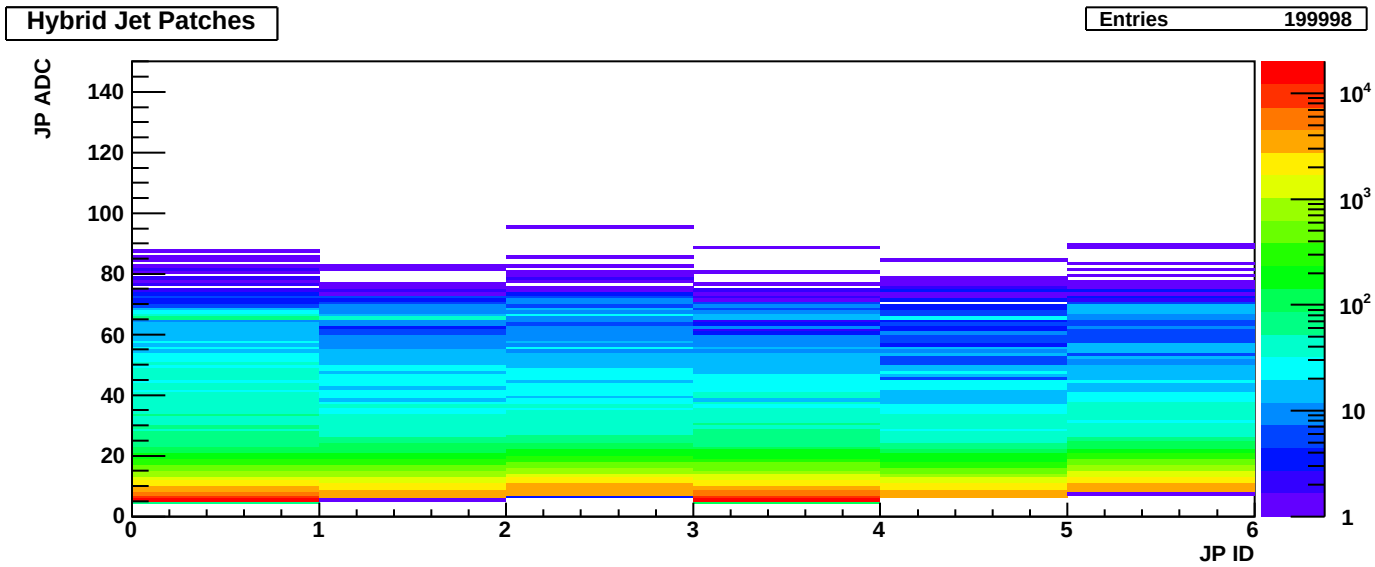

Barrel EMC L0 Input - Patch Sum
Entries 2.99997e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4


Endcap EMC L0 Input - High Tower
Entries 8999910
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

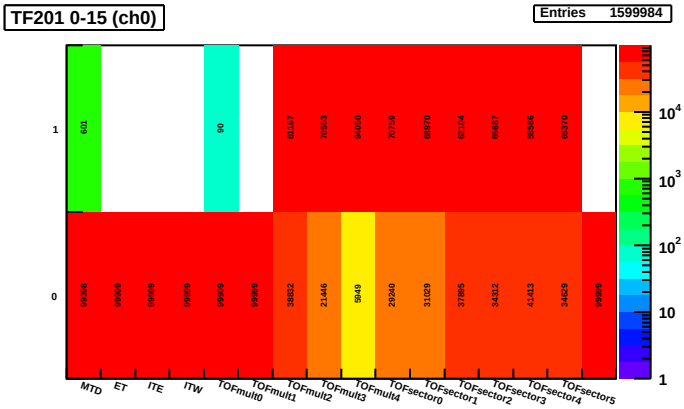

Endcap EMC L0 Input - Patch Sum
Entries 8999910
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 1799982
JP ADC
JP ID

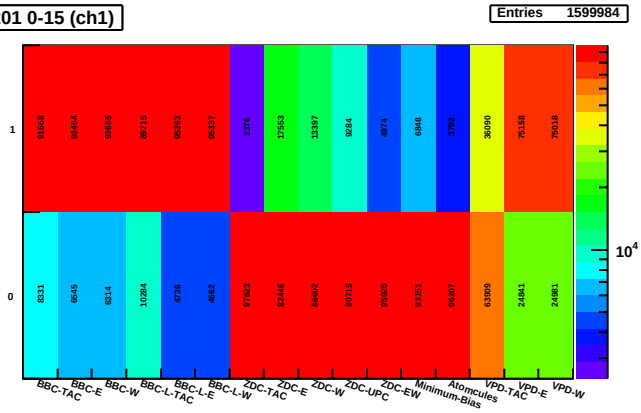

Endcap Jet Patches
Entries 599994
JP ADC
JP ID


Hybrid Jet Patches
Entries 199998
JP ADC
JP ID

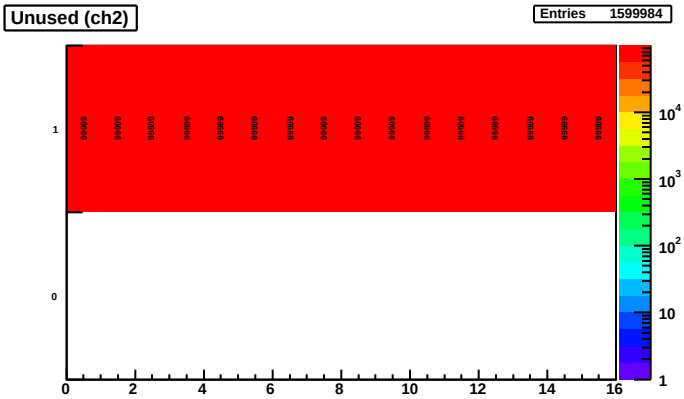
TF201 0-15 (ch0)



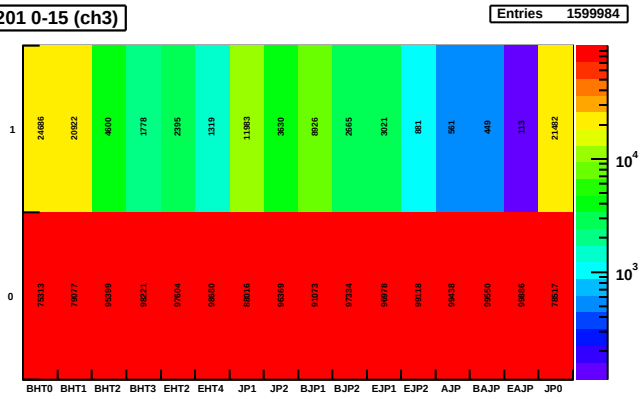
VT201 0-15 (ch1)



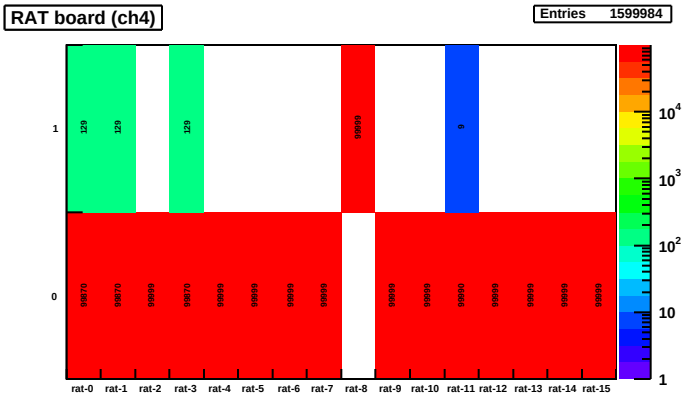
Unused (ch2)



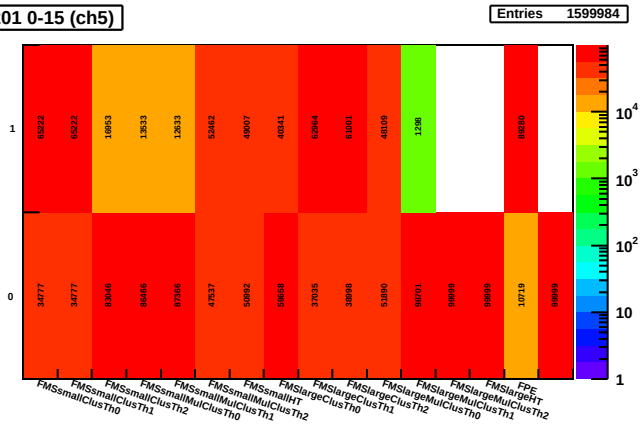
EM201 0-15 (ch3)



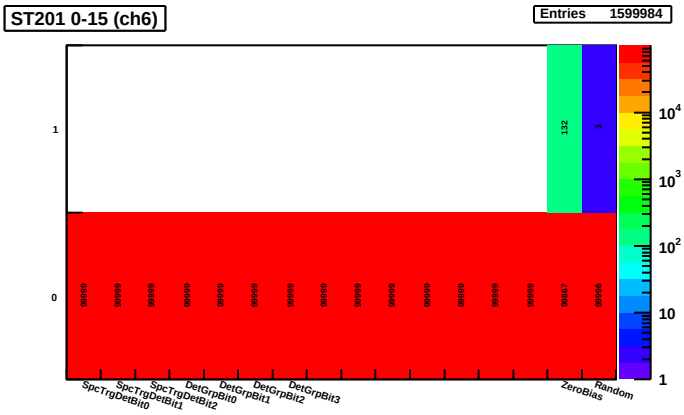
RAT board (ch4)



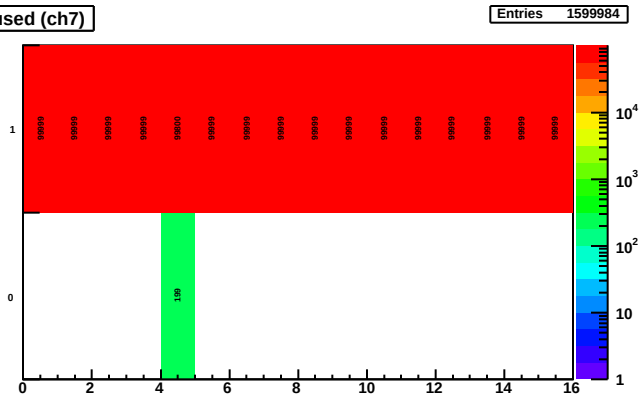
FP201 0-15 (ch5)

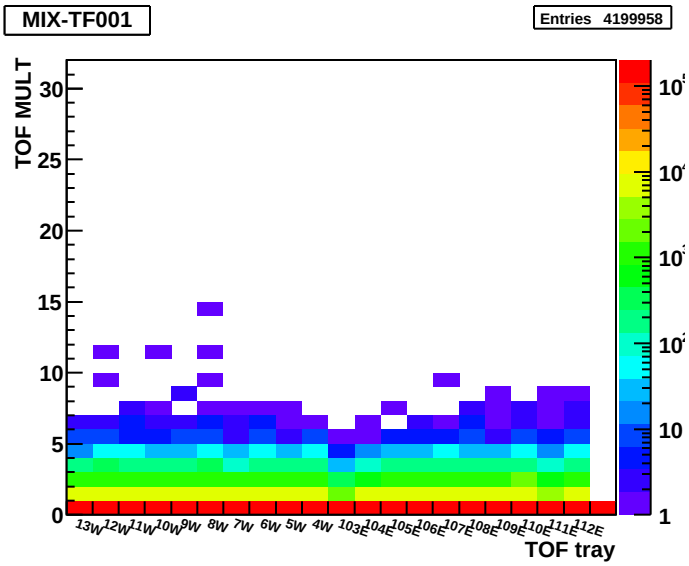


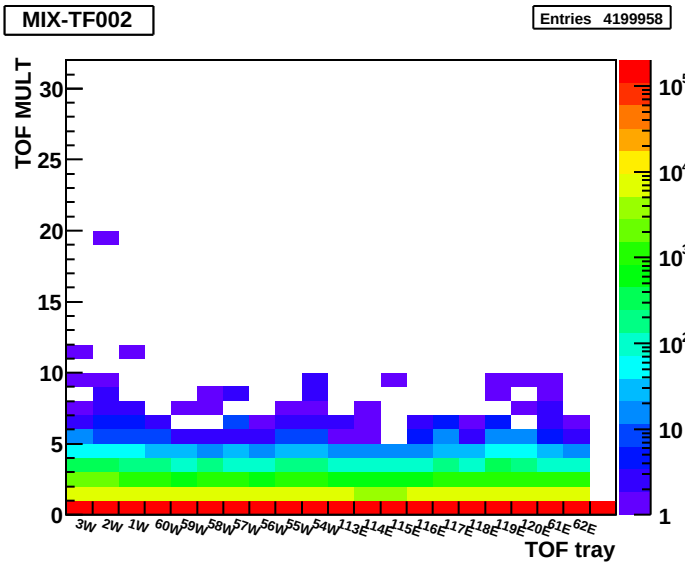
ST201 0-15 (ch6)

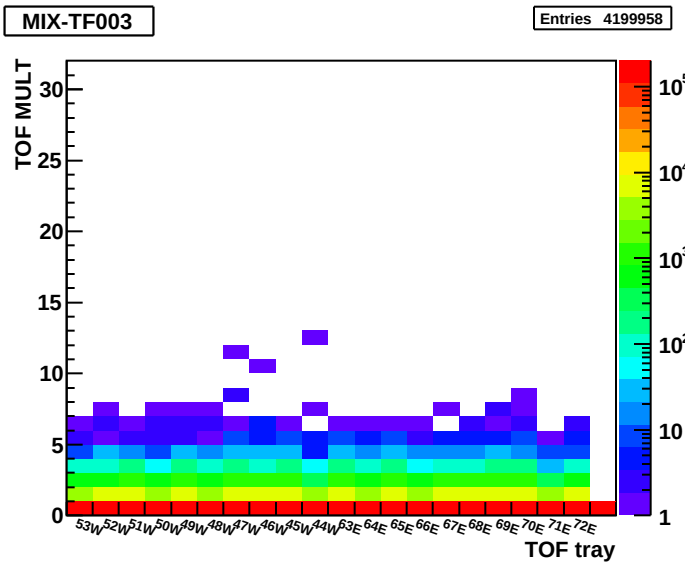


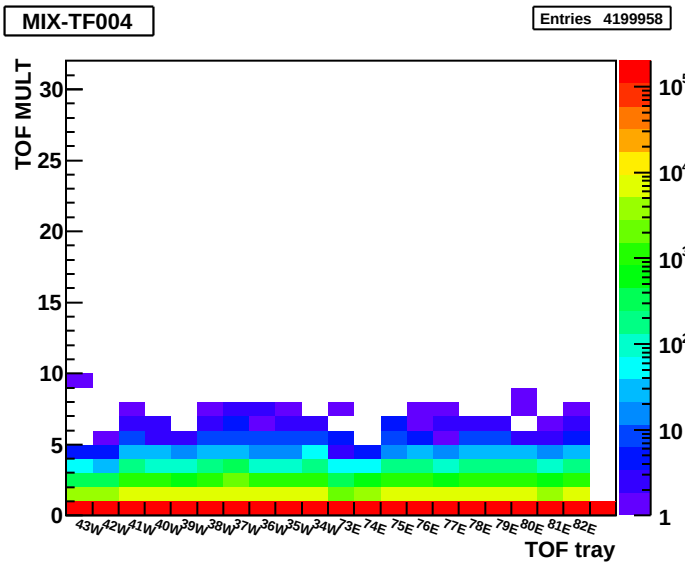
Unused (ch7)

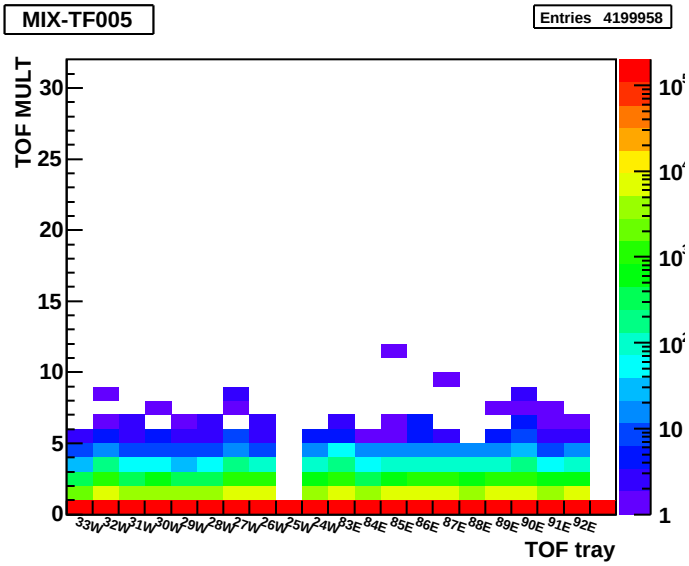


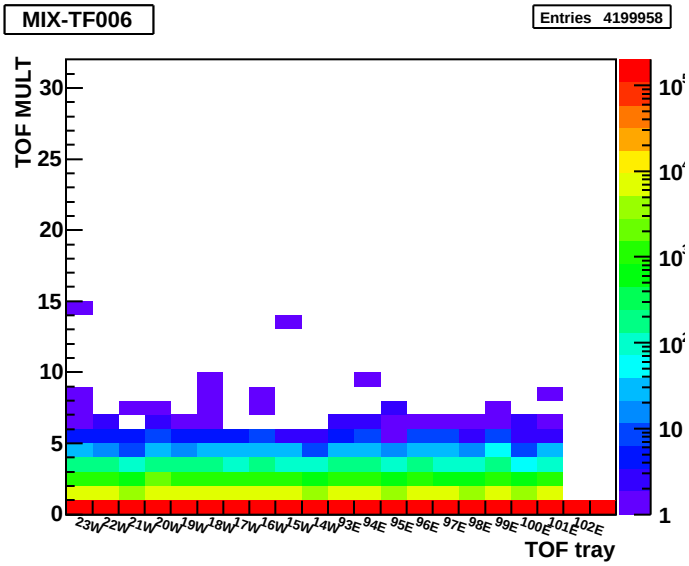

MIX-TF001
Entries 4199958
TOF MULT
TOF tray

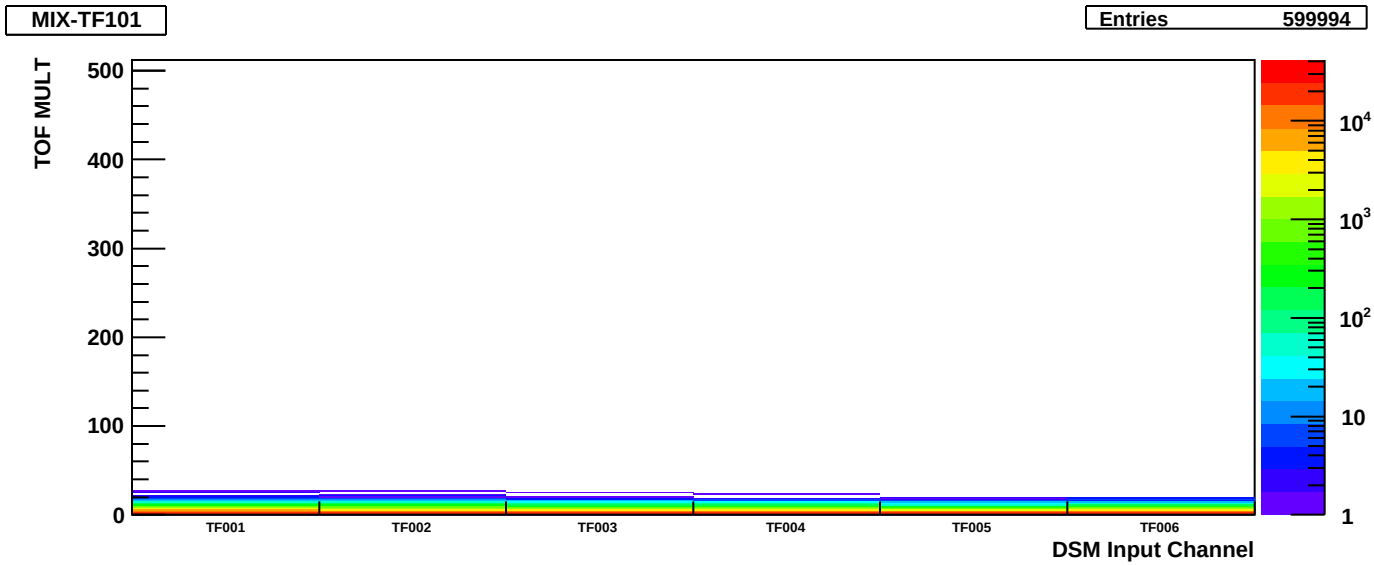

MIX-TF002
Entries 4199958
TOF MULT
TOF tray

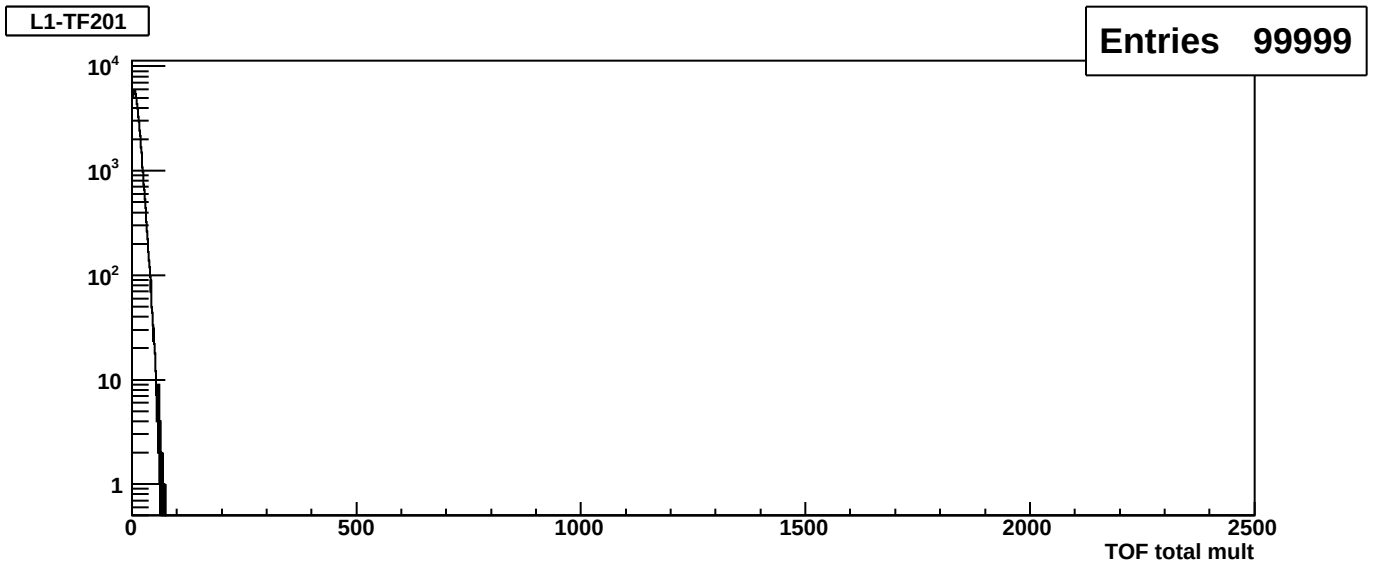

MIX-TF003
Entries 4199958
TOF MULT
TOF tray

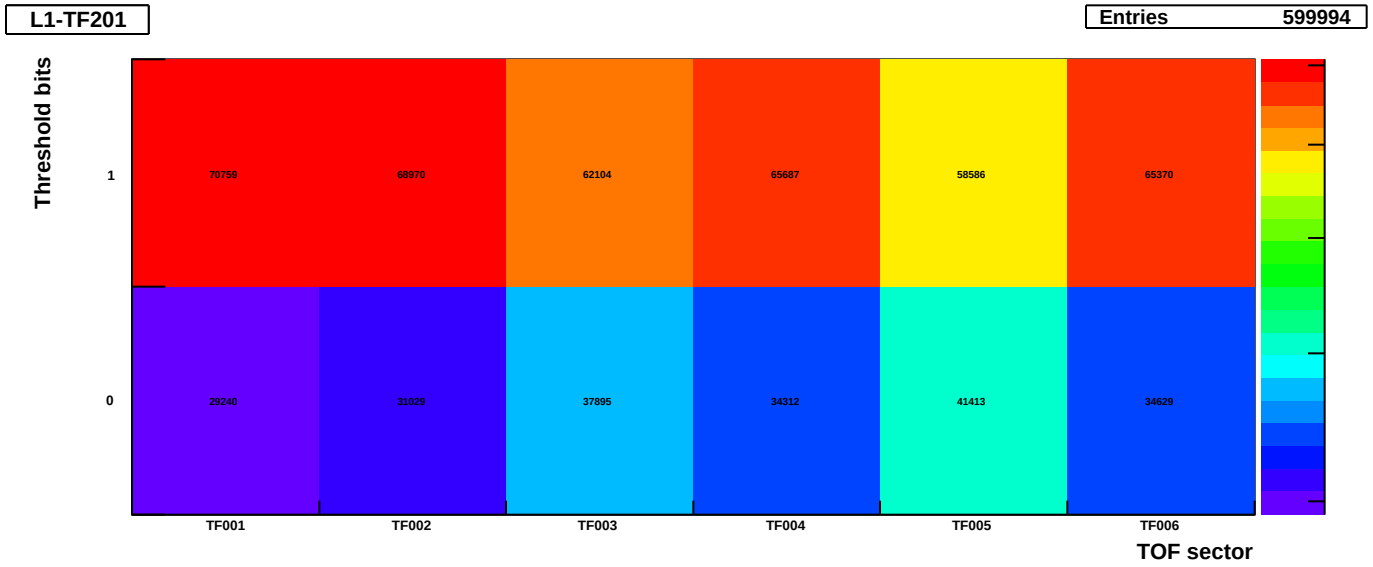

MIX-TF004
Entries 4199958
TOF MULT
TOF tray

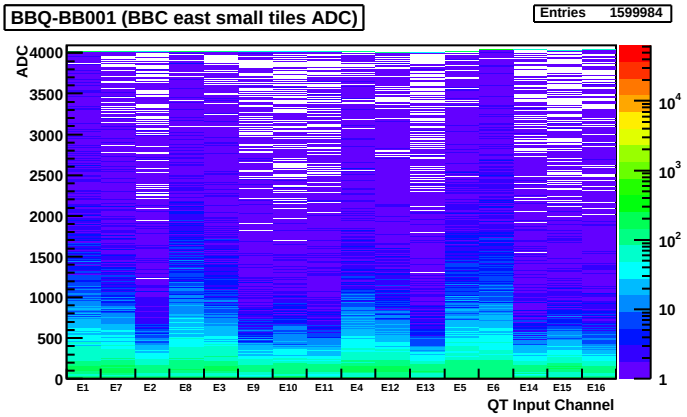

MIX-TF005
Entries 4199958
TOF MULT
TOF tray

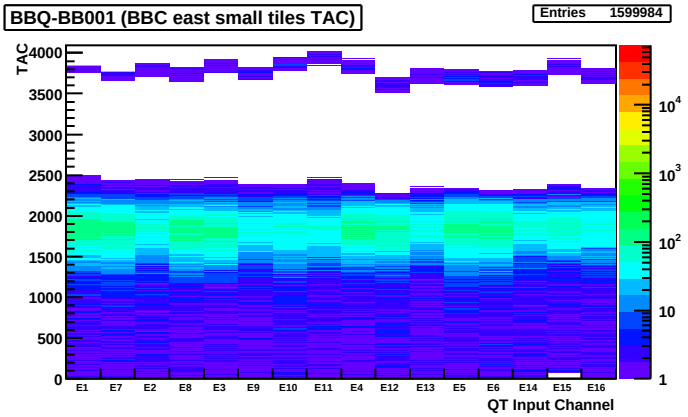

MIX-TF006
Entries 4199958
TOF MULT
TOF tray

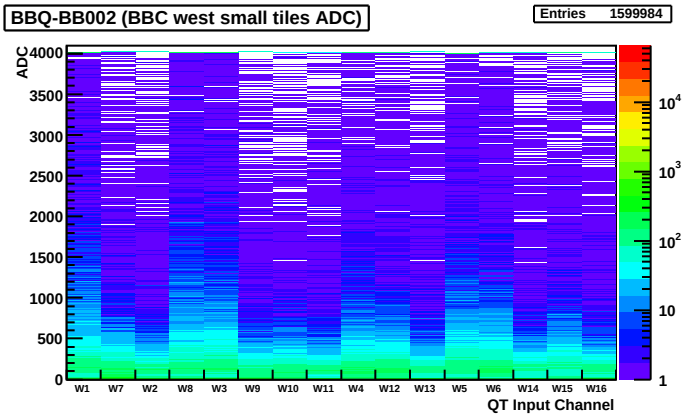

MIX-TF101
Entries 599994
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

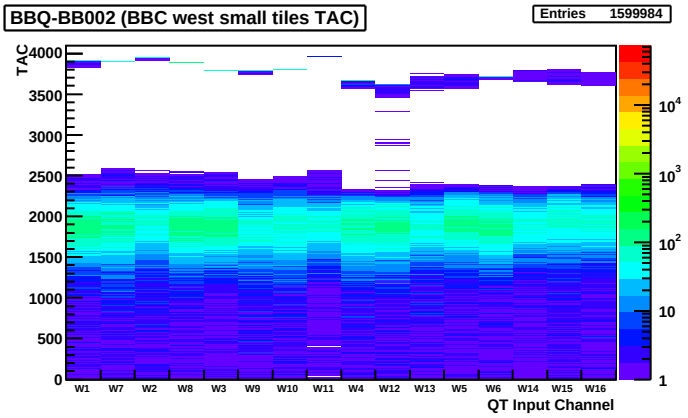

L1-TF201
Entries 99999
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

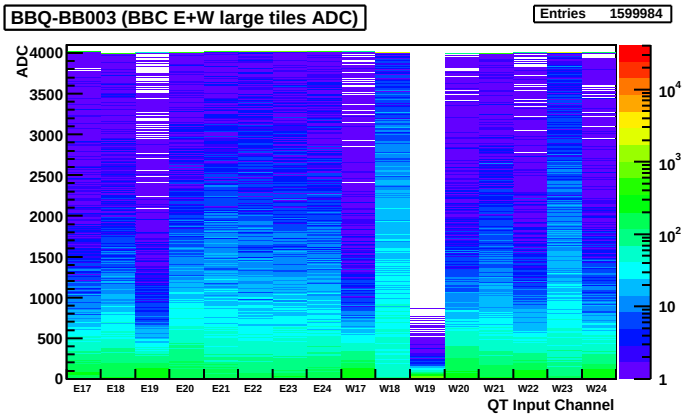

L1-TF201
Entries 599994
Threshold bits
1
0
70759
68970
62104
65687
58586
65370
29240
31029
37895
34312
41413
34629
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

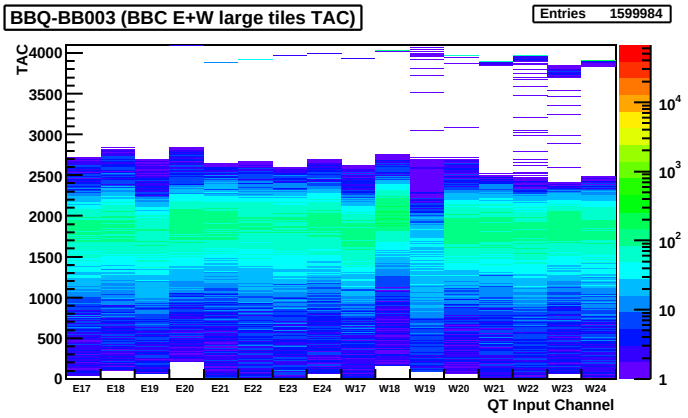

BBQ-BB001 (BBC east small tiles ADC)
Entries 1599984
ADC
QT Input Channel

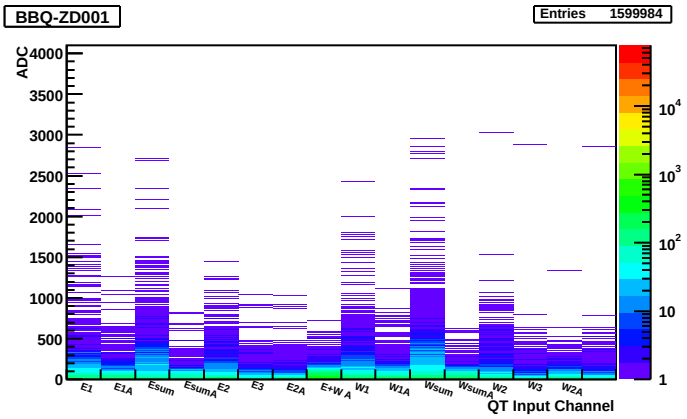

BBQ-BB001 (BBC east small tiles TAC)
Entries 1599984
TAC
QT Input Channel

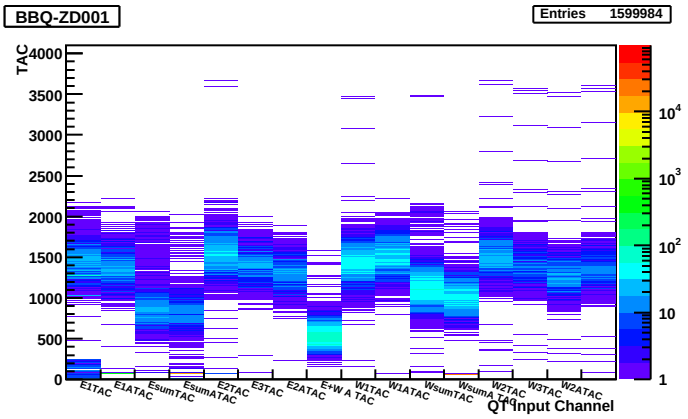

BBQ-BB002 (BBC west small tiles ADC)
Entries 1599984
ADC
QT Input Channel

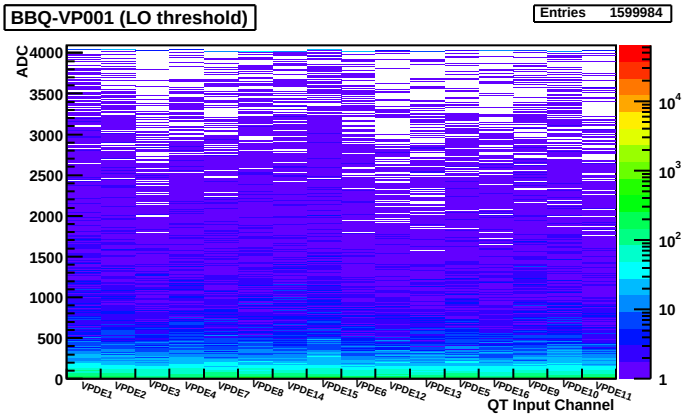

BBQ-BB002 (BBC west small tiles TAC)
Entries 1599984
TAC
QT Input Channel

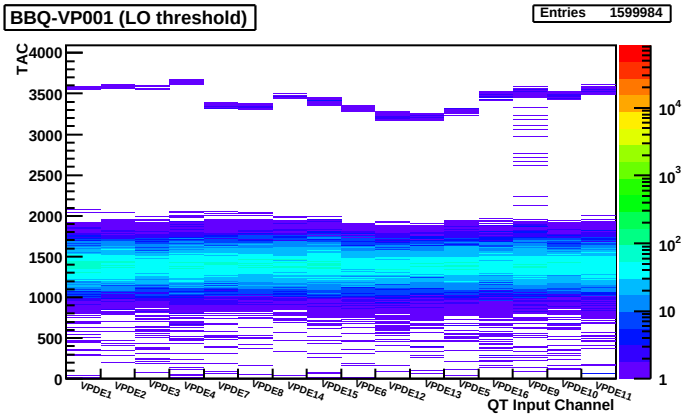

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1599984
ADC
QT Input Channel

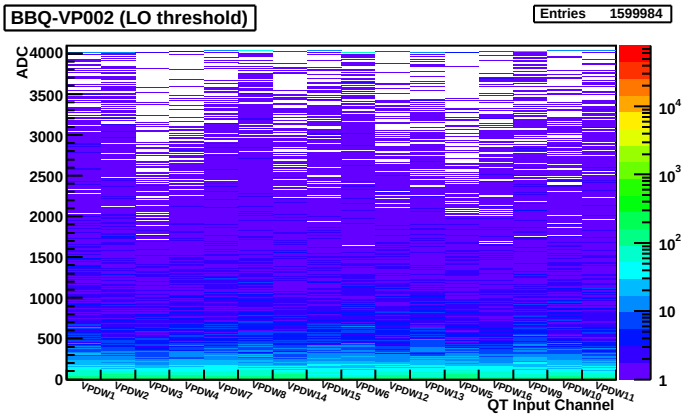

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1599984
TAC
QT Input Channel

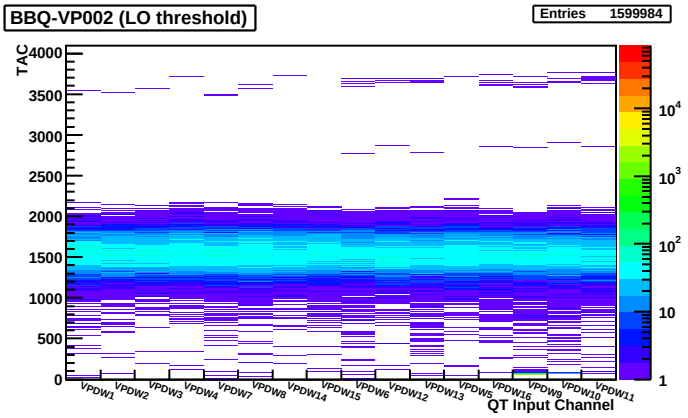

BBQ-ZD001
Entries 1599984
ADC
QT Input Channel

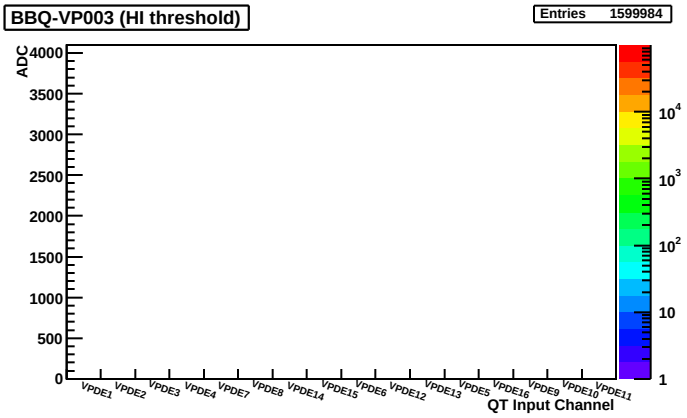

BBQ-ZD001
Entries 1599984
TAC
QT Input Channel

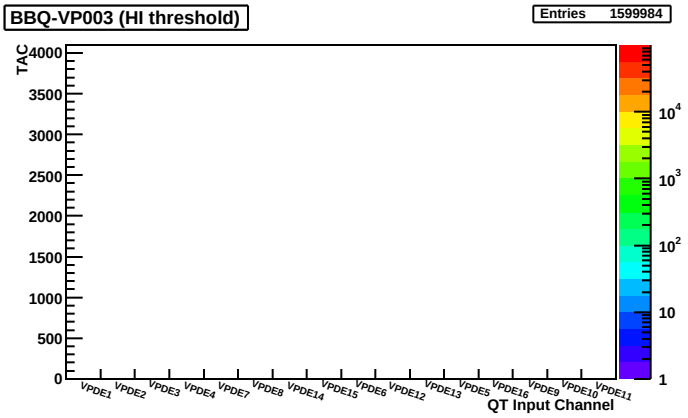

BBQ-VP001 (LO threshold)
Entries 1599984
ADC
QT Input Channel

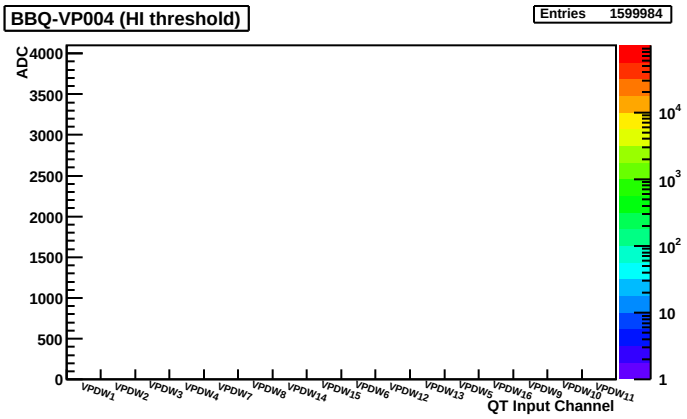

BBQ-VP001 (LO threshold)
Entries 1599984
TAC
QT Input Channel

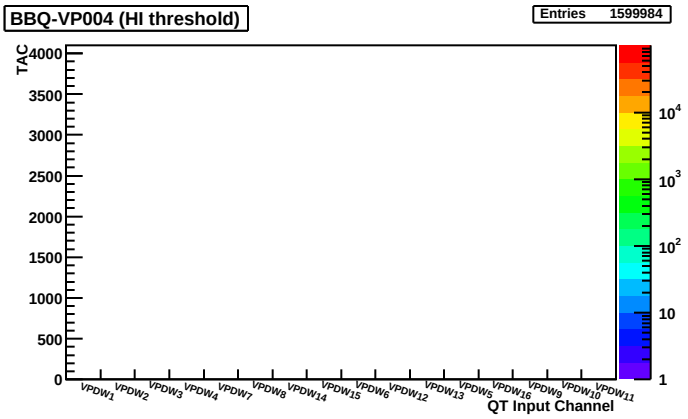

BBQ-VP002 (LO threshold)
Entries 1599984
ADC
QT Input Channel

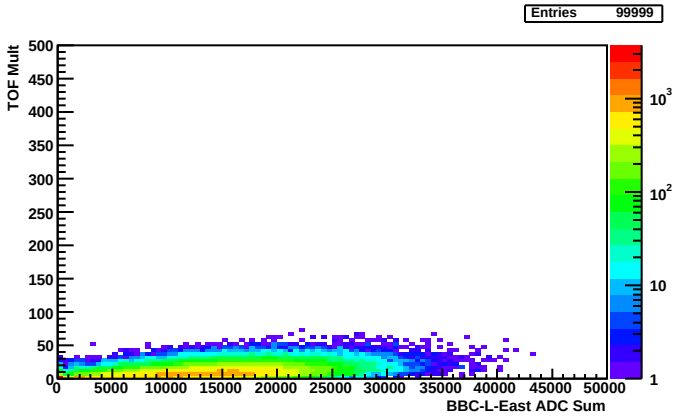

BBQ-VP002 (LO threshold)
Entries 1599984
TAC
QT Input Channel

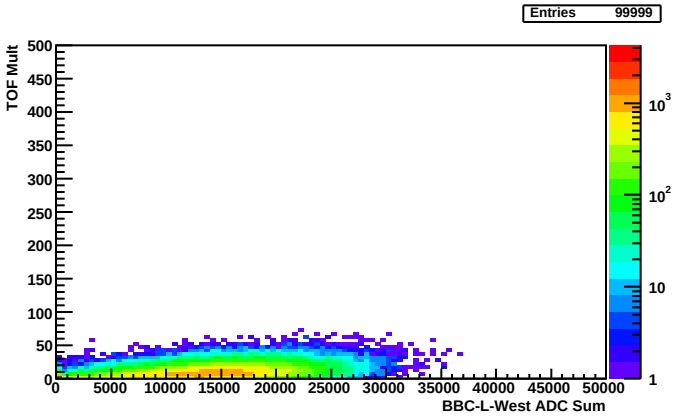

BBQ-VP003 (HI threshold)
Entries 1599984
ADC
QT Input Channel

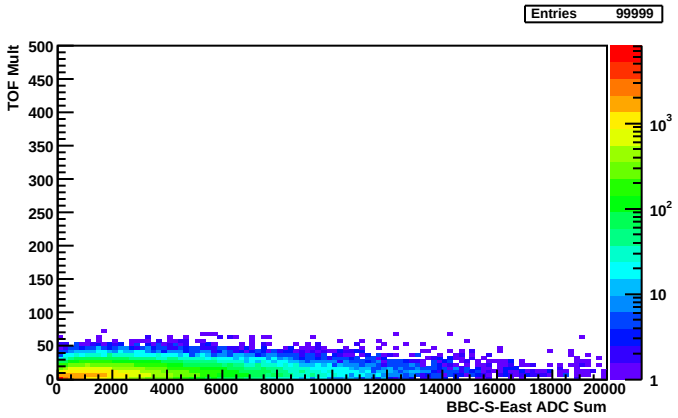

BBQ-VP003 (HI threshold)
Entries 1599984
TAC
QT Input Channel

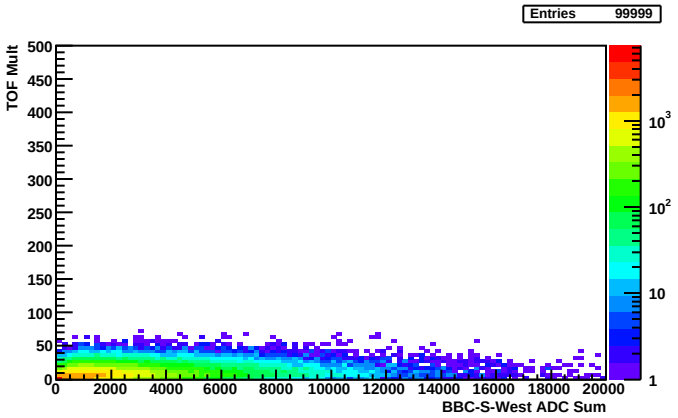

BBQ-VP004 (HI threshold)
Entries 1599984
ADC
QT Input Channel

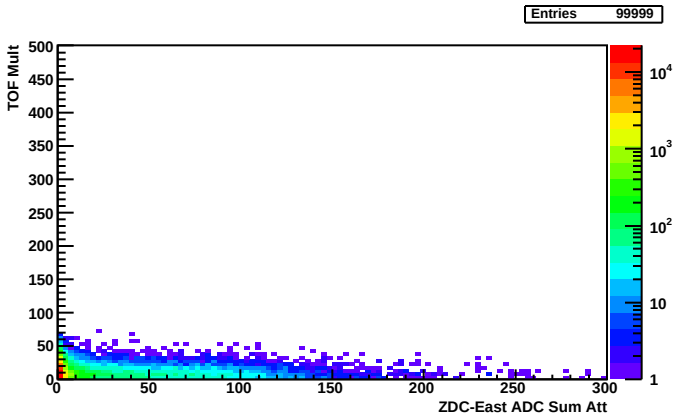

BBQ-VP004 (HI threshold)
Entries 1599984
TAC
QT Input Channel

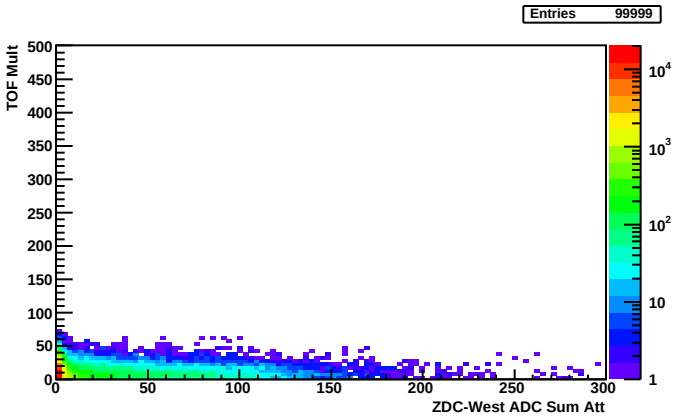

Entries 99999
TOF Mult
BBC-L-East ADC Sum

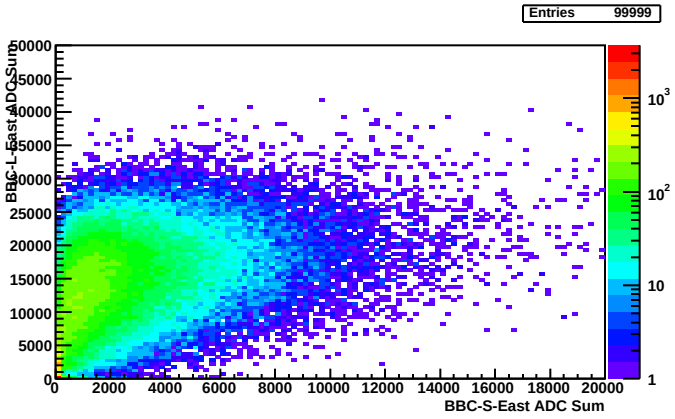

Entries 99999
TOF Mult
BBC-L-West ADC Sum

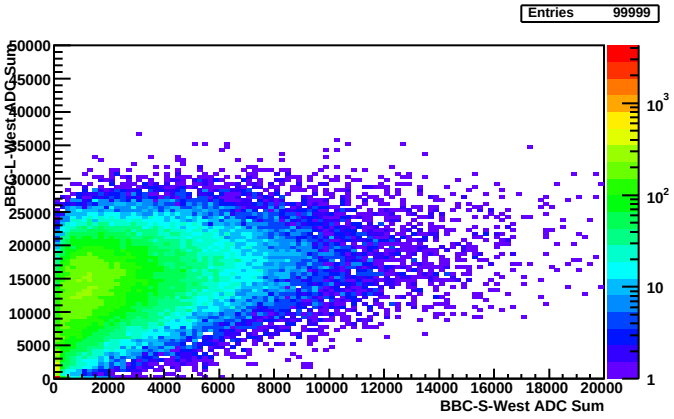

Entries 99999
TOF Mult
BBC-S-East ADC Sum

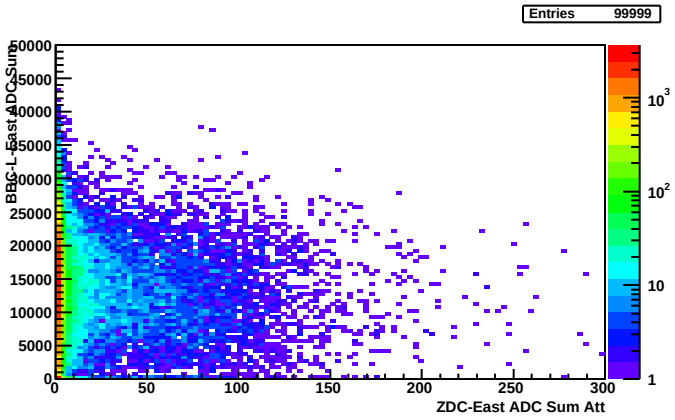

Entries 99999
TOF Mult
BBC-S-West ADC Sum

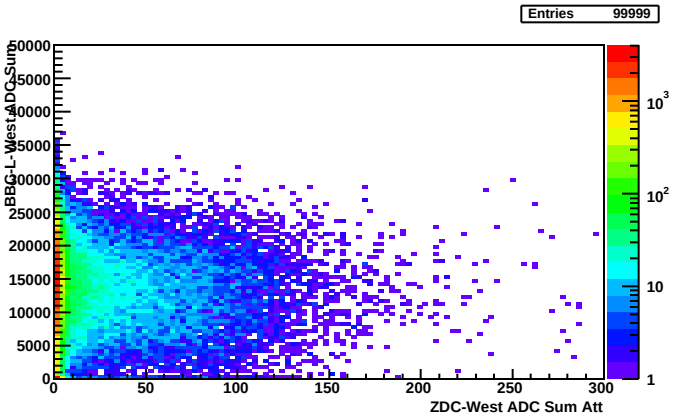

Entries 99999
TOF Mult
ZDC-East ADC Sum Att

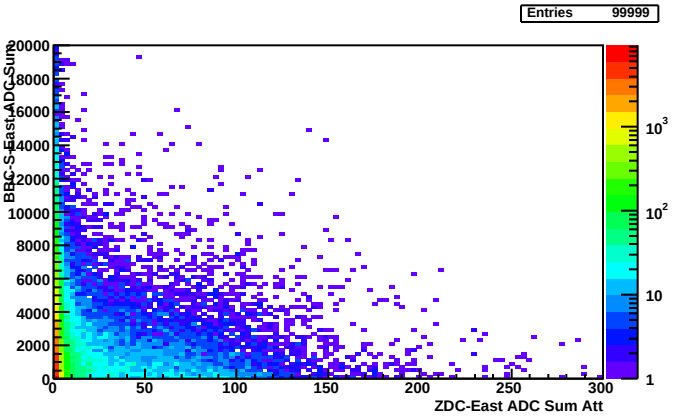

Entries 99999
TOF Mult
ZDC-West ADC Sum Att

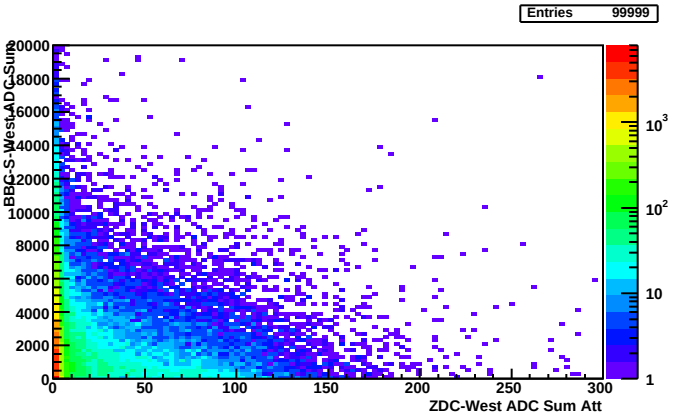

Entries 99999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

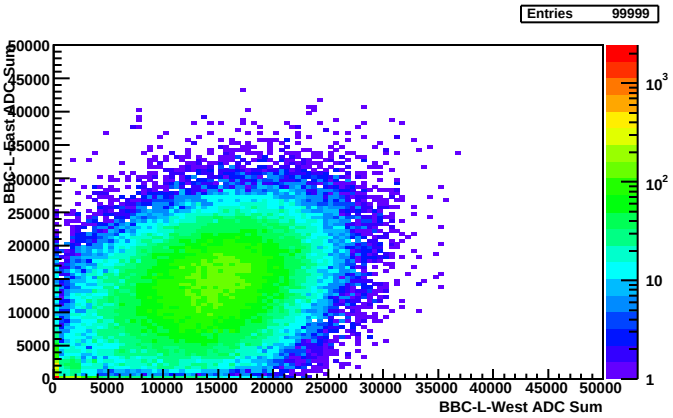

Entries 99999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

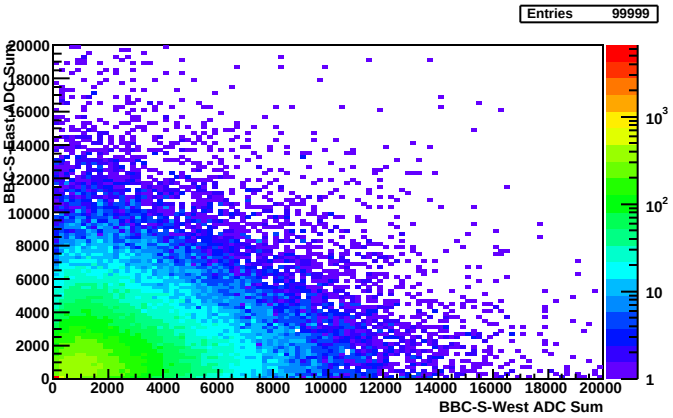

Entries 99999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

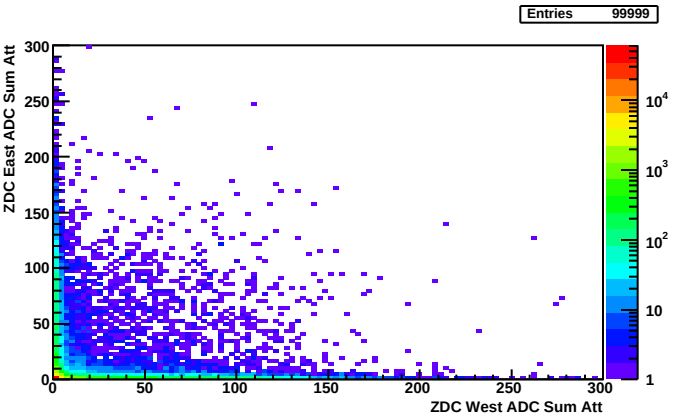

Entries 99999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

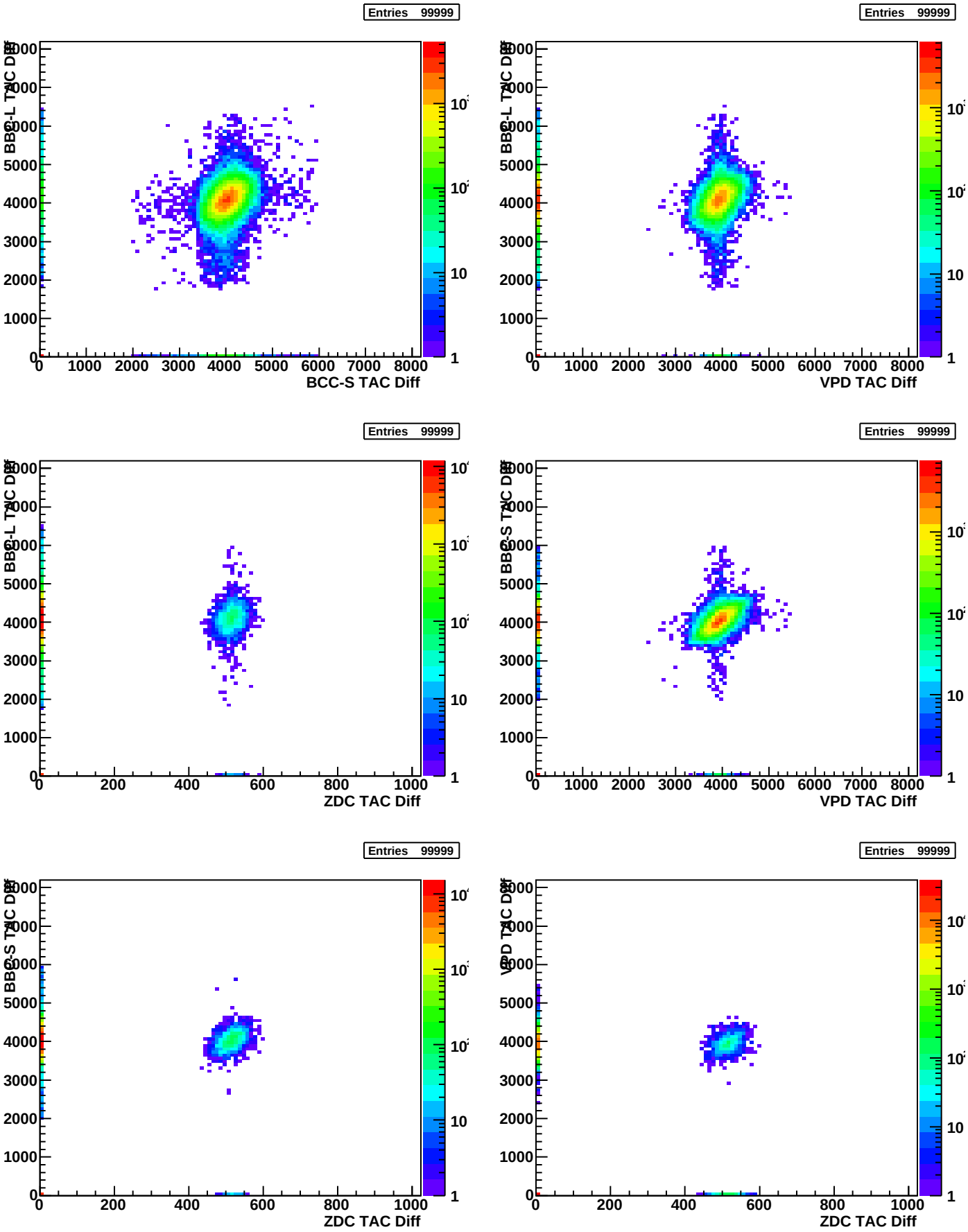

Entries 99999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

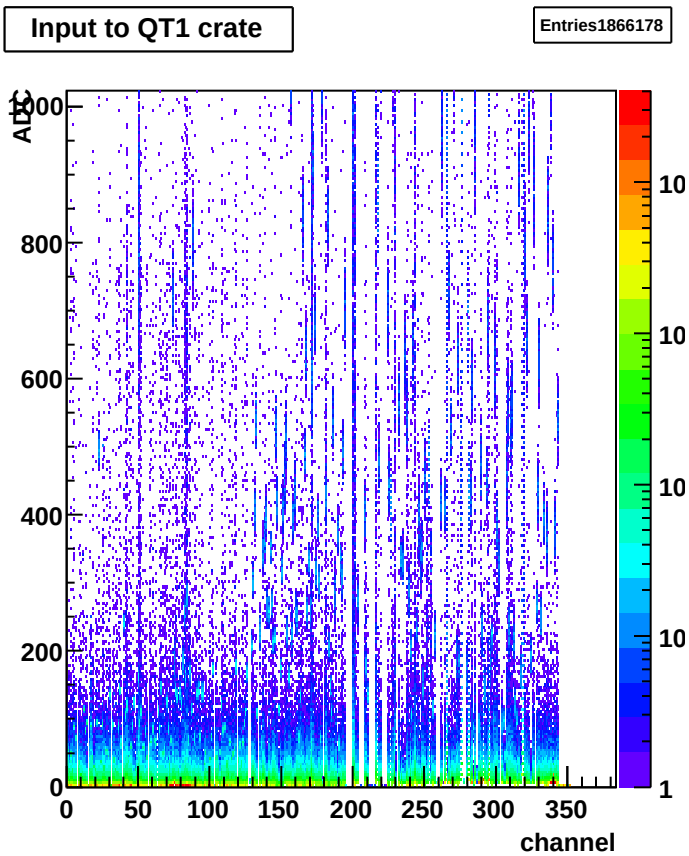

Entries 99999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

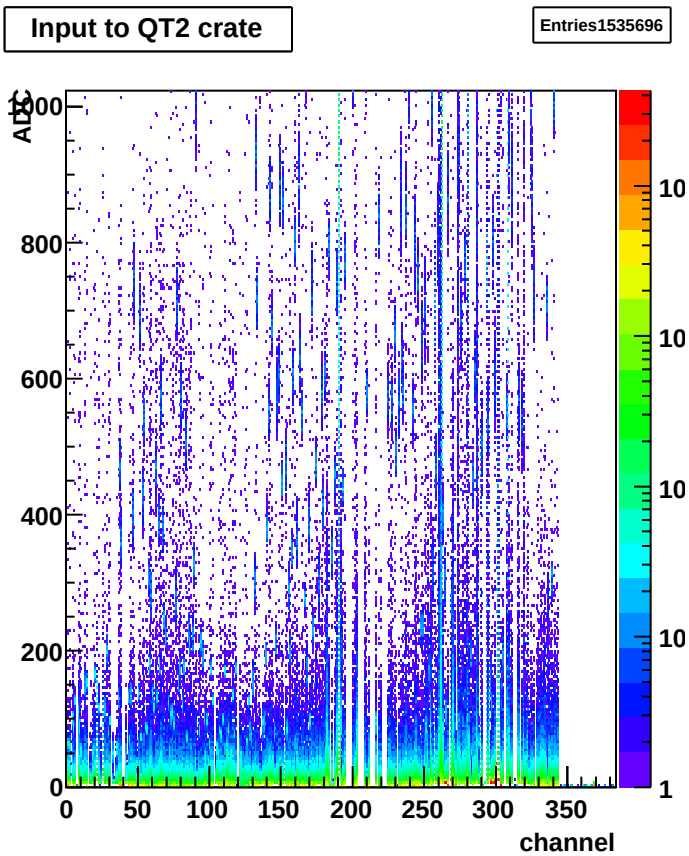

Entries 99999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

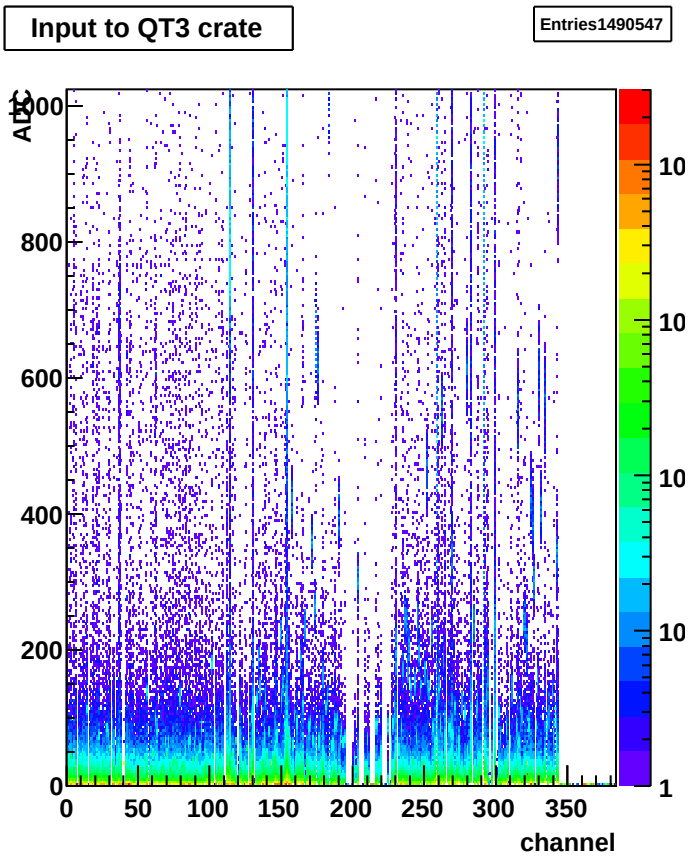

Entries 99999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

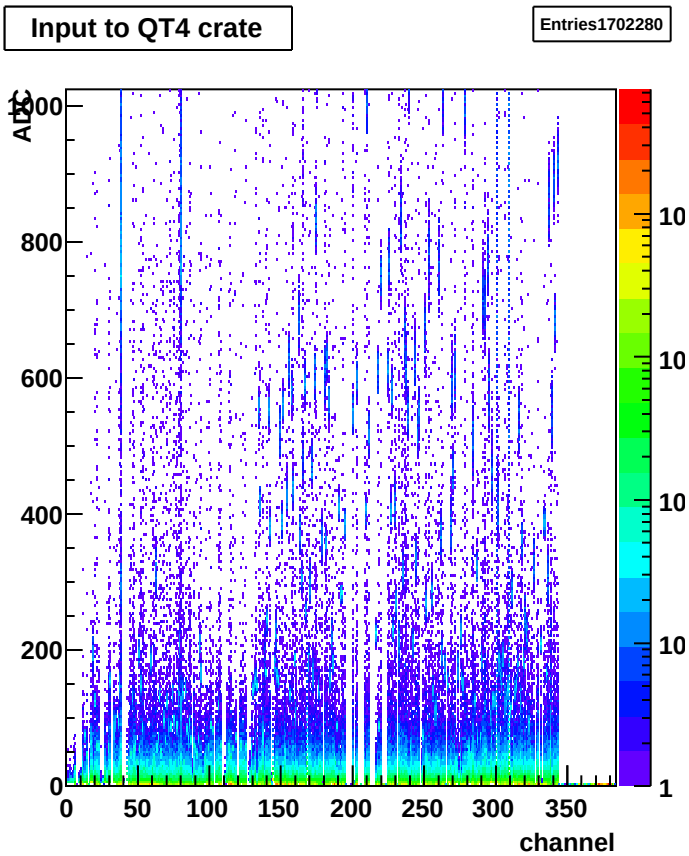

Entries 99999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

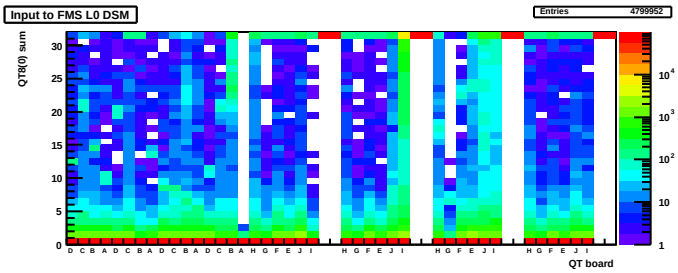

Entries 99999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 99999
BBC-L TAC Diff
VPD TAC Diff
Entries 99999
BBC-L TAC Diff
ZDC TAC Diff
Entries 99999
BBC-S TAC Diff
VPD TAC Diff
Entries 99999
BBC-S TAC Diff
ZDC TAC Diff
Entries 99999
VPD TAC Diff
ZDC TAC Diff

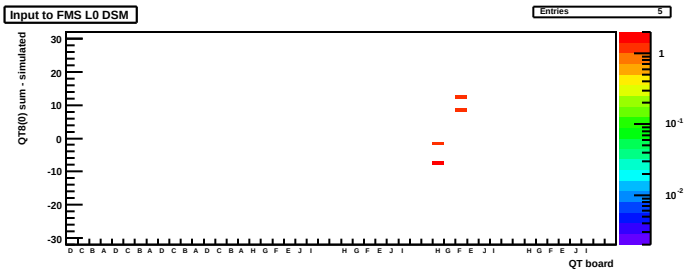

Input to QT1 crate
Entries1866178
ADC
channel

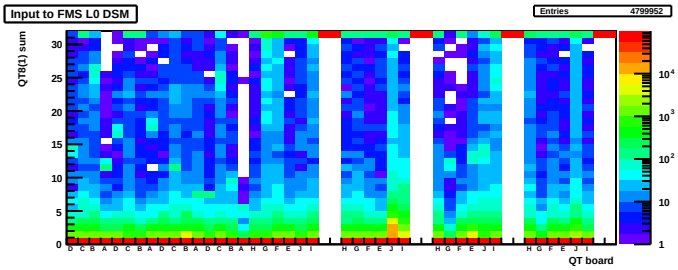

Input to QT2 crate
Entries1535696
ADC
channel

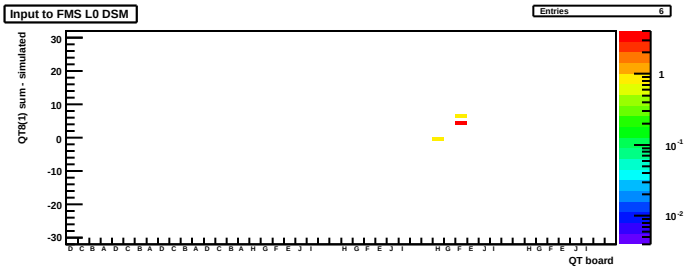

Input to QT3 crate
Entries1490547
ADC
channel

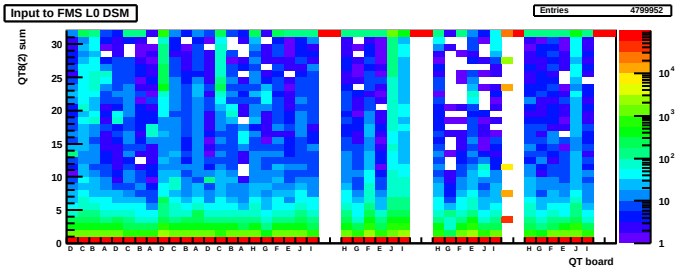

Input to QT4 crate
Entries1702280
ADC
channel

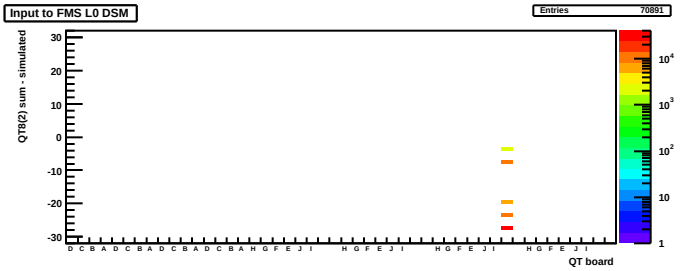

Input to FMS L0 DSM
Entries 4799952
QT8(0) sum
QT board

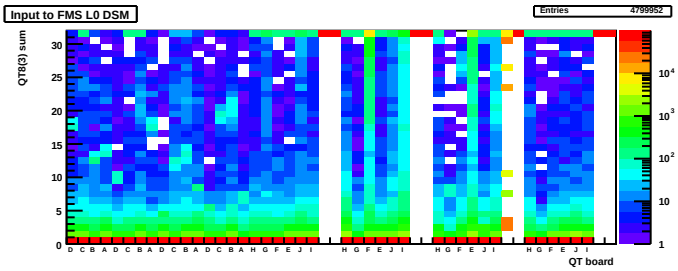

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

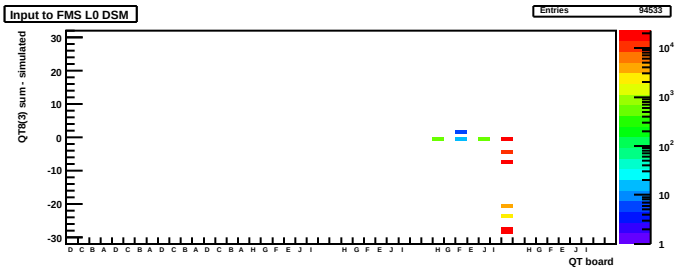

Input to FMS L0 DSM
Entries 4799952
QT8(1) sum
QT board

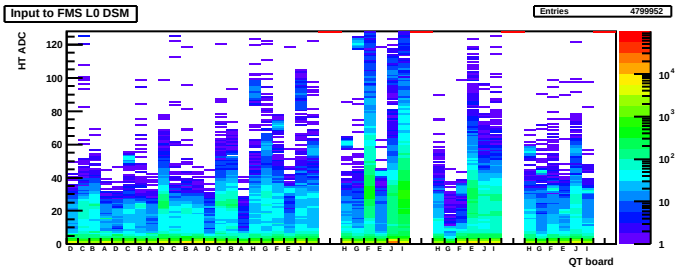

Input to FMS L0 DSM
Entries 6
QT8(1) sum - simulated
QT board

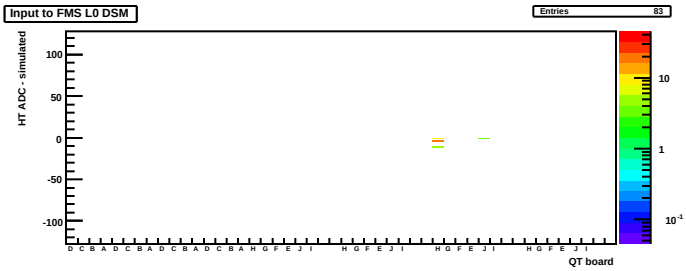

Input to FMS L0 DSM
Entries 4799952
QT8(2) sum
QT board

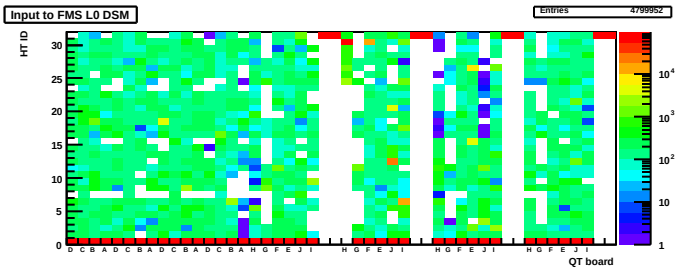

Input to FMS L0 DSM
Entries 70891
QT8(2) sum - simulated
QT board

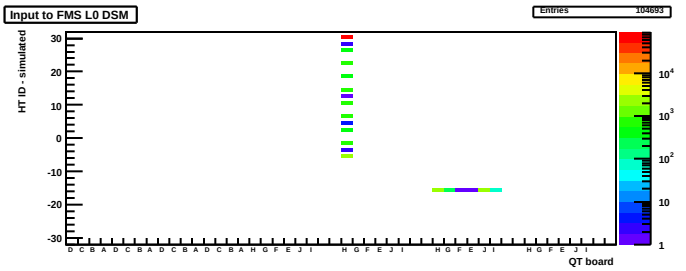

Input to FMS L0 DSM
Entries 4799952
QT8(3) sum
QT board

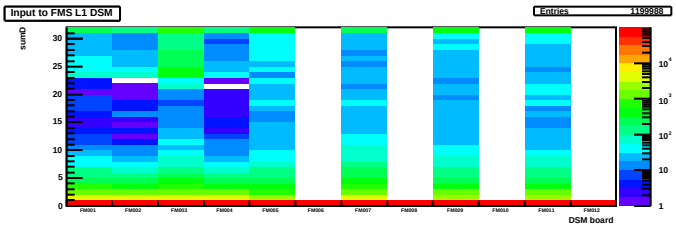

Input to FMS L0 DSM
Entries 94533
QT8(3) sum - simulated
QT board

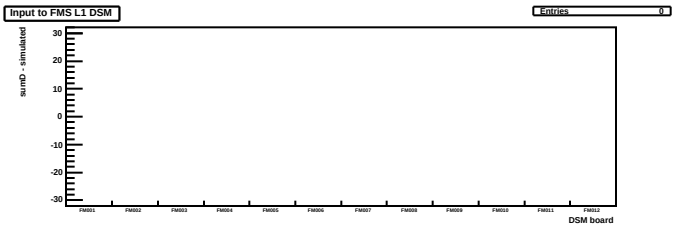

Input to FMS L0 DSM
Entries 4799952
HT ADC
QT board

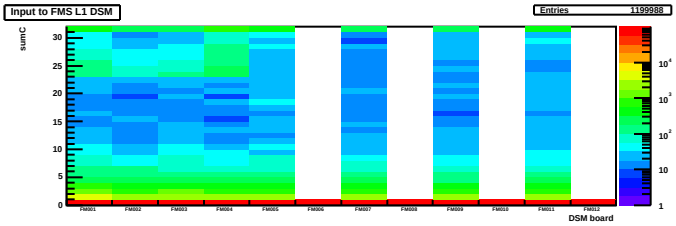

Input to FMS L0 DSM
Entries 83
HT ADC - simulated
QT board

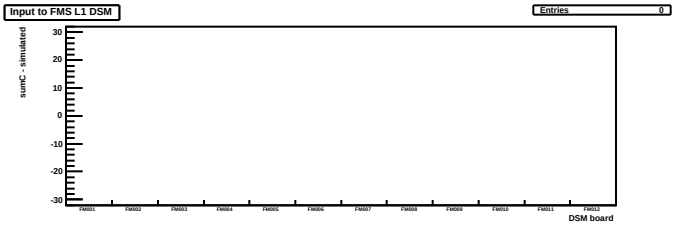

Input to FMS L0 DSM
Entries 4799952
HT ID
QT board

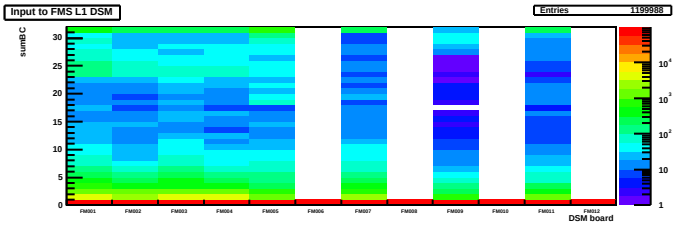

Input to FMS L0 DSM
Entries 104693
HT ID - simulated
QT board

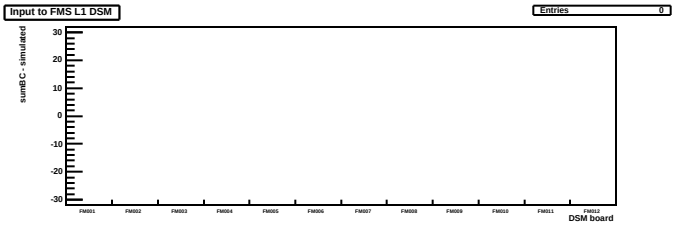

Input to FMS L1 DSM
Entries 1199988
sumD
DSM board

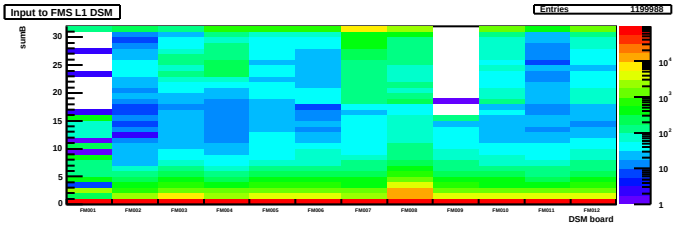

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

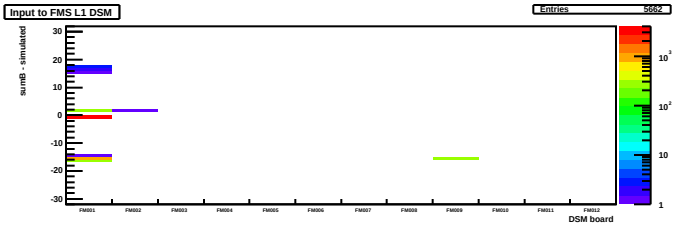

Input to FMS L1 DSM
Entries 1199988
sumC
DSM board

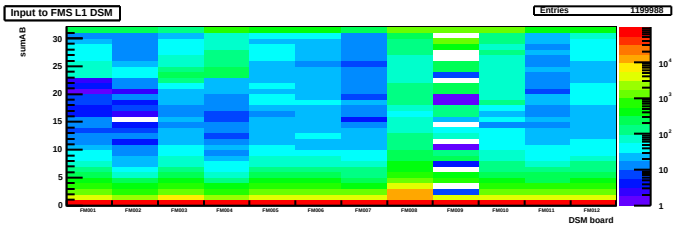

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

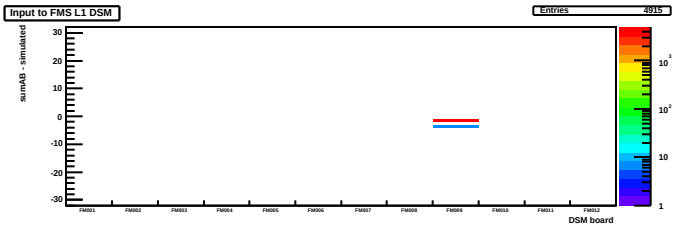

Input to FMS L1 DSM
Entries 1199988
sumBC
DSM board

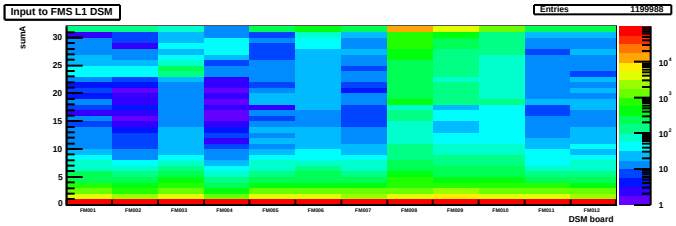

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

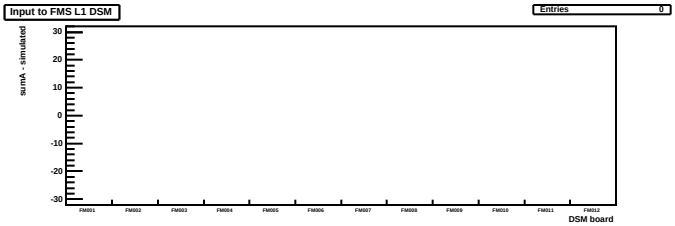

Input to FMS L1 DSM
Entries 1199988
sumB
DSM board

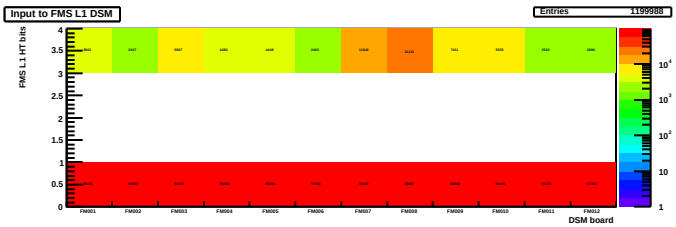

Input to FMS L1 DSM
Entries 5662
sumB - simulated
DSM board

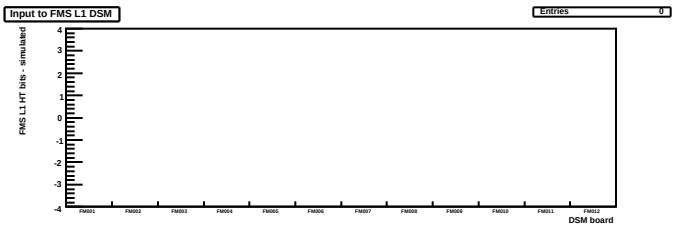

Input to FMS L1 DSM
Entries 1199988
sumAB
DSM board

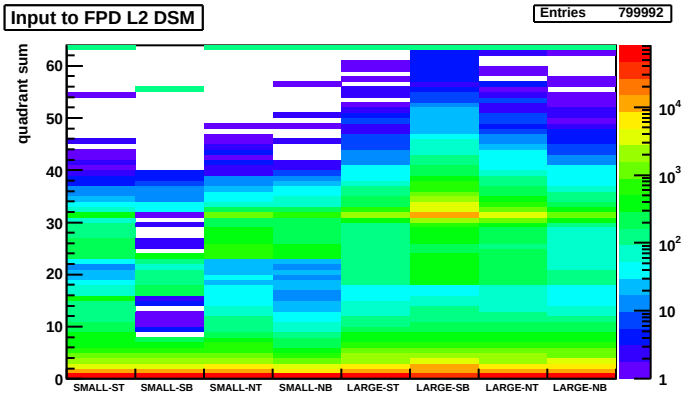

Input to FMS L1 DSM
Entries 4915
sumAB - simulated
DSM board

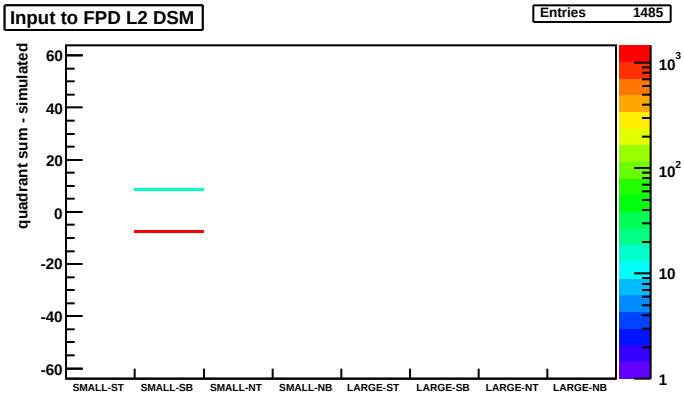

Input to FMS L1 DSM
Entries 1199988
sumA
DSM board

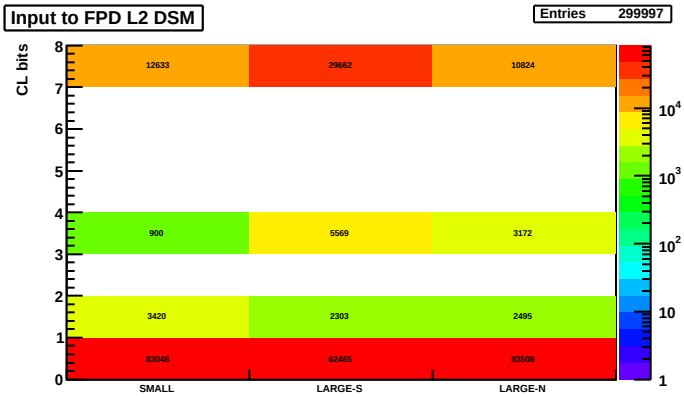

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

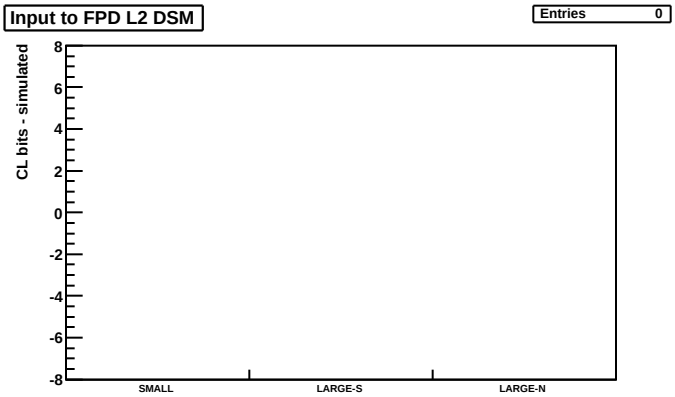

Input to FMS L1 DSM
Entries 1199988
FMS L1 HT bits
DSM board

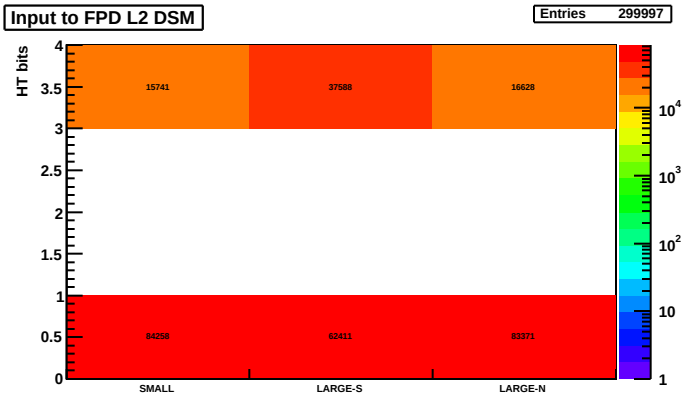

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

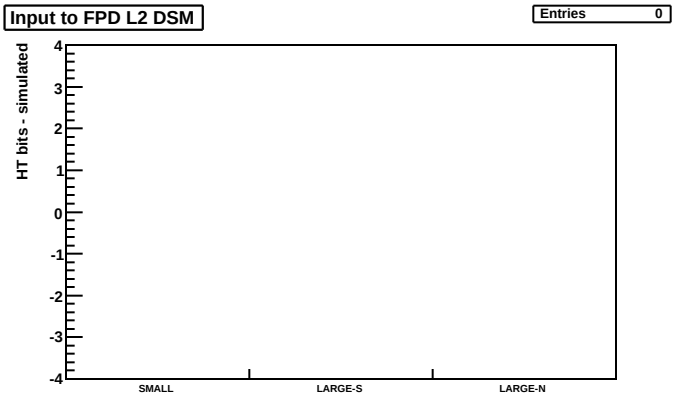

Input to FPD L2 DSM
Entries 799992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

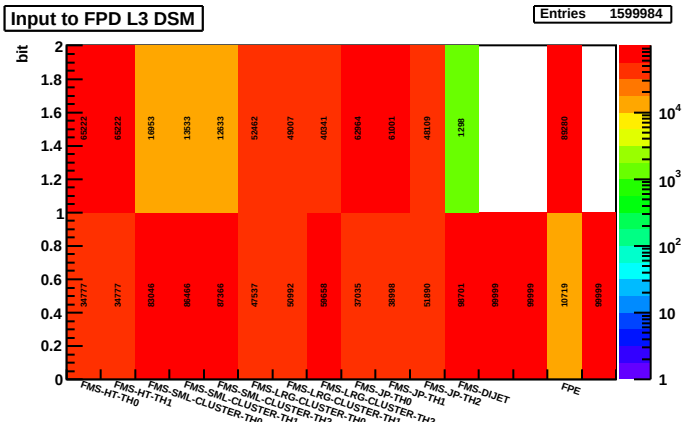

Input to FPD L2 DSM
Entries 1485
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

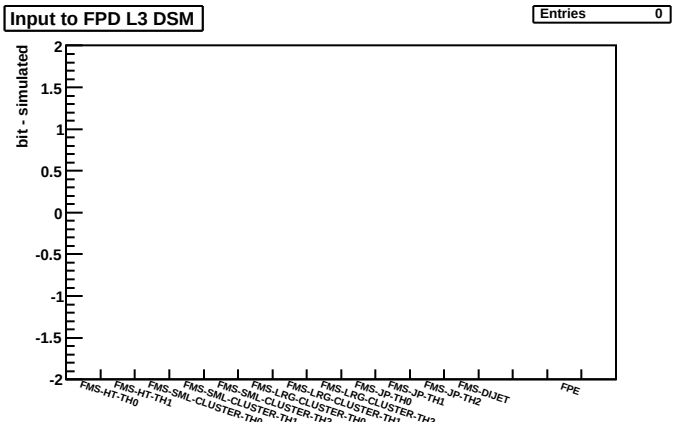

Input to FPD L2 DSM
Entries 299997
CL bits
12633
29662
10824
900
5569
3172
3420
2303
2495
83046
62465
83508
SMALL
LARGE-S
LARGE-N

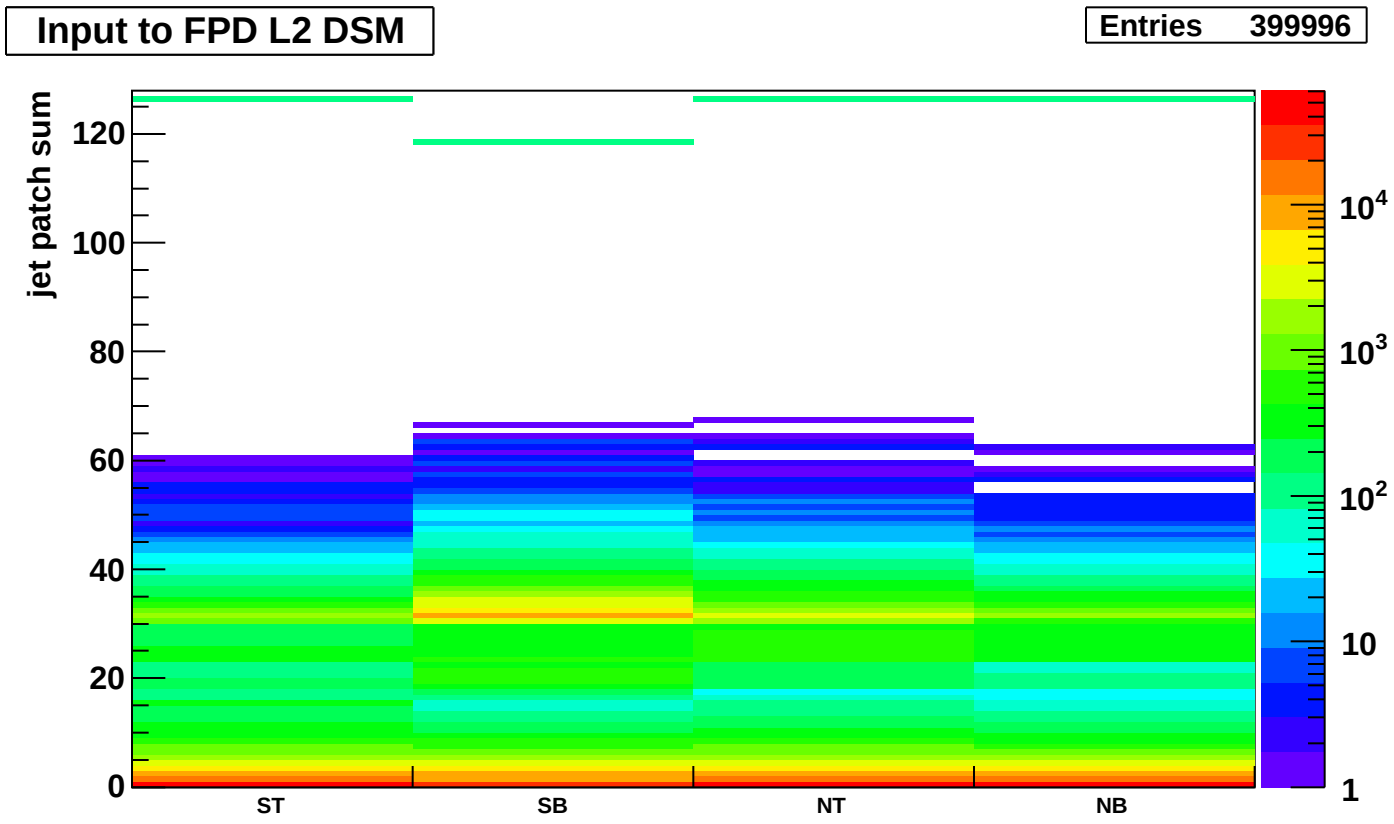

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

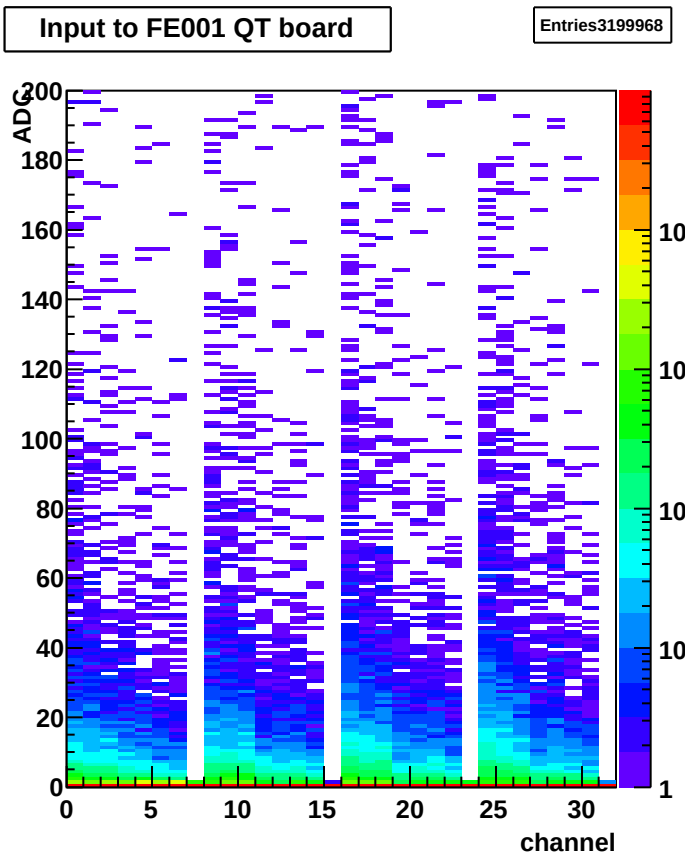

Input to FPD L2 DSM
Entries 299997
HT bits
15741
37588
16628
84258
62411
83371
SMALL
LARGE-S
LARGE-N

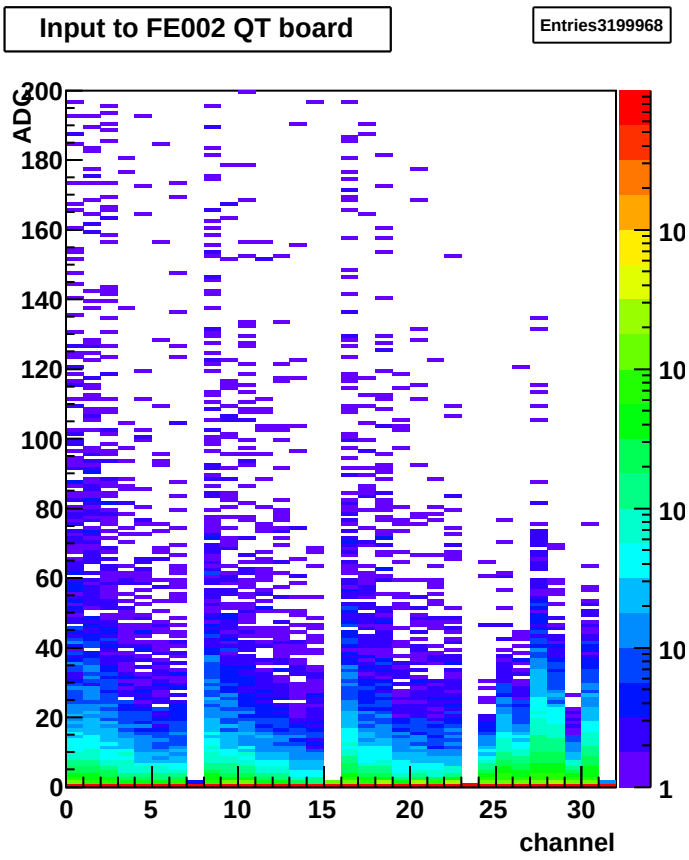

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

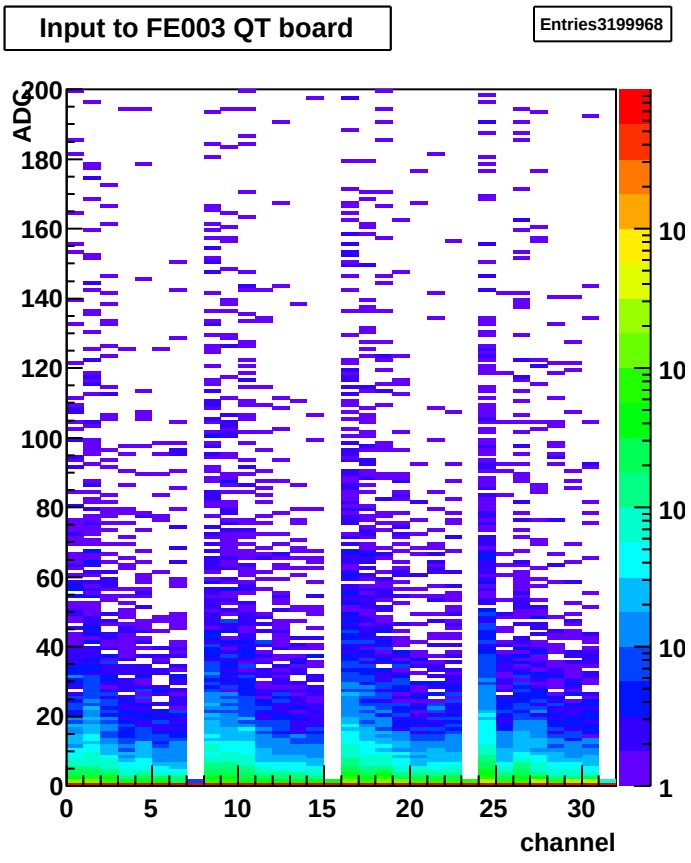

Input to FPD L3 DSM
Entries 1599984
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

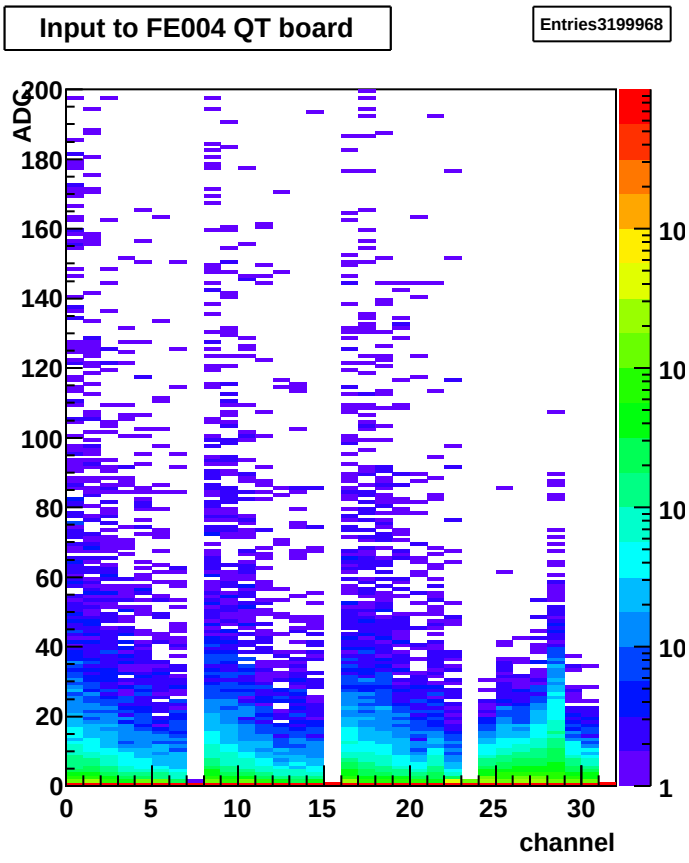

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

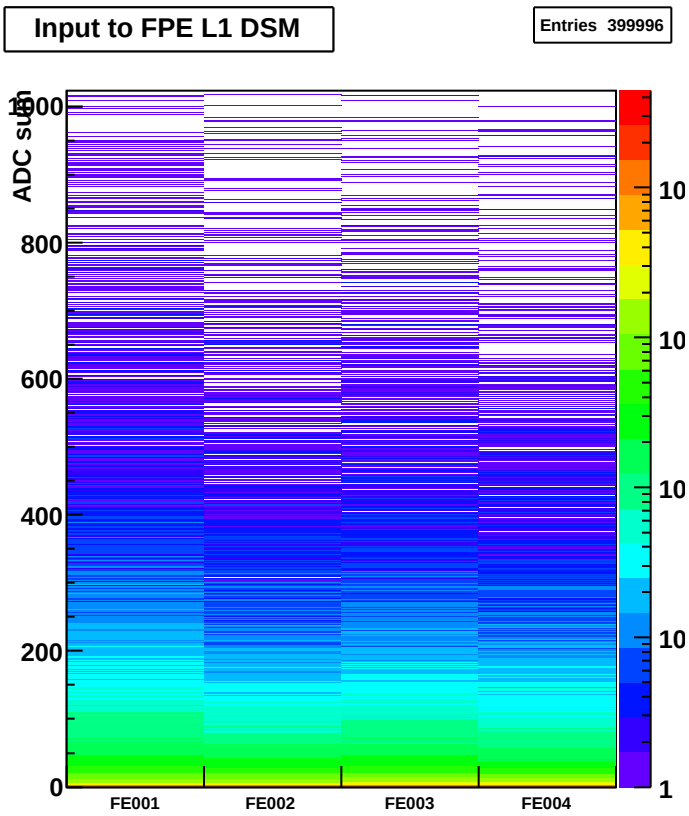

Input to FPD L2 DSM
Entries 399996
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

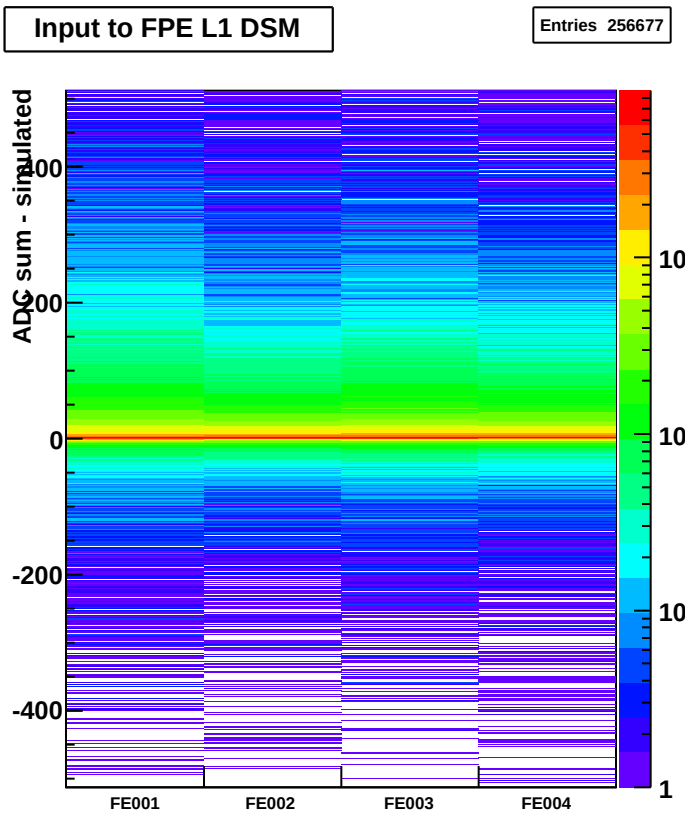

Input to FE001 QT board
Entries3199968
ADC
channel

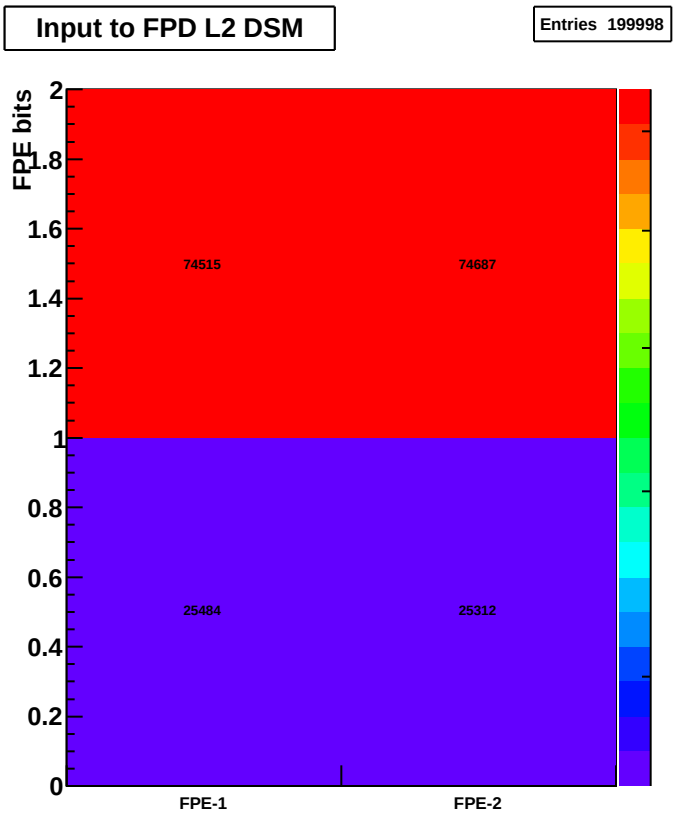

Input to FE002 QT board
Entries3199968
ADC
channel

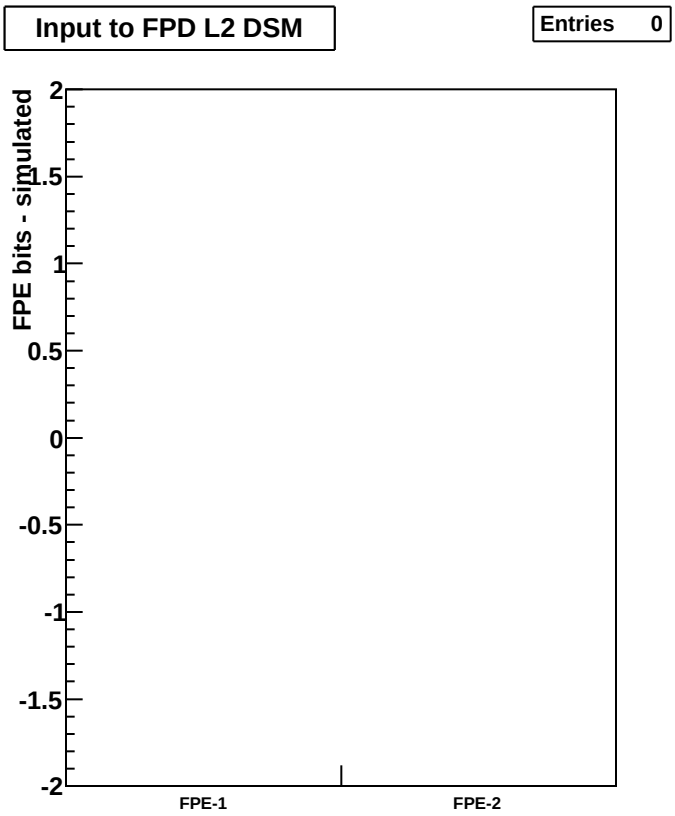

Input to FE003 QT board
Entries3199968
ADC
channel


Input to FE004 QT board
Entries3199968
ADC
channel


Input to FPE L1 DSM
Entries 399996
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 256677
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 199998
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
74515
74687
25484
25312
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2