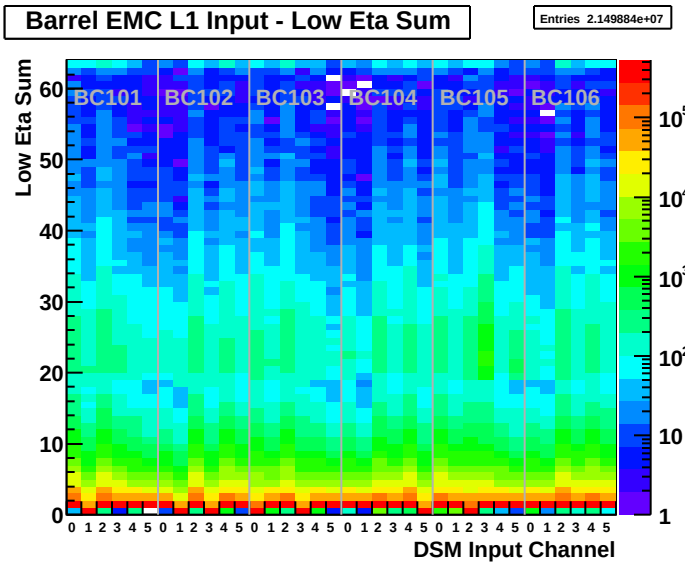
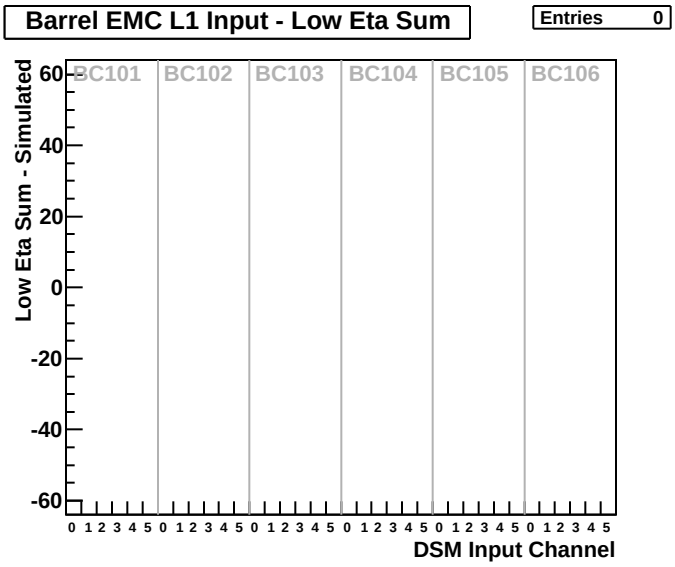
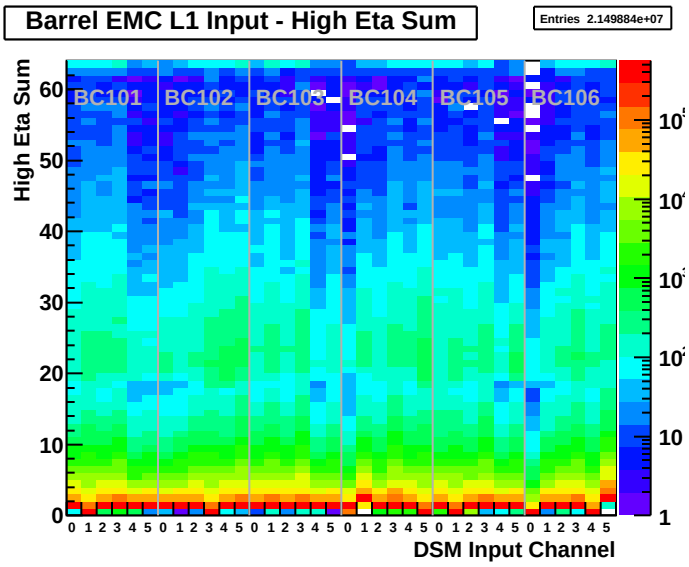
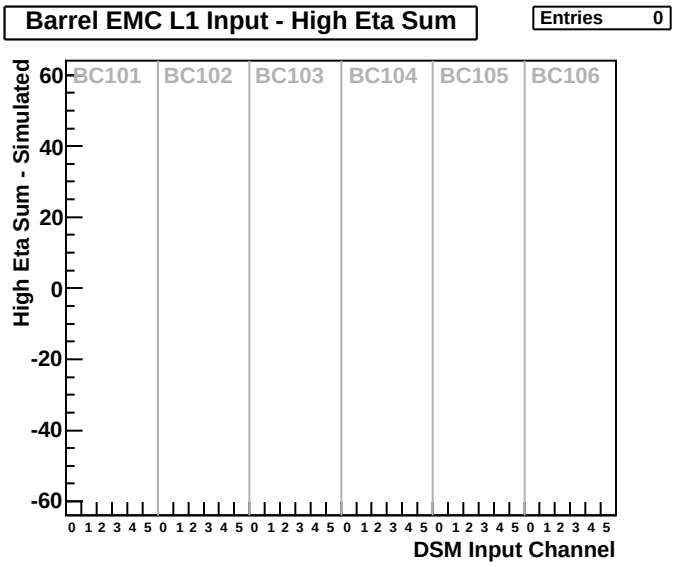
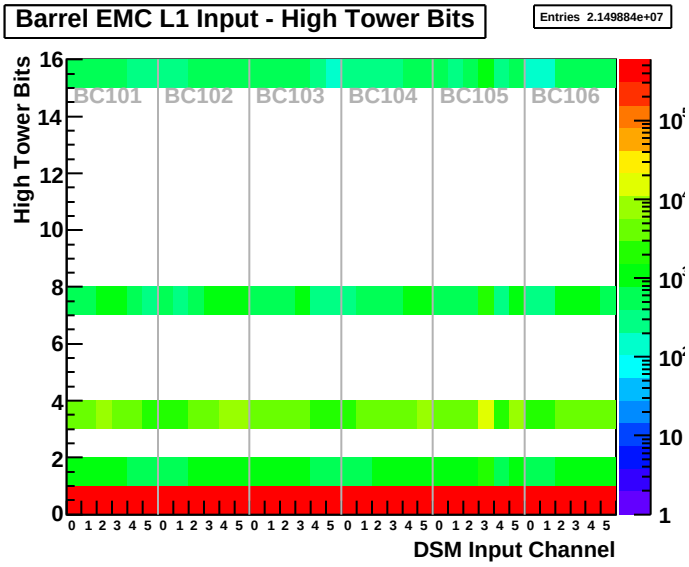
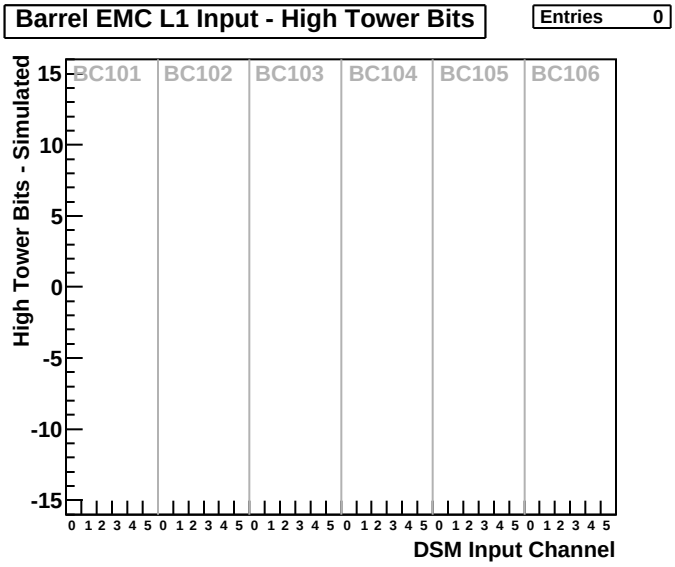

Barrel EMC L1 Input - Low Eta Sum
Entries 2.149884e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

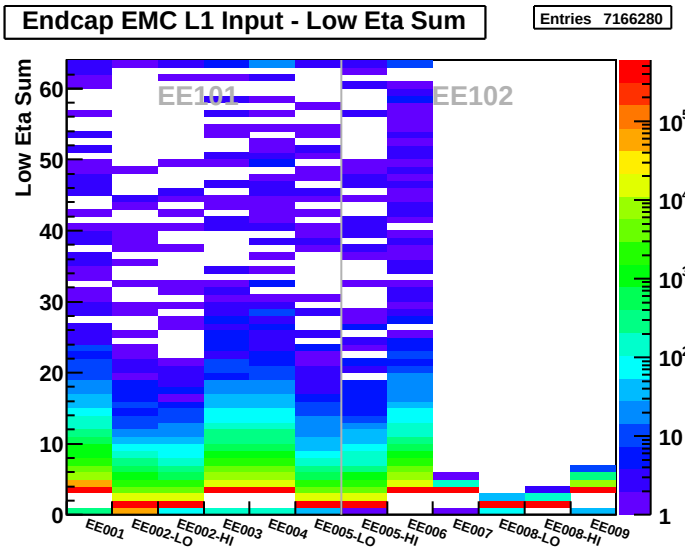

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

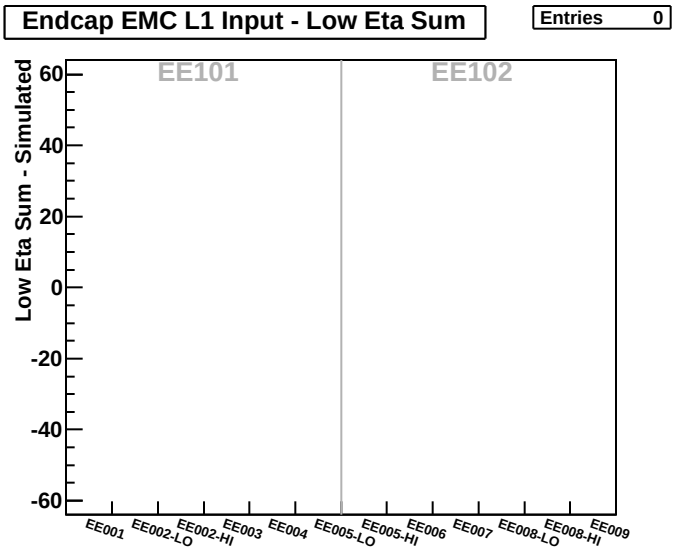

Barrel EMC L1 Input - High Eta Sum
Entries 2.149884e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

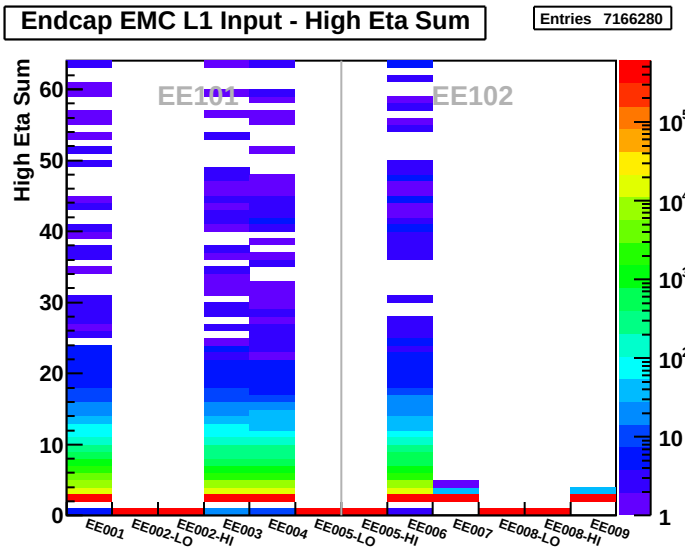

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

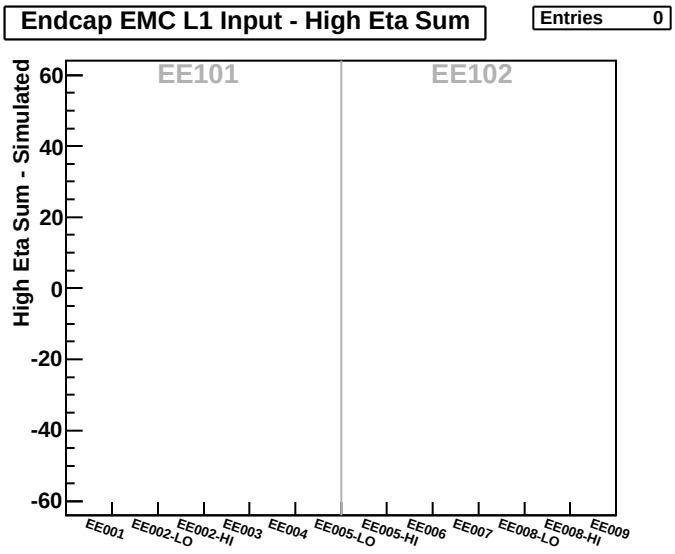

Barrel EMC L1 Input - High Tower Bits
Entries 2.149884e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

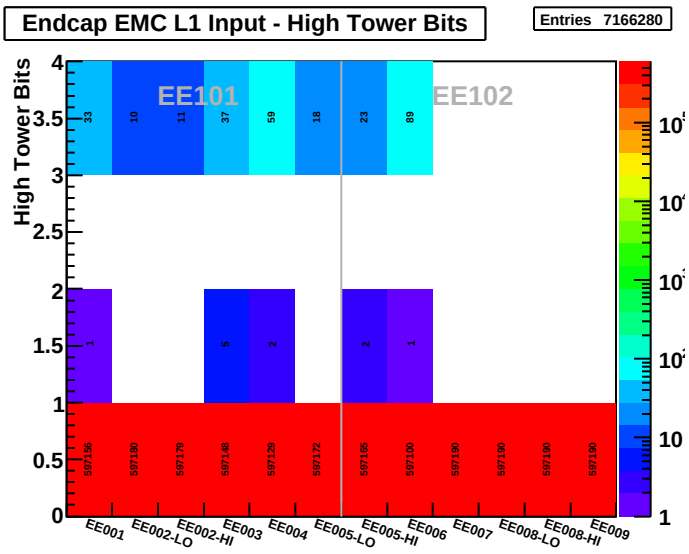

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

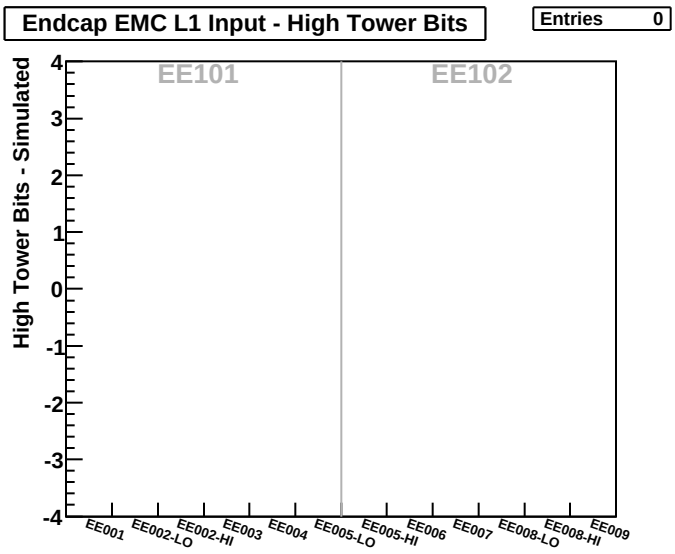

Endcap EMC L1 Input - Low Eta Sum
Entries 7166280
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

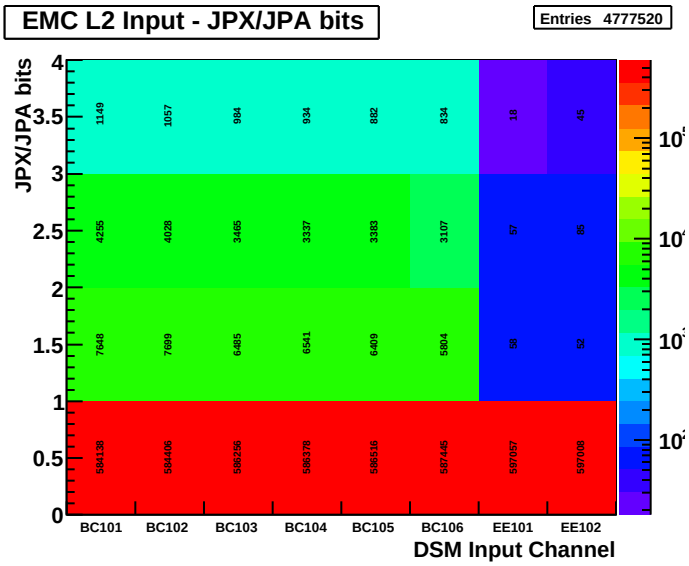

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

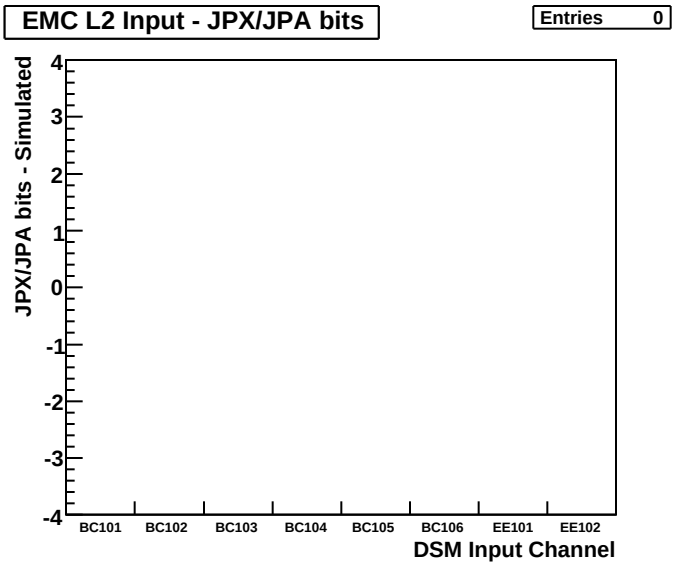

Endcap EMC L1 Input - High Eta Sum
Entries 7166280
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

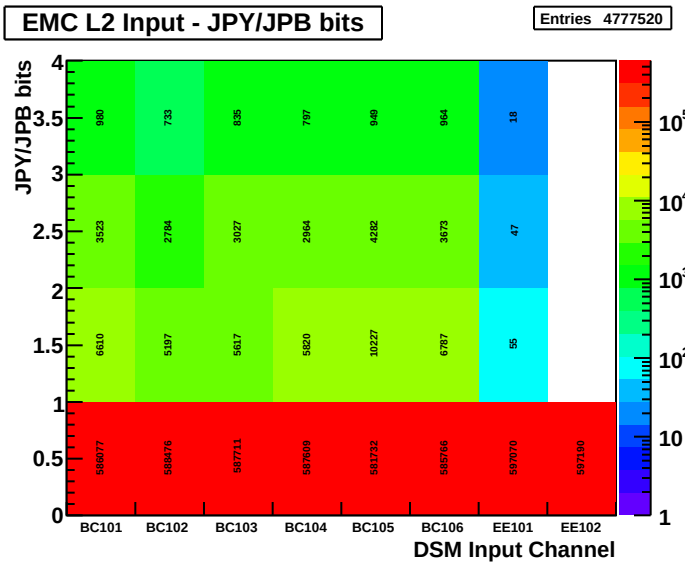

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

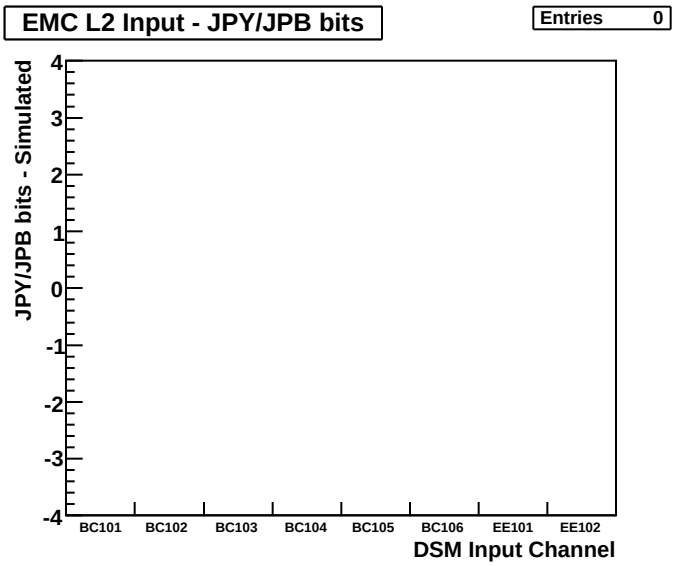

Endcap EMC L1 Input - High Tower Bits
Entries 7166280
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

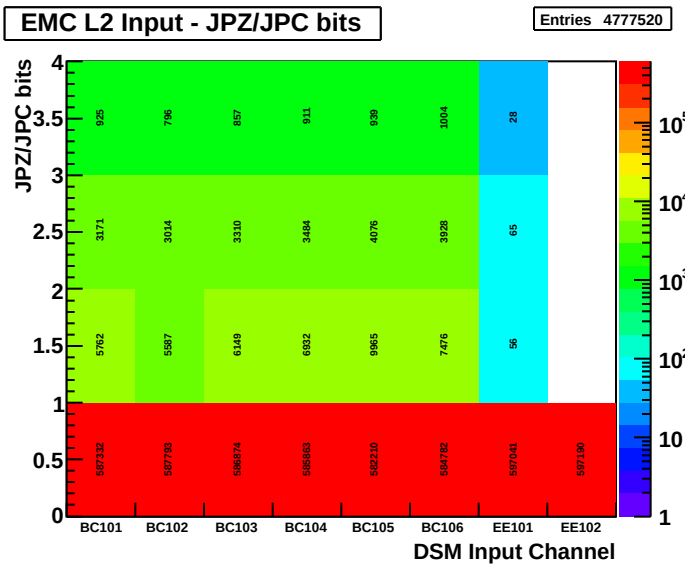

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

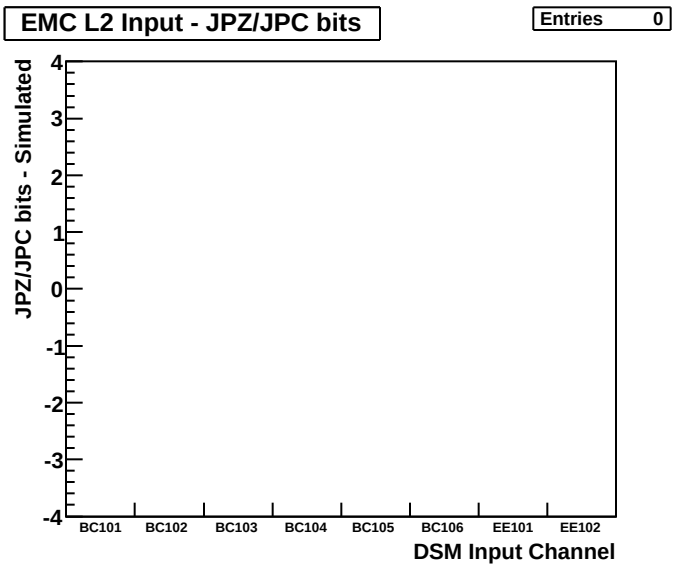

EMC L2 Input - JPX/JPA bits
Entries 4777520
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1149 1057 984 934 882 834 18 45
4255 4028 3465 3337 3383 3107 57 85
7648 7899 6485 6541 6409 5804 58 52
584138 584406 582256 583378 586516 587445 597057 597008

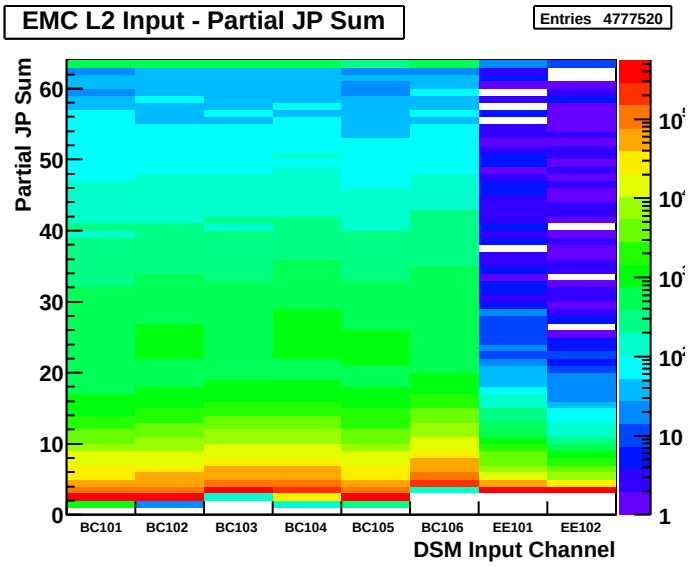

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

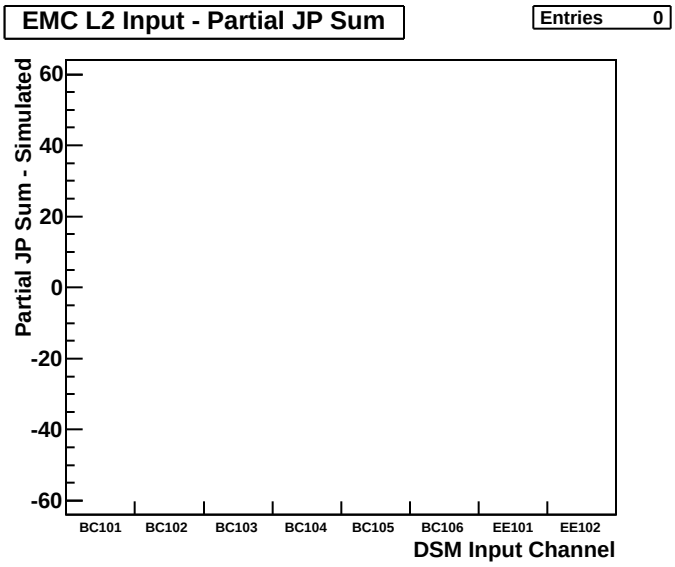

EMC L2 Input - JPY/JPB bits
Entries 4777520
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
980 733 835 797 949 964 18
3523 2784 3027 2964 4282 3673 47
6610 5197 5617 5820 10227 6787 55
586077 588476 587711 587609 581732 585766 597070 597190

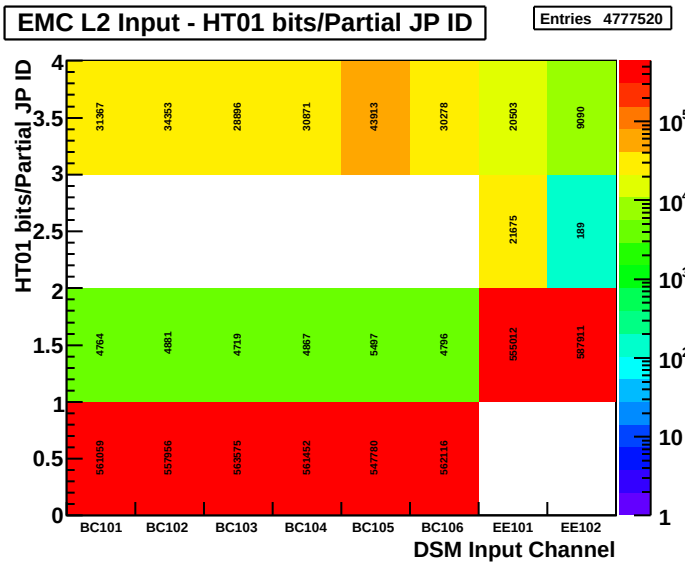

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

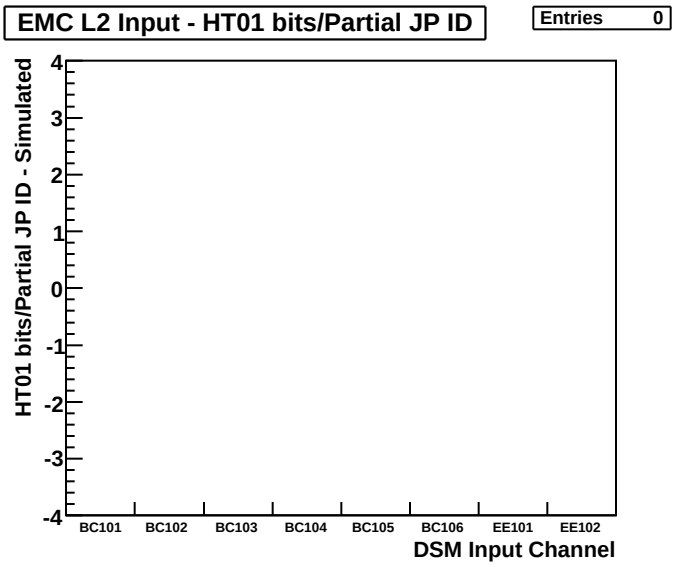

EMC L2 Input - JPZ/JPC bits
Entries 4777520
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
925 796 857 911 939 1004 28
3171 3014 3310 3484 4076 3928 65
5762 5587 6149 6932 9965 7476 56
587332 587793 586874 585863 582210 584782 597041 597190

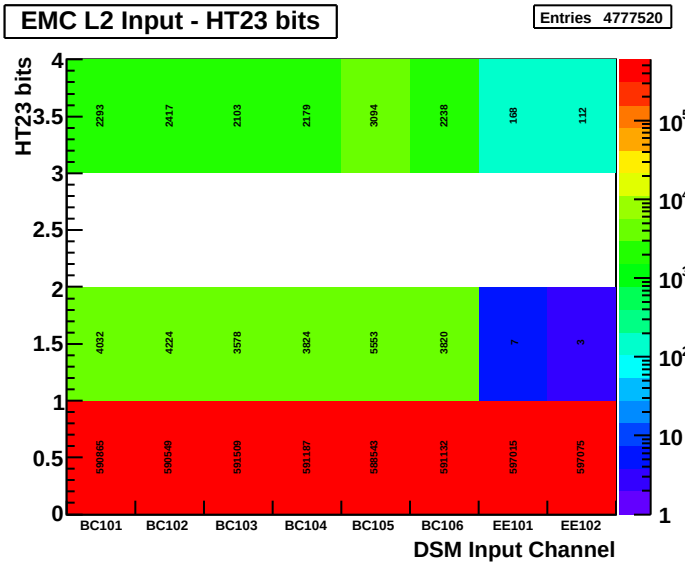

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

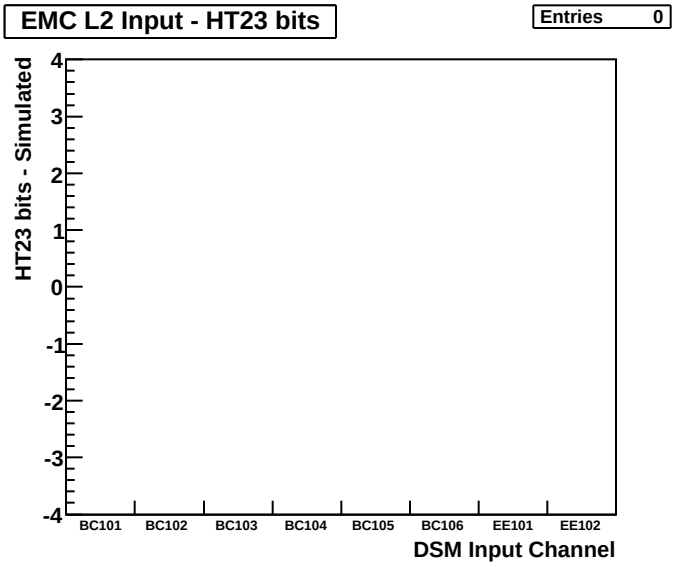

EMC L2 Input - Partial JP Sum
Entries 4777520
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

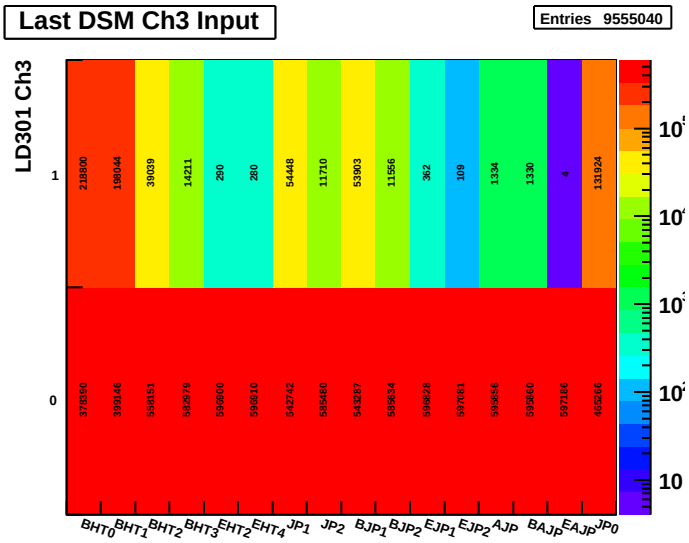

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

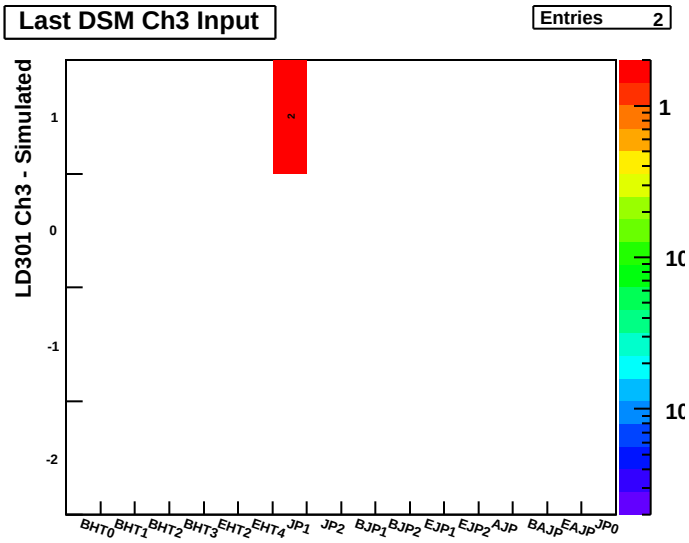

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4777520
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

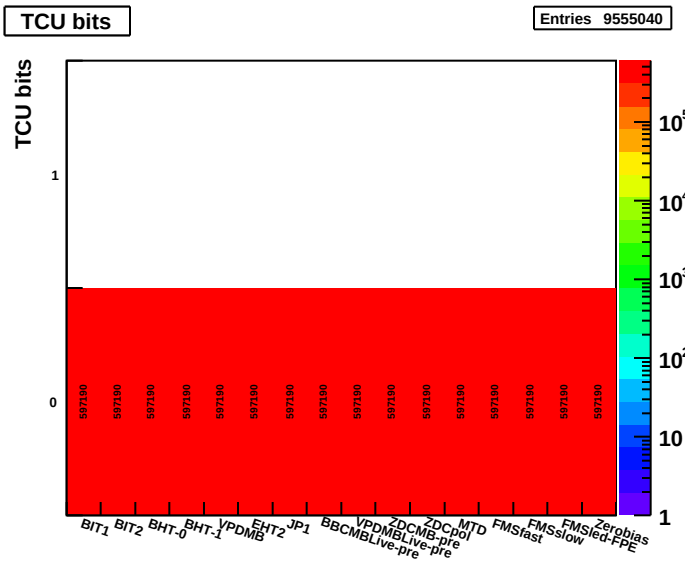

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

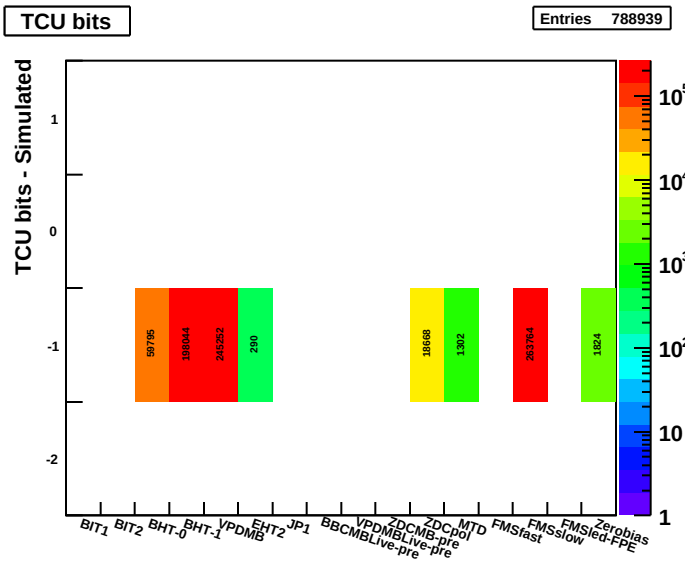

EMC L2 Input - HT23 bits
Entries 4777520
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

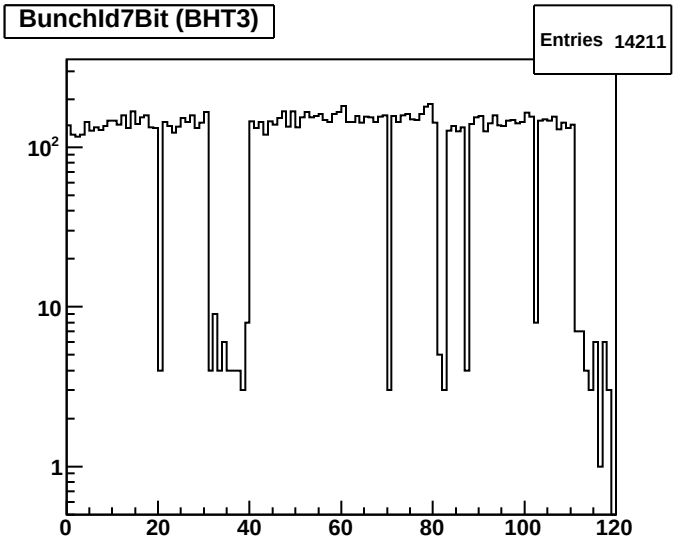

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

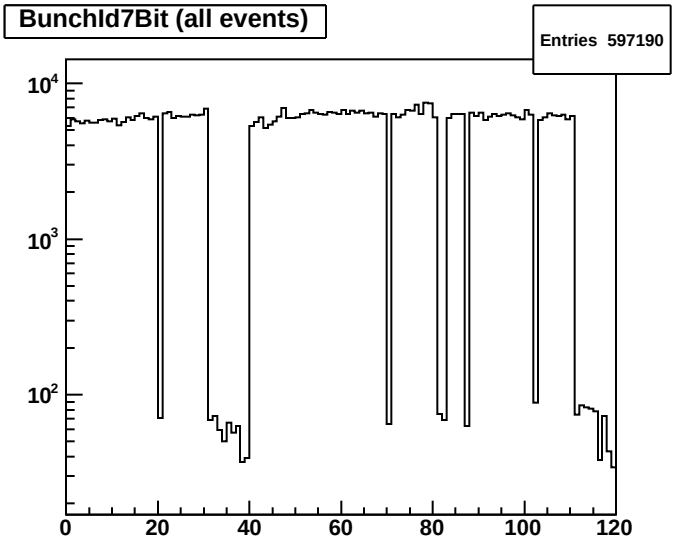

Last DSM Ch3 Input
Entries 9555040
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

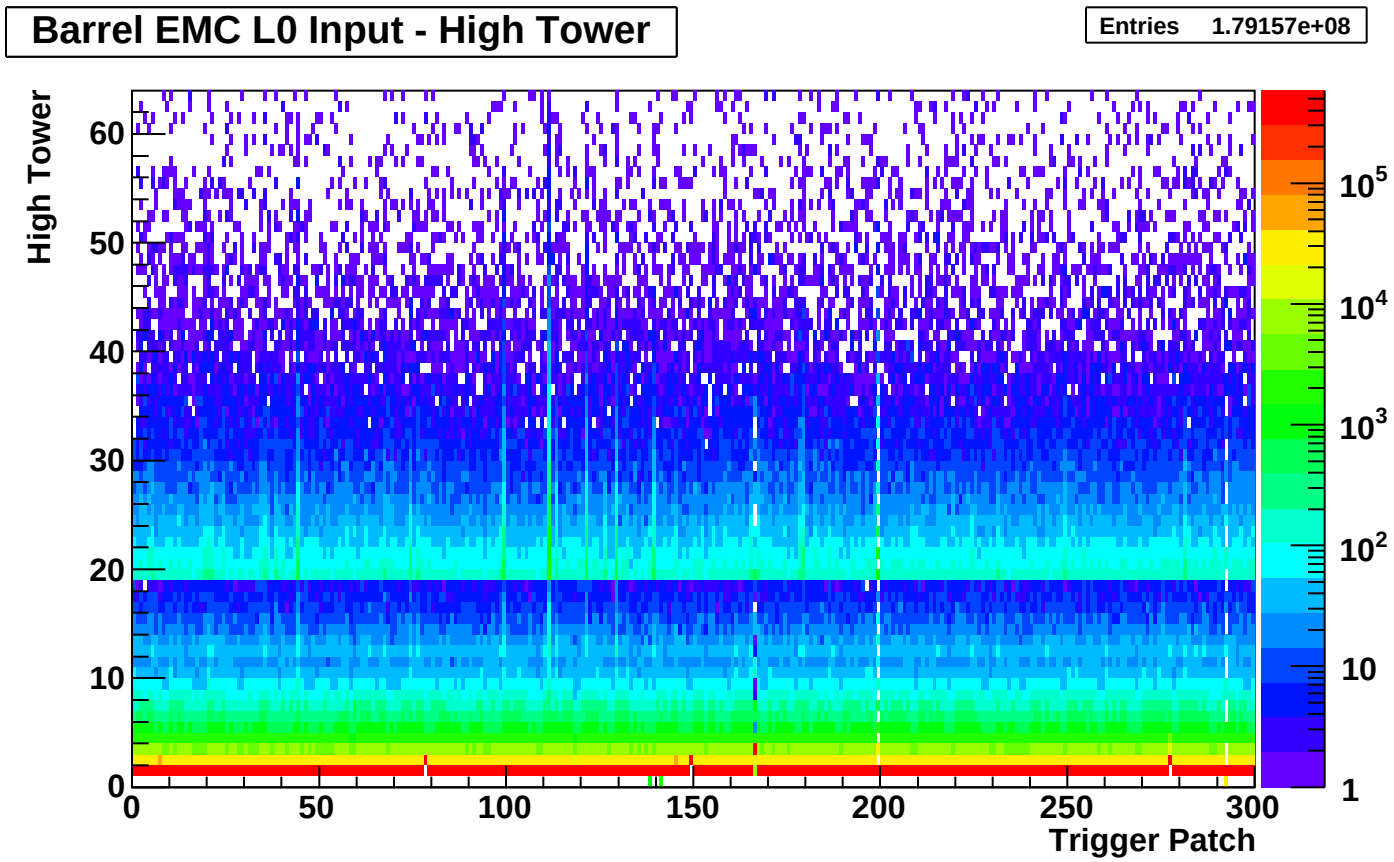

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

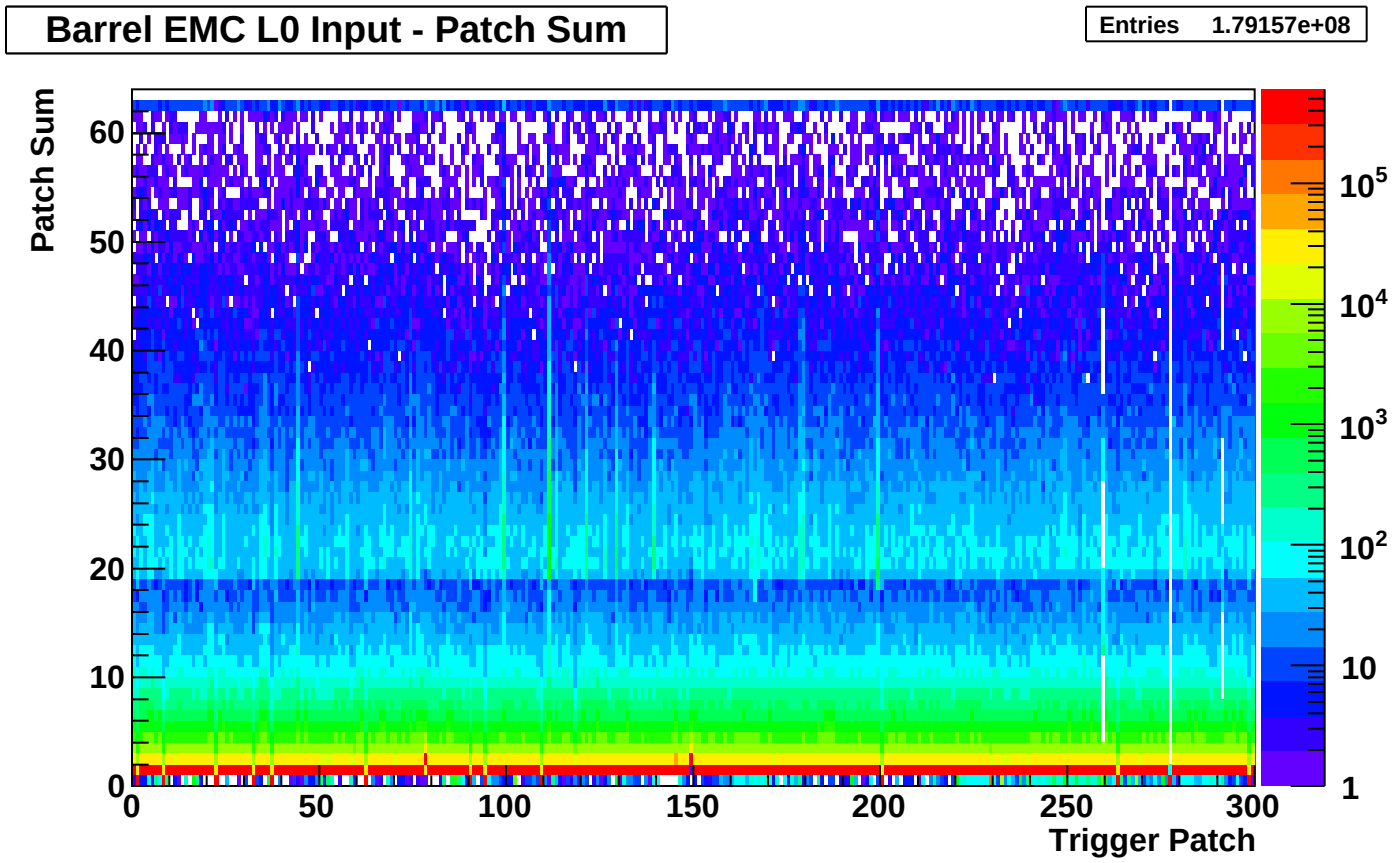

TCU bits
Entries 9555040
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

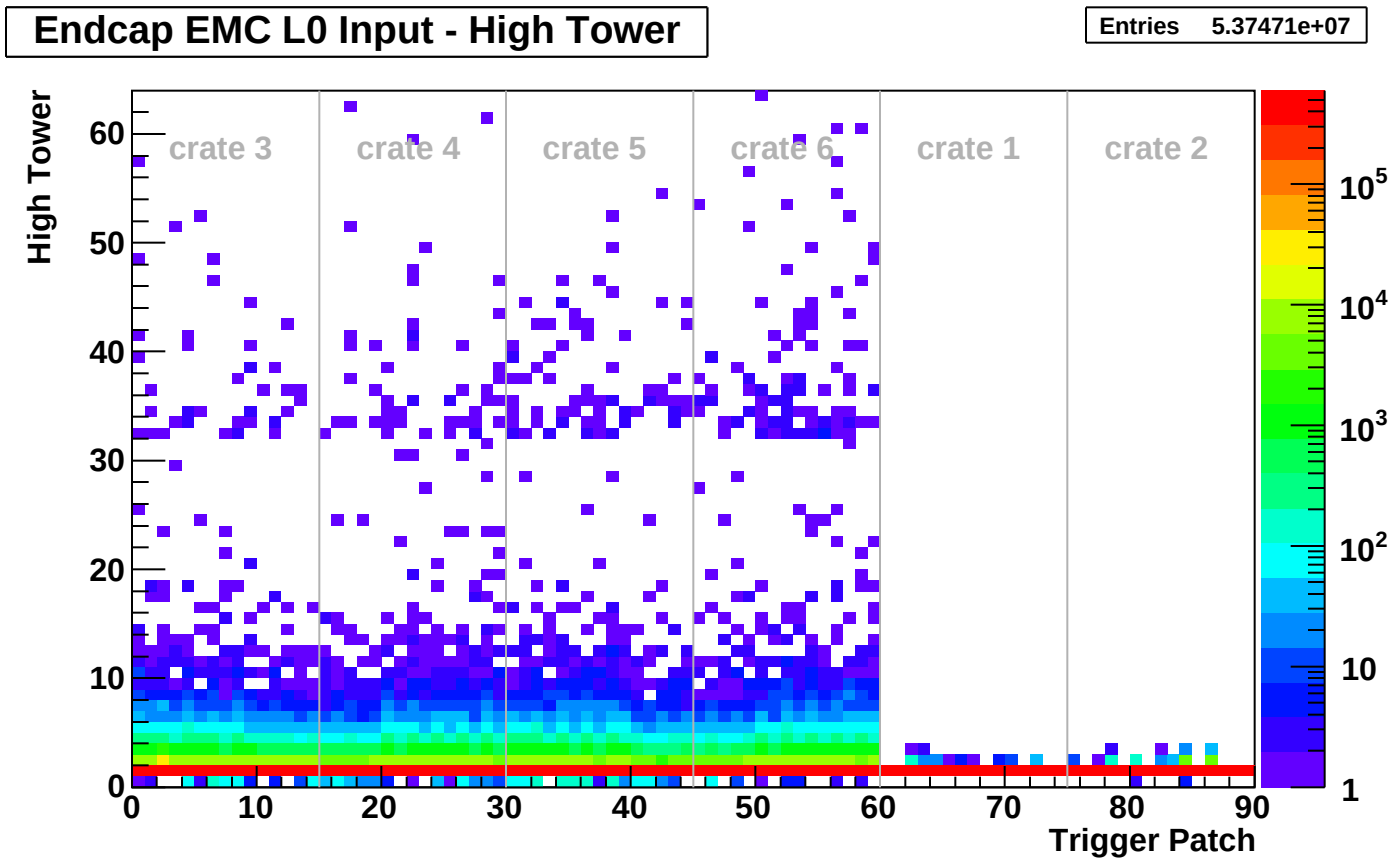

TCU bits
Entries 788939
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

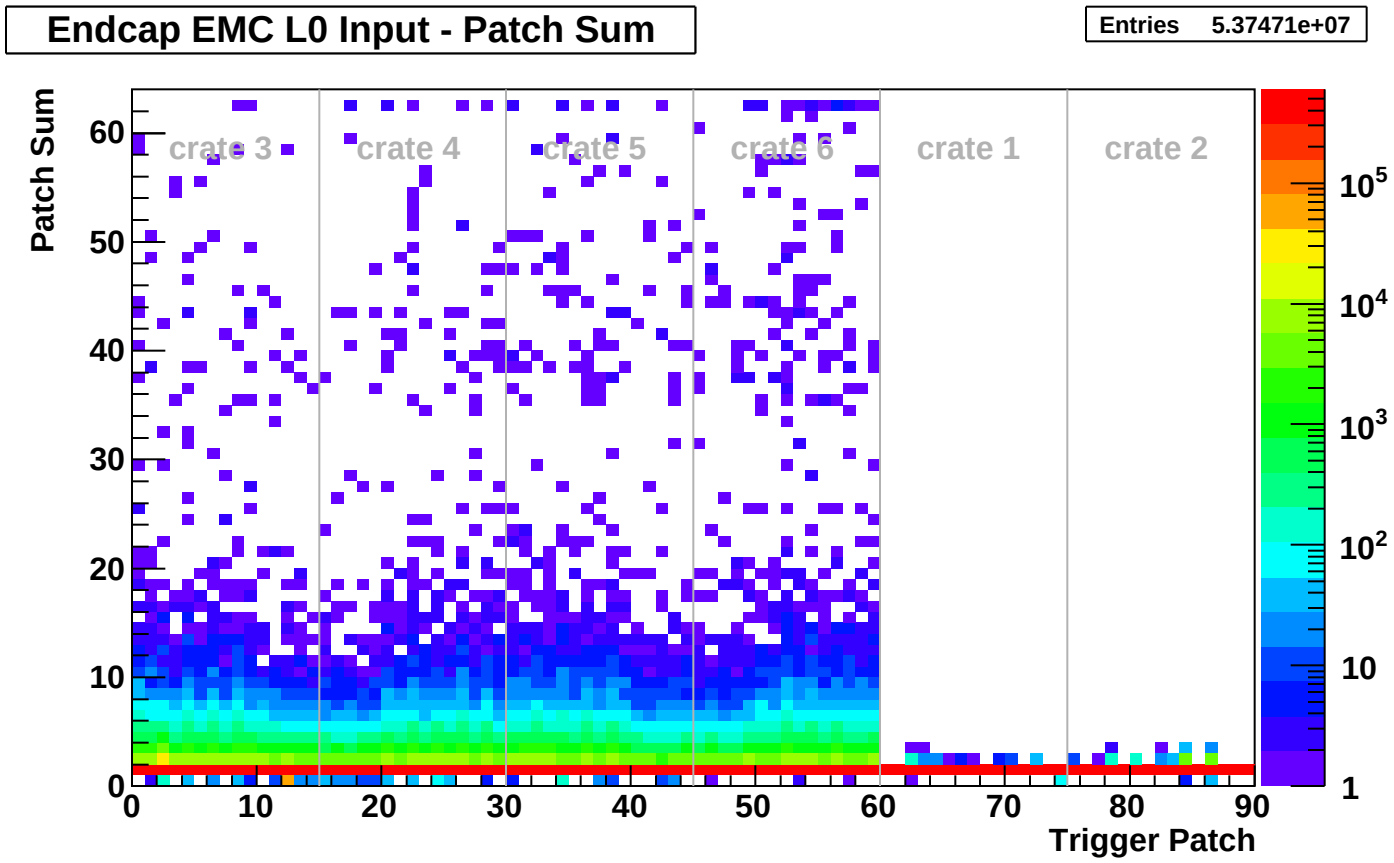

BunchId7Bit (BHT3)
Entries 14211

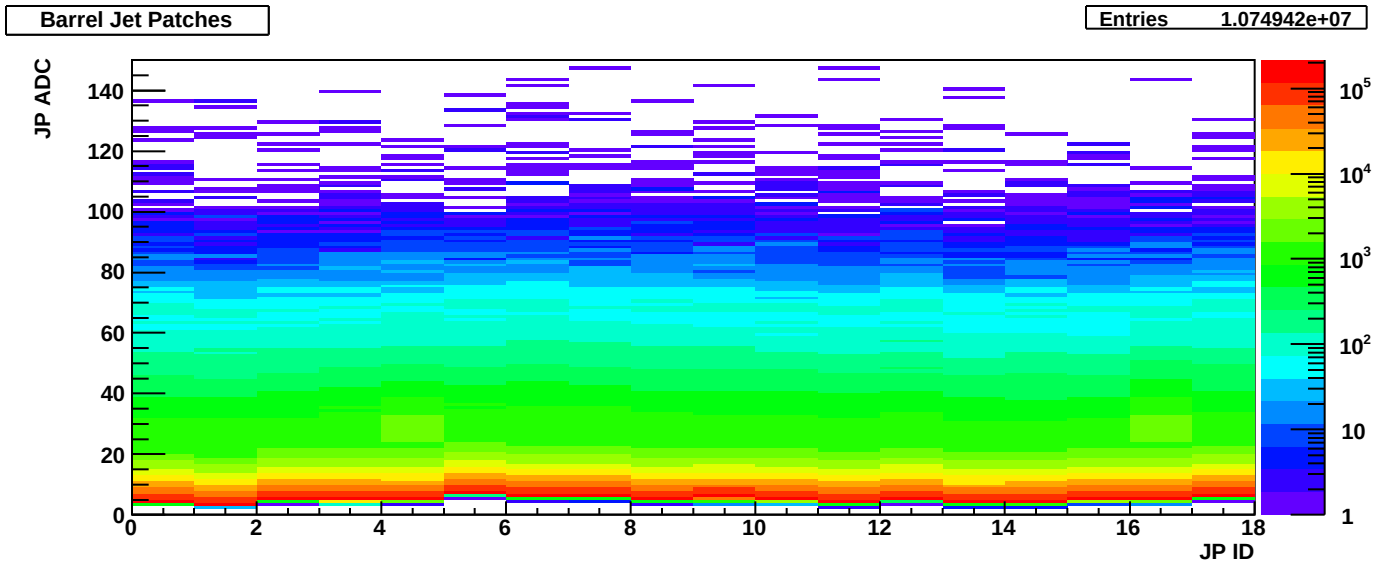

BunchId7Bit (all events)
Entries 597190

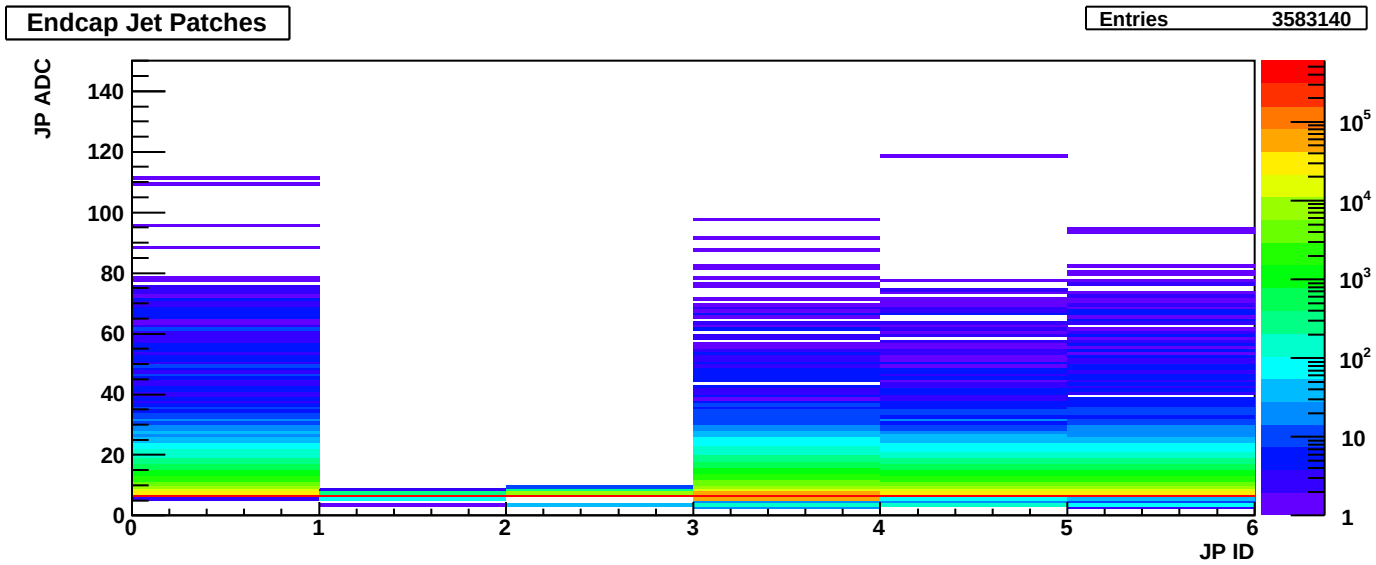

Barrel EMC L0 Input - High Tower
Entries 1.79157e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

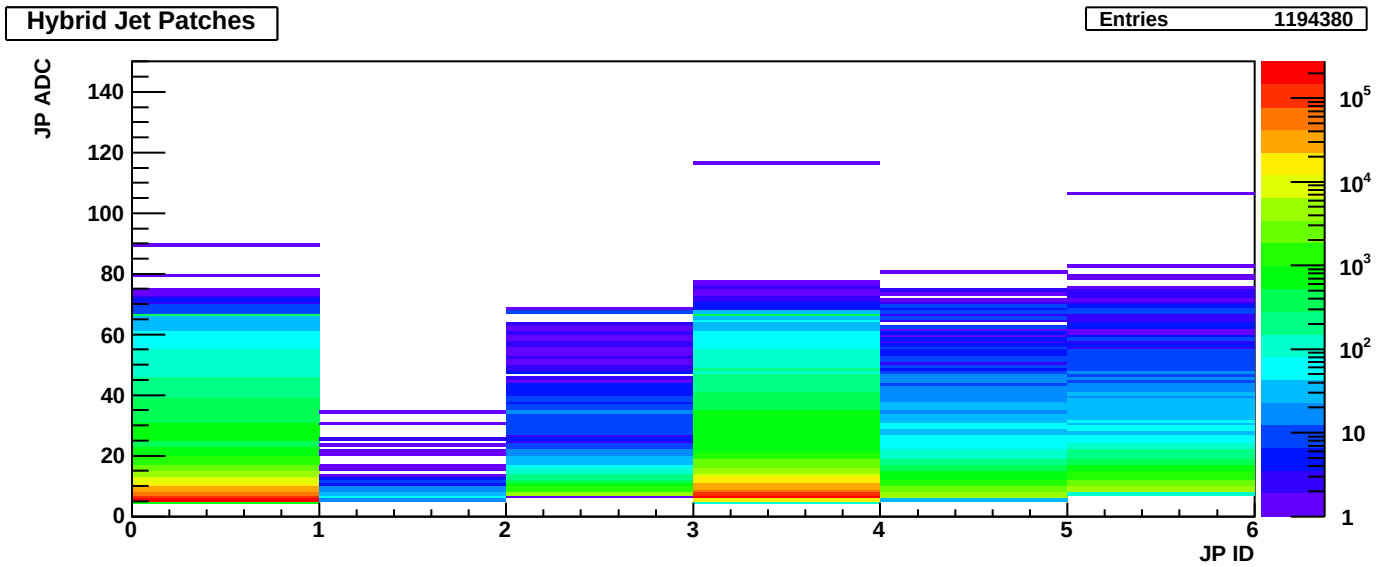

Barrel EMC L0 Input - Patch Sum
Entries 1.79157e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

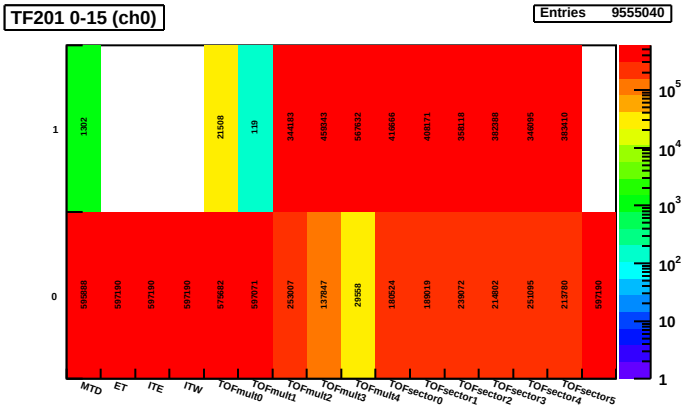

Endcap EMC L0 Input - High Tower
Entries 5.37471e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

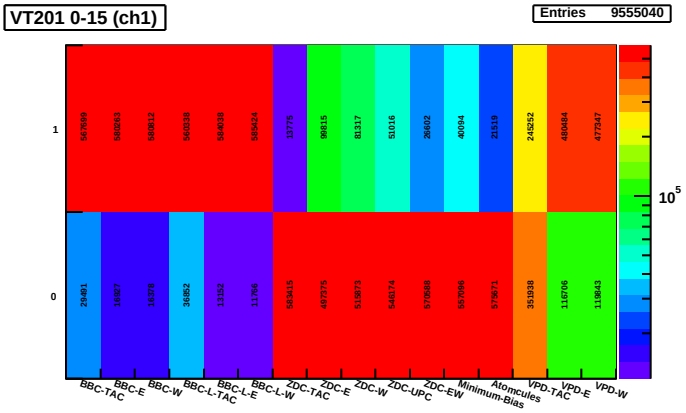

Endcap EMC L0 Input - Patch Sum
Entries 5.37471e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

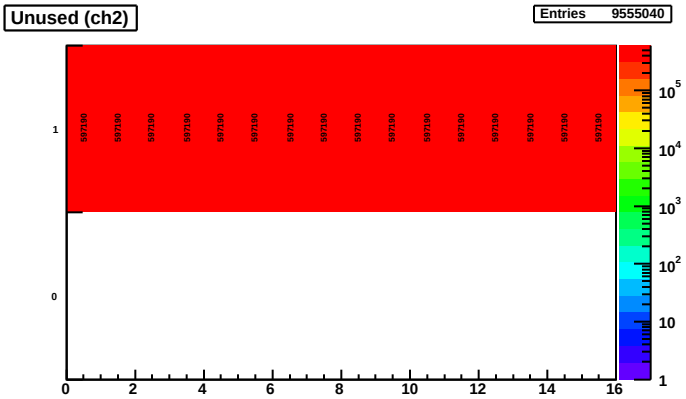

Barrel Jet Patches
Entries 1.074942e+07
JP ADC
JP ID

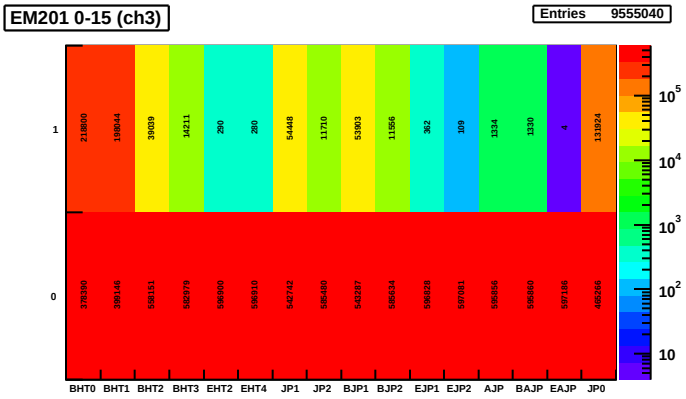

Endcap Jet Patches
Entries 3583140
JP ADC
JP ID

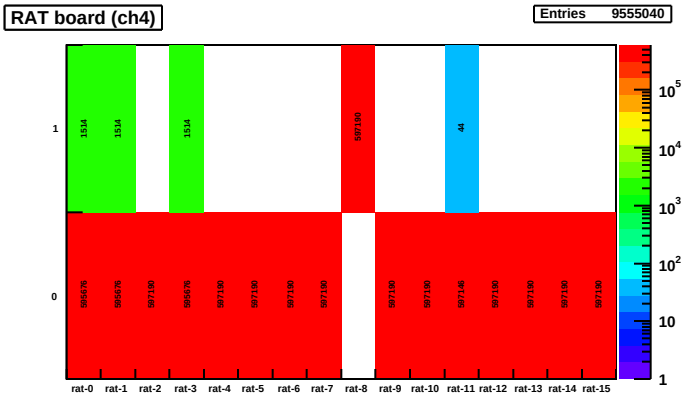

Hybrid Jet Patches
Entries 1194380
JP ADC
JP ID

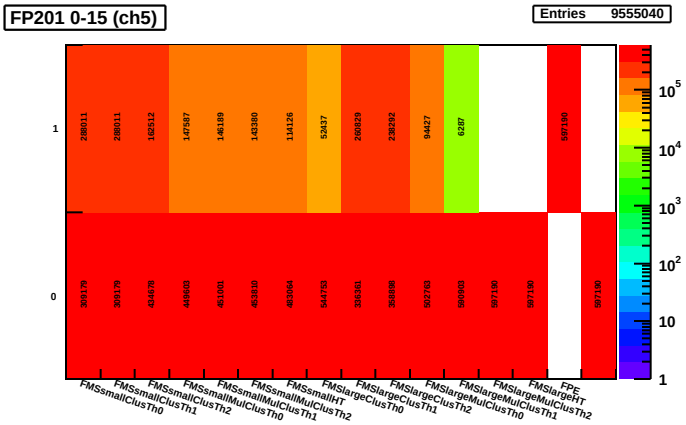

TF201 0-15 (ch0)
Entries 9555040
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

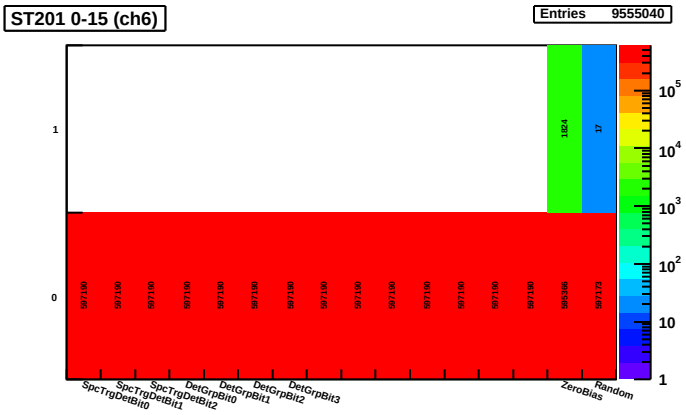

VT201 0-15 (ch1)
Entries 9555040
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

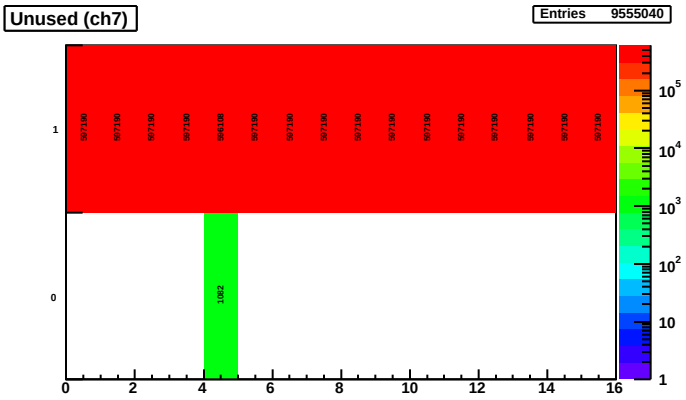

Unused (ch2)
Entries 9555040

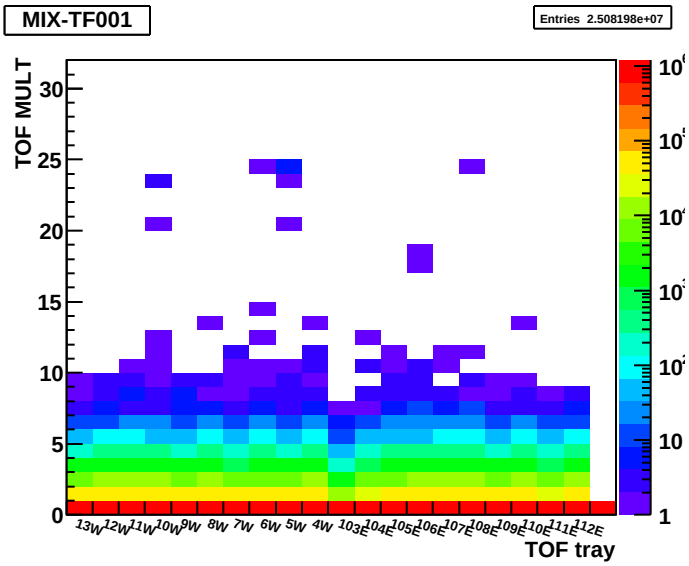

EM201 0-15 (ch3)
Entries 9555040
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

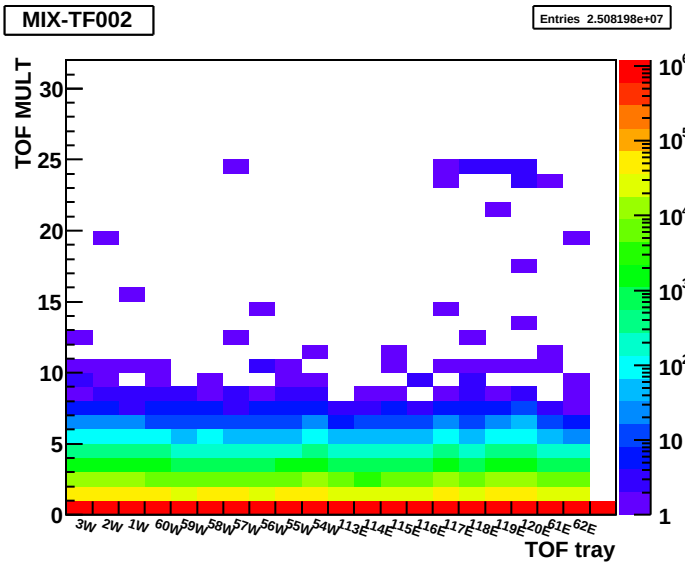

RAT board (ch4)
Entries 9555040
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

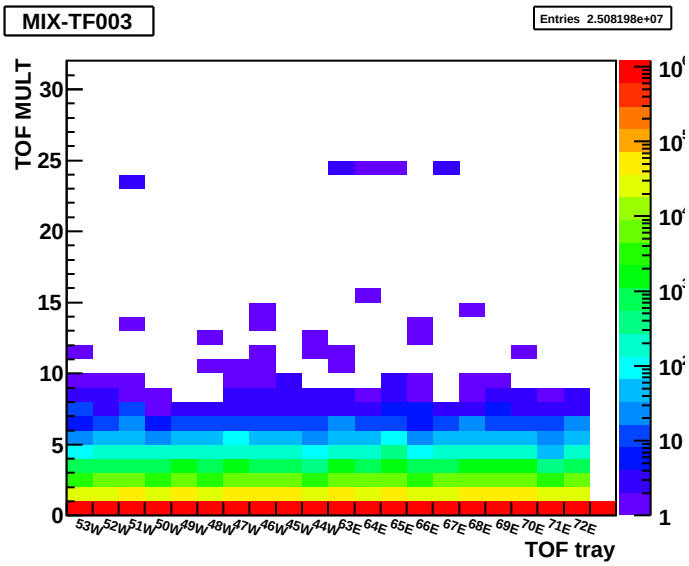

FP201 0-15 (ch5)
Entries 9555040
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

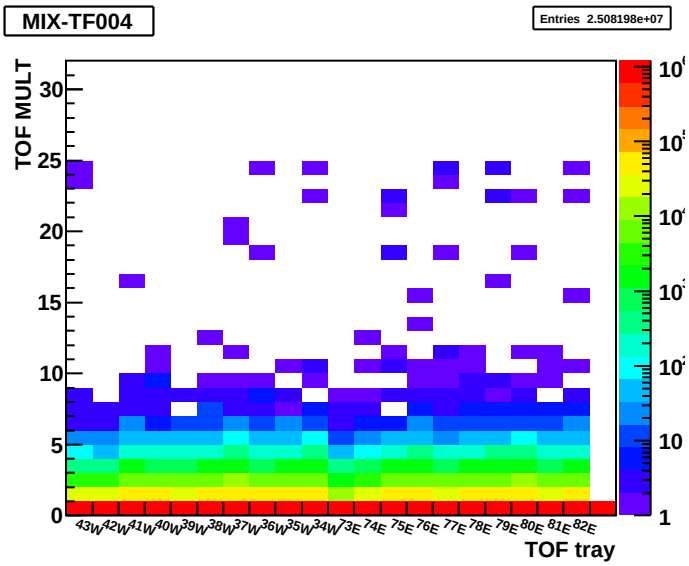

ST201 0-15 (ch6)
Entries 9555040
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

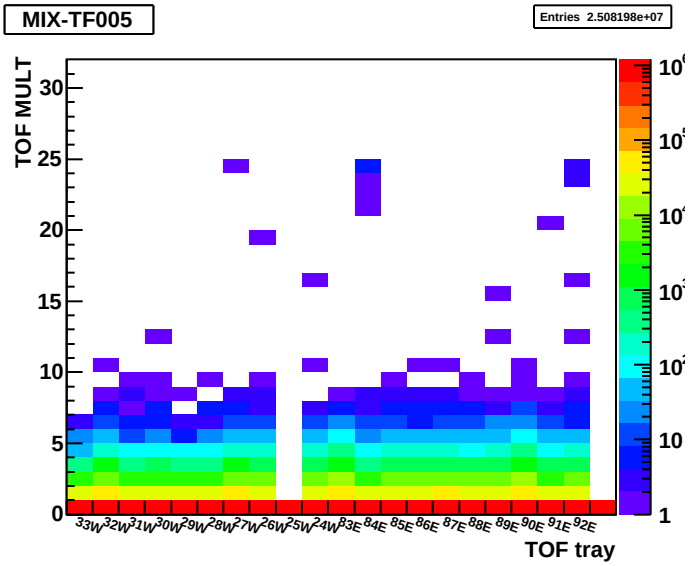

Unused (ch7)
Entries 9555040

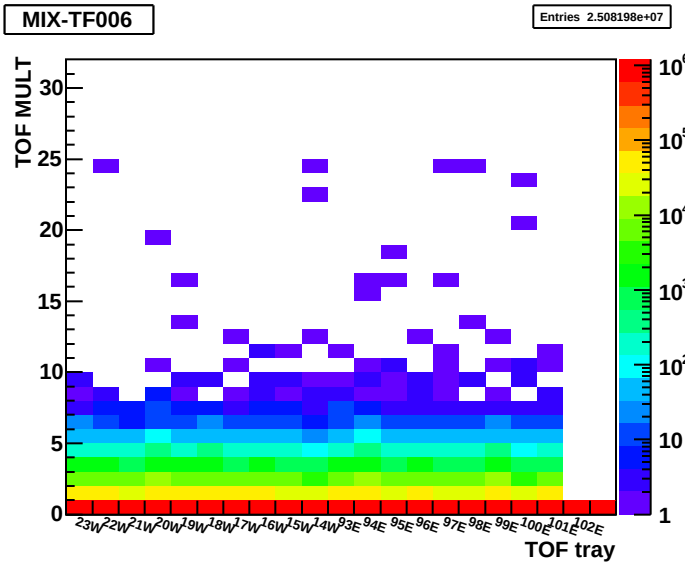

MIX-TF001
Entries 2.508198e+07
TOF MULT
TOF tray

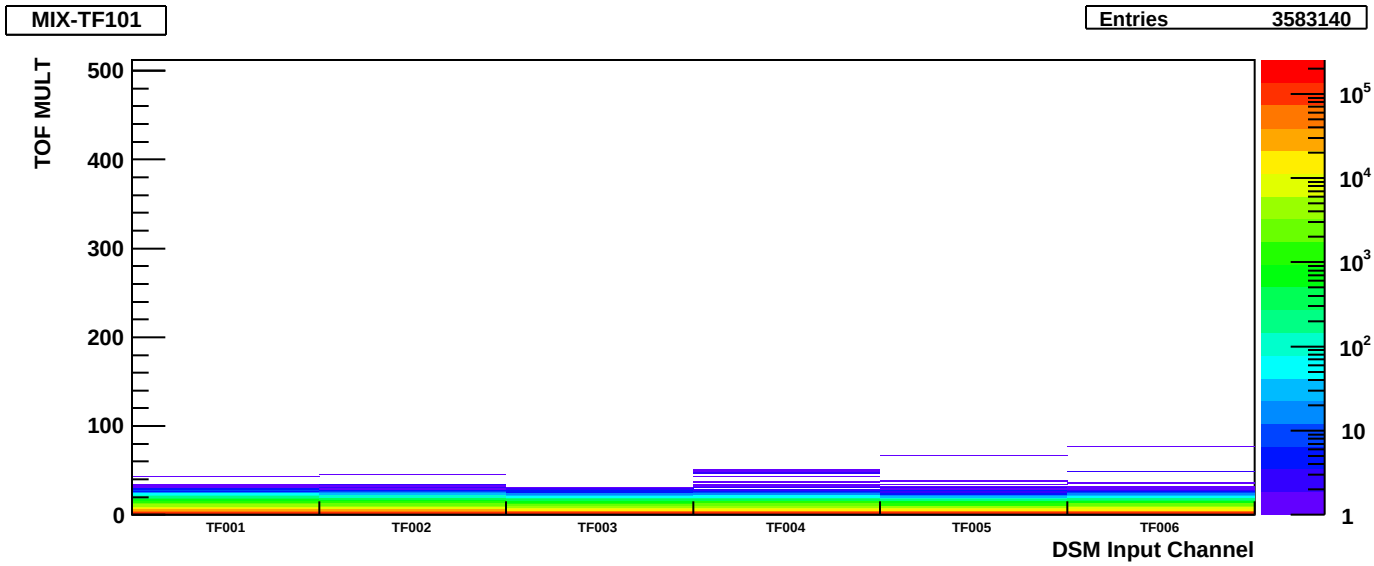

MIX-TF002
Entries 2.508198e+07
TOF MULT
TOF tray

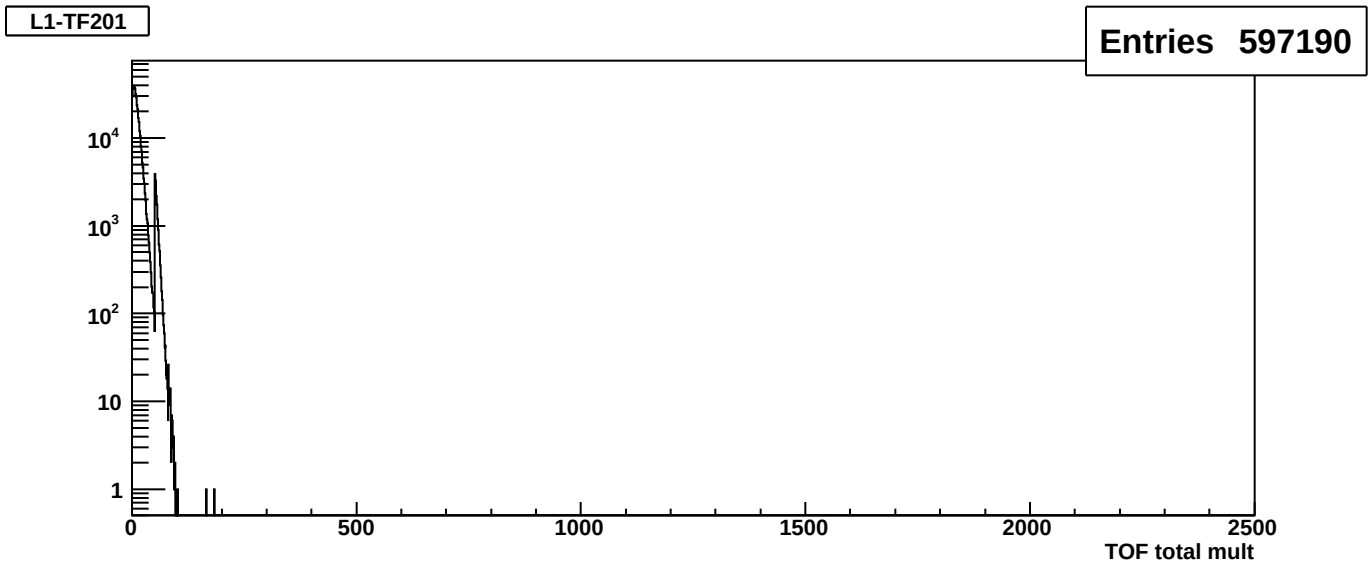

MIX-TF003
Entries 2.508198e+07
TOF MULT
TOF tray

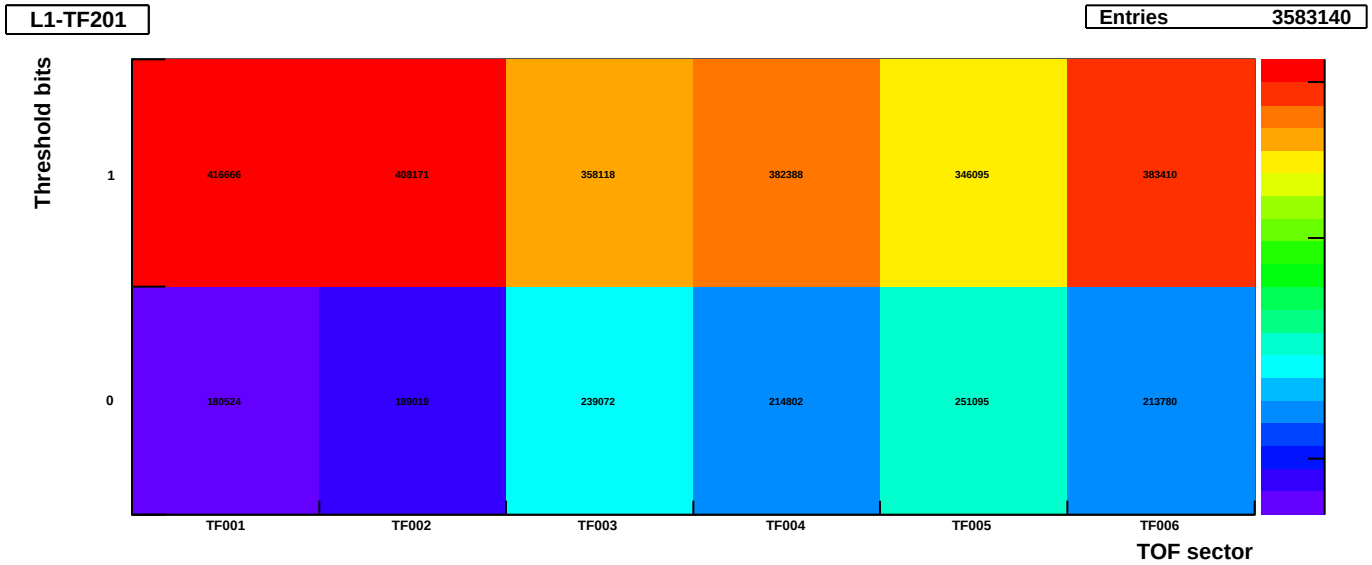

MIX-TF004
Entries 2.508198e+07
TOF MULT
TOF tray

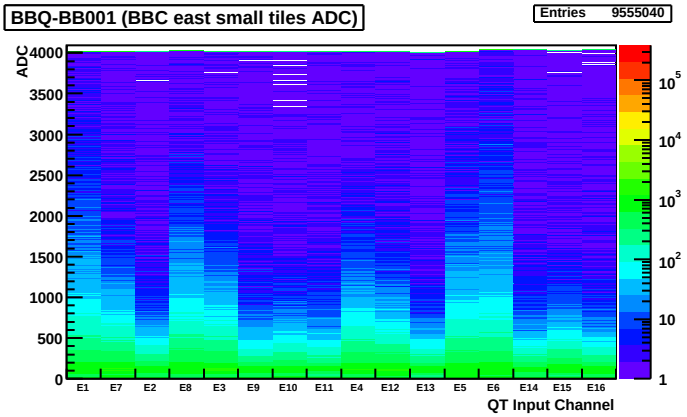

MIX-TF005
Entries 2.508198e+07
TOF MULT
TOF tray

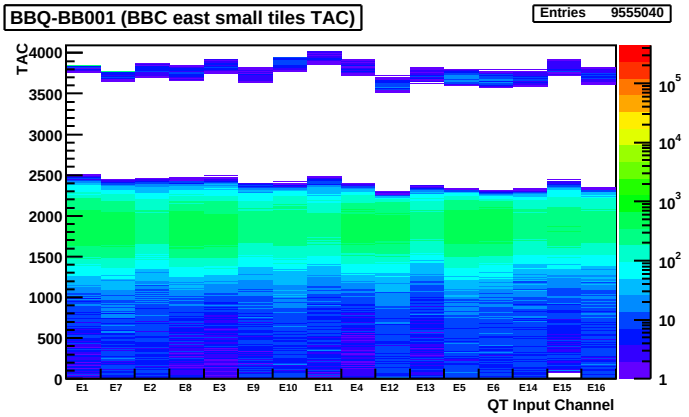

MIX-TF006
Entries 2.508198e+07
TOF MULT
TOF tray

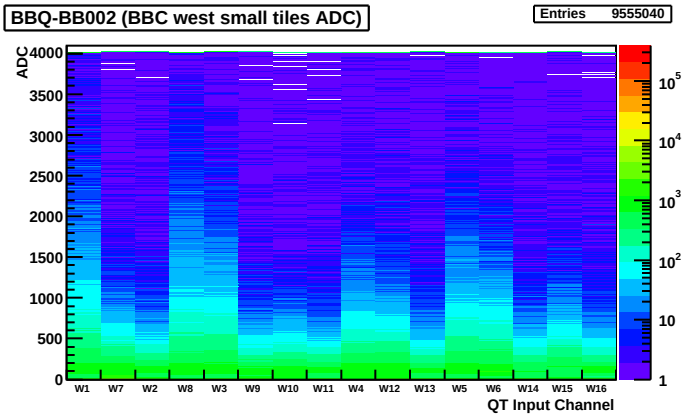

MIX-TF101
Entries 3583140
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

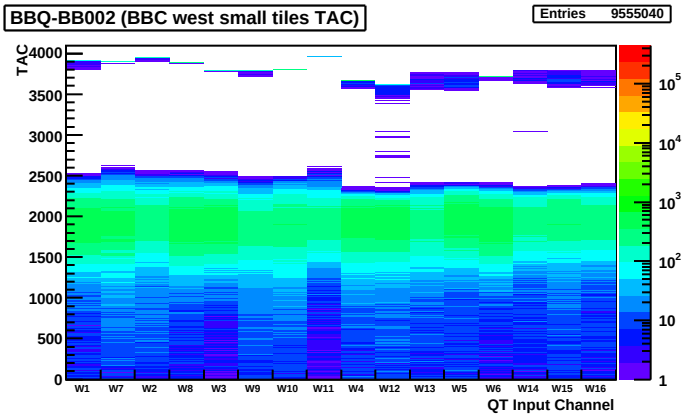

L1-TF201
Entries 597190
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

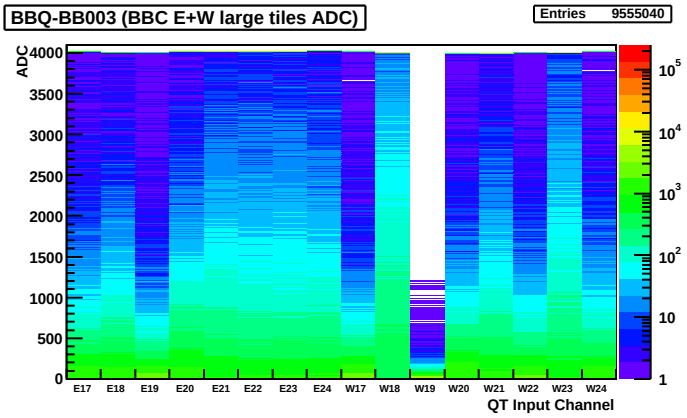

L1-TF201
Entries 3583140
Threshold bits
1
0
416666
408171
358118
382388
346095
383410
180524
189019
239072
214802
251095
213780
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

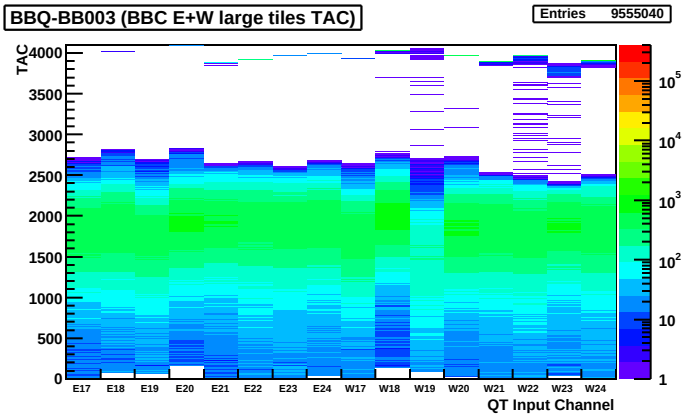

BBQ-BB001 (BBC east small tiles ADC)
Entries 9555040
ADC
QT Input Channel

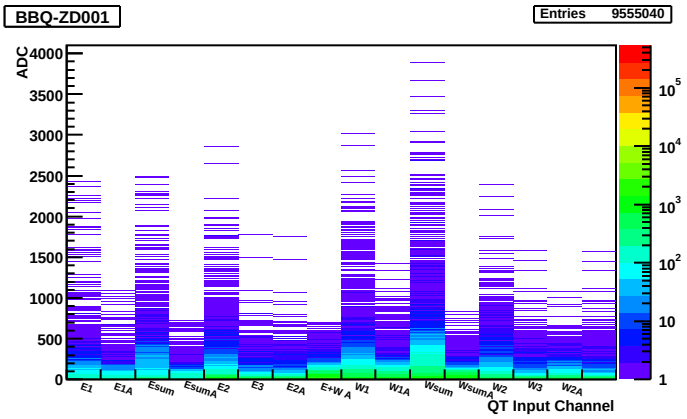

BBQ-BB001 (BBC east small tiles TAC)
Entries 9555040
TAC
QT Input Channel

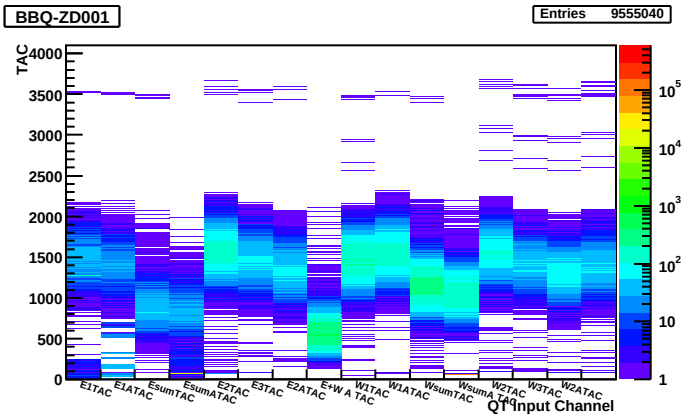

BBQ-BB002 (BBC west small tiles ADC)
Entries 9555040
ADC
QT Input Channel

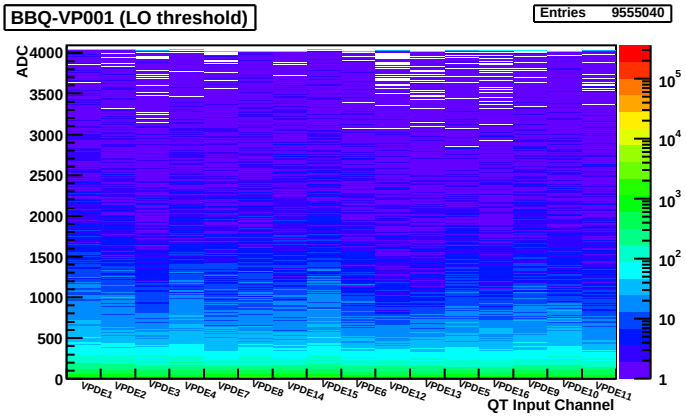

BBQ-BB002 (BBC west small tiles TAC)
Entries 9555040
TAC
QT Input Channel

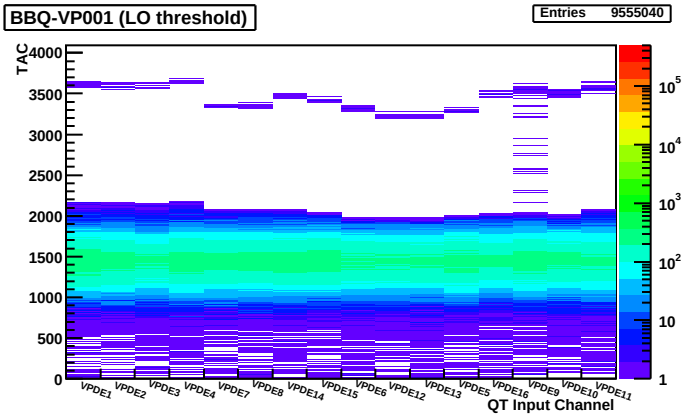

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9555040
ADC
QT Input Channel

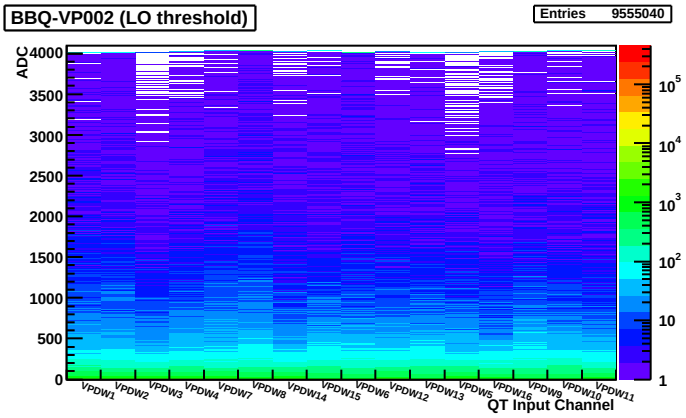

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9555040
TAC
QT Input Channel

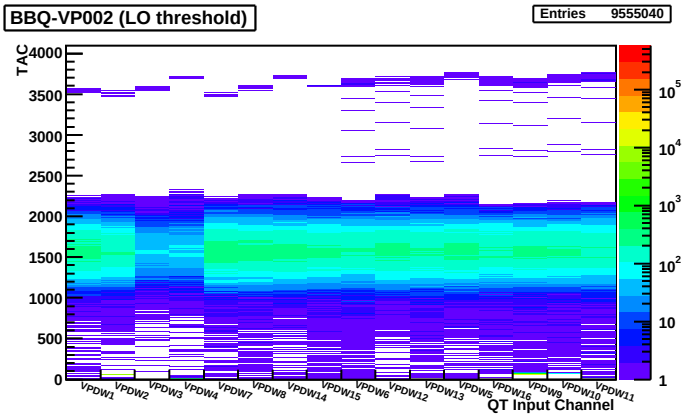

BBQ-ZD001
Entries 9555040
ADC
QT Input Channel

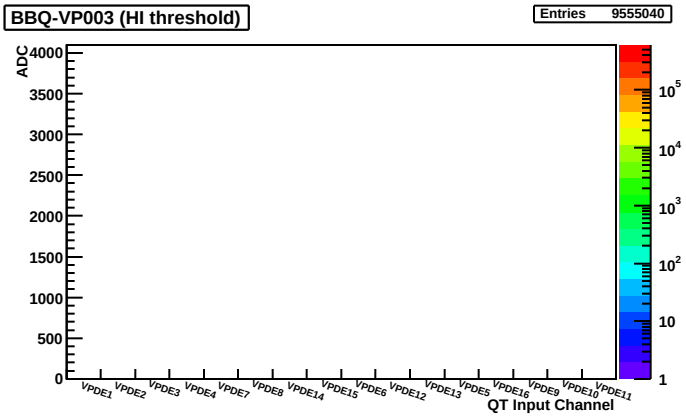

BBQ-ZD001
Entries 9555040
TAC
QT Input Channel

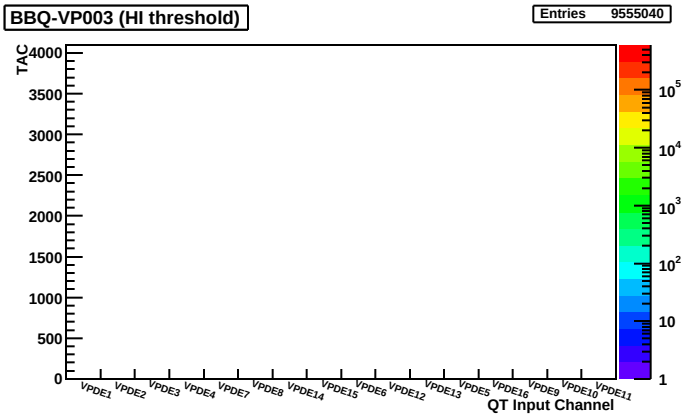

BBQ-VP001 (LO threshold)
Entries 9555040
ADC
QT Input Channel

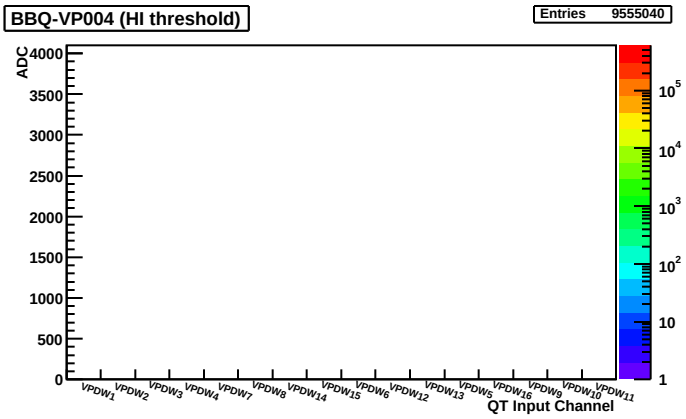

BBQ-VP001 (LO threshold)
Entries 9555040
TAC
QT Input Channel

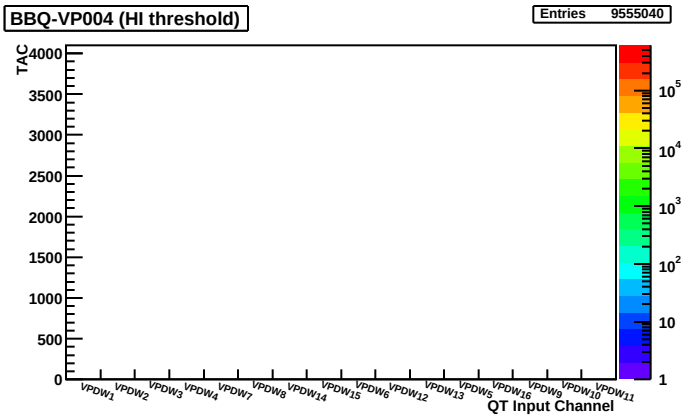

BBQ-VP002 (LO threshold)
Entries 9555040
ADC
QT Input Channel

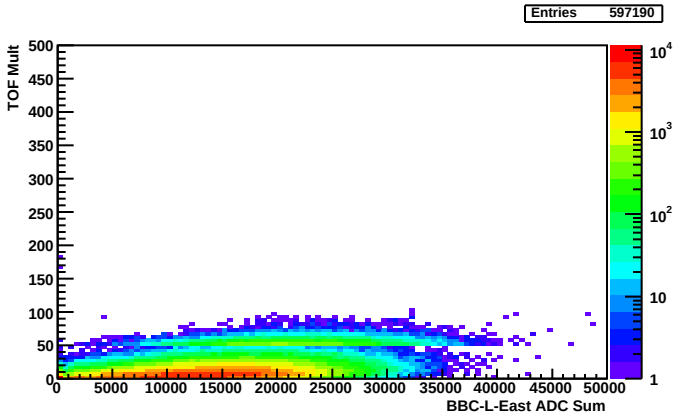

BBQ-VP002 (LO threshold)
Entries 9555040
TAC
QT Input Channel

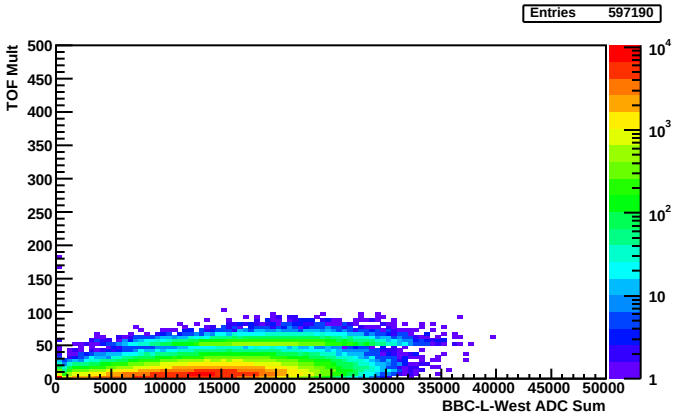

BBQ-VP003 (HI threshold)
Entries 9555040
ADC
QT Input Channel

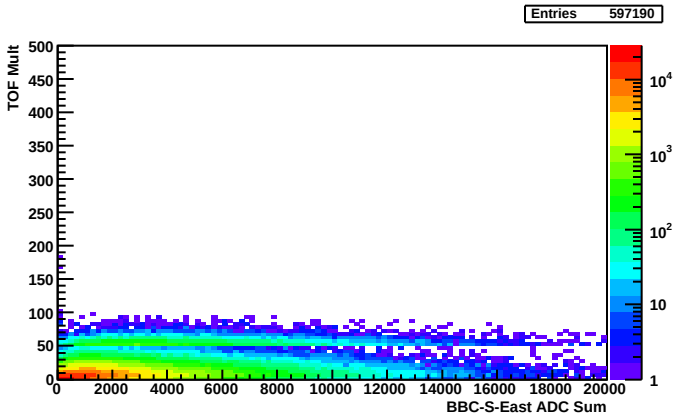

BBQ-VP003 (HI threshold)
Entries 9555040
TAC
QT Input Channel

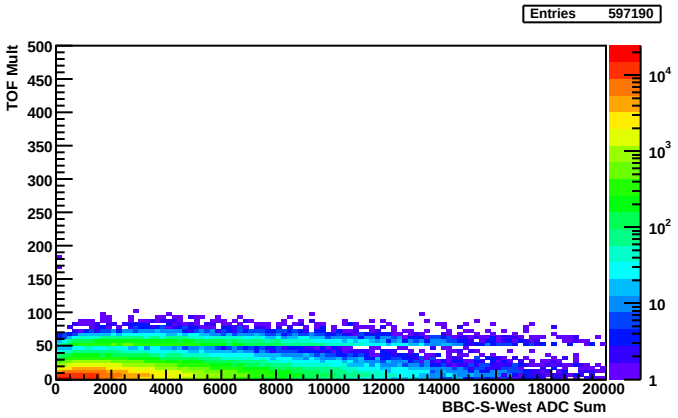

BBQ-VP004 (HI threshold)
Entries 9555040
ADC
QT Input Channel

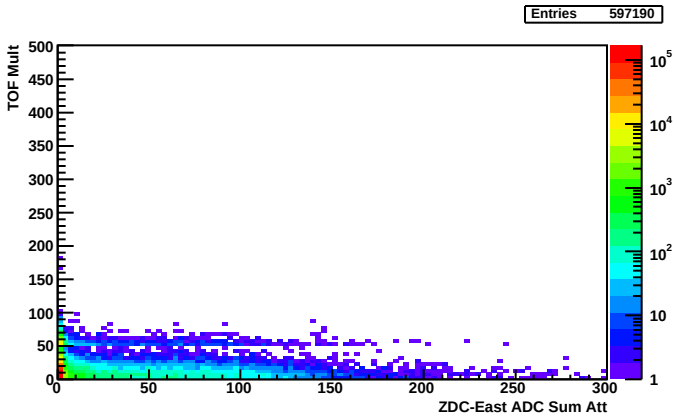

BBQ-VP004 (HI threshold)
Entries 9555040
TAC
QT Input Channel

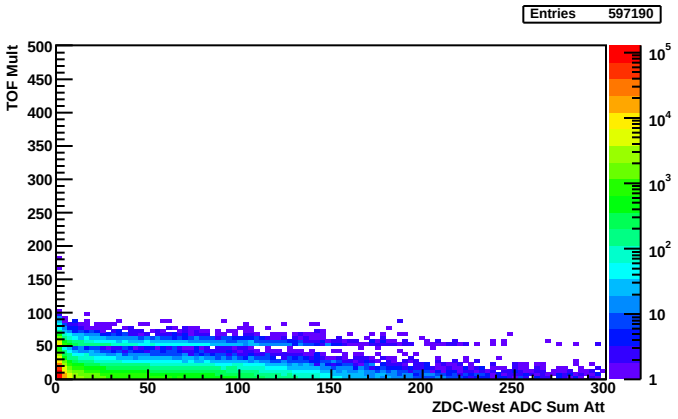

Entries 597190
TOF Mult
BBC-L-East ADC Sum

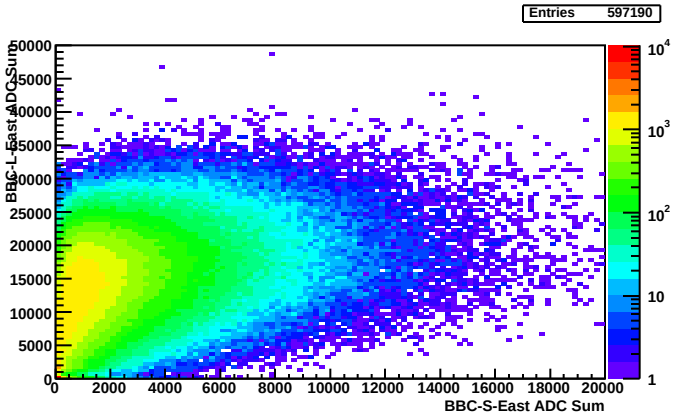

Entries 597190
TOF Mult
BBC-L-West ADC Sum

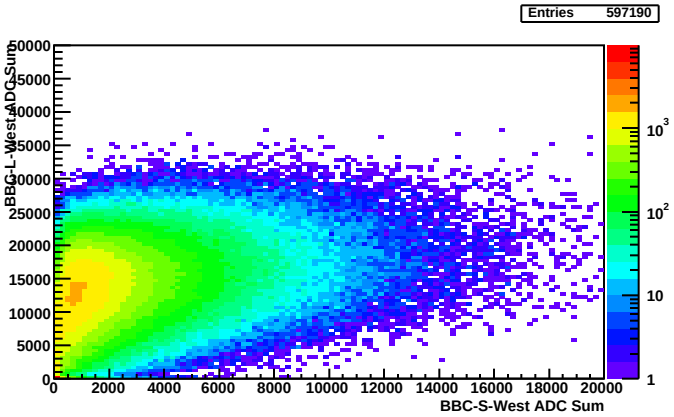

Entries 597190
TOF Mult
BBC-S-East ADC Sum

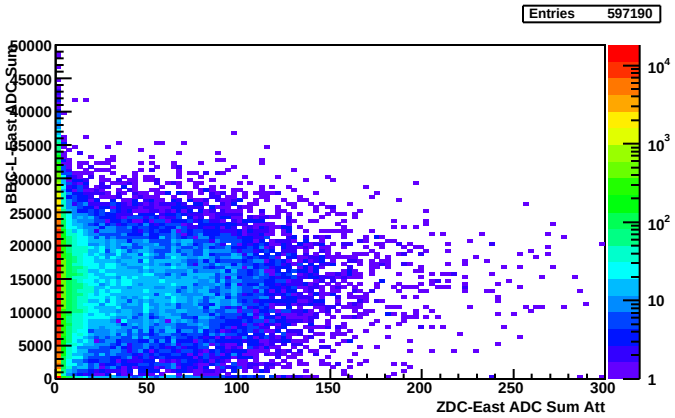

Entries 597190
TOF Mult
BBC-S-West ADC Sum

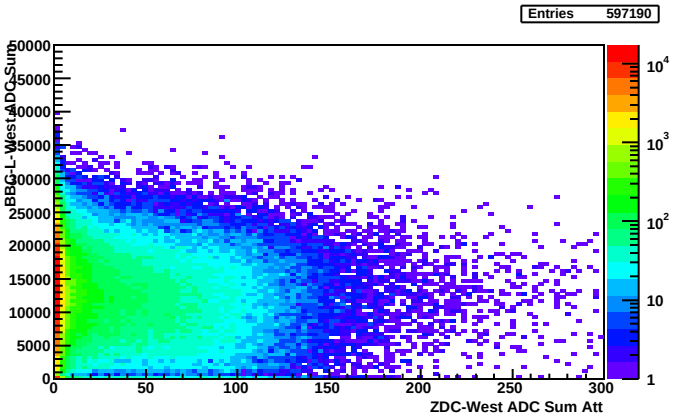

Entries 597190
TOF Mult
ZDC-East ADC Sum Att

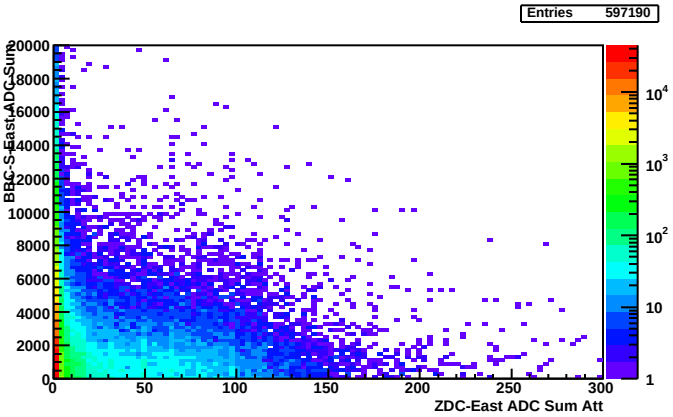

Entries 597190
TOF Mult
ZDC-West ADC Sum Att

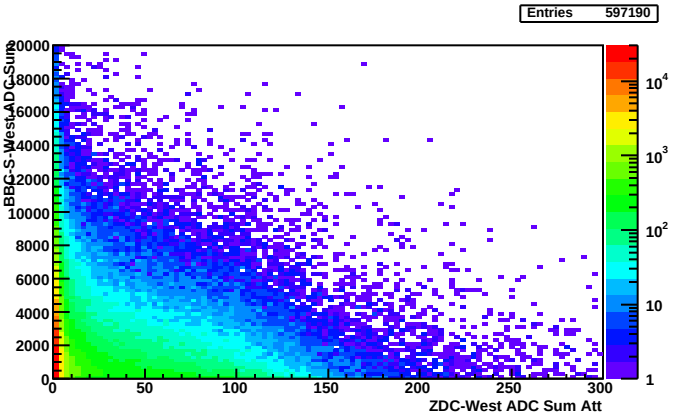

Entries 597190
BBC-L-East ADC Sum
BBC-S-East ADC Sum

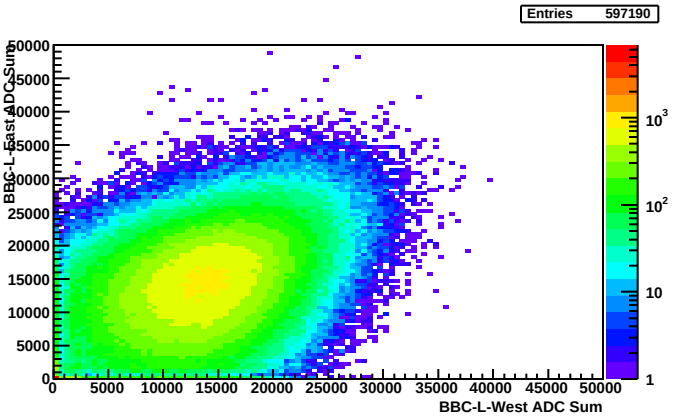

Entries 597190
BBC-L-West ADC Sum
BBC-S-West ADC Sum

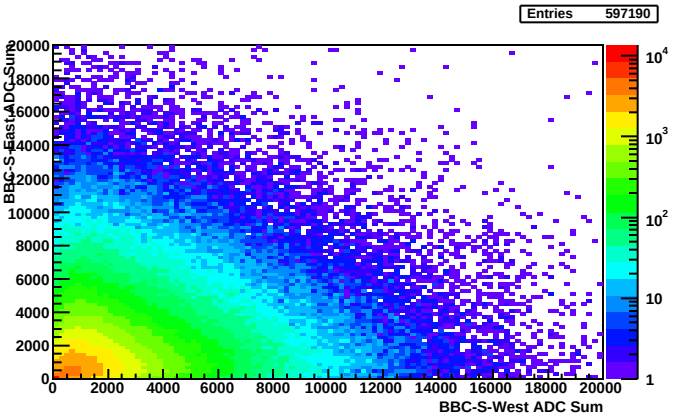

Entries 597190
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

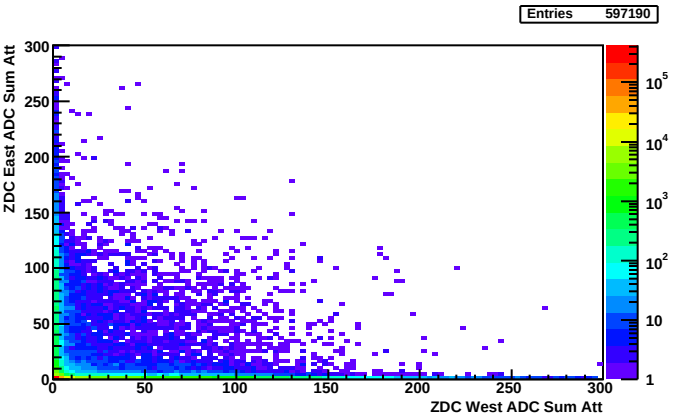

Entries 597190
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

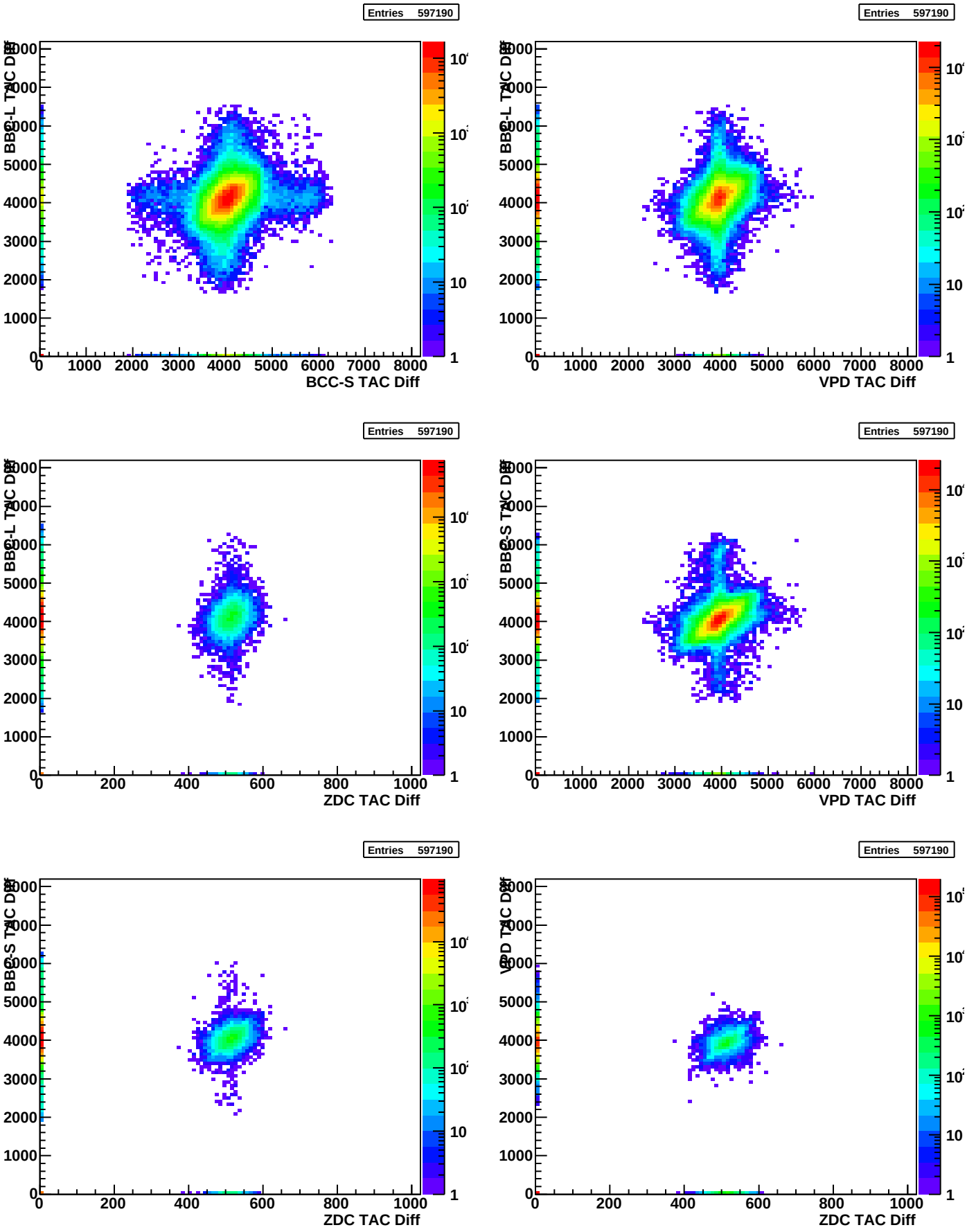

Entries 597190
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

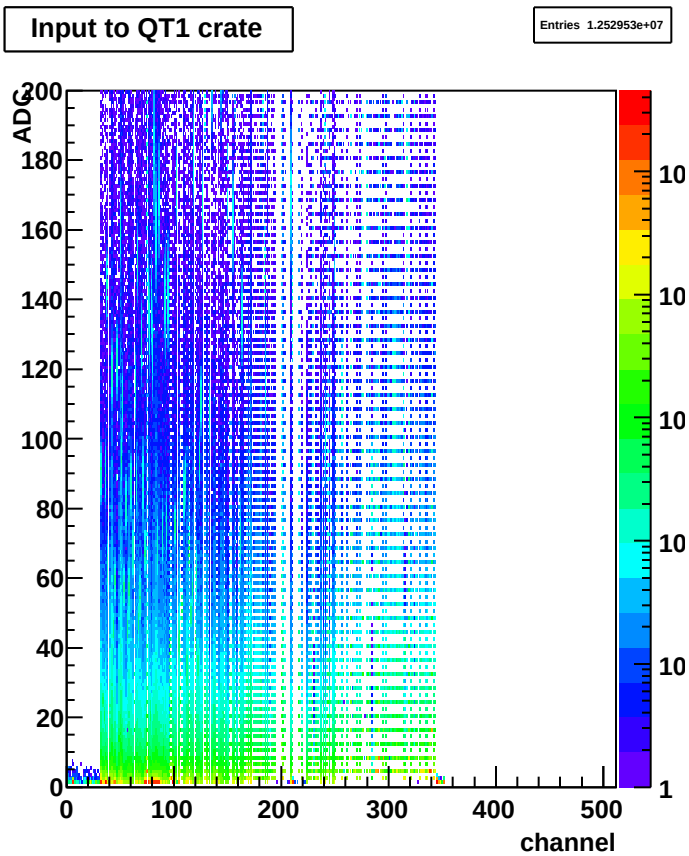

Entries 597190
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

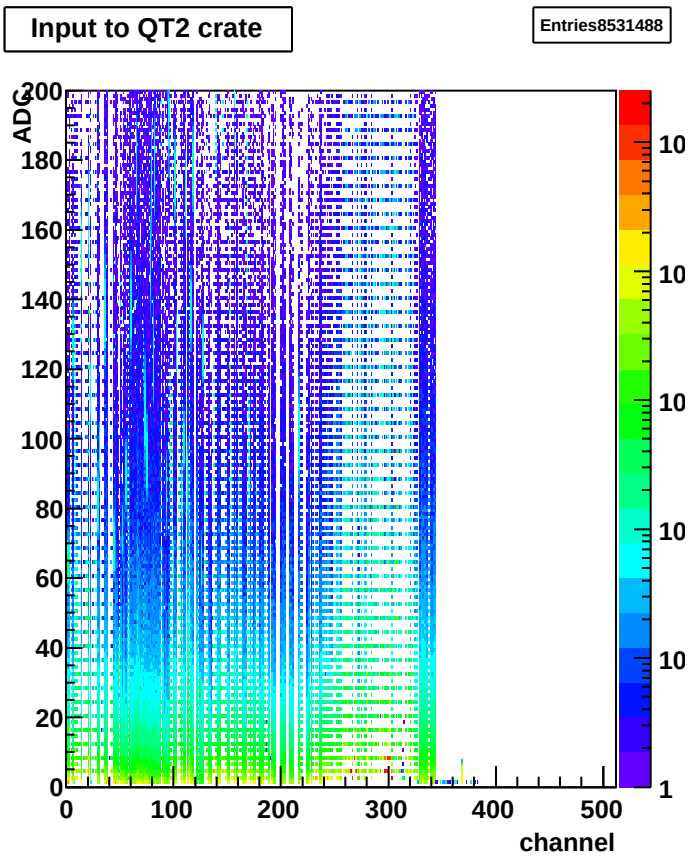

Entries 597190
BBC-L-East ADC Sum
BBC-L-West ADC Sum

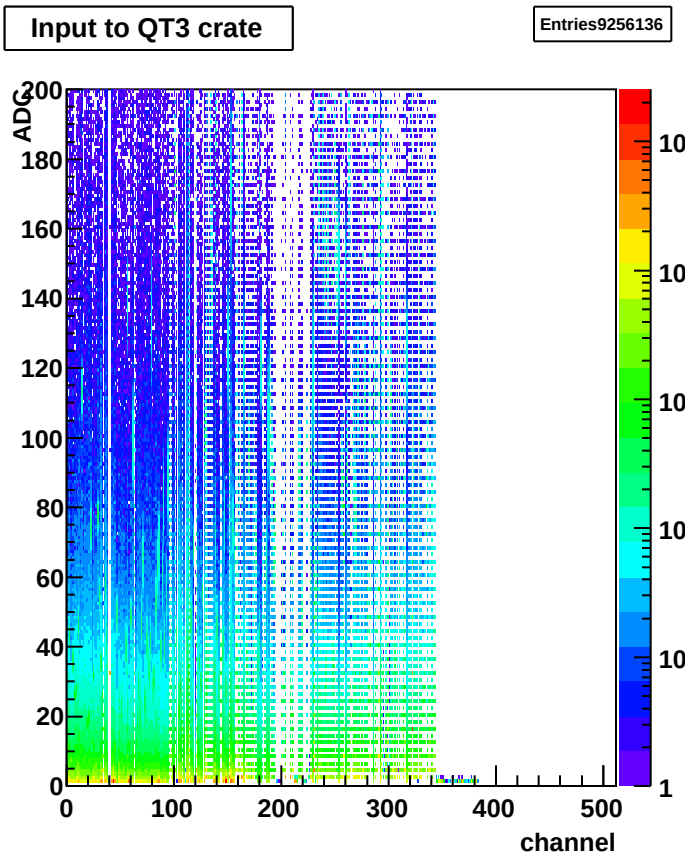

Entries 597190
BBC-S-East ADC Sum
BBC-S-West ADC Sum

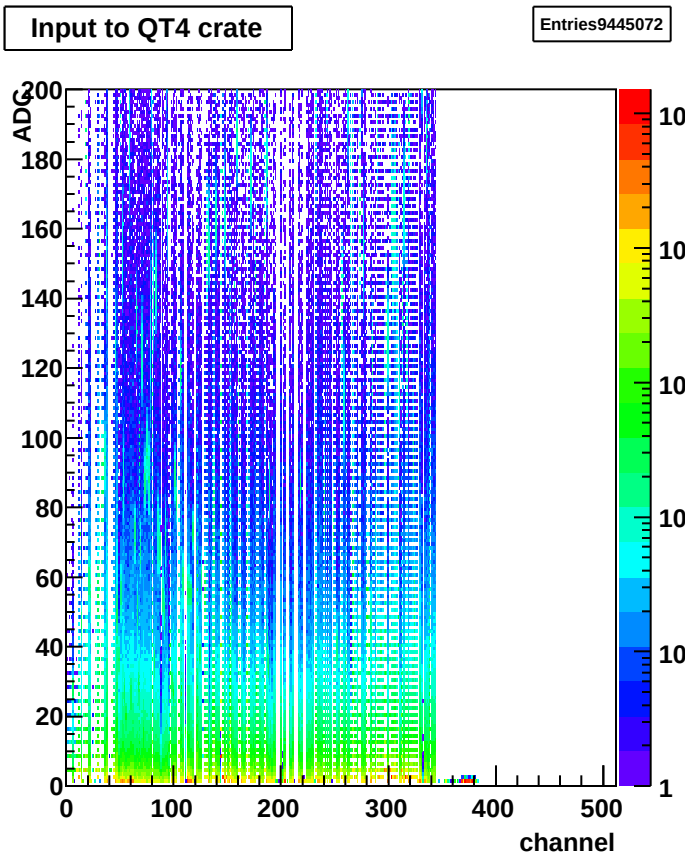

Entries 597190
ZDC East ADC Sum Att
ZDC West ADC Sum Att

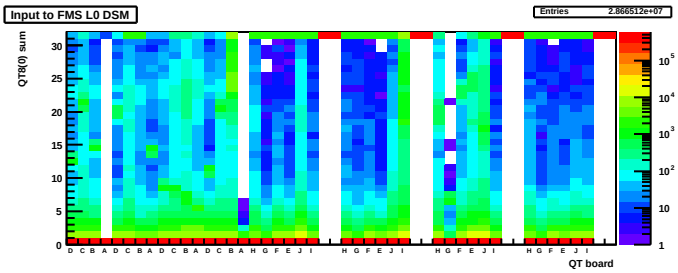

Entries 597190
BBC-L TAC Diff
BCC-S TAC Diff
Entries 597190
BBC-L TAC Diff
VPD TAC Diff
Entries 597190
BBC-L TAC Diff
ZDC TAC Diff
Entries 597190
BBC-S TAC Diff
VPD TAC Diff
Entries 597190
BBC-S TAC Diff
ZDC TAC Diff
Entries 597190
VPD TAC Diff
ZDC TAC Diff

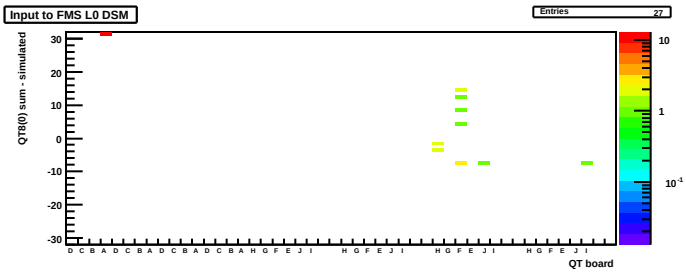

Input to QT1 crate
Entries 1.252953e+07
ADC
channel

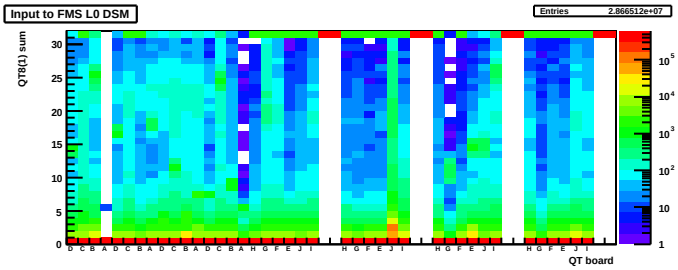

Input to QT2 crate
Entries8531488
ADC
channel

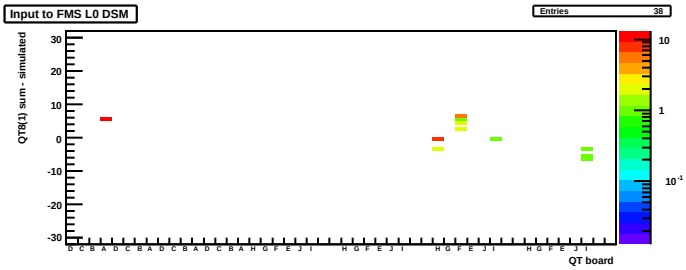

Input to QT3 crate
Entries9256136
ADC
channel

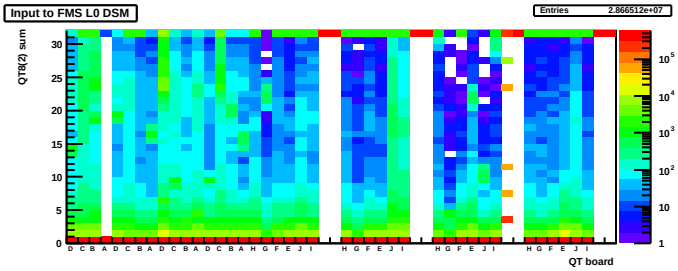

Input to QT4 crate
Entries9445072
ADC
channel

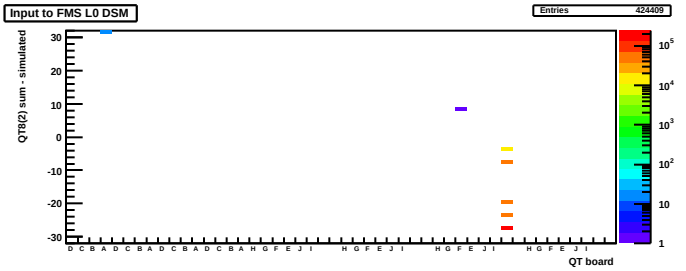

Input to FMS L0 DSM
Entries 2.866512e+07
QT8(0) sum
QT board

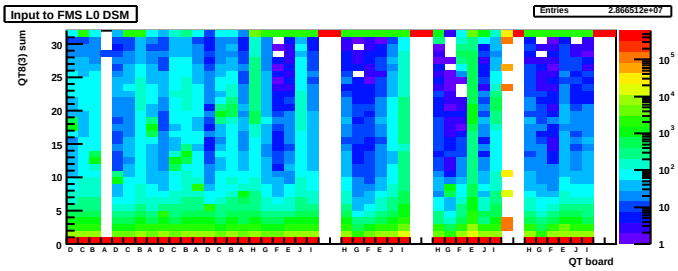

Input to FMS L0 DSM
Entries 27
QT8(0) sum - simulated
QT board

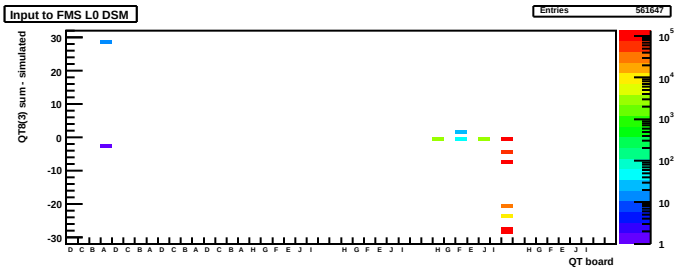

Input to FMS L0 DSM
Entries 2.866512e+07
QT8(1) sum
QT board

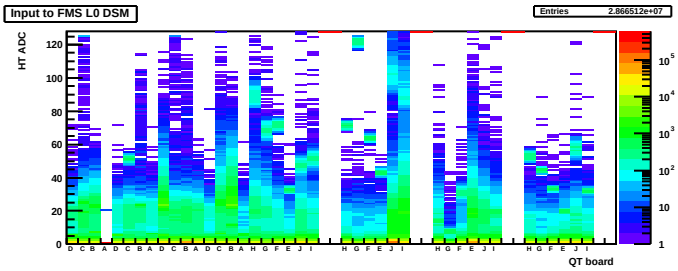

Input to FMS L0 DSM
Entries 38
QT8(1) sum - simulated
QT board

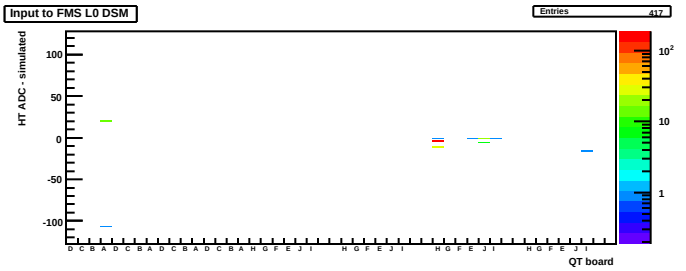

Input to FMS L0 DSM
Entries 2.866512e+07
QT8(2) sum
QT board

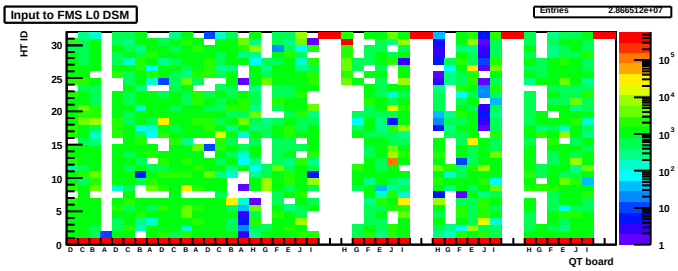

Input to FMS L0 DSM
Entries 424409
QT8(2) sum - simulated
QT board

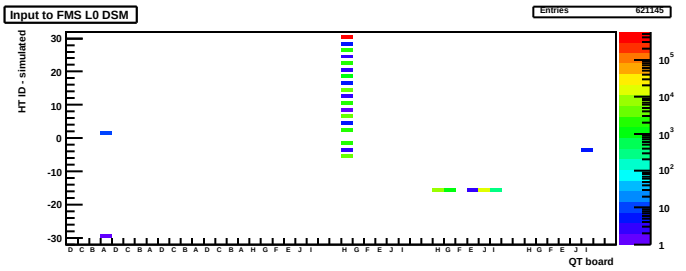

Input to FMS L0 DSM
Entries 2.866512e+07
QT8(3) sum
QT board

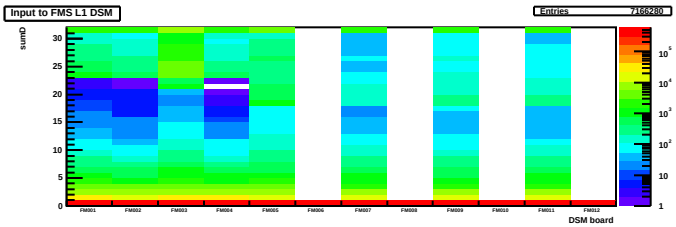

Input to FMS L0 DSM
Entries 561647
QT8(3) sum - simulated
QT board

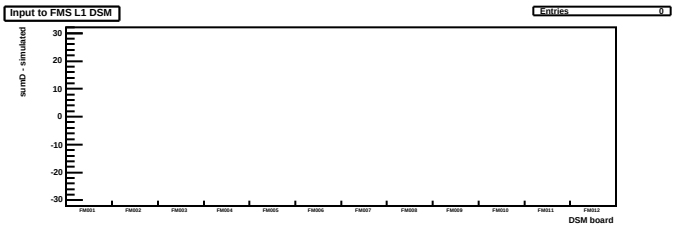

Input to FMS L0 DSM
Entries 2.866512e+07
HT ADC
QT board

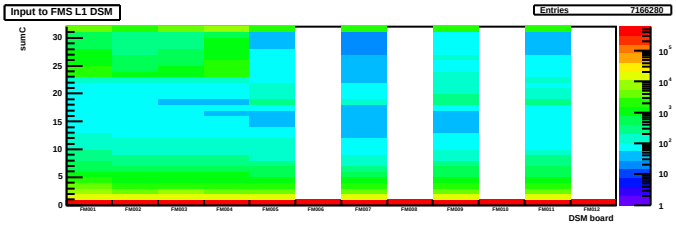

Input to FMS L0 DSM
Entries 417
HT ADC - simulated
QT board

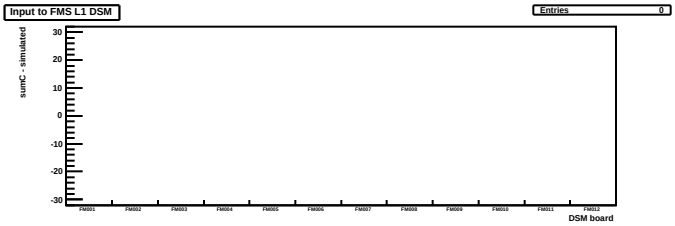

Input to FMS L0 DSM
Entries 2.866512e+07
HT ID
QT board

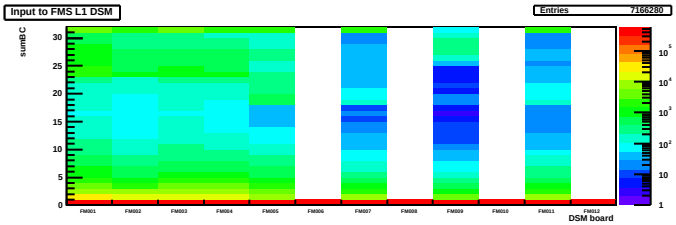

Input to FMS L0 DSM
Entries 621145
HT ID - simulated
QT board

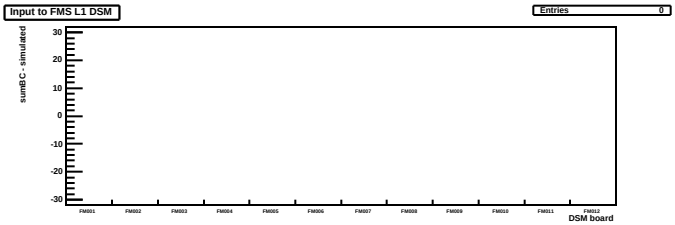

Input to FMS L1 DSM
Entries 7166280
sumD
DSM board

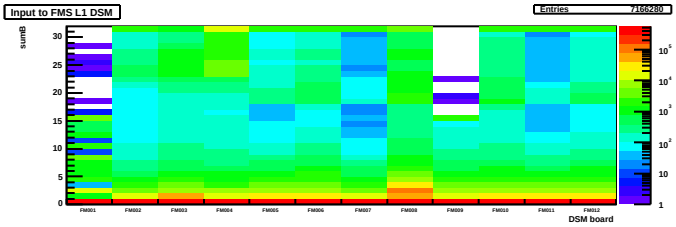

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

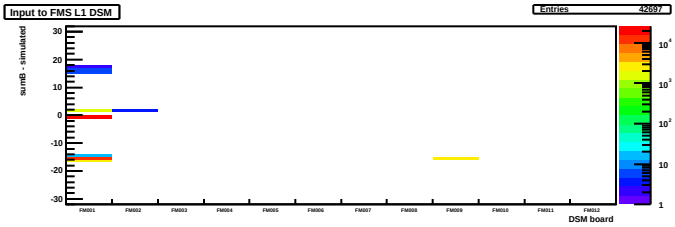

Input to FMS L1 DSM
Entries 7166280
sumC
DSM board

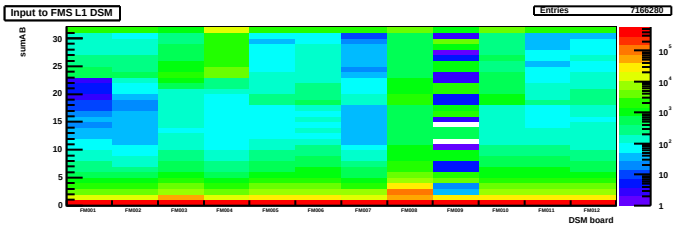

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

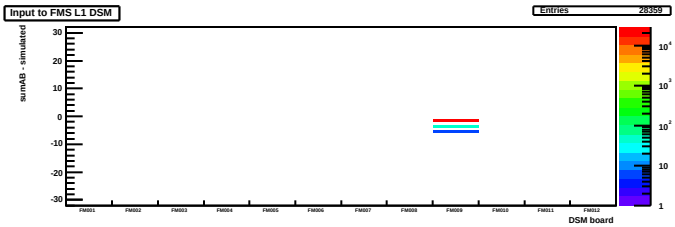

Input to FMS L1 DSM
Entries 7166280
sumBC
DSM board

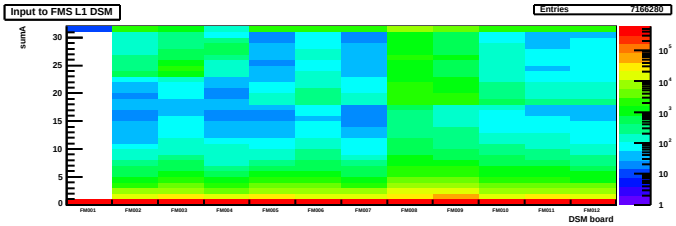

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

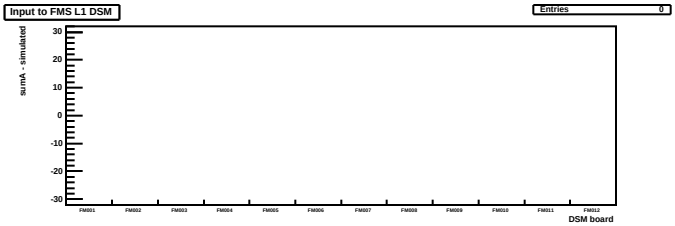

Input to FMS L1 DSM
Entries 7166280
sumB
DSM board

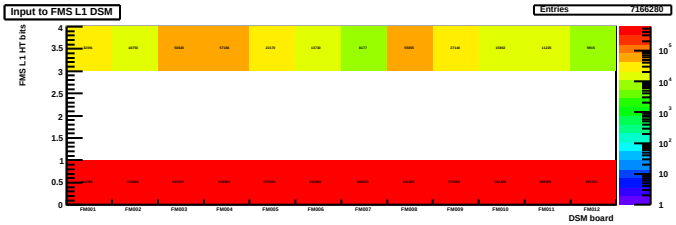

Input to FMS L1 DSM
Entries 42697
sumB - simulated
DSM board

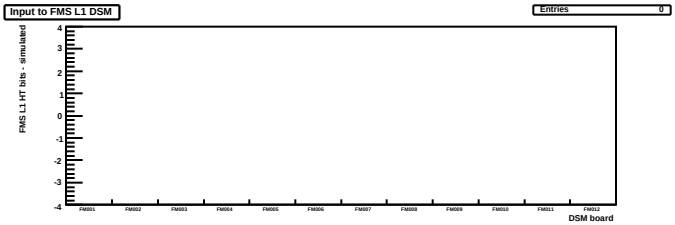

Input to FMS L1 DSM
Entries 7166280
sumAB
DSM board

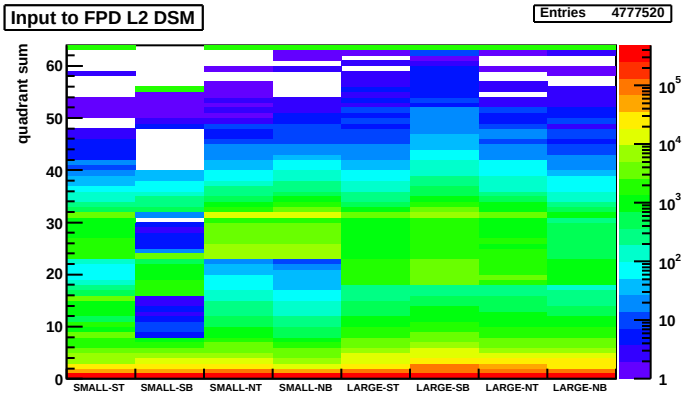

Input to FMS L1 DSM
Entries 28359
sumAB - simulated
DSM board

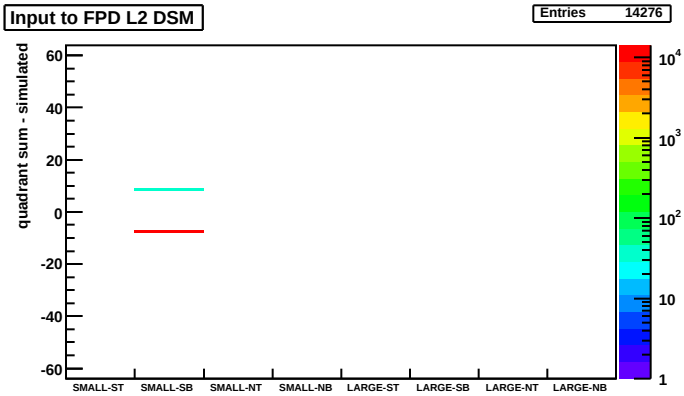

Input to FMS L1 DSM
Entries 7166280
sumA
DSM board

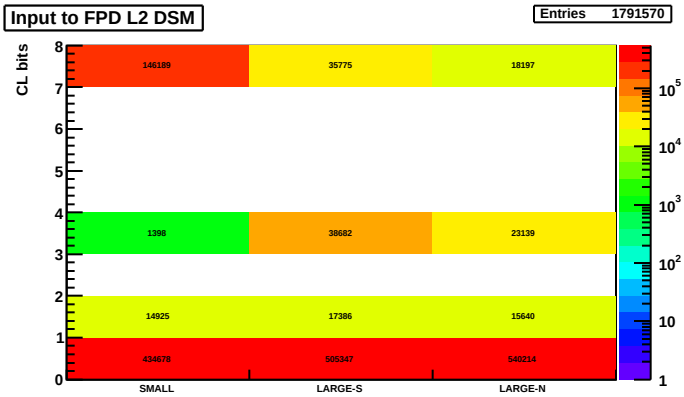

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

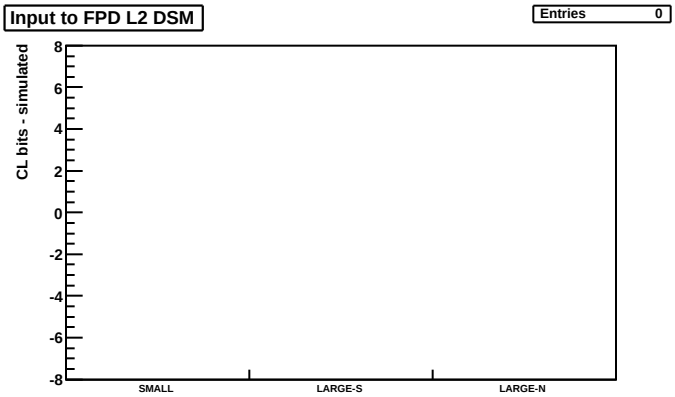

Input to FMS L1 DSM
Entries 7166280
FMS L1 HT bits
DSM board

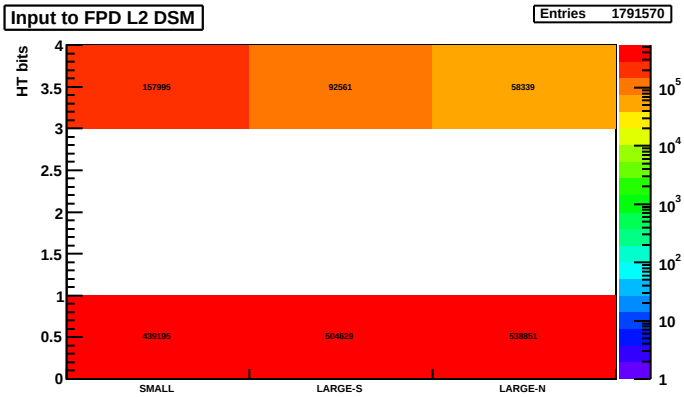

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

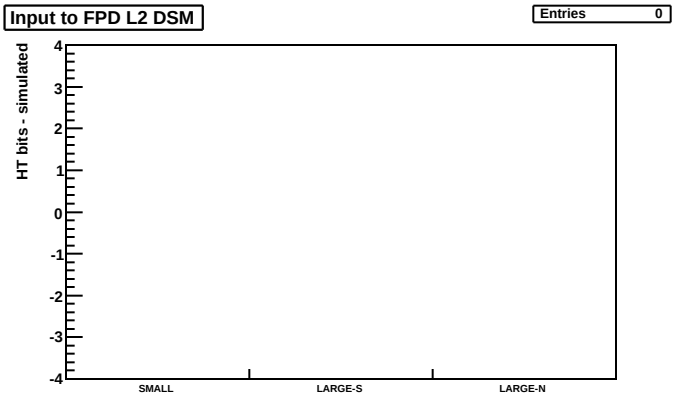

Input to FPD L2 DSM
Entries 4777520
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

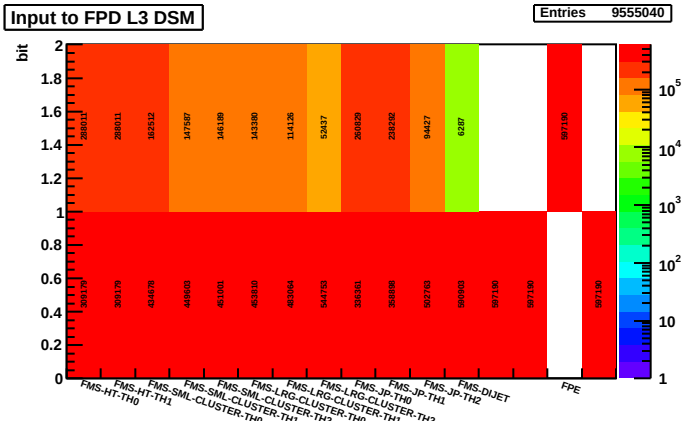

Input to FPD L2 DSM
Entries 14276
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

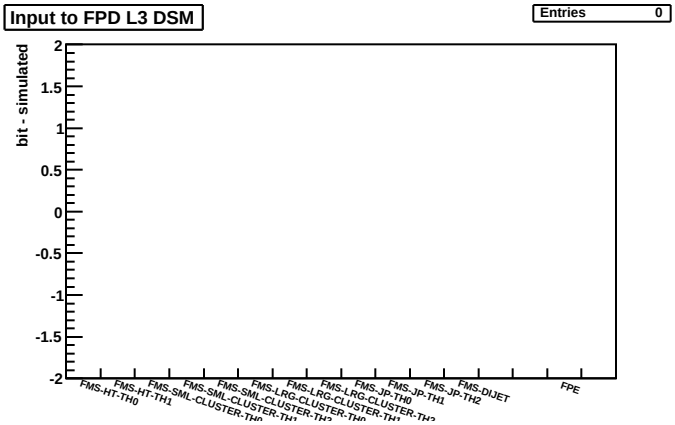

Input to FPD L2 DSM
Entries 1791570
CL bits
146189
35775
18197
1398
38682
23139
14925
17386
15640
434678
505347
540214
SMALL
LARGE-S
LARGE-N

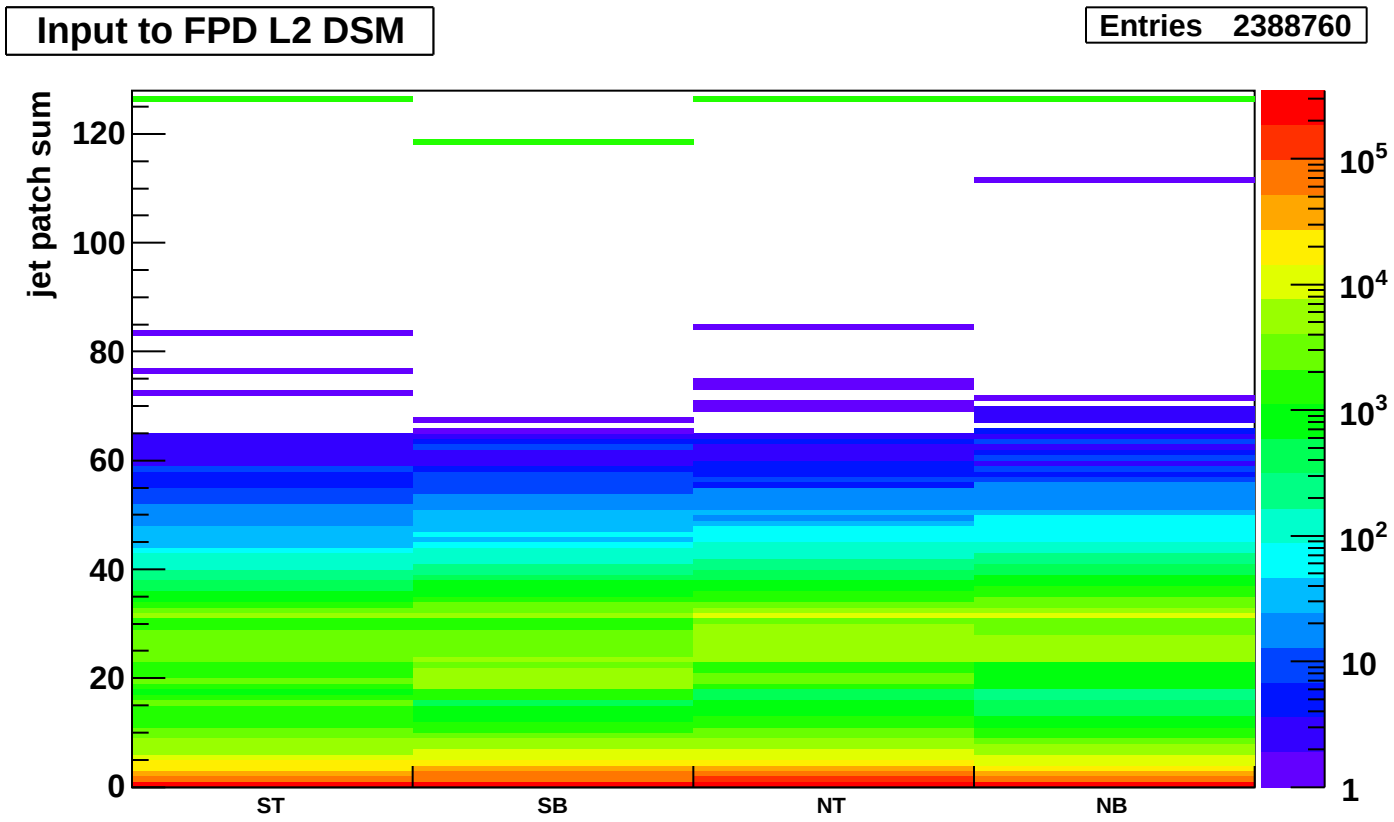

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

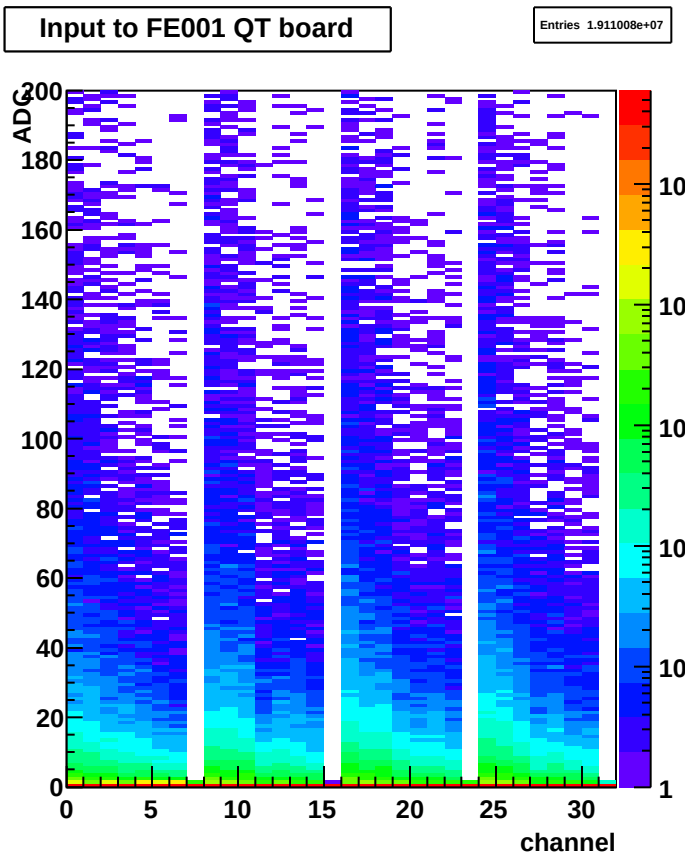

Input to FPD L2 DSM
Entries 1791570
HT bits
157995
92561
58339
439195
504629
538851
SMALL
LARGE-S
LARGE-N

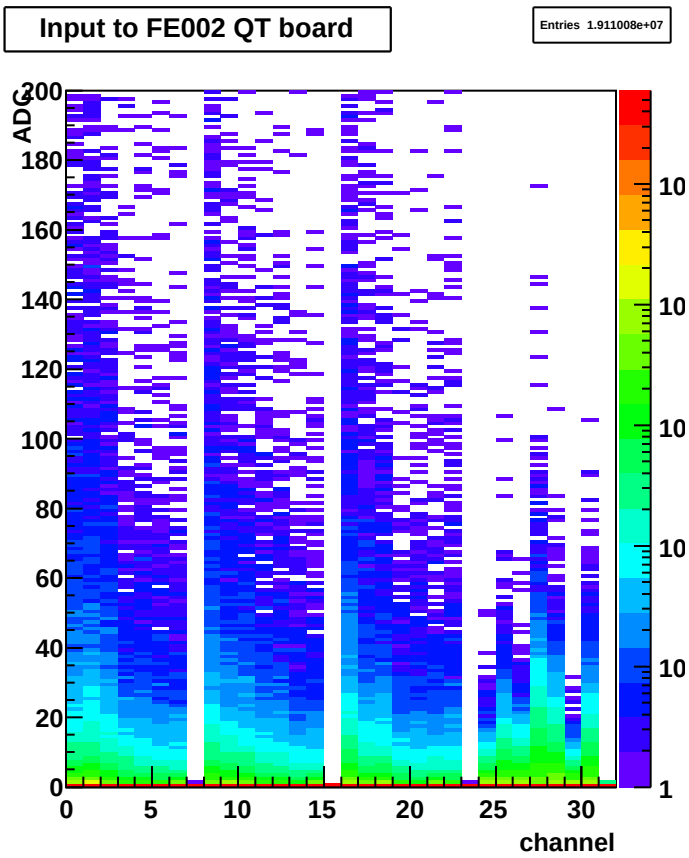

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

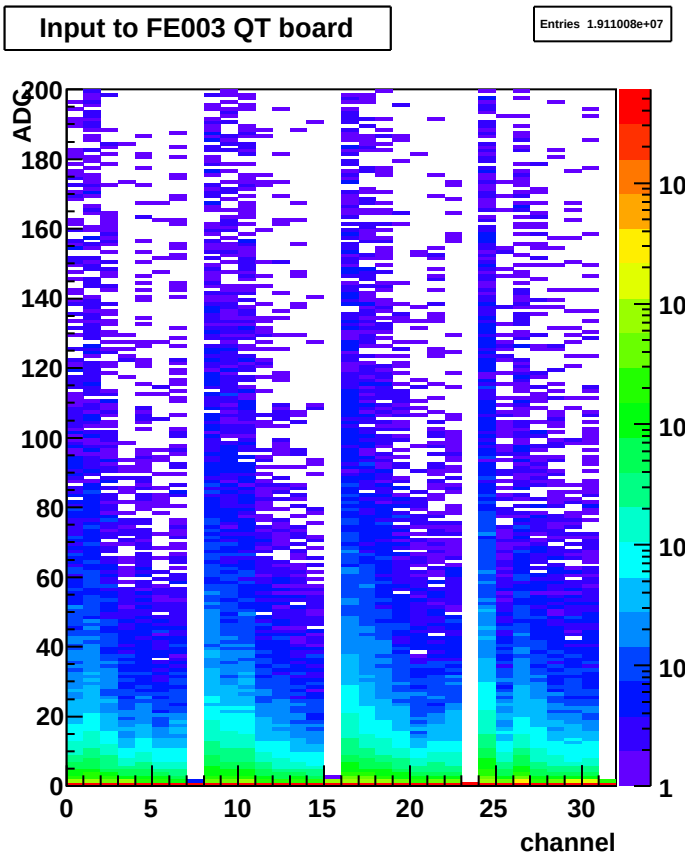

Input to FPD L3 DSM
Entries 9555040
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

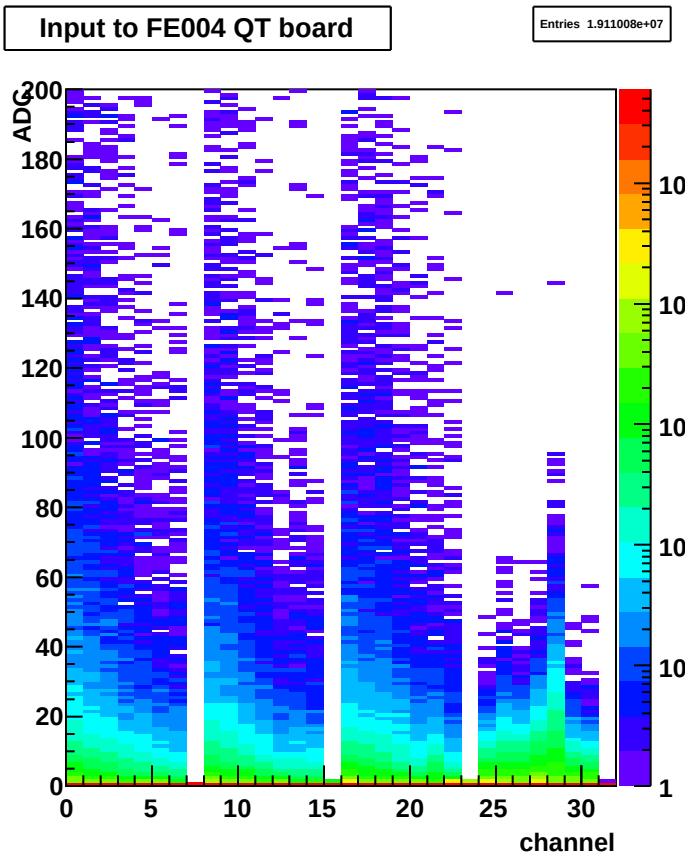

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

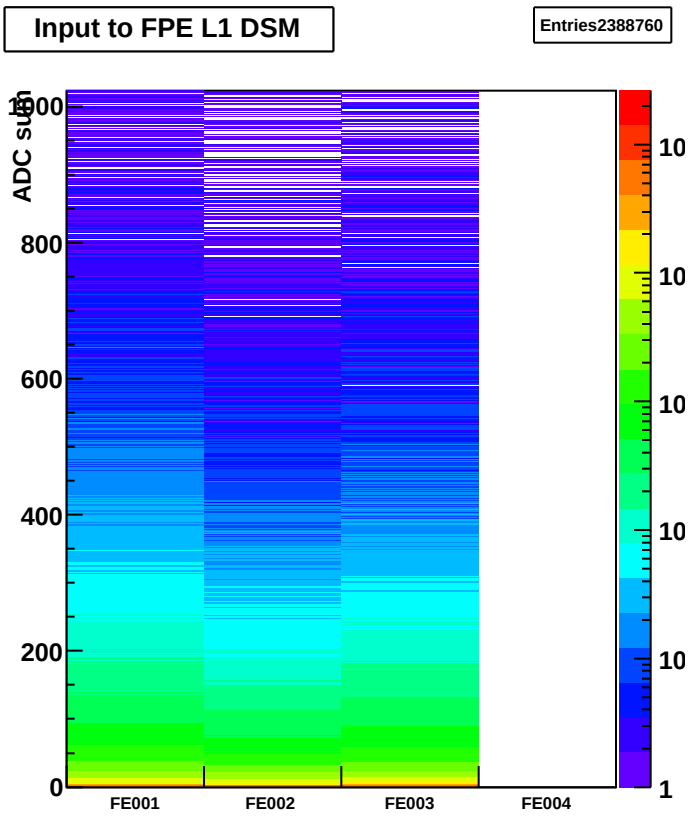

Input to FPD L2 DSM
Entries 2388760
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

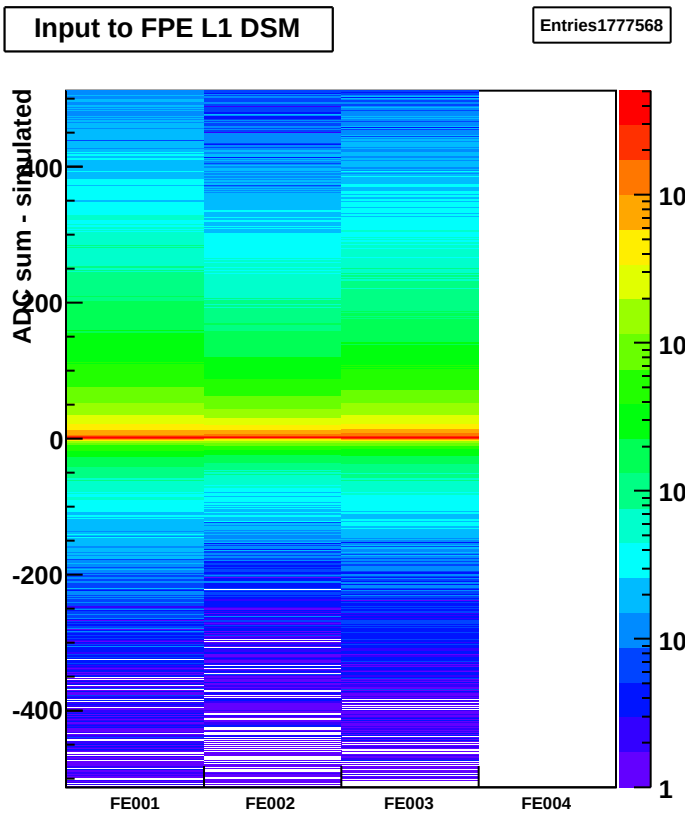

Input to FE001 QT board
Entries 1.911008e+07
ADC
channel

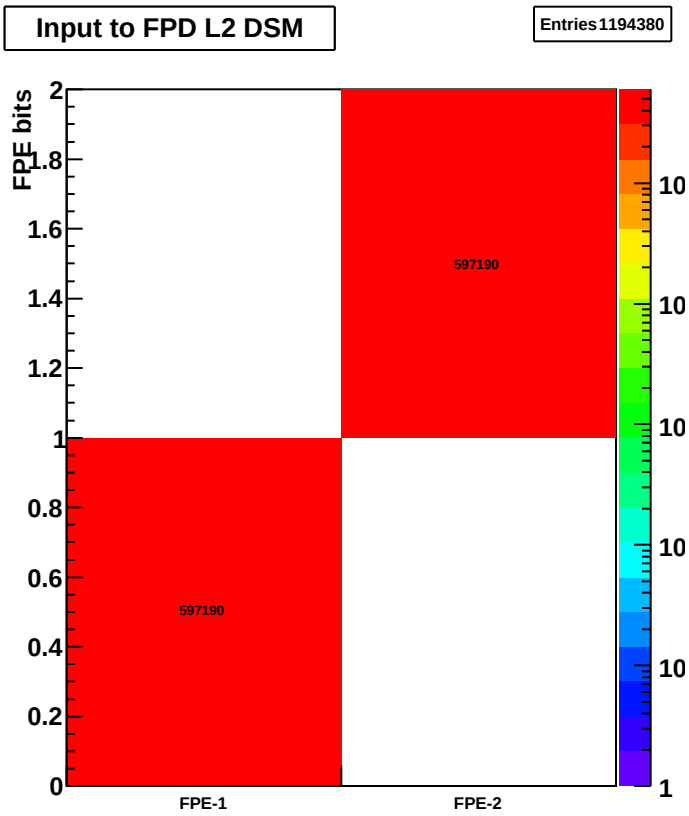

Input to FE002 QT board
Entries 1.911008e+07
ADC
channel

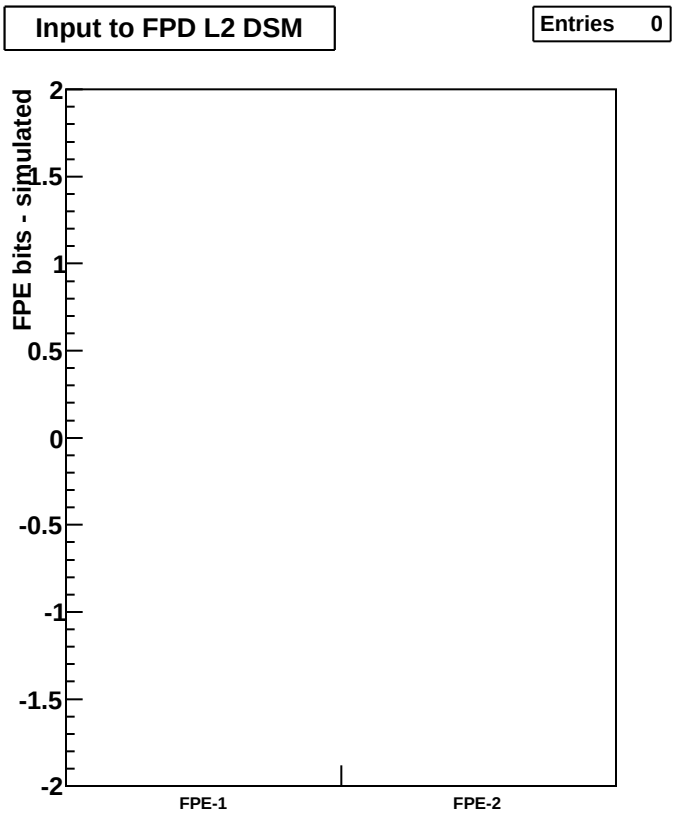

Input to FE003 QT board
Entries 1.911008e+07
ADC
channel


Input to FE004 QT board
Entries 1.911008e+07
ADC
channel


Input to FPE L1 DSM
Entries2388760
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries1777568
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries1194380
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
597190
597190
FPE-1
FPE-2
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2