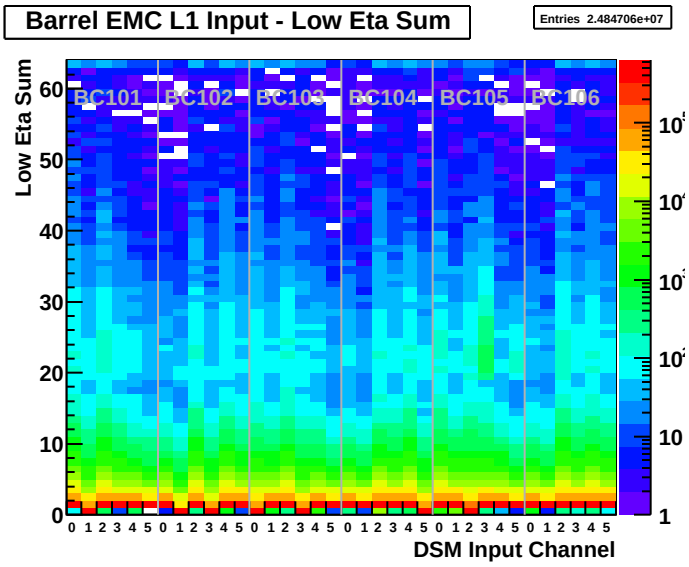
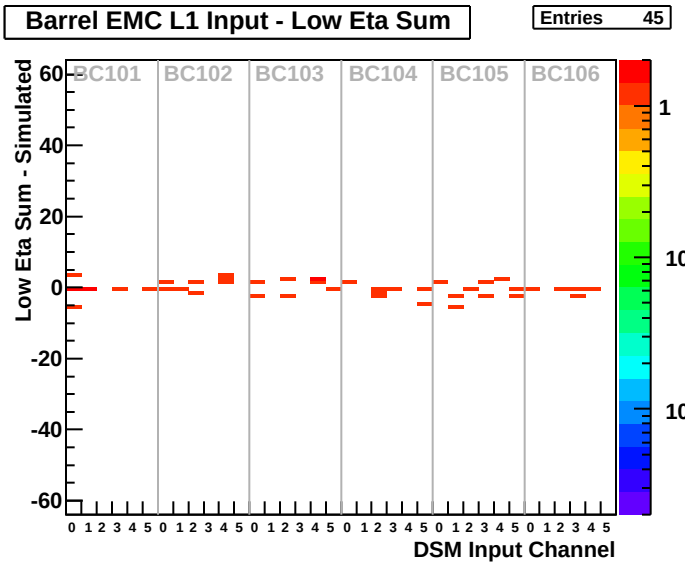
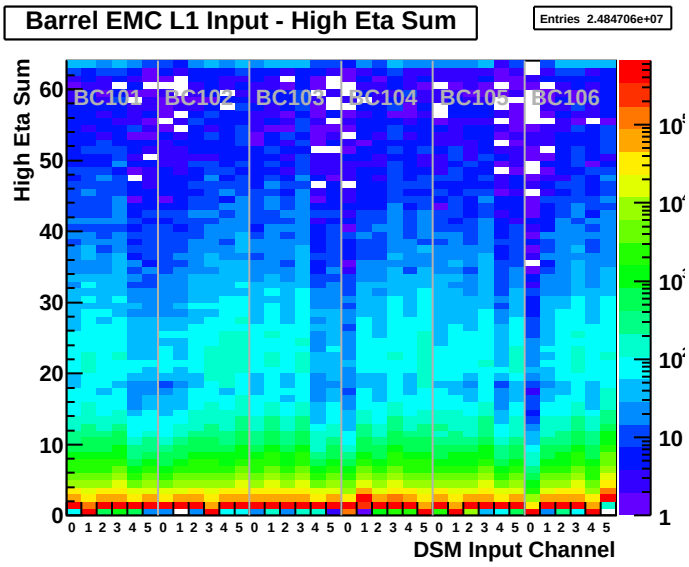
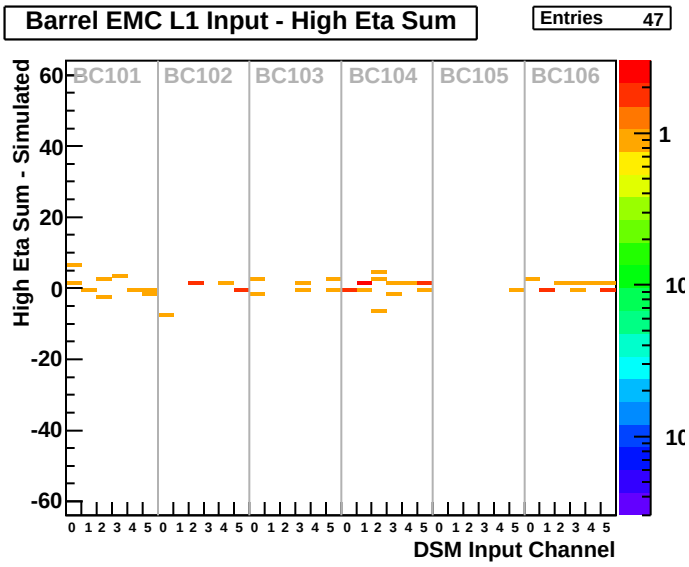
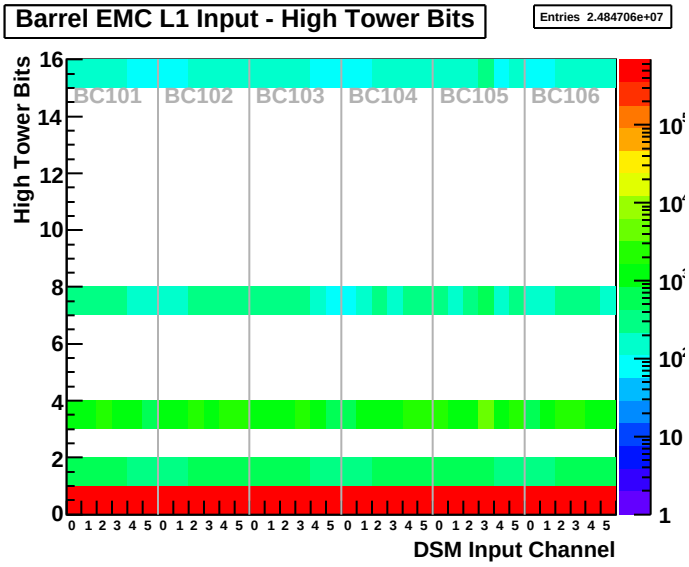
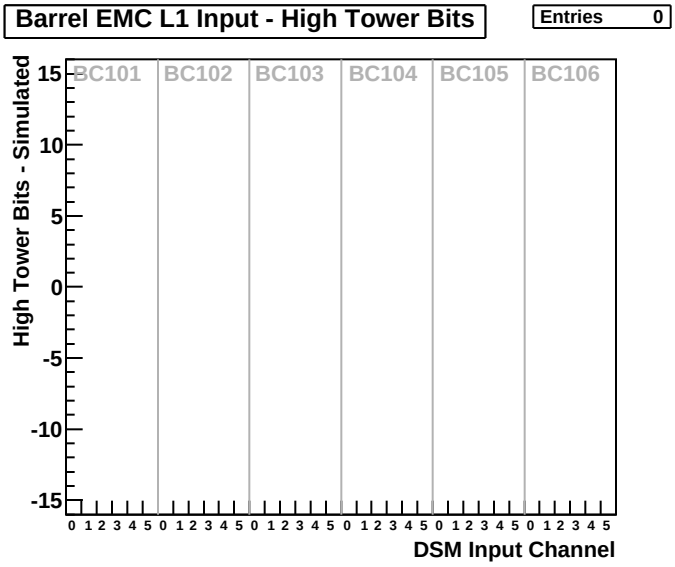

Barrel EMC L1 Input - Low Eta Sum
Entries 2.484706e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

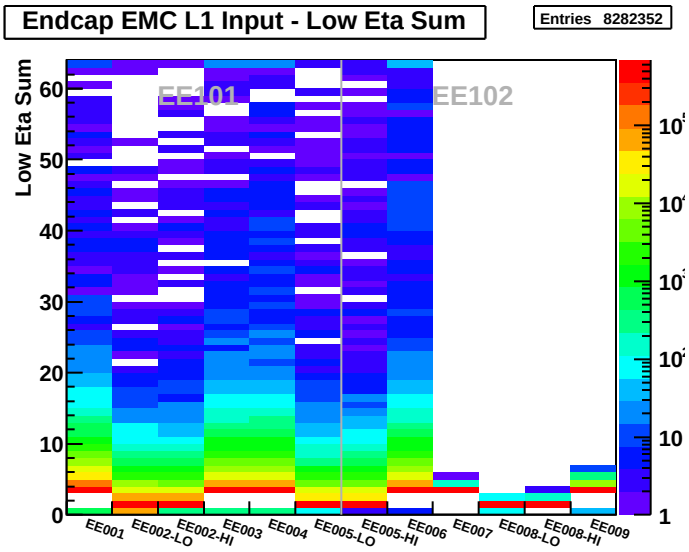

Barrel EMC L1 Input - Low Eta Sum
Entries 45
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

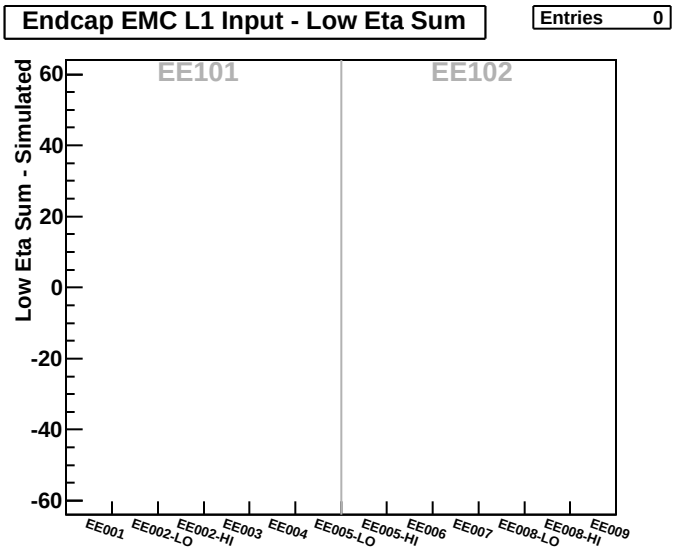

Barrel EMC L1 Input - High Eta Sum
Entries 2.484706e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

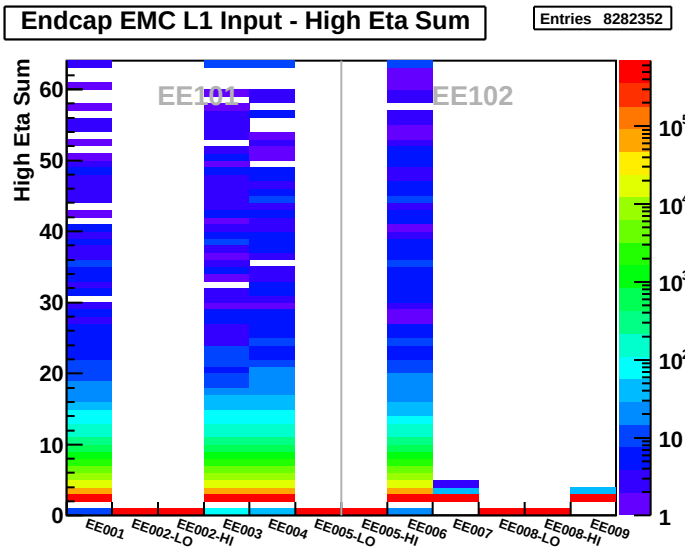

Barrel EMC L1 Input - High Eta Sum
Entries 47
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

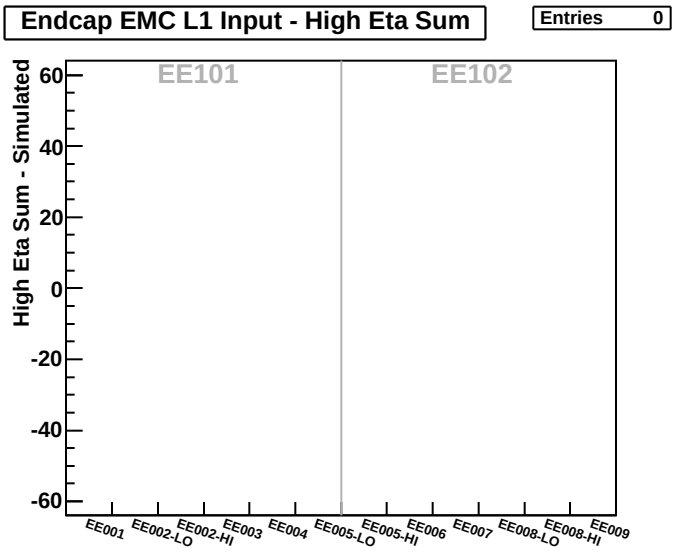

Barrel EMC L1 Input - High Tower Bits
Entries 2.484706e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

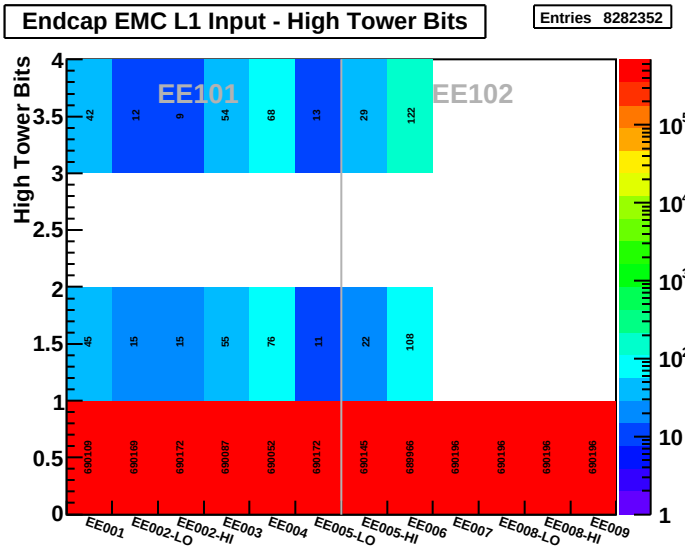

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

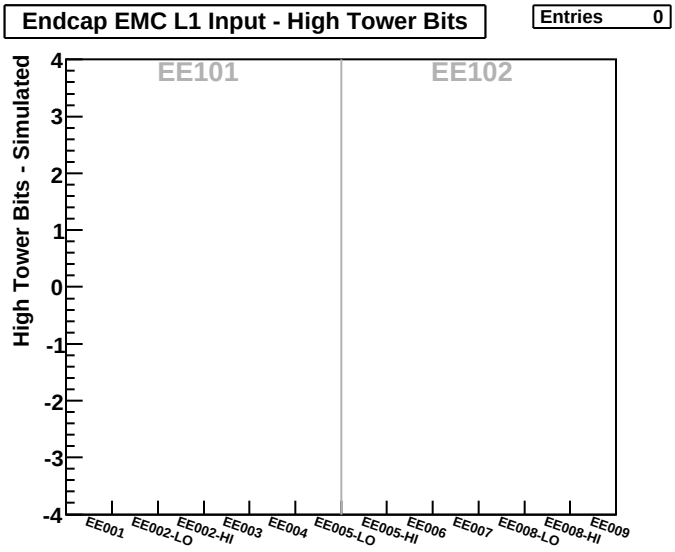

Endcap EMC L1 Input - Low Eta Sum
Entries 8282352
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

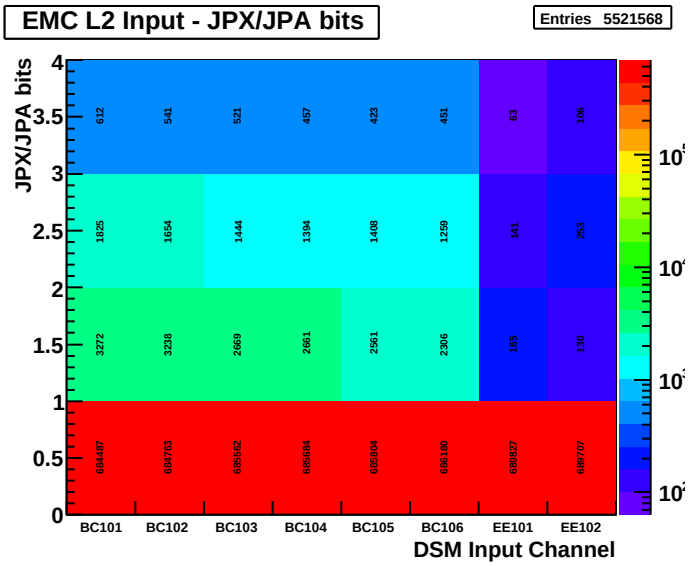

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

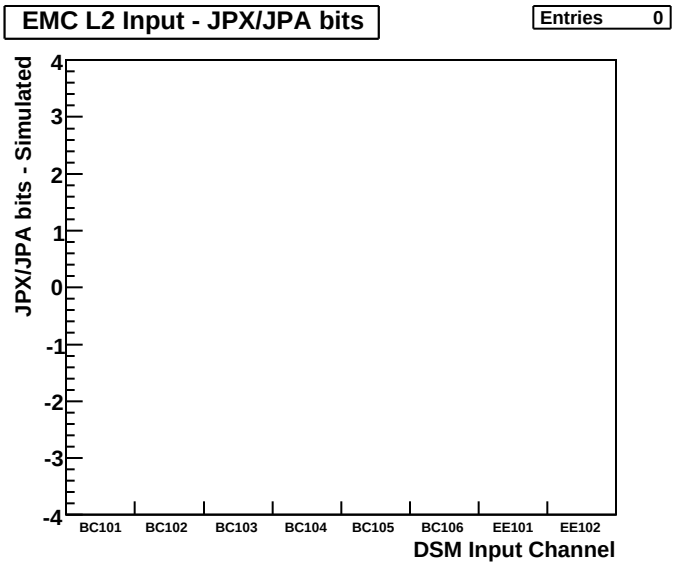

Endcap EMC L1 Input - High Eta Sum
Entries 8282352
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

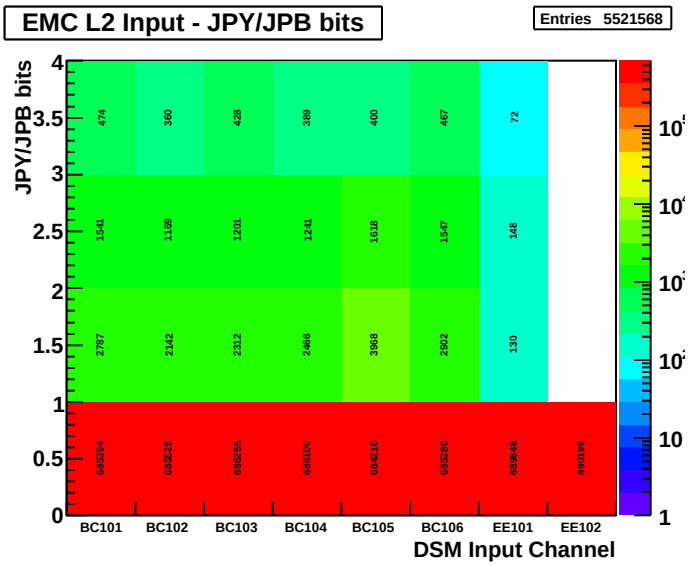

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

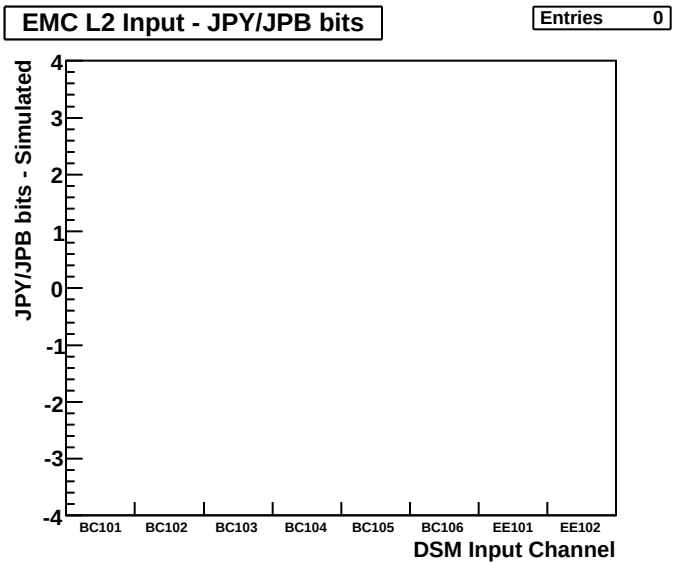

Endcap EMC L1 Input - High Tower Bits
Entries 8282352
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

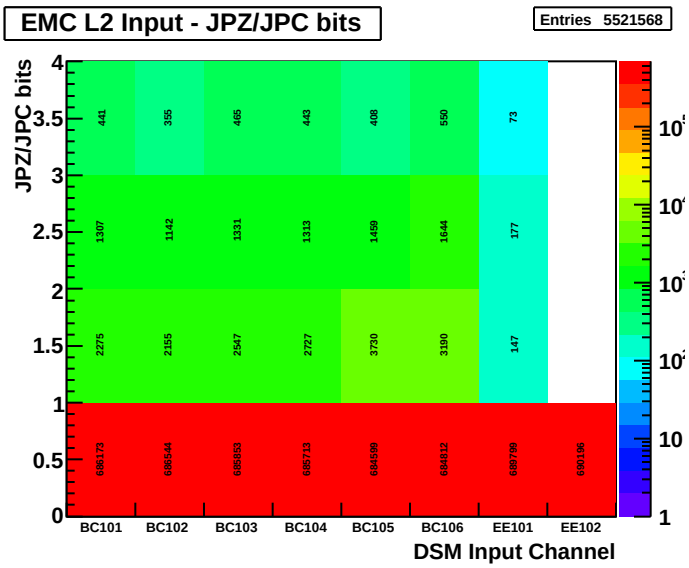

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

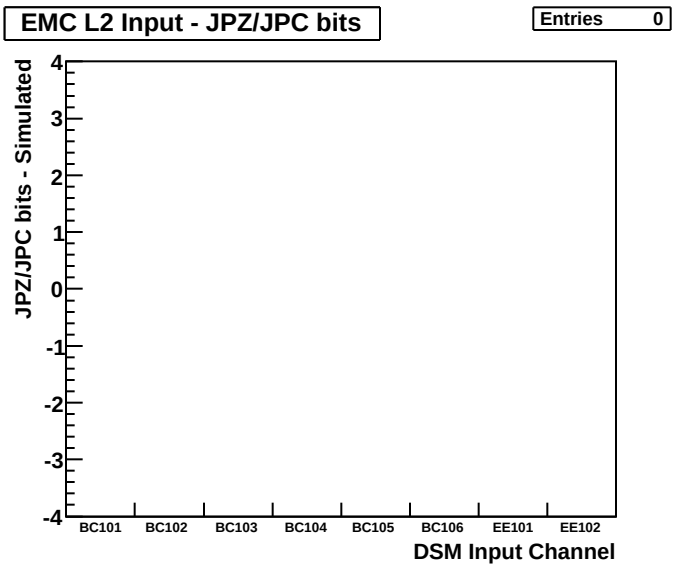

EMC L2 Input - JPX/JPA bits
Entries 5521568
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
612 541 521 457 423 451 63 106
1825 1654 1444 1394 1408 1259 141 253
3272 3238 2669 2661 2561 2306 165 130
684467 684763 685562 685694 685804 685180 689827 689707

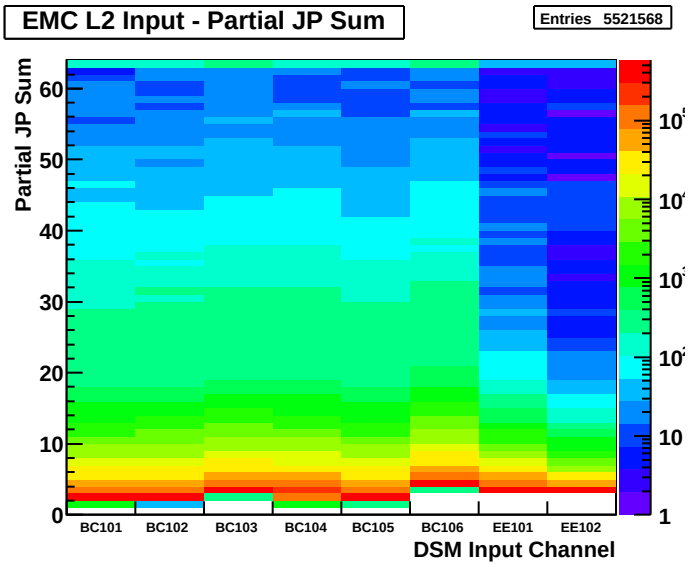

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

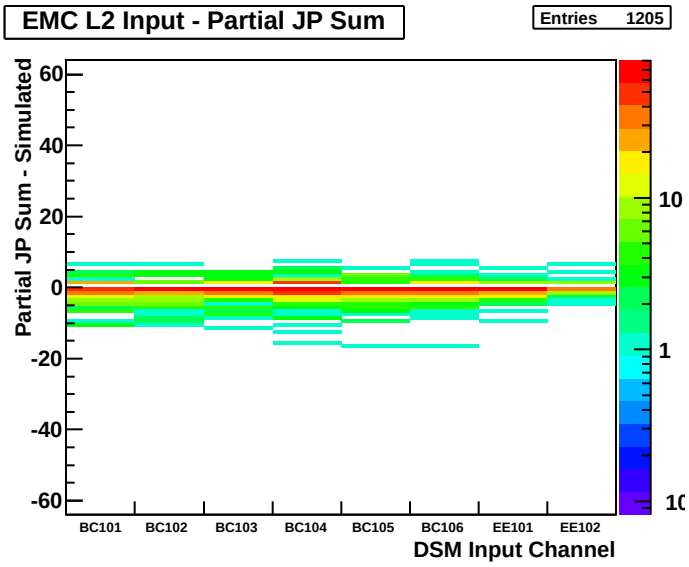

EMC L2 Input - JPY/JPB bits
Entries 5521568
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
474 360 428 389 400 467 72
1541 1169 1201 1241 1618 1547 148
2787 2342 2312 2466 3968 2902 130
686394 686525 686255 686100 684210 685280 689846 690196

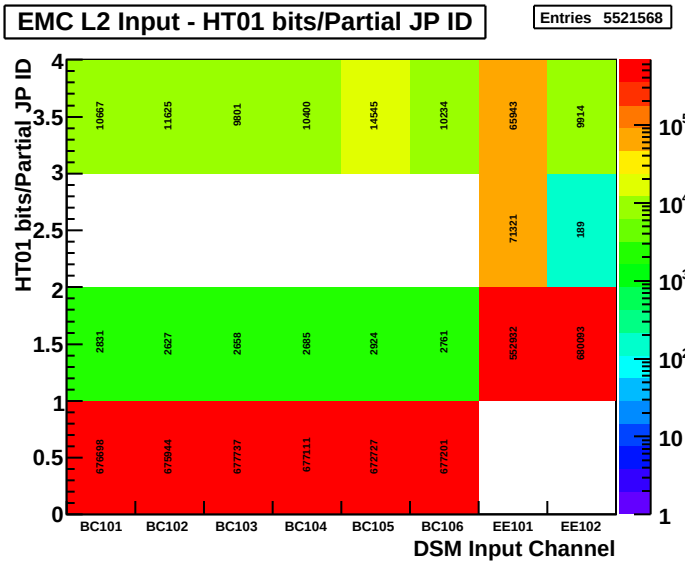

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

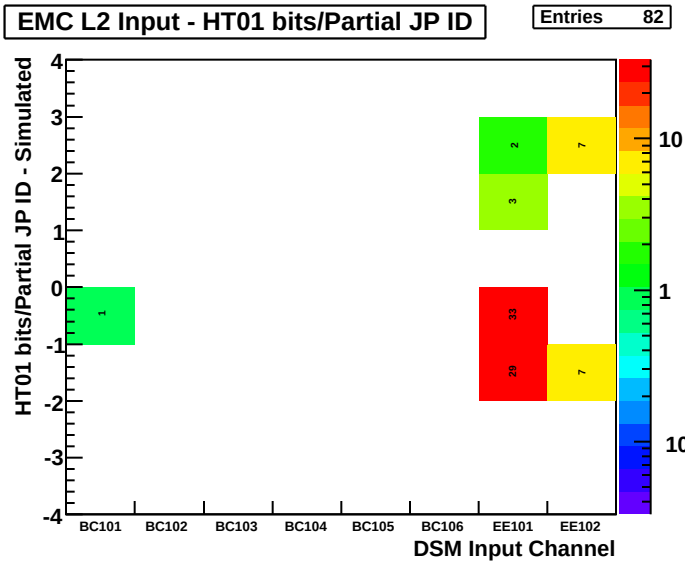

EMC L2 Input - JPZ/JPC bits
Entries 5521568
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
441 355 465 443 408 550 73
1307 1142 1331 1313 1459 1644 177
2275 2155 2547 2727 3730 3190 147
686173 686544 685853 685713 684599 684812 689799 690196

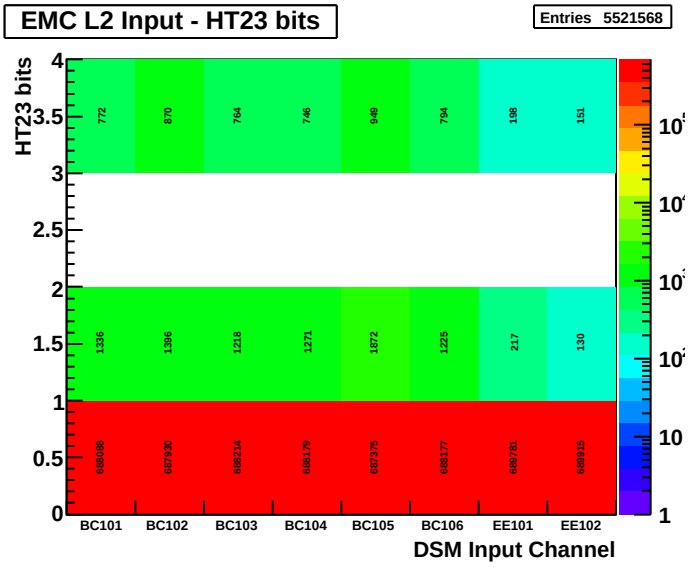

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

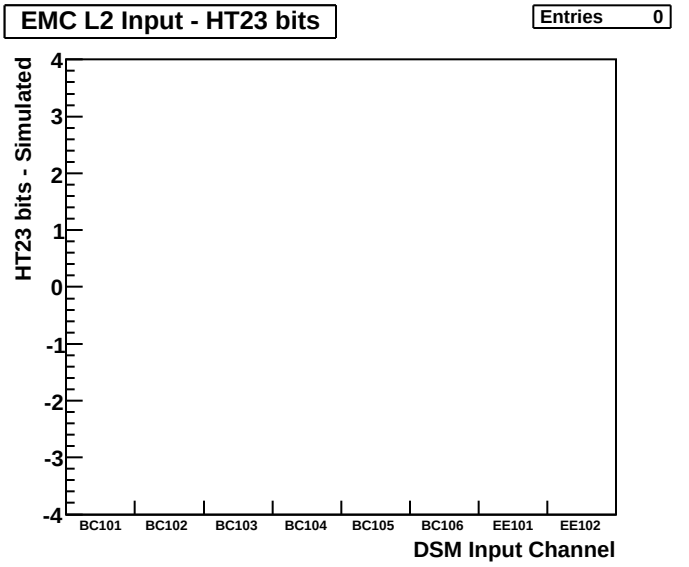

EMC L2 Input - Partial JP Sum
Entries 5521568
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

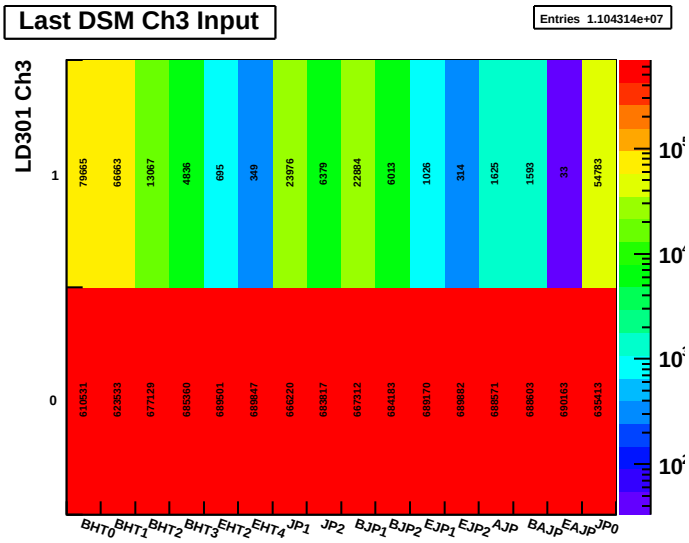

EMC L2 Input - Partial JP Sum
Entries 1205
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

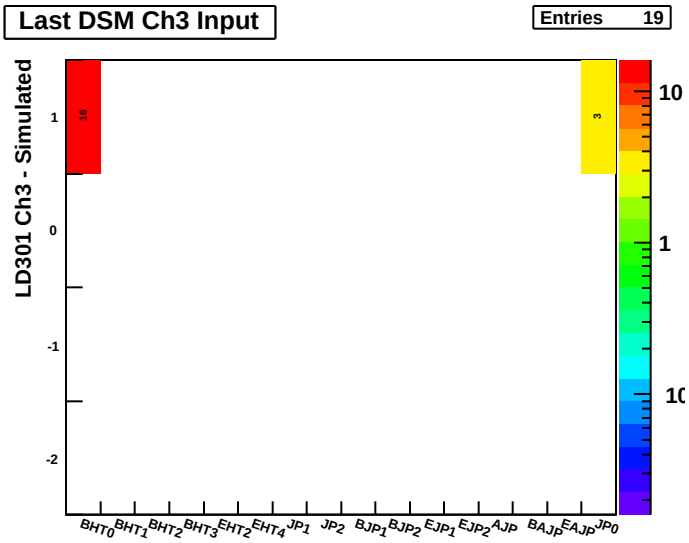

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5521568
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

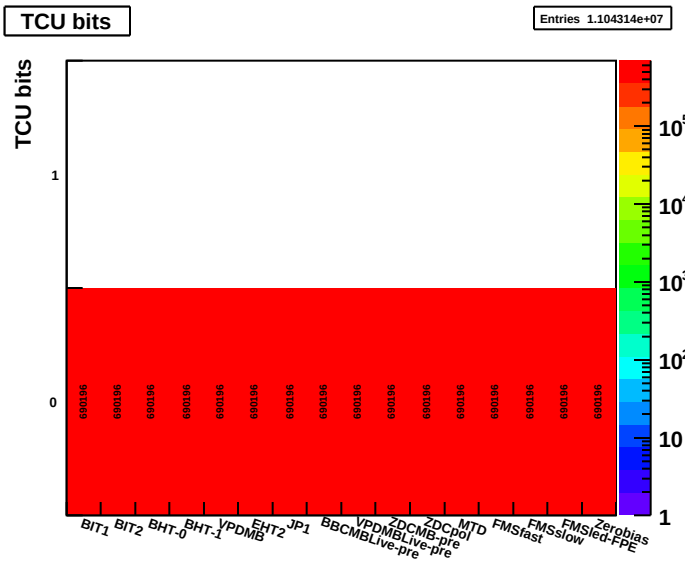

EMC L2 Input - HT01 bits/Partial JP ID
Entries 82
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

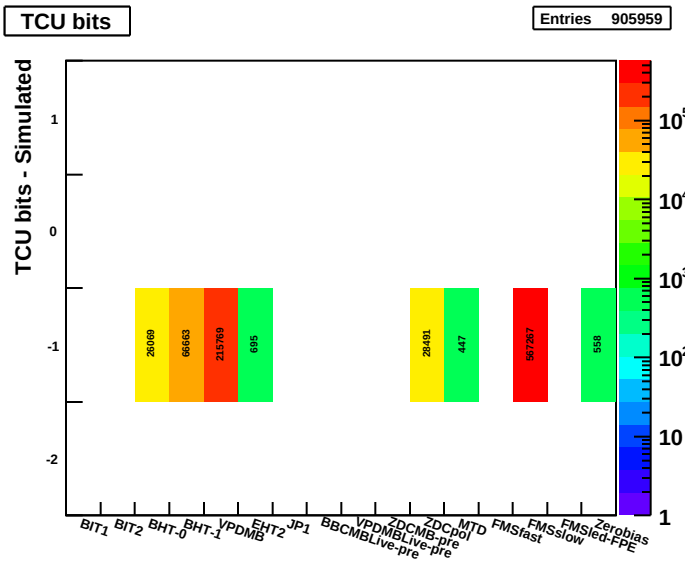

EMC L2 Input - HT23 bits
Entries 5521568
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

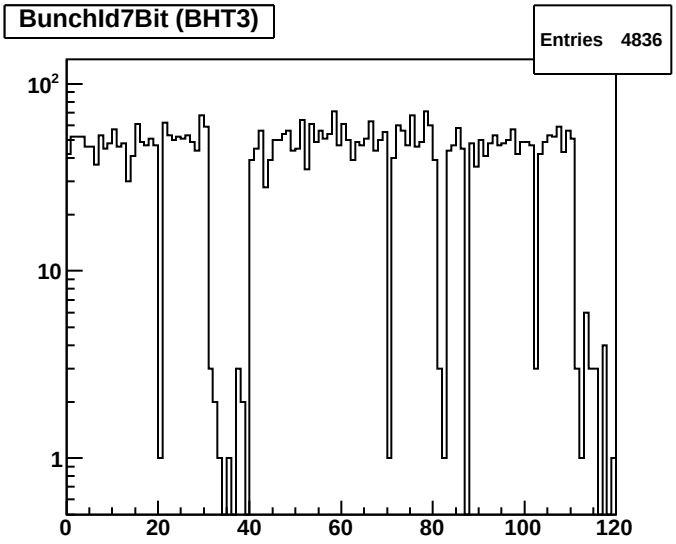

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

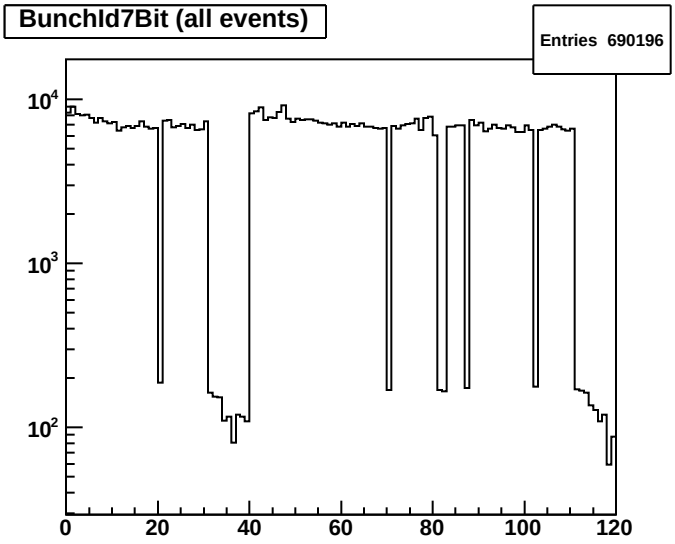

Last DSM Ch3 Input
Entries 1.104314e+07
LD301 Ch3
79665 66663 13067 4836 695 349 23976 6379 22884 6013 1026 314 1625 1593 33 54783
610531 623533 677129 685360 689501 688847 666220 683017 667312 684183 689170 689882 688571 688603 690163 635413
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

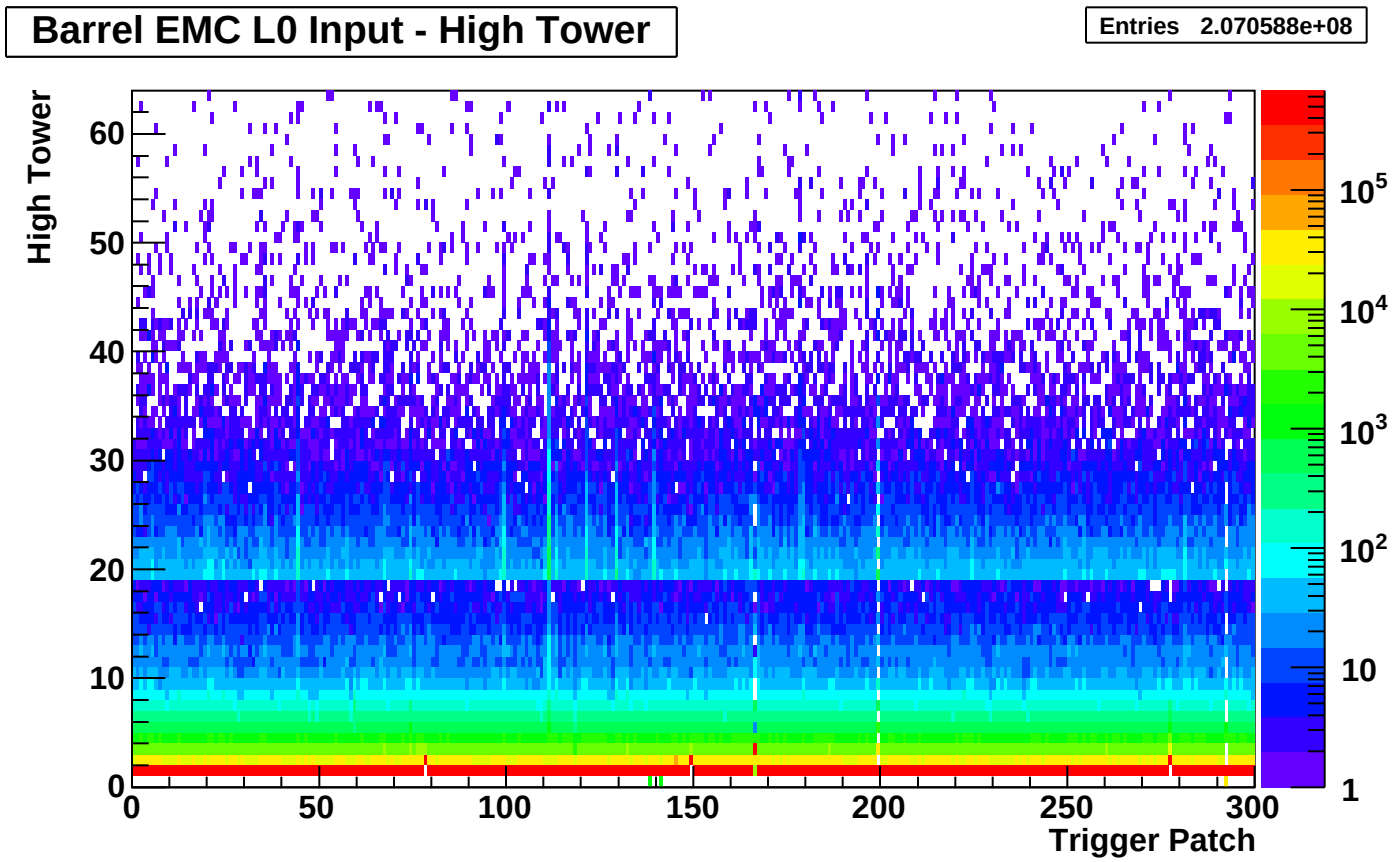

Last DSM Ch3 Input
Entries 19
LD301 Ch3 - Simulated
16
3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

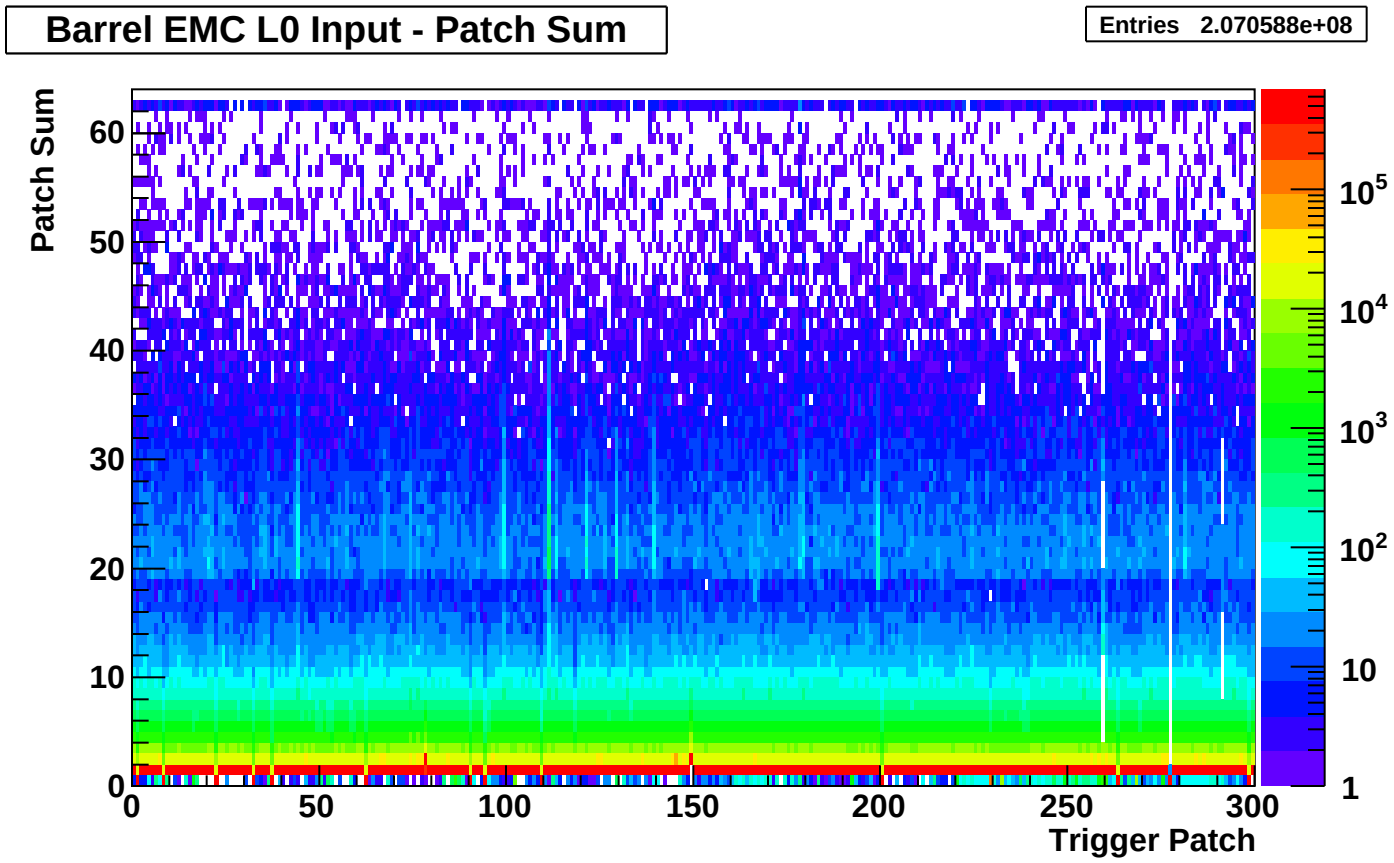

TCU bits
Entries 1.104314e+07
690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196 690196
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

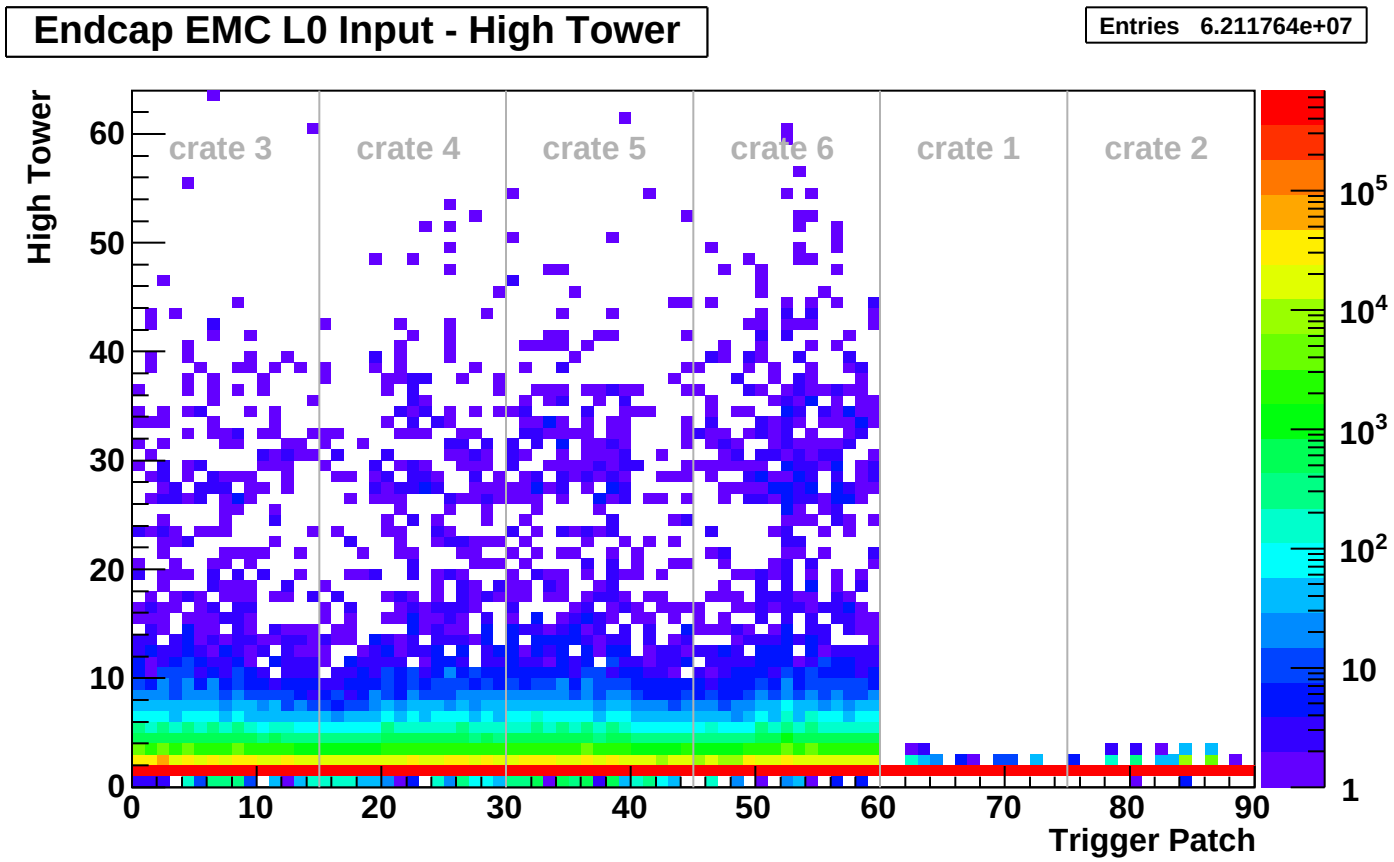

TCU bits
Entries 905959
TCU bits - Simulated
26069 66663 215769 695 28491 447 567267 558
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

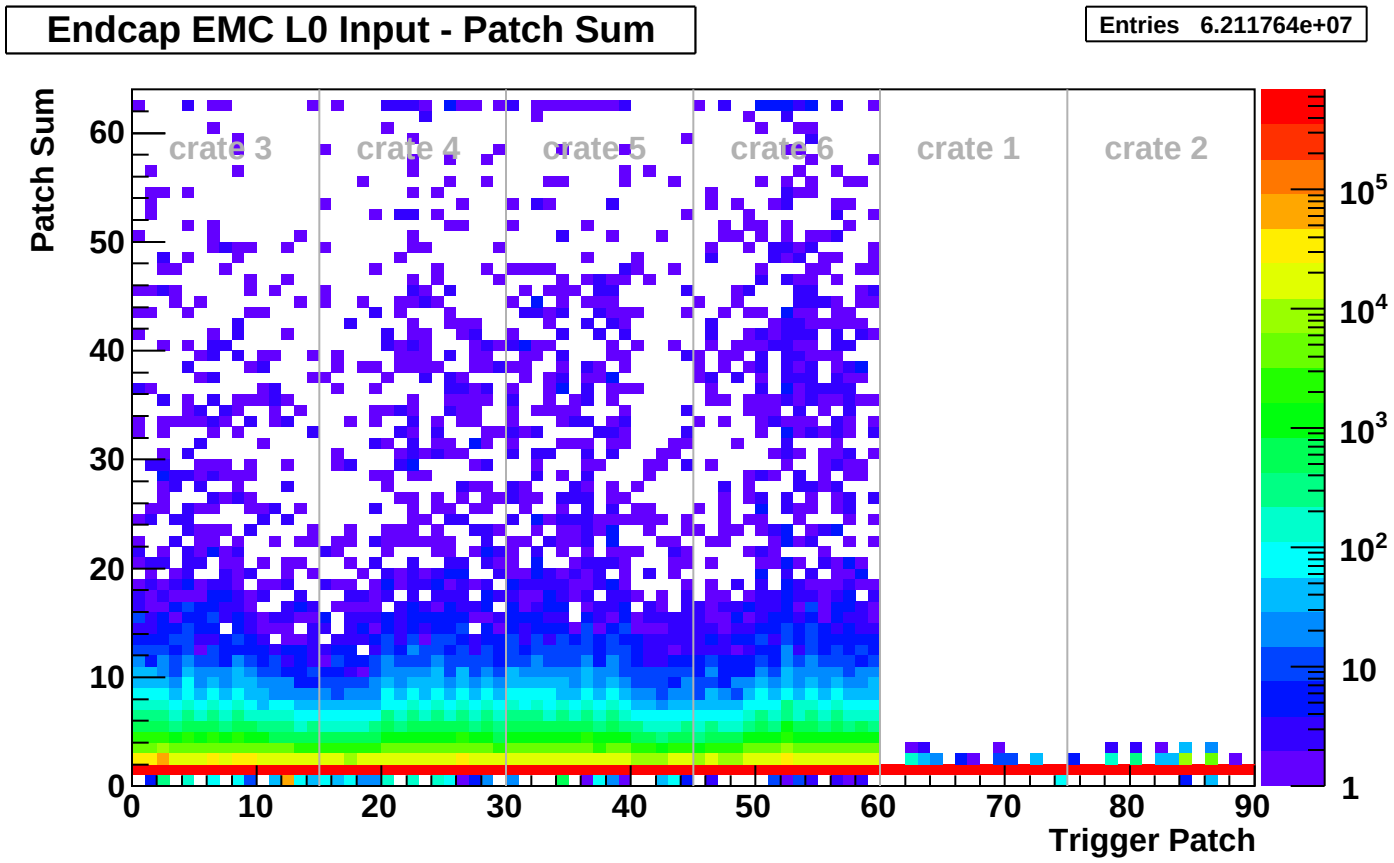

BunchId7Bit (BHT3)
Entries 4836

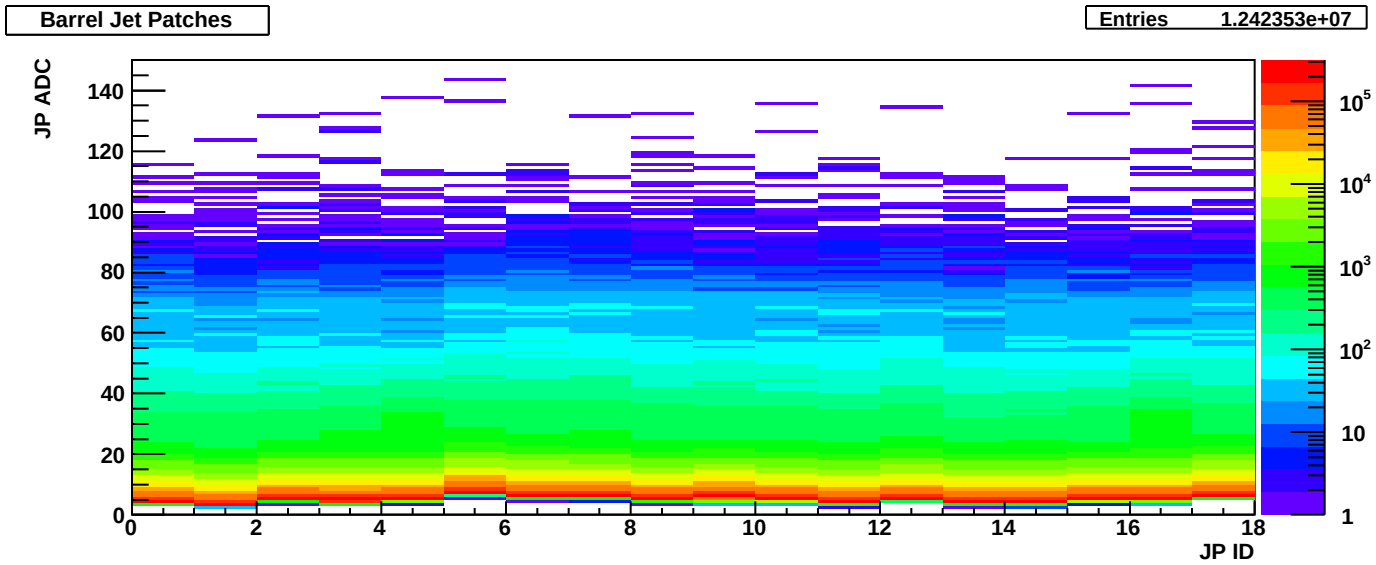

BunchId7Bit (all events)
Entries 690196

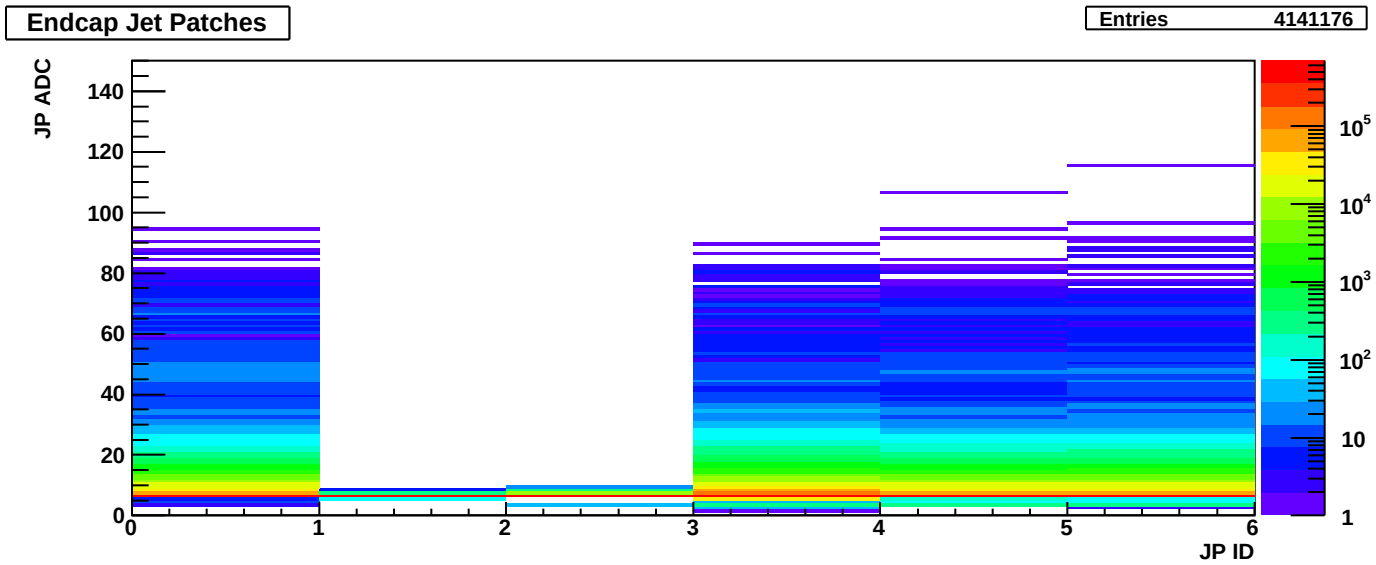

Barrel EMC L0 Input - High Tower
Entries 2.070588e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

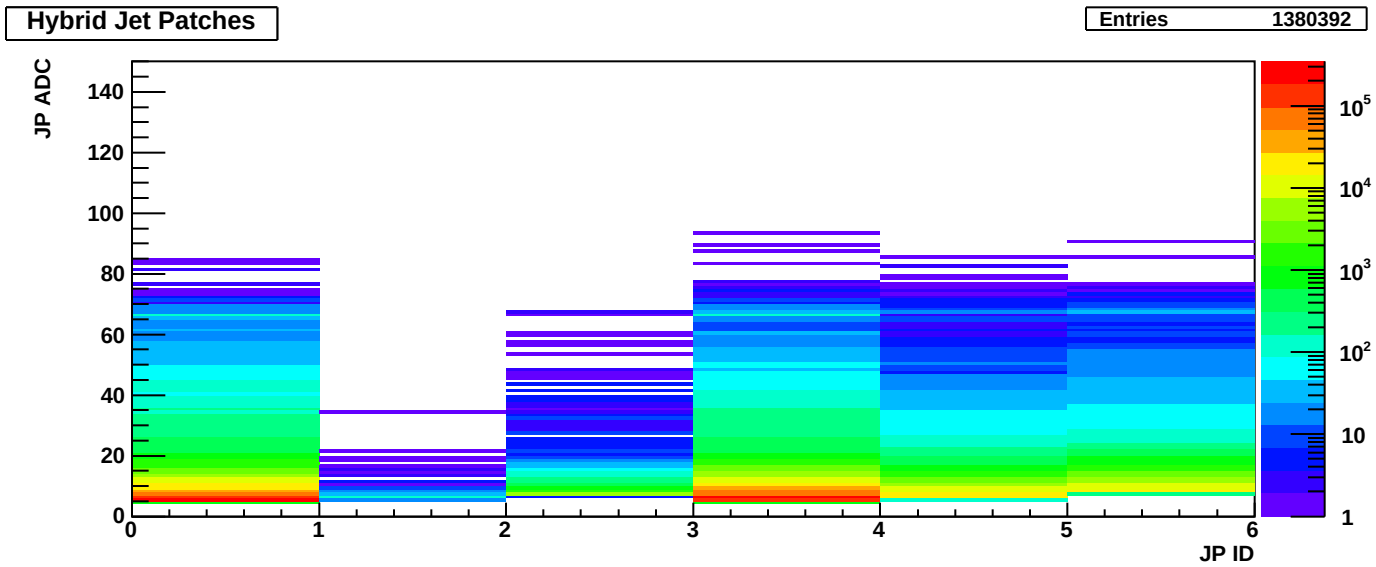

Barrel EMC L0 Input - Patch Sum
Entries 2.070588e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

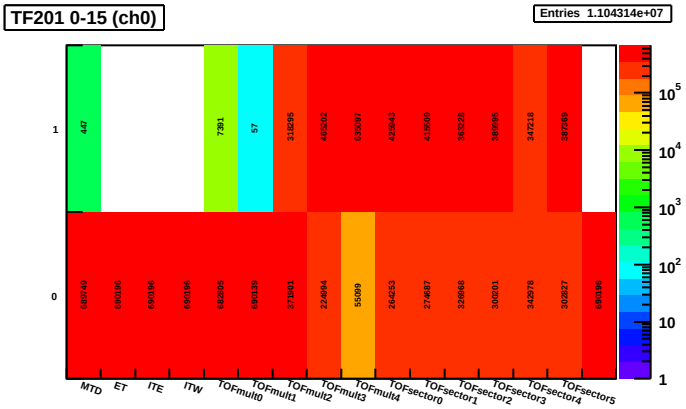

Endcap EMC L0 Input - High Tower
Entries 6.211764e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

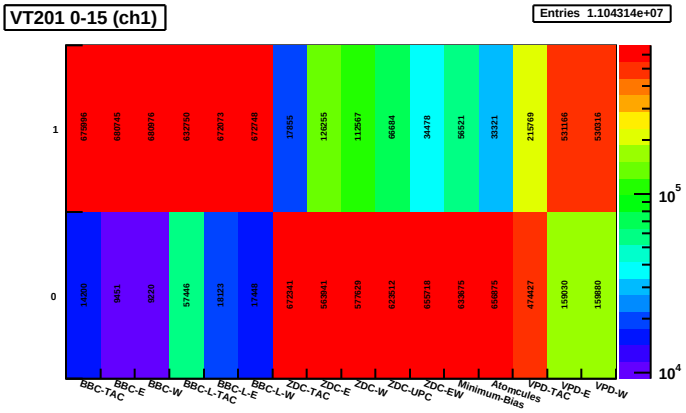

Endcap EMC L0 Input - Patch Sum
Entries 6.211764e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

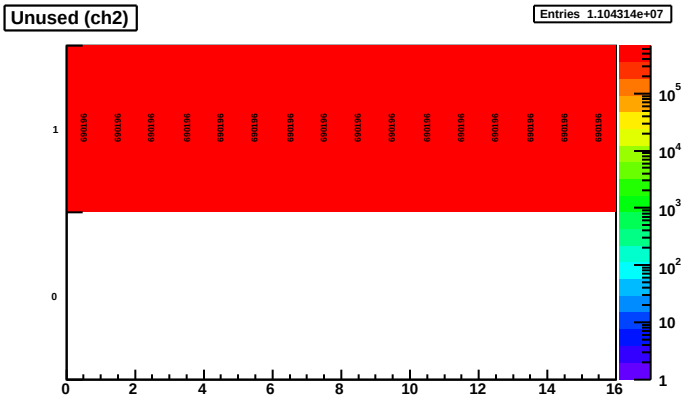

Barrel Jet Patches
Entries 1.242353e+07
JP ADC
JP ID

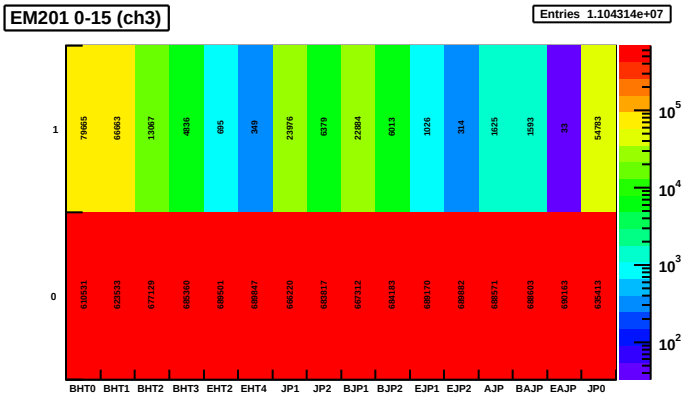

Endcap Jet Patches
Entries 4141176
JP ADC
JP ID

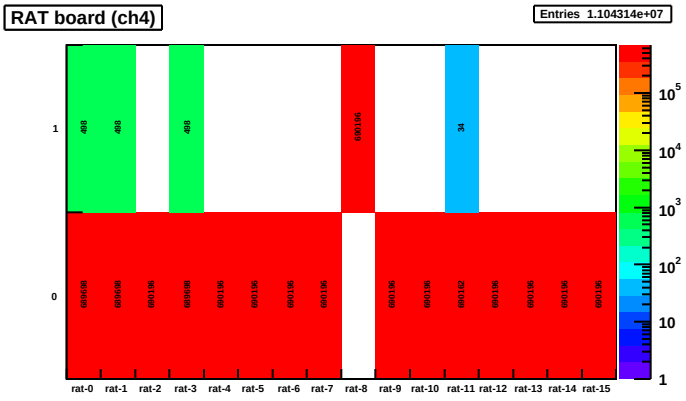

Hybrid Jet Patches
Entries 1380392
JP ADC
JP ID

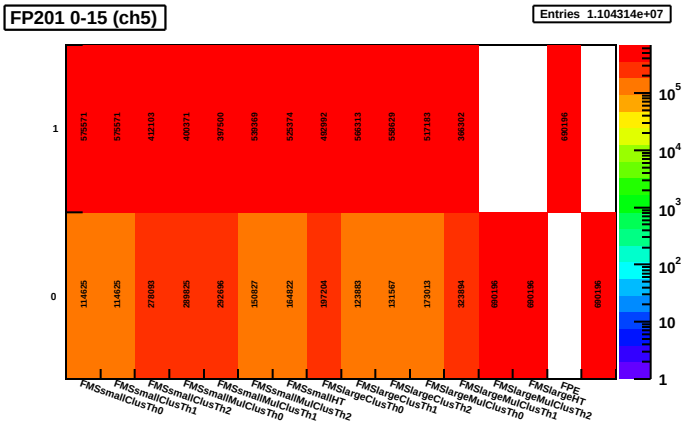

TF201 0-15 (ch0)
Entries 1.104314e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

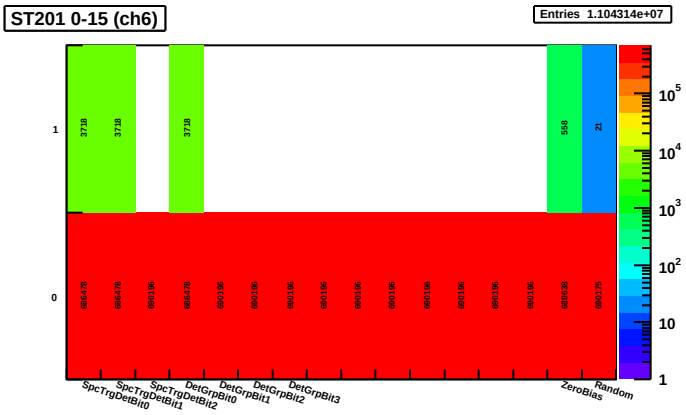

VT201 0-15 (ch1)
Entries 1.104314e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

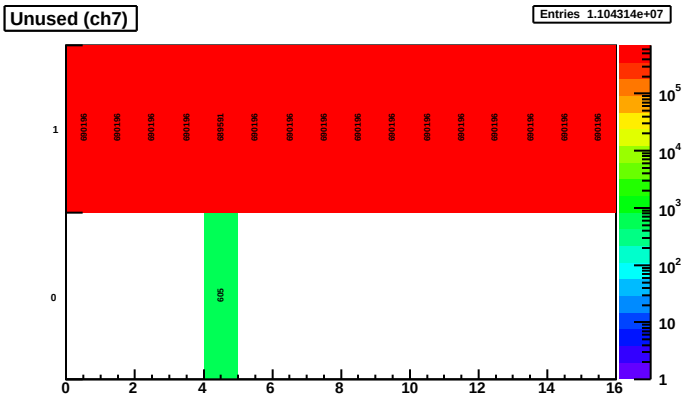

Unused (ch2)
Entries 1.104314e+07

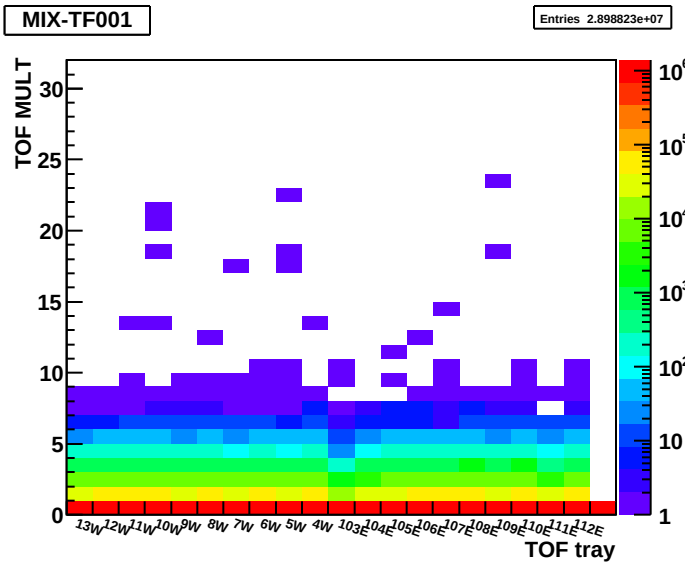

EM201 0-15 (ch3)
Entries 1.104314e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

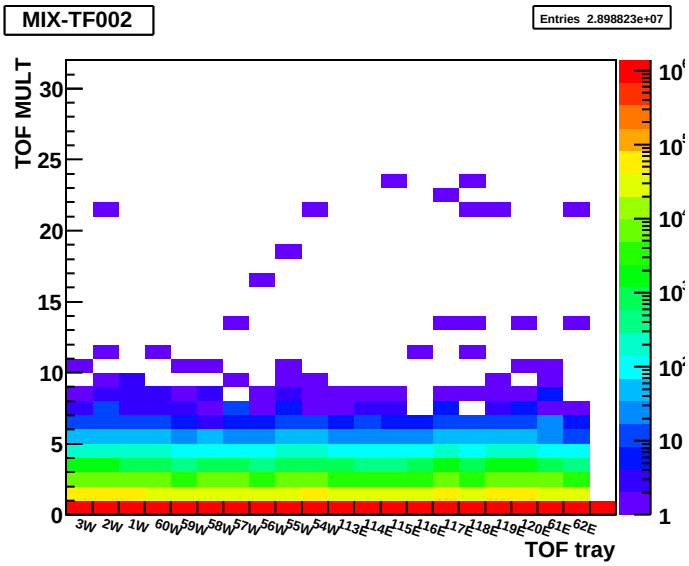

RAT board (ch4)
Entries 1.104314e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

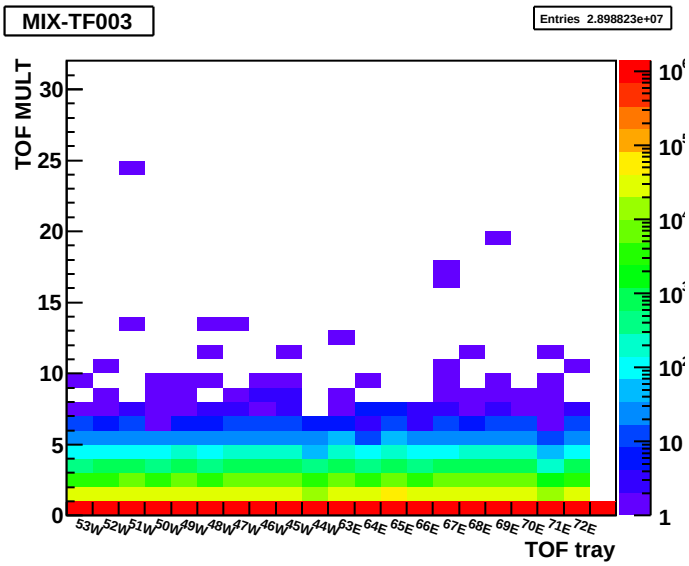

FP201 0-15 (ch5)
Entries 1.104314e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

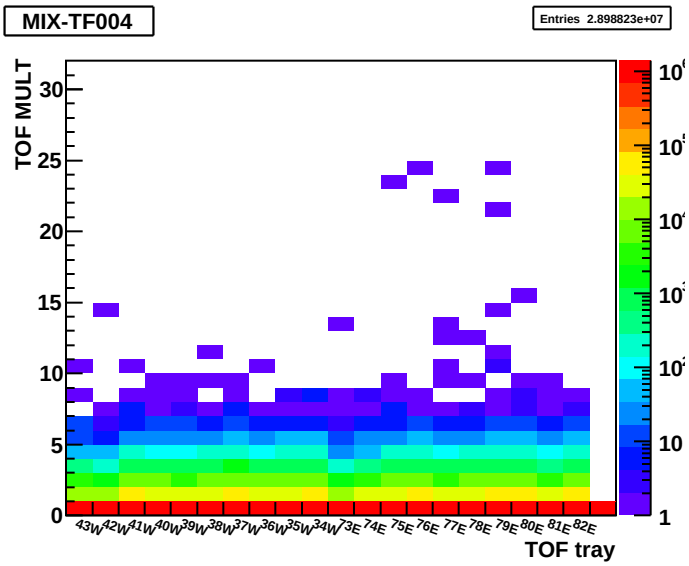

ST201 0-15 (ch6)
Entries 1.104314e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

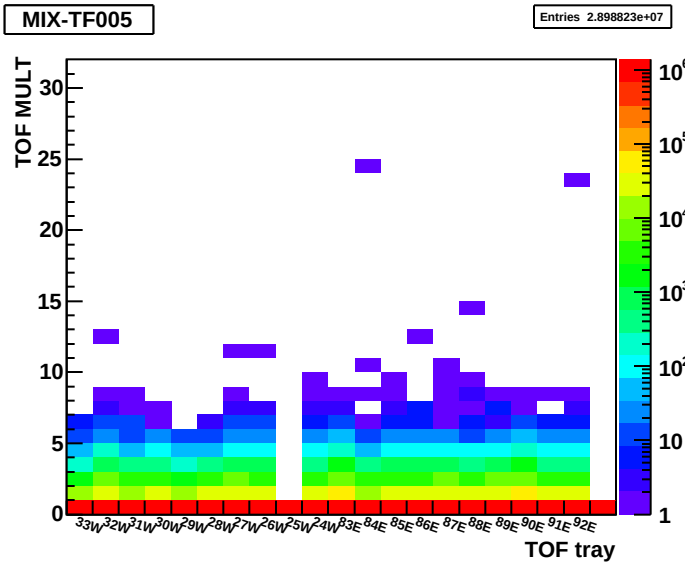

Unused (ch7)
Entries 1.104314e+07

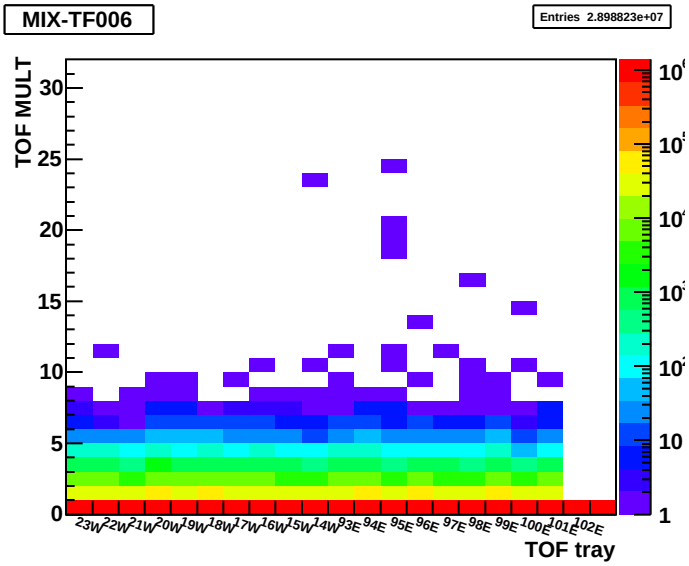

MIX-TF001
Entries 2.898823e+07
TOF MULT
TOF tray

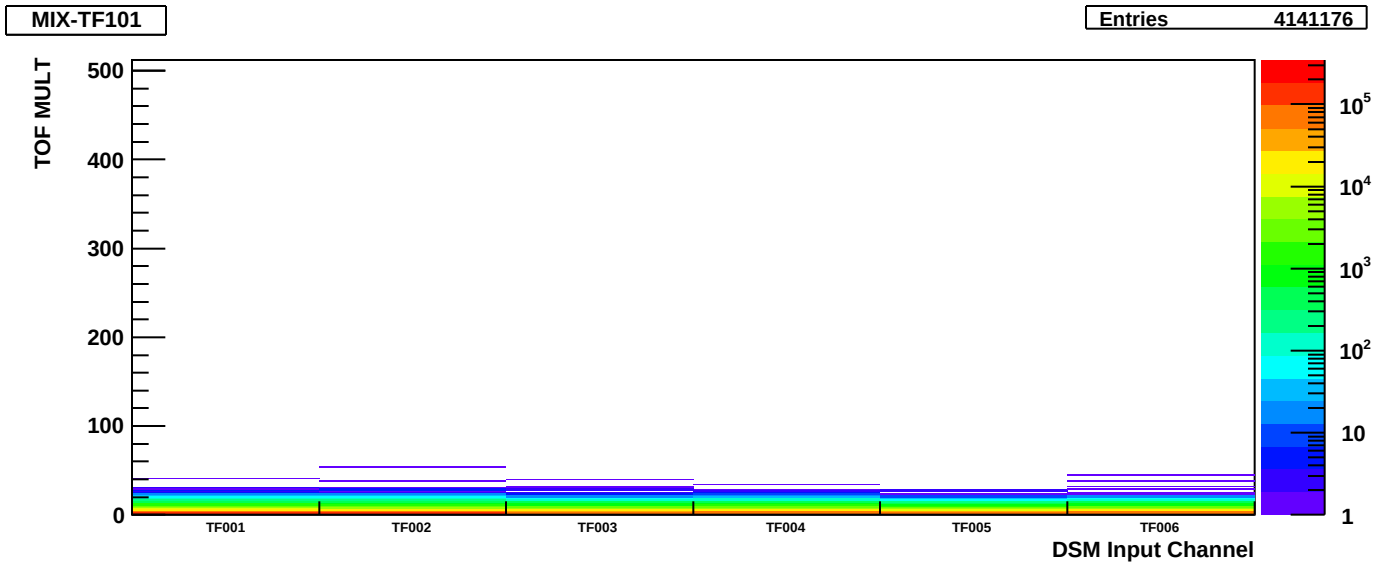

MIX-TF002
Entries 2.898823e+07
TOF MULT
TOF tray

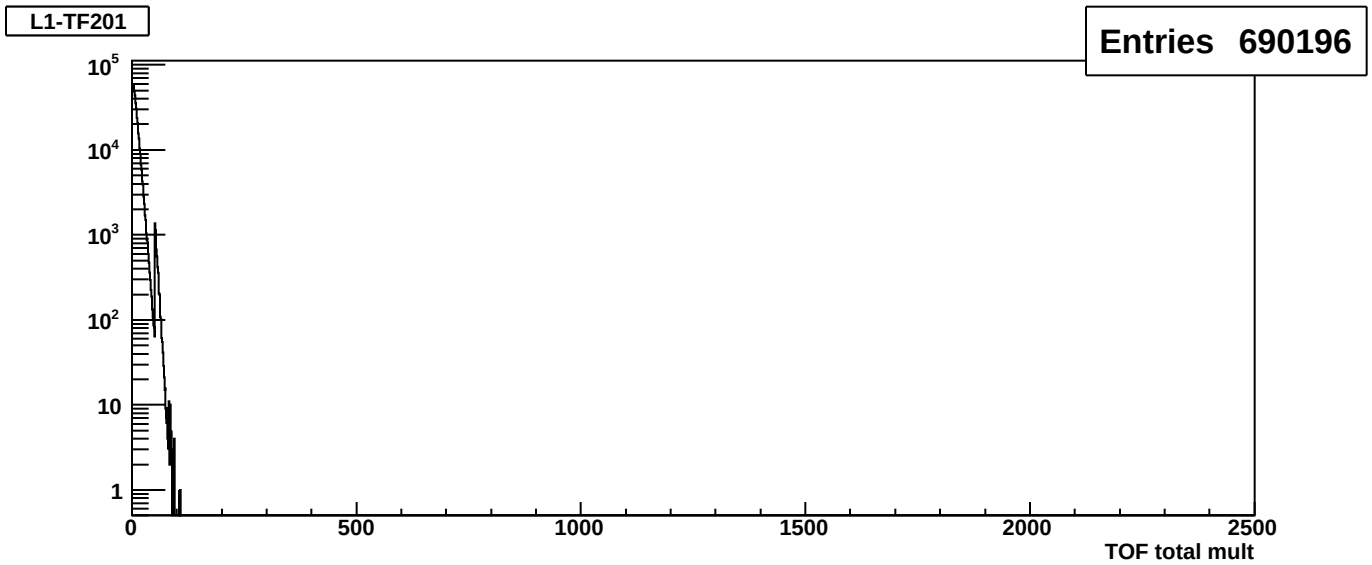

MIX-TF003
Entries 2.898823e+07
TOF MULT
TOF tray

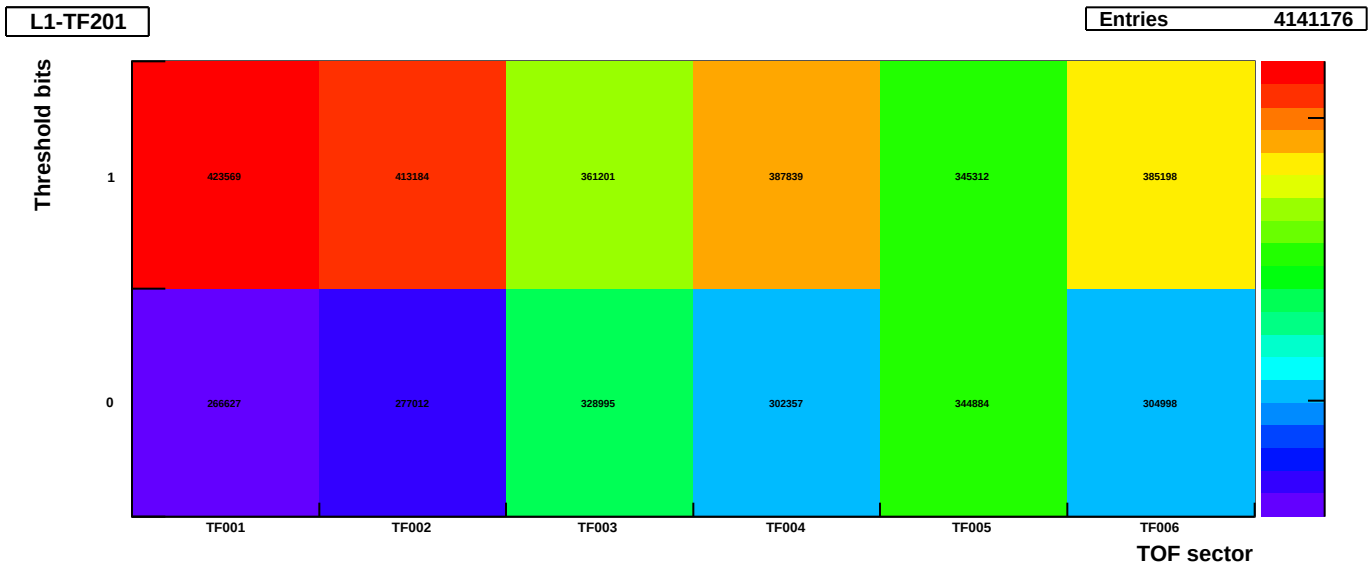

MIX-TF004
Entries 2.898823e+07
TOF MULT
TOF tray

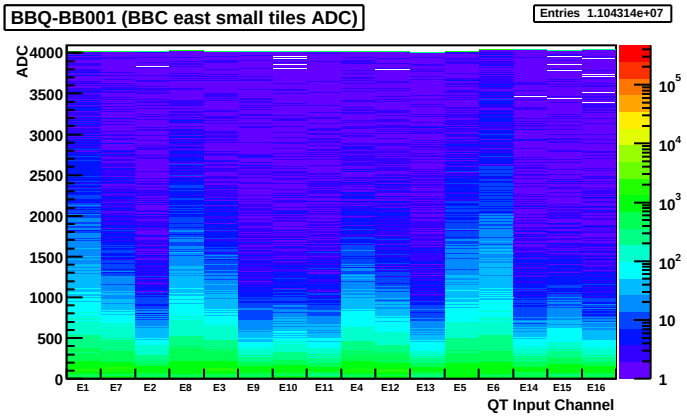

MIX-TF005
Entries 2.898823e+07
TOF MULT
TOF tray

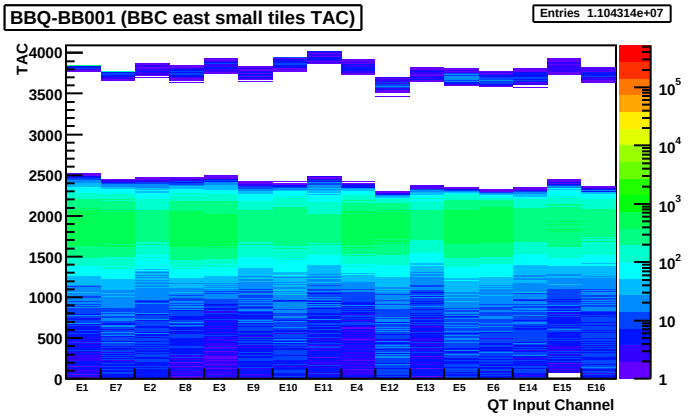

MIX-TF006
Entries 2.898823e+07
TOF MULT
TOF tray

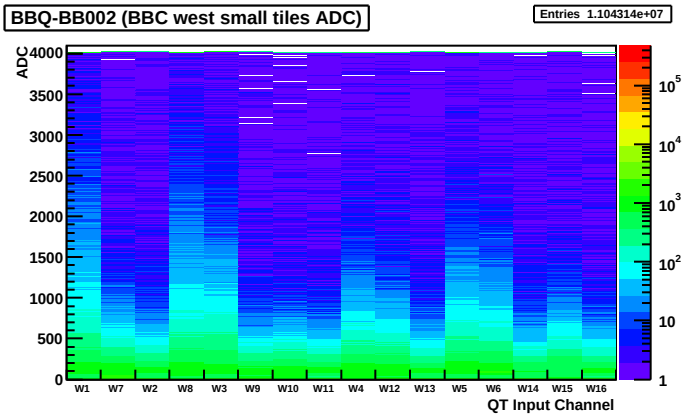

MIX-TF101
Entries 4141176
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

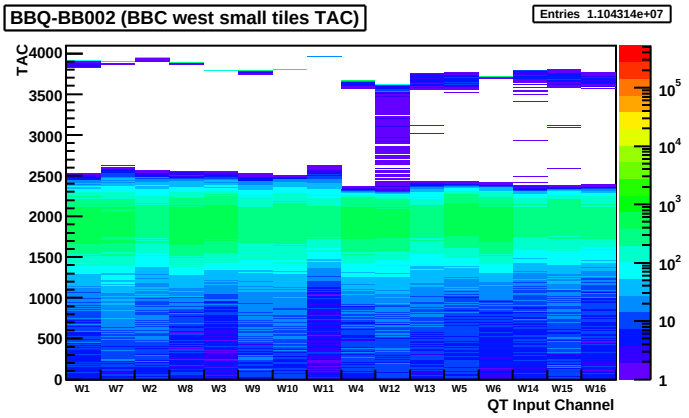

L1-TF201
Entries 690196
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

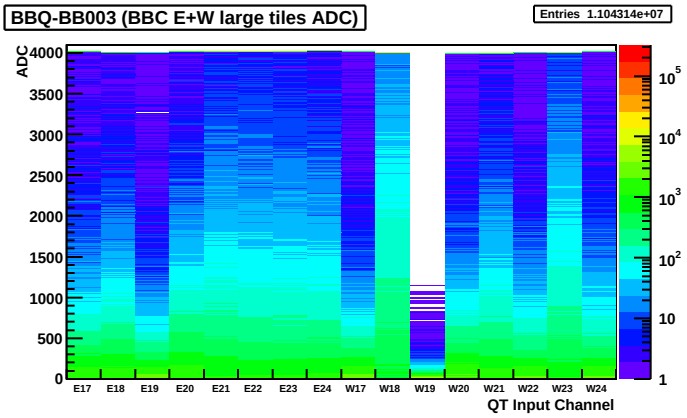

L1-TF201
Entries 4141176
Threshold bits
1
0
423569
413184
361201
387839
345312
385198
266627
277012
328995
302357
344884
304998
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

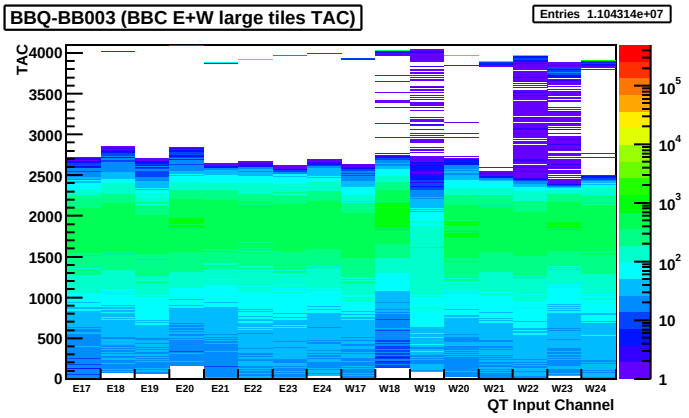

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.104314e+07
ADC
QT Input Channel

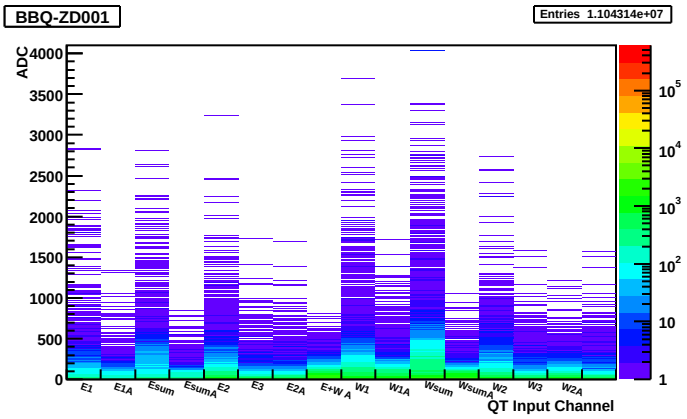

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.104314e+07
TAC
QT Input Channel

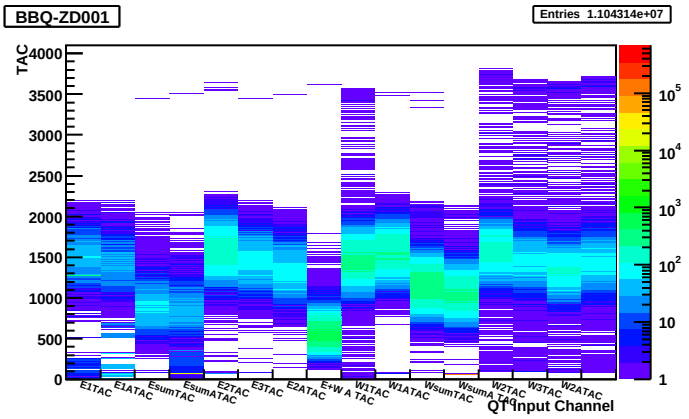

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.104314e+07
ADC
QT Input Channel

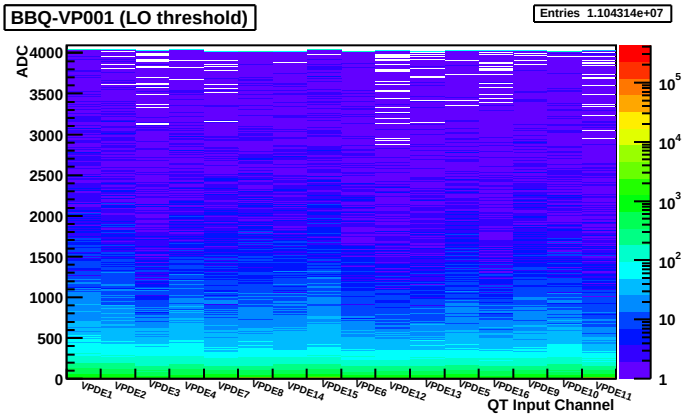

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.104314e+07
TAC
QT Input Channel

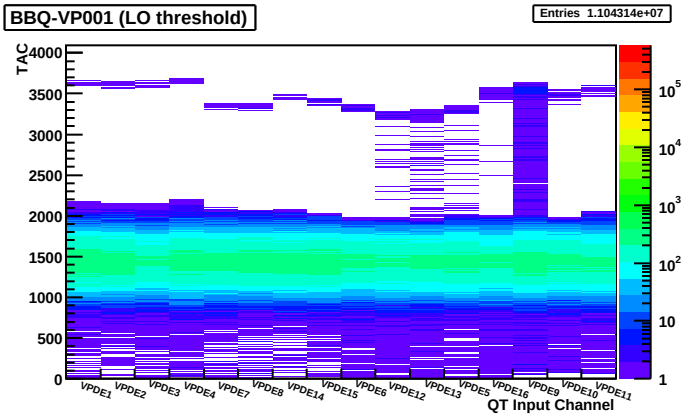

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.104314e+07
ADC
QT Input Channel

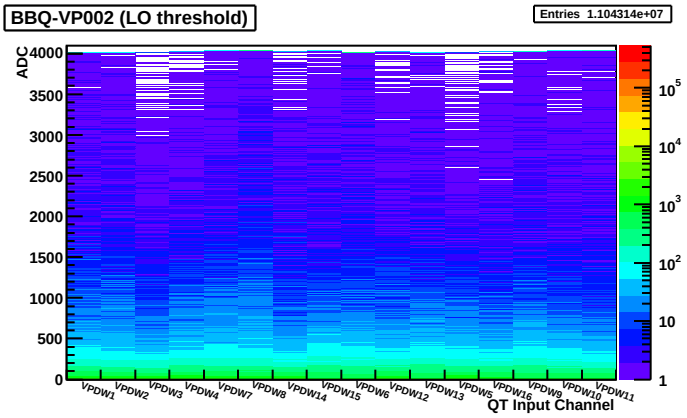

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.104314e+07
TAC
QT Input Channel

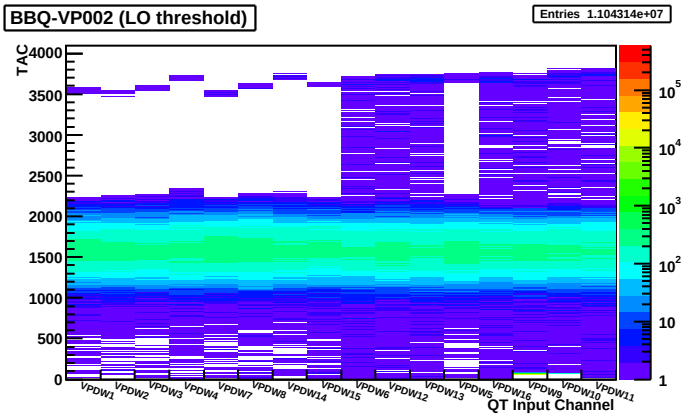

BBQ-ZD001
Entries 1.104314e+07
ADC
QT Input Channel

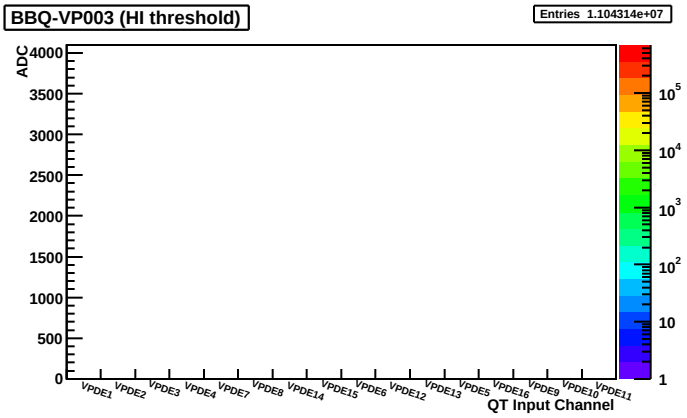

BBQ-ZD001
Entries 1.104314e+07
TAC
QT Input Channel

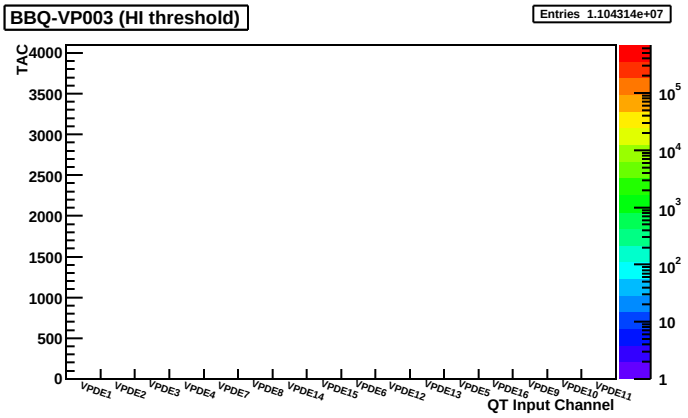

BBQ-VP001 (LO threshold)
Entries 1.104314e+07
ADC
QT Input Channel

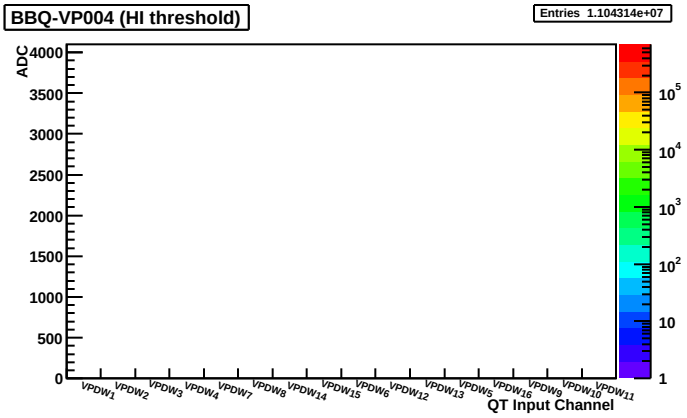

BBQ-VP001 (LO threshold)
Entries 1.104314e+07
TAC
QT Input Channel

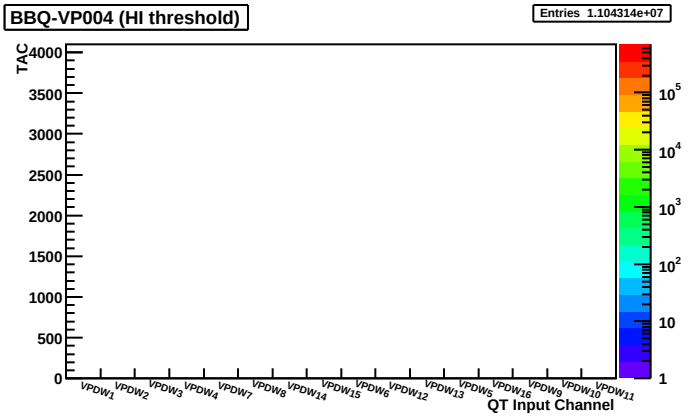

BBQ-VP002 (LO threshold)
Entries 1.104314e+07
ADC
QT Input Channel

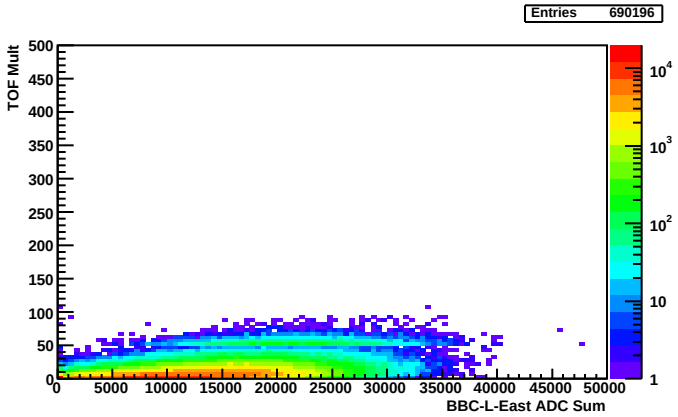

BBQ-VP002 (LO threshold)
Entries 1.104314e+07
TAC
QT Input Channel

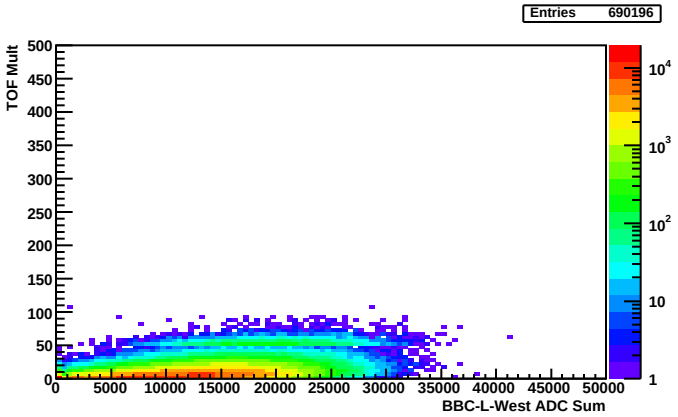

BBQ-VP003 (HI threshold)
Entries 1.104314e+07
ADC
QT Input Channel

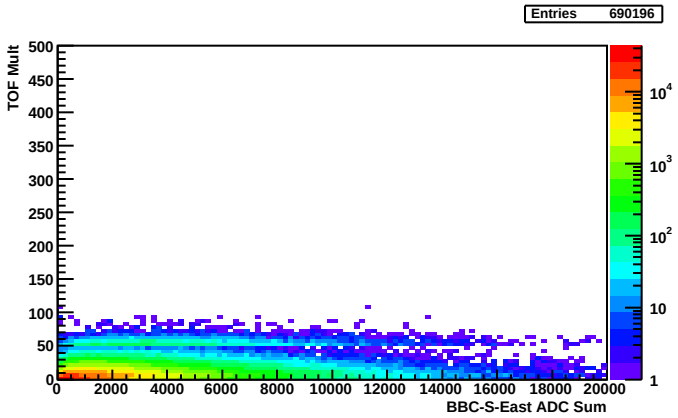

BBQ-VP003 (HI threshold)
Entries 1.104314e+07
TAC
QT Input Channel

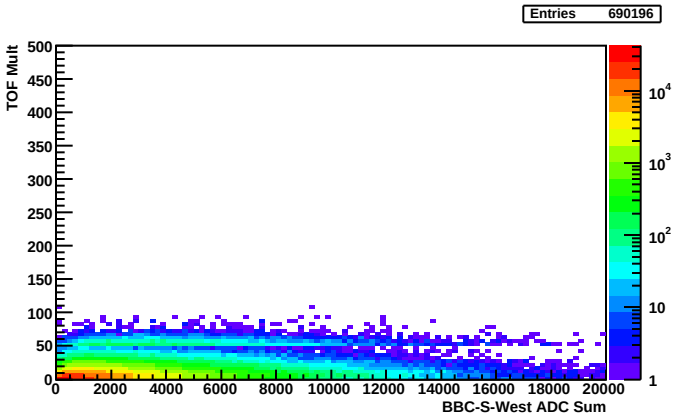

BBQ-VP004 (HI threshold)
Entries 1.104314e+07
ADC
QT Input Channel

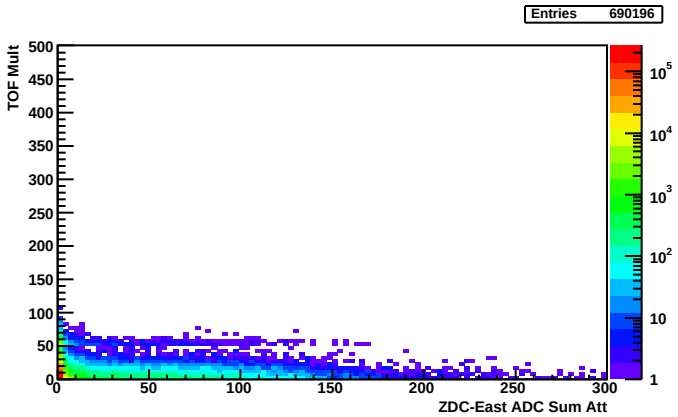

BBQ-VP004 (HI threshold)
Entries 1.104314e+07
TAC
QT Input Channel

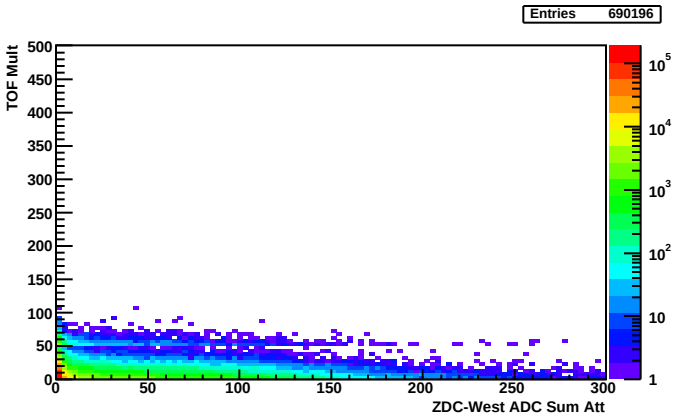

Entries 690196
TOF Mult
BBC-L-East ADC Sum

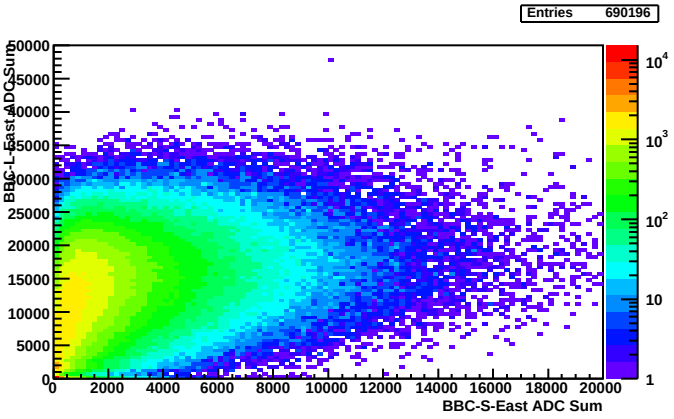

Entries 690196
TOF Mult
BBC-L-West ADC Sum

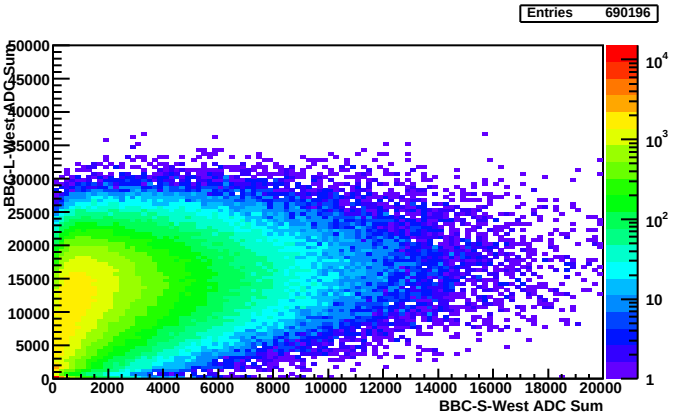

Entries 690196
TOF Mult
BBC-S-East ADC Sum

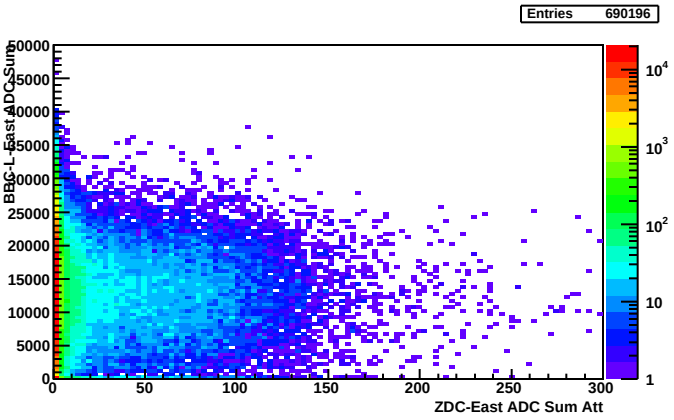

Entries 690196
TOF Mult
BBC-S-West ADC Sum

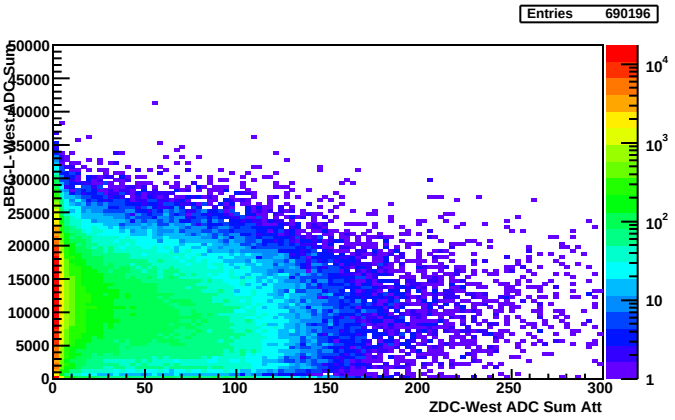

Entries 690196
TOF Mult
ZDC-East ADC Sum Att

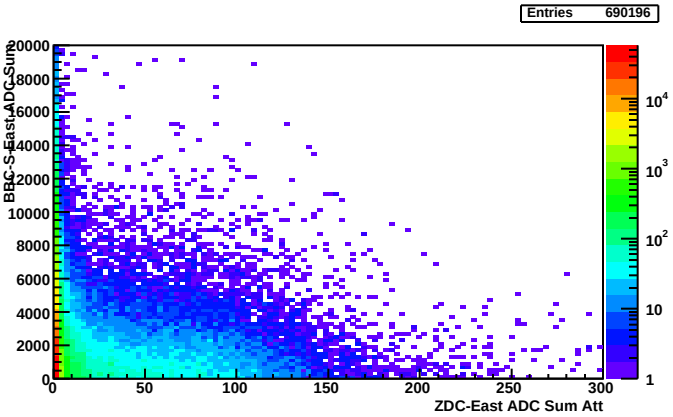

Entries 690196
TOF Mult
ZDC-West ADC Sum Att

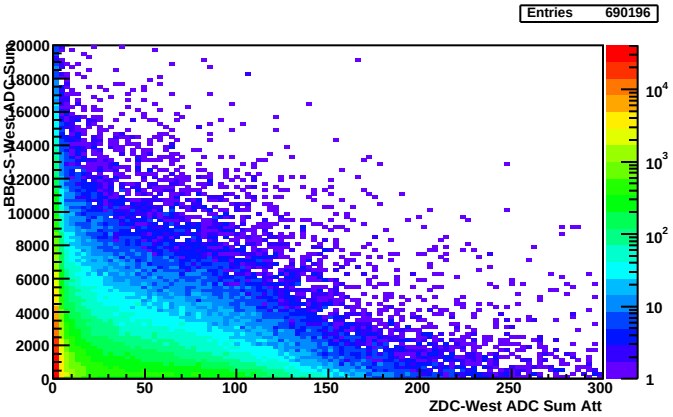

Entries 690196
BBC-L-East ADC Sum
BBC-S-East ADC Sum

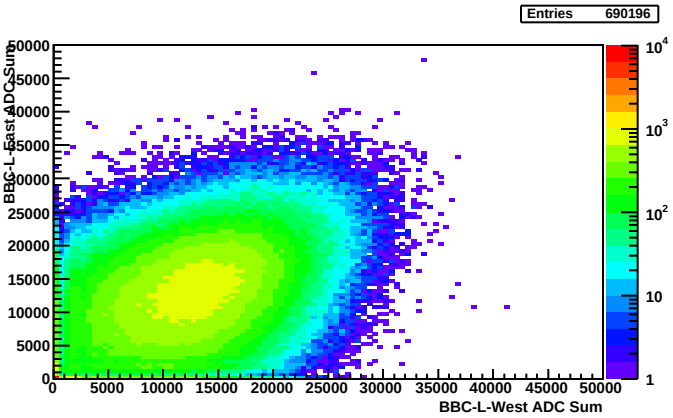

Entries 690196
BBC-L-West ADC Sum
BBC-S-West ADC Sum

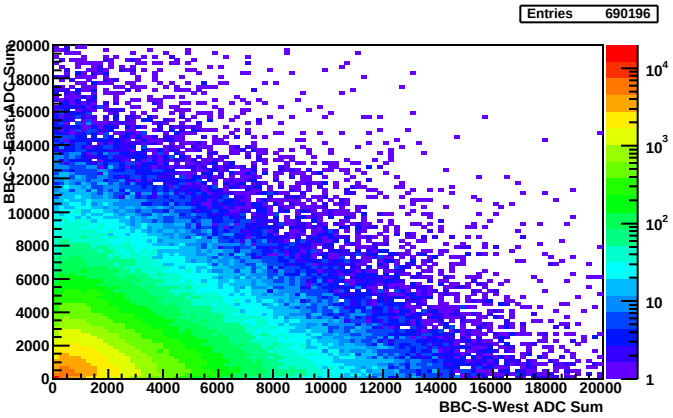

Entries 690196
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

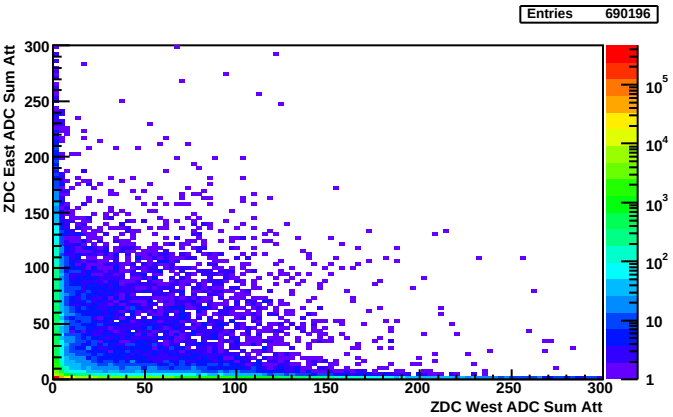

Entries 690196
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

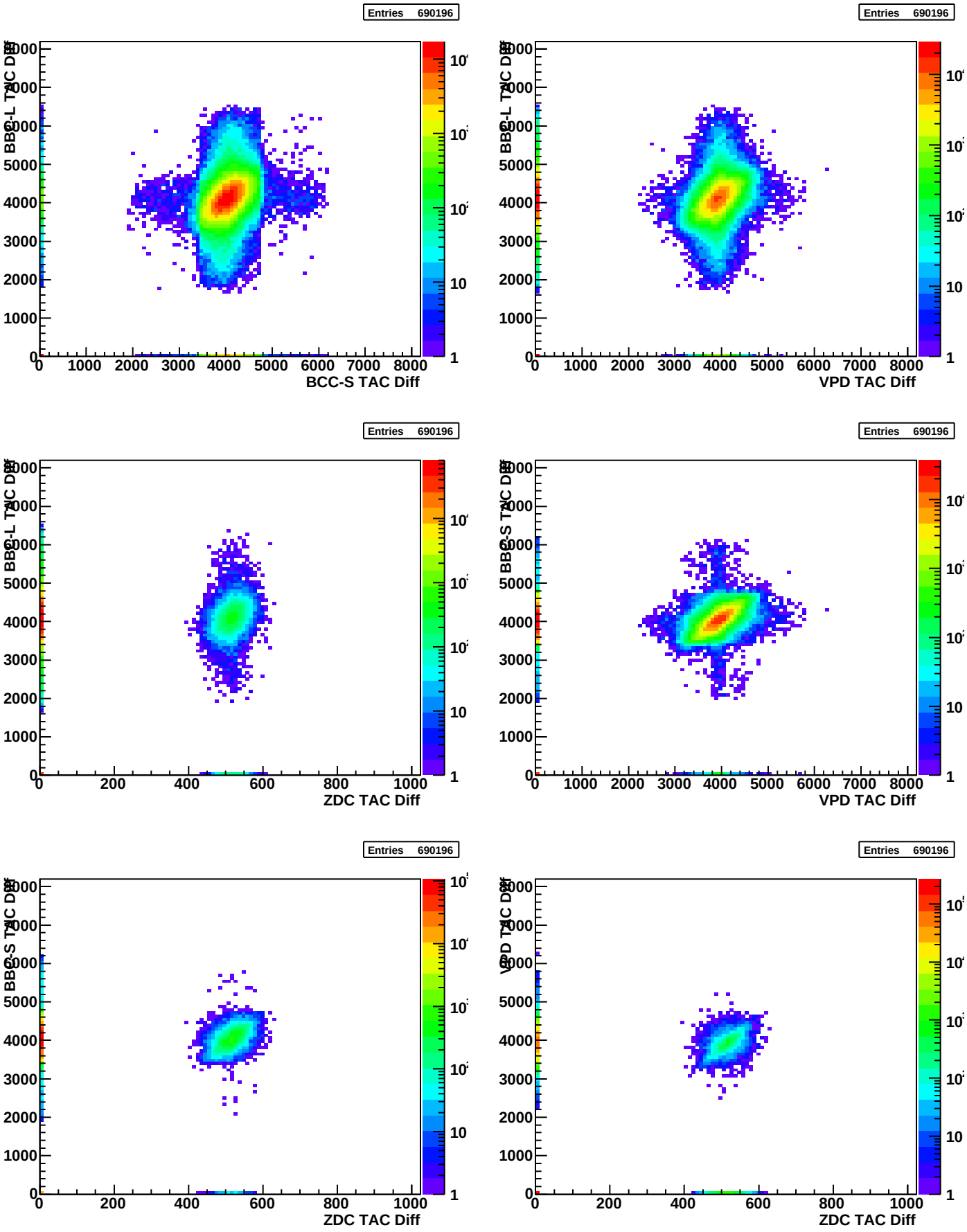

Entries 690196
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

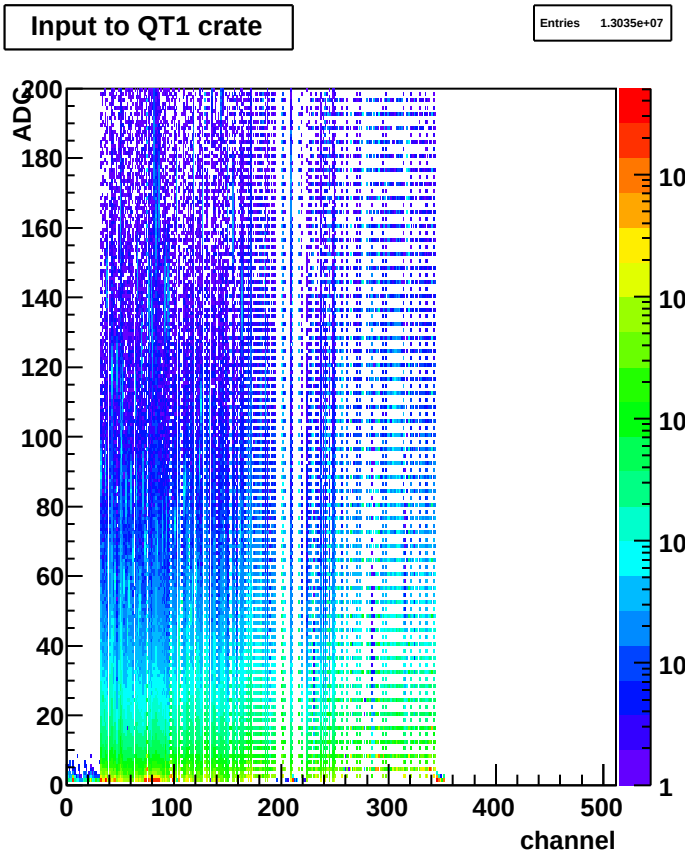

Entries 690196
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

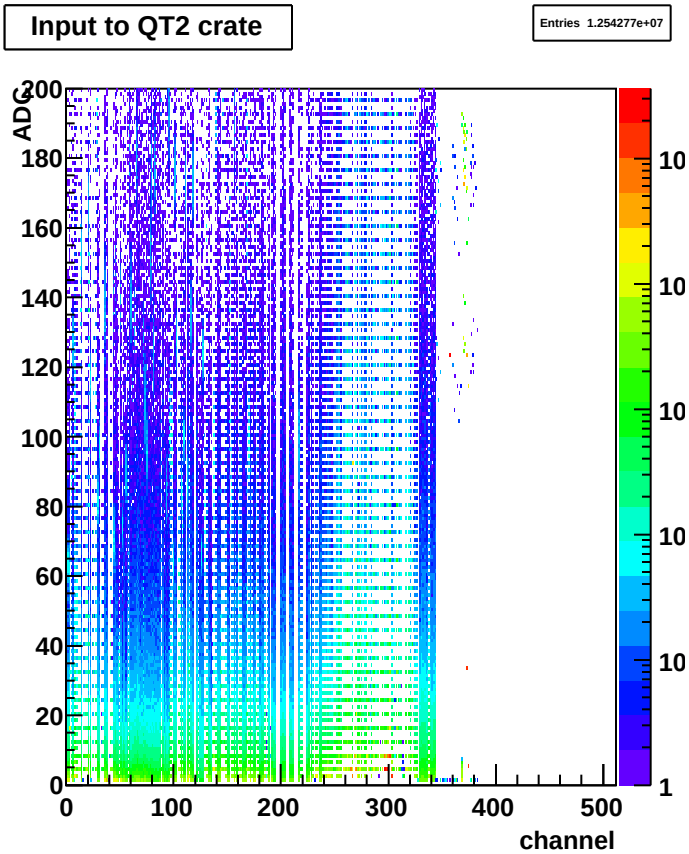

Entries 690196
BBC-L-East ADC Sum
BBC-L-West ADC Sum

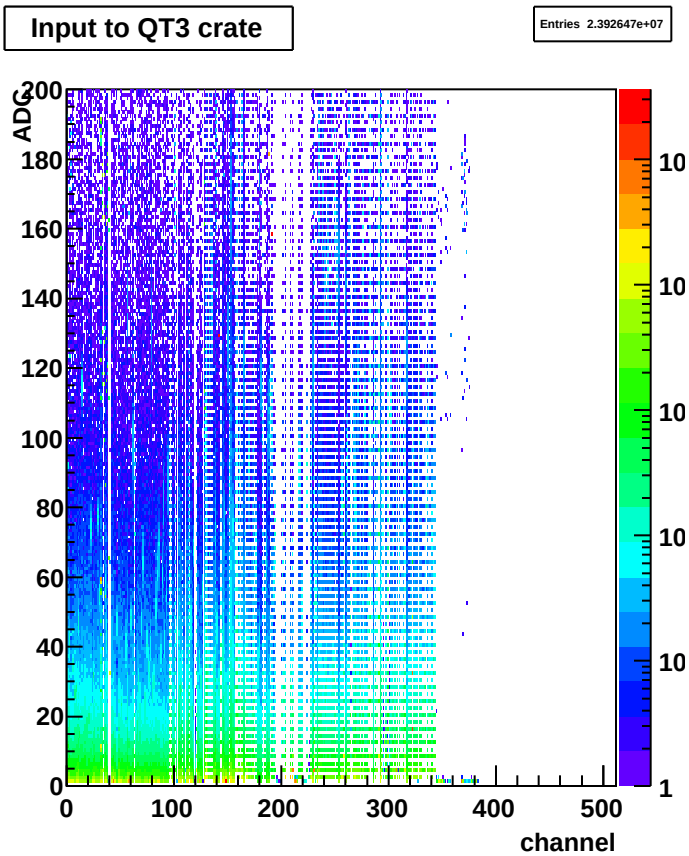

Entries 690196
BBC-S-East ADC Sum
BBC-S-West ADC Sum

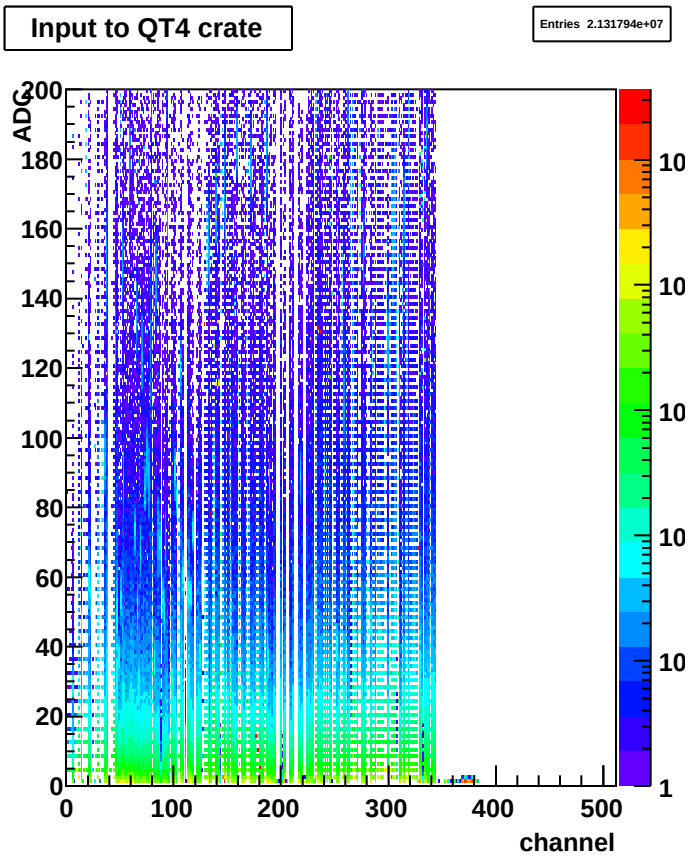

Entries 690196
ZDC East ADC Sum Att
ZDC West ADC Sum Att

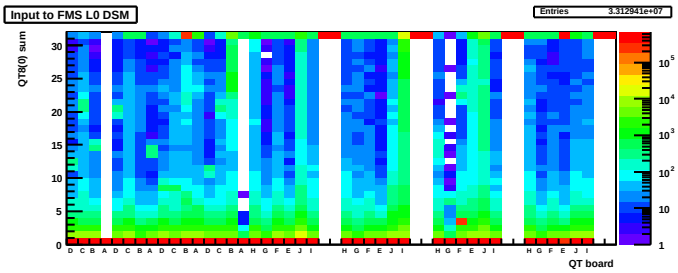

Entries 690196
BBC-L TAC Diff
BCC-S TAC Diff
Entries 690196
BBC-L TAC Diff
VPD TAC Diff
Entries 690196
BBC-L TAC Diff
ZDC TAC Diff
Entries 690196
BBC-S TAC Diff
VPD TAC Diff
Entries 690196
BBC-S TAC Diff
ZDC TAC Diff
Entries 690196
VPD TAC Diff
ZDC TAC Diff

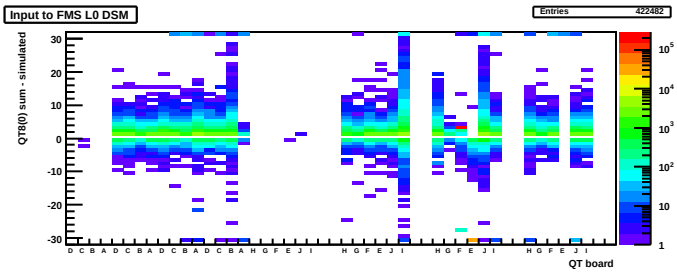

Input to QT1 crate
Entries 1.3035e+07
ADC
channel

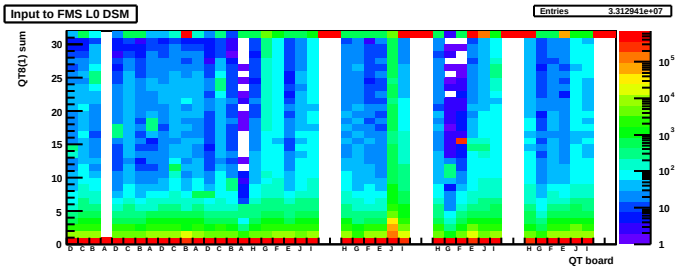

Input to QT2 crate
Entries 1.254277e+07
ADC
channel

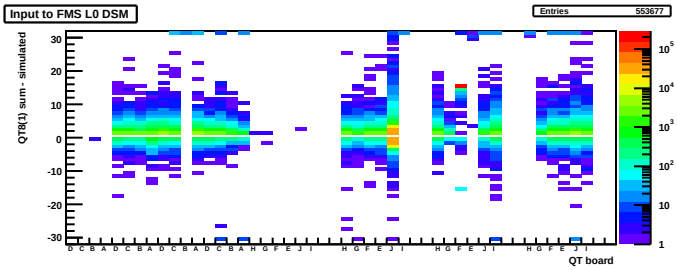

Input to QT3 crate
Entries 2.392647e+07
ADC
channel

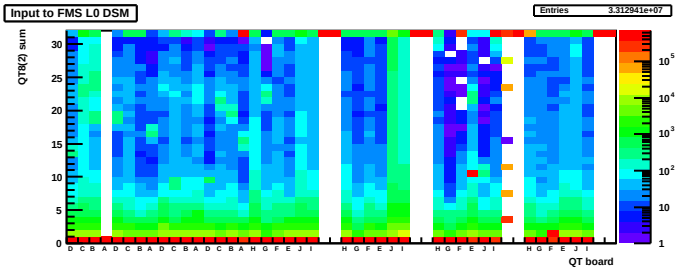

Input to QT4 crate
Entries 2.131794e+07
ADC
channel

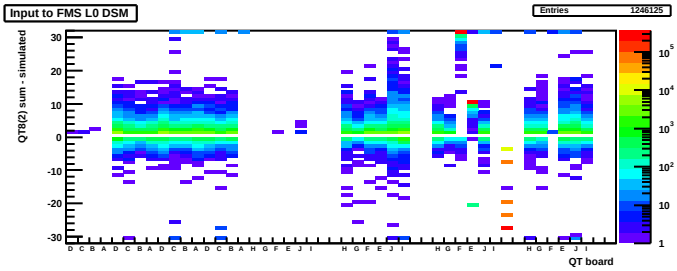

Input to FMS L0 DSM
Entries 3.312941e+07
QT8(0) sum
QT board

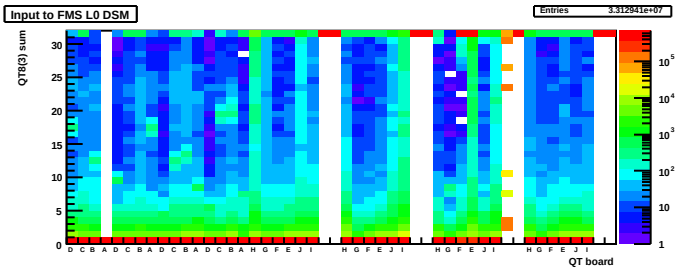

Input to FMS L0 DSM
Entries 422482
QT8(0) sum - simulated
QT board

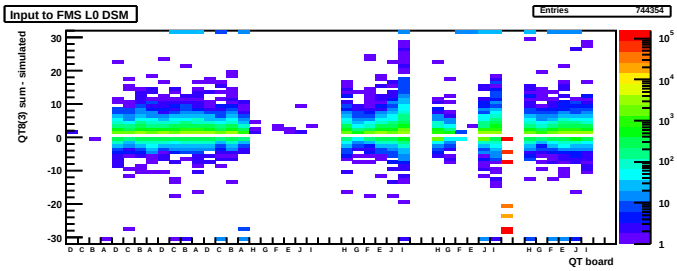

Input to FMS L0 DSM
Entries 3.312941e+07
QT8(1) sum
QT board

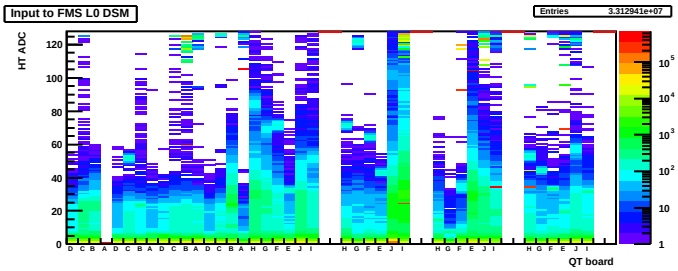

Input to FMS L0 DSM
Entries 553677
QT8(1) sum - simulated
QT board

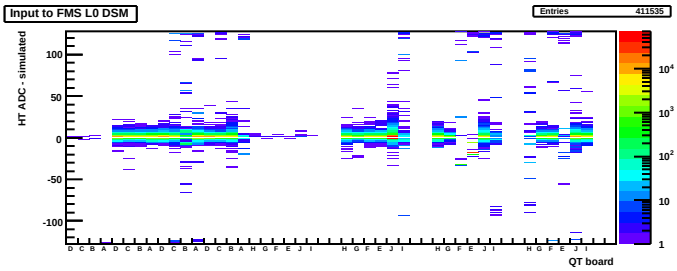

Input to FMS L0 DSM
Entries 3.312941e+07
QT8(2) sum
QT board

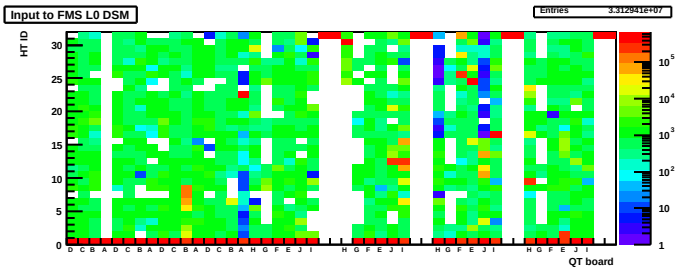

Input to FMS L0 DSM
Entries 1246125
QT8(2) sum - simulated
QT board

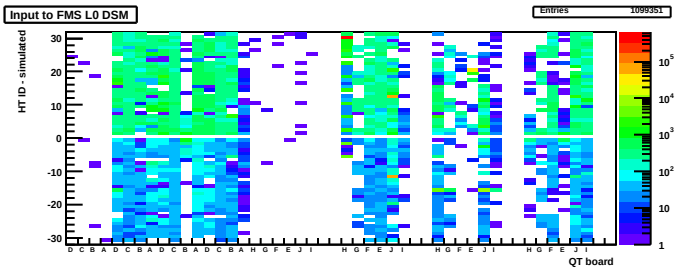

Input to FMS L0 DSM
Entries 3.312941e+07
QT8(3) sum
QT board

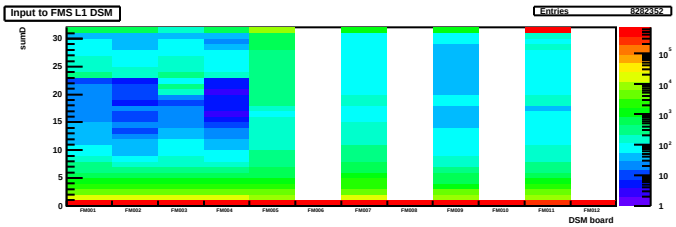

Input to FMS L0 DSM
Entries 744354
QT8(3) sum - simulated
QT board

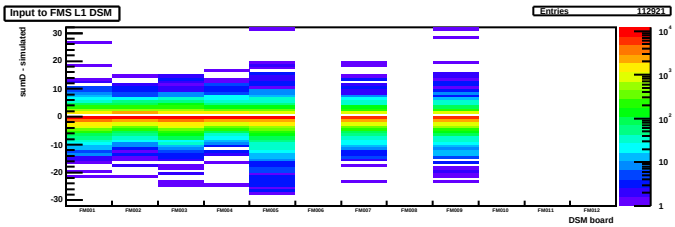

Input to FMS L0 DSM
Entries 3.312941e+07
HT ADC
QT board

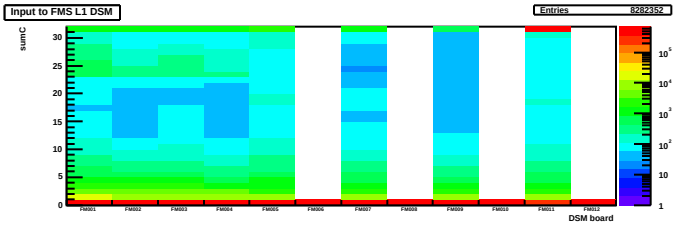

Input to FMS L0 DSM
Entries 411535
HT ADC - simulated
QT board

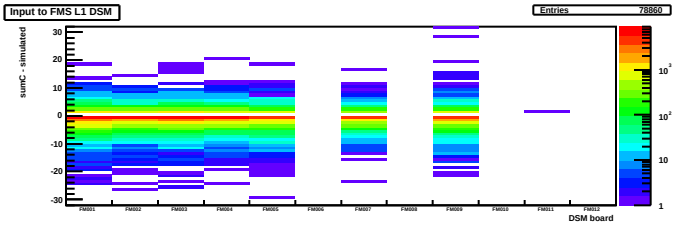

Input to FMS L0 DSM
Entries 3.312941e+07
HT ID
QT board

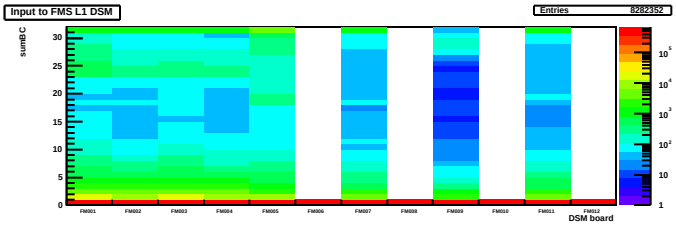

Input to FMS L0 DSM
Entries 1099351
HT ID - simulated
QT board

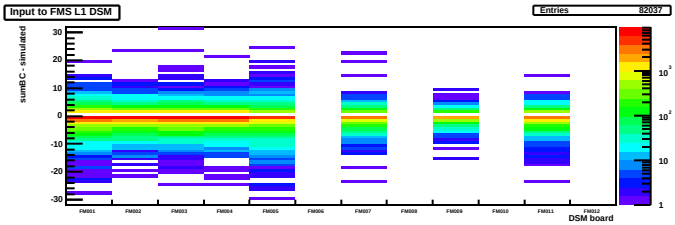

Input to FMS L1 DSM
Entries 8282352
sumD
DSM board

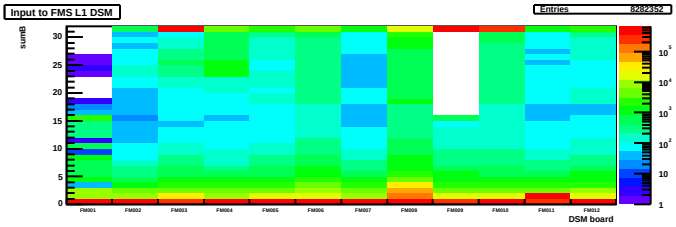

Input to FMS L1 DSM
Entries 112921
sumD - simulated
DSM board

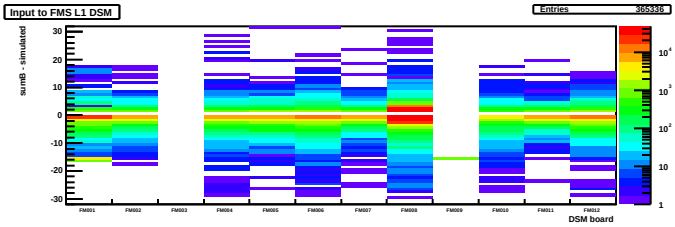

Input to FMS L1 DSM
Entries 8282352
sumC
DSM board

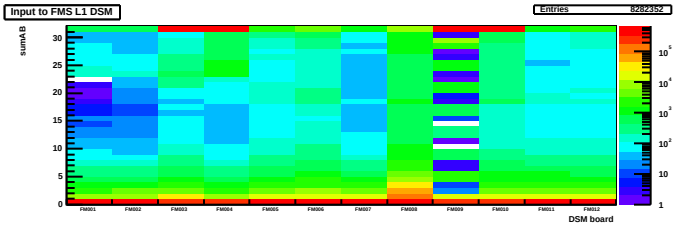

Input to FMS L1 DSM
Entries 78860
sumC - simulated
DSM board

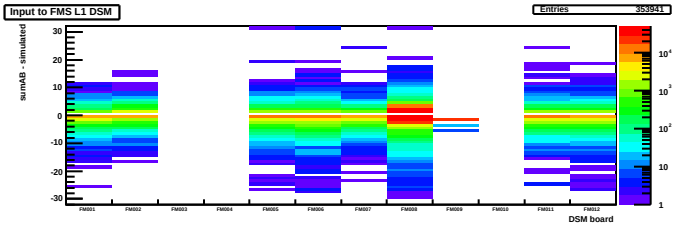

Input to FMS L1 DSM
Entries 8282352
sumBC
DSM board

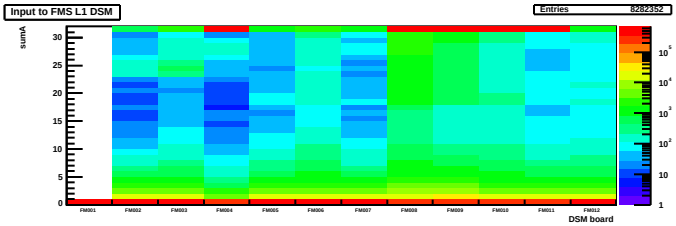

Input to FMS L1 DSM
Entries 82037
sumBC - simulated
DSM board

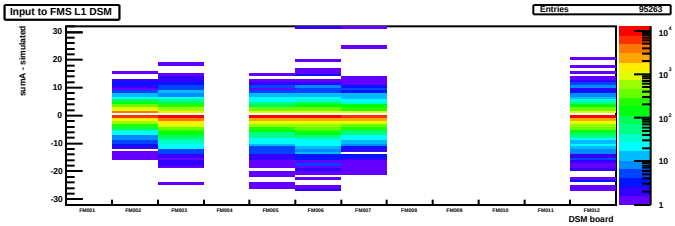

Input to FMS L1 DSM
Entries 8282352
sumB
DSM board

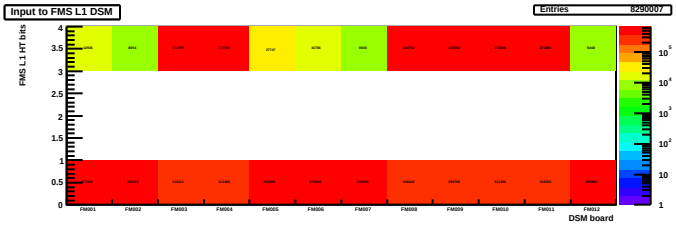

Input to FMS L1 DSM
Entries 365336
sumB - simulated
DSM board

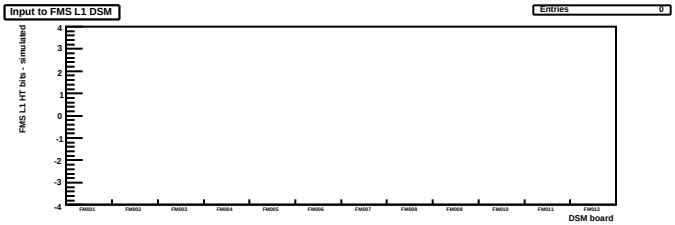

Input to FMS L1 DSM
Entries 8282352
sumAB
DSM board

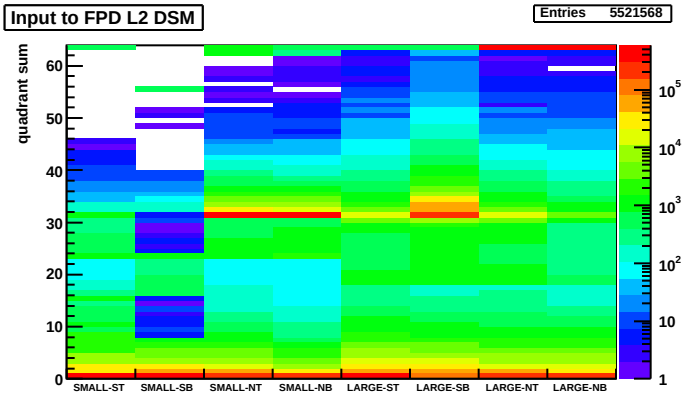

Input to FMS L1 DSM
Entries 353941
sumAB - simulated
DSM board

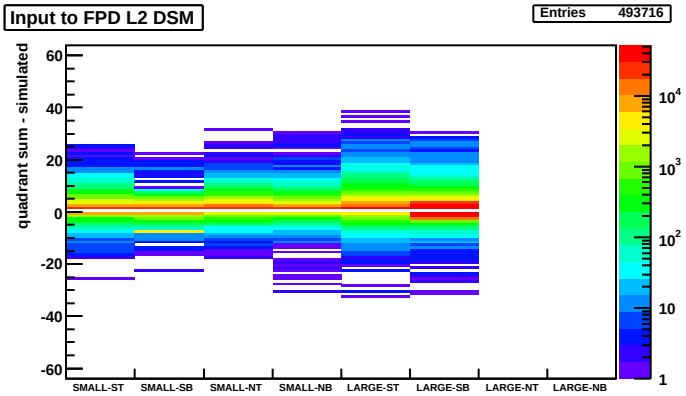

Input to FMS L1 DSM
Entries 8282352
sumA
DSM board

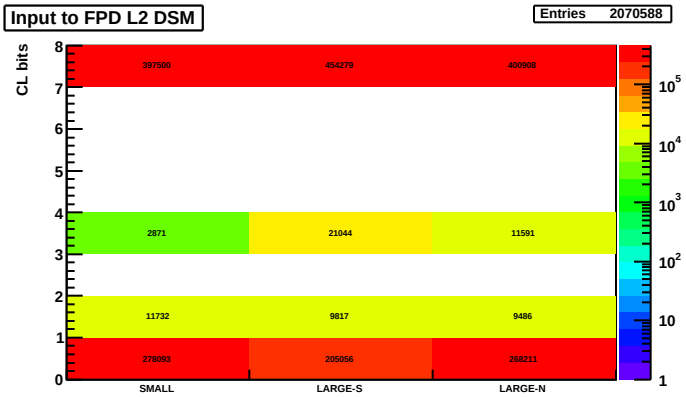

Input to FMS L1 DSM
Entries 95263
sumA - simulated
DSM board

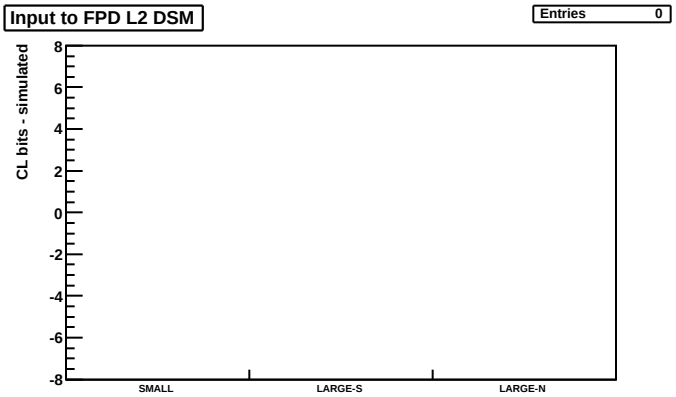

Input to FMS L1 DSM
Entries 8290007
FMS L1 HT bits
DSM board

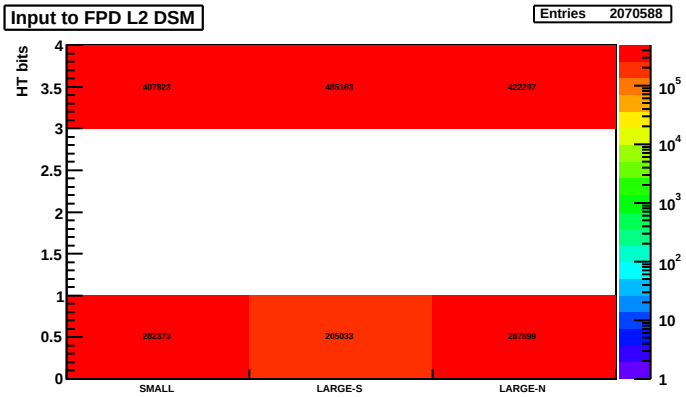

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

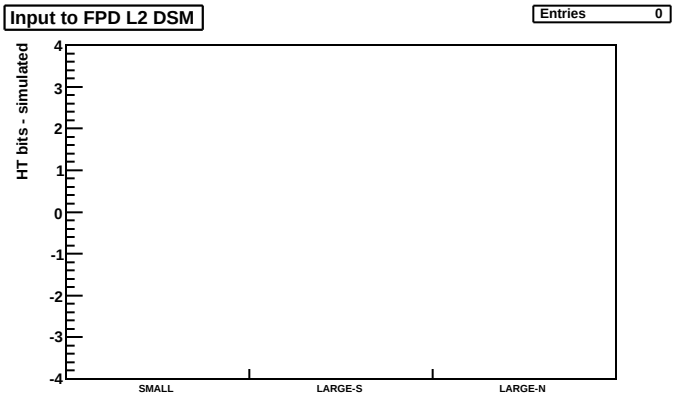

Input to FPD L2 DSM
Entries 5521568
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

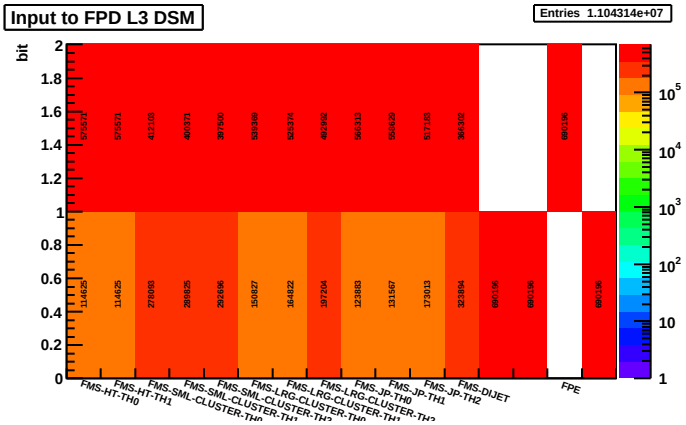

Input to FPD L2 DSM
Entries 493716
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

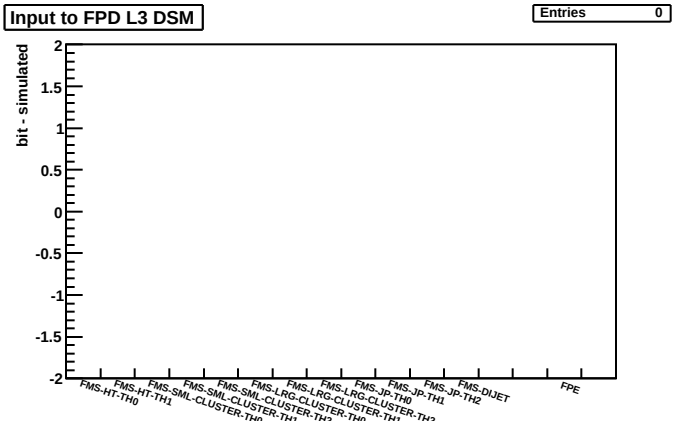

Input to FPD L2 DSM
Entries 2070588
CL bits
397500
454279
400908
2871
21044
11591
11732
9817
9486
278093
205056
268211
SMALL
LARGE-S
LARGE-N

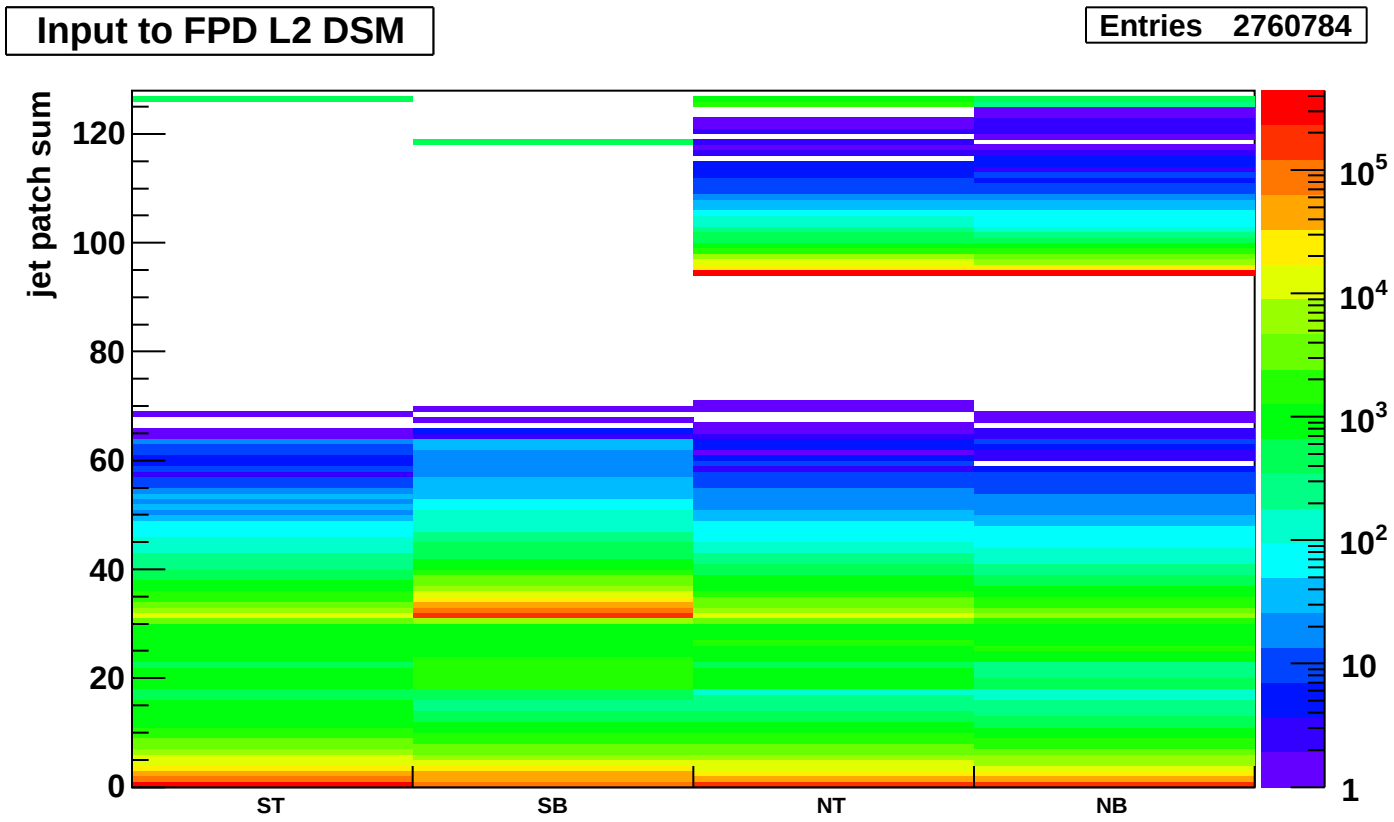

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

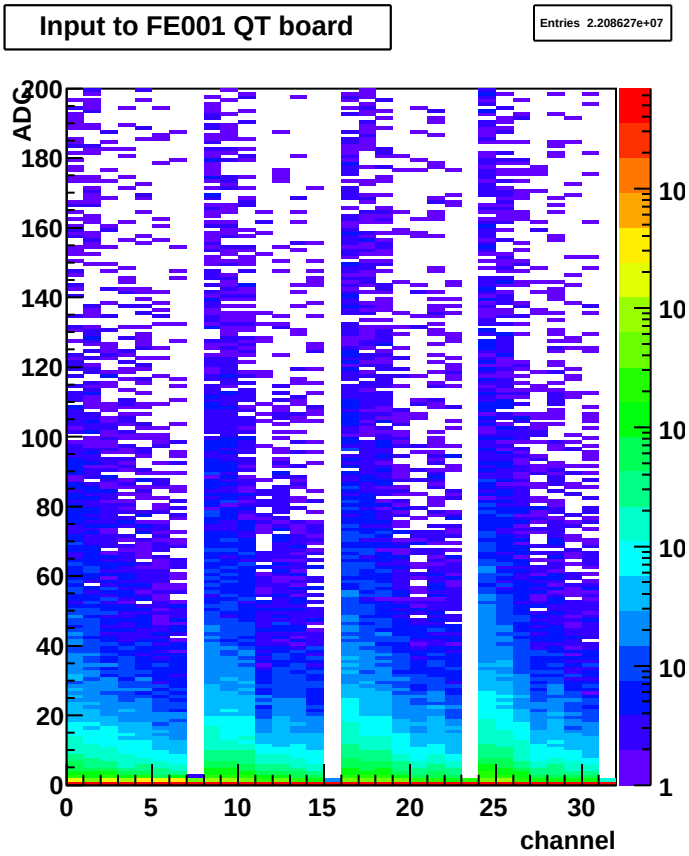

Input to FPD L2 DSM
Entries 2070588
HT bits
407823
485163
422297
282373
205033
267899
SMALL
LARGE-S
LARGE-N

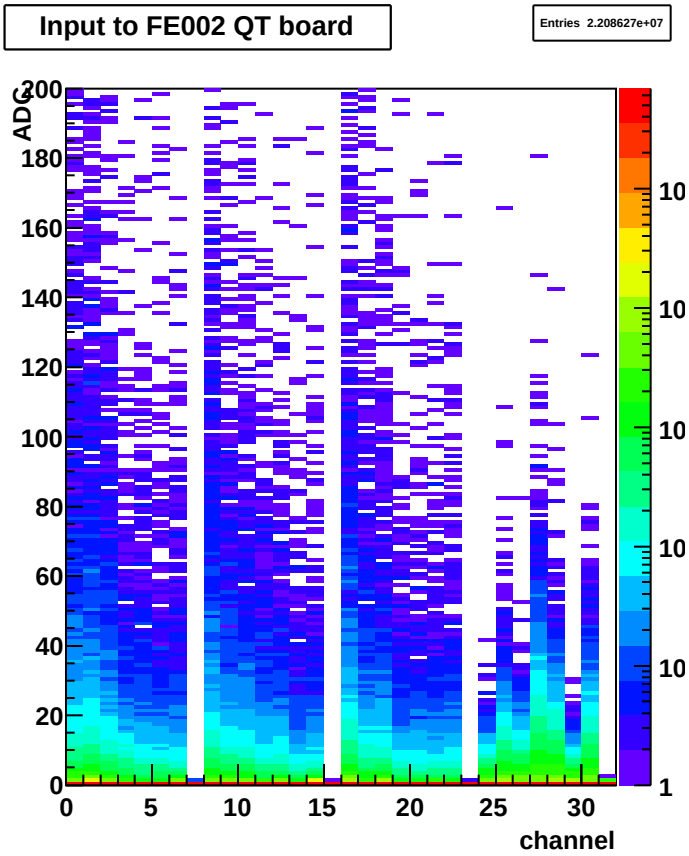

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

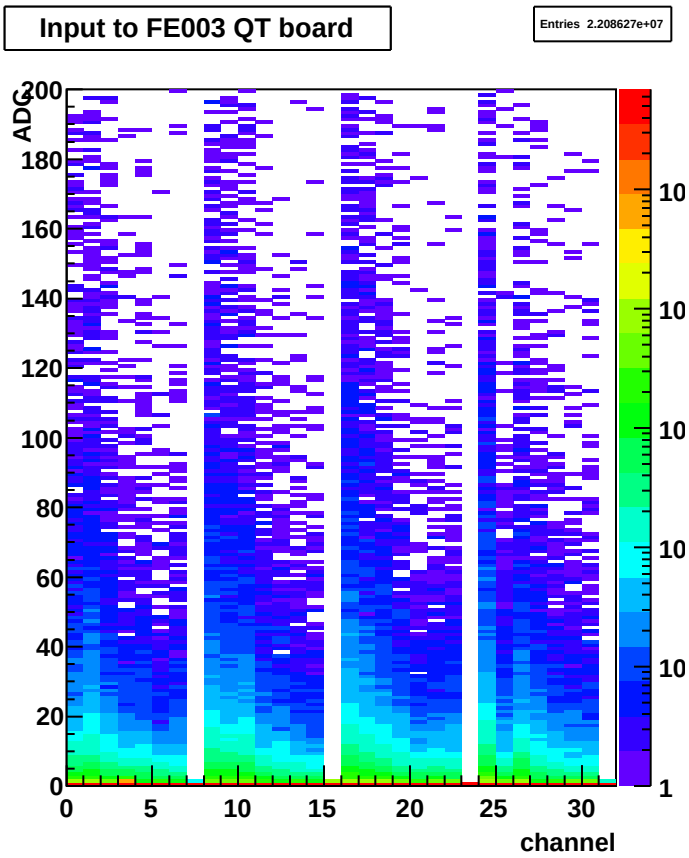

Input to FPD L3 DSM
Entries 1.104314e+07
bit
575572
575571
412103
400371
397500
539369
525374
492982
566313
558629
517183
366302
690196
114625
114625
278093
289825
292596
150827
164822
197204
123883
131567
173013
223894
690196
690196
690196
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

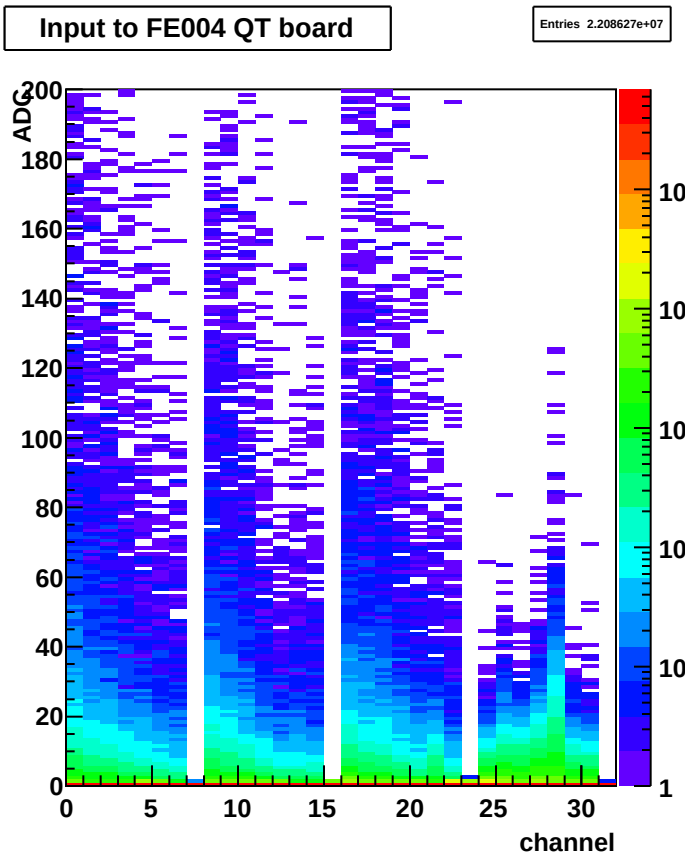

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

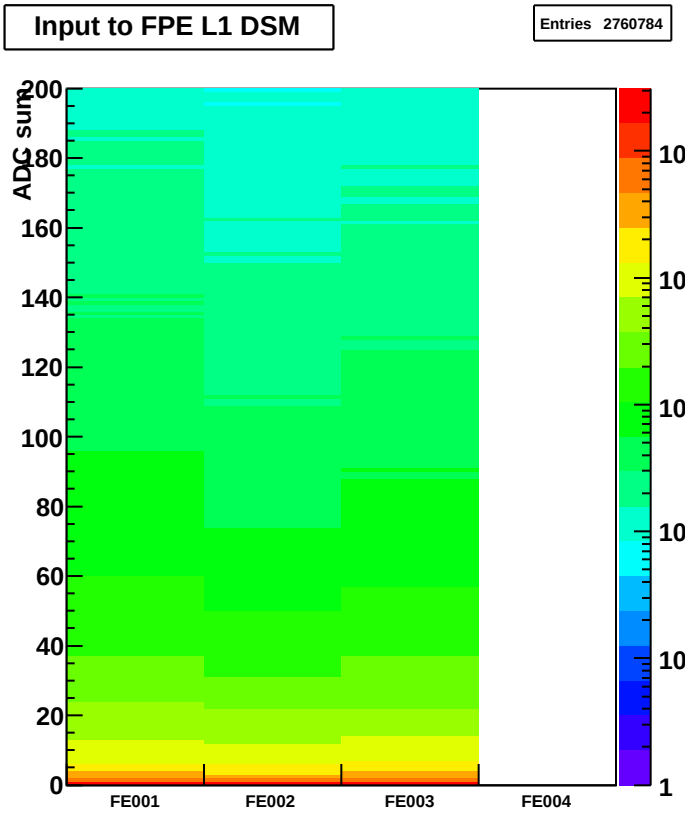

Input to FPD L2 DSM
Entries 2760784
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

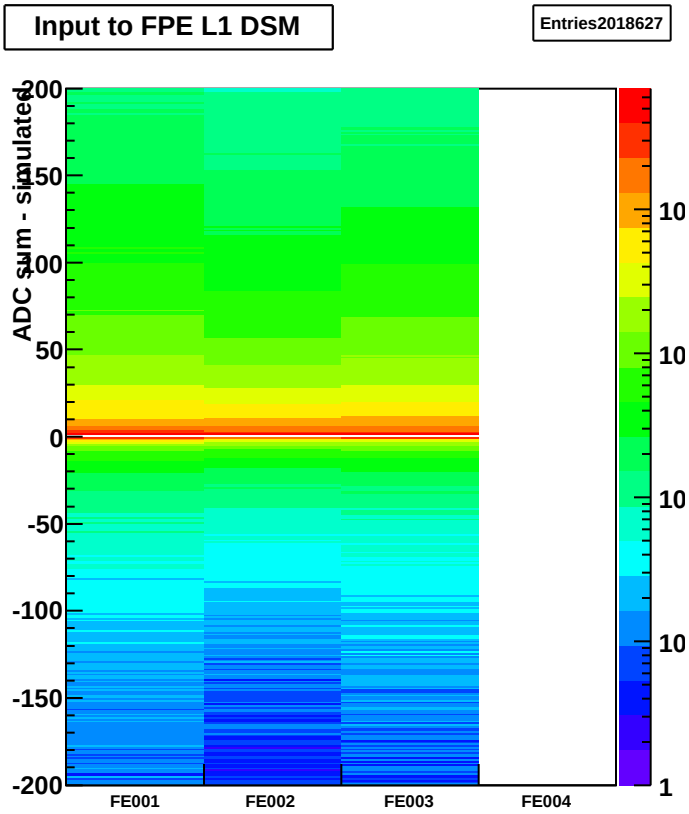

Input to FE001 QT board
Entries 2.208627e+07
ADC
channel

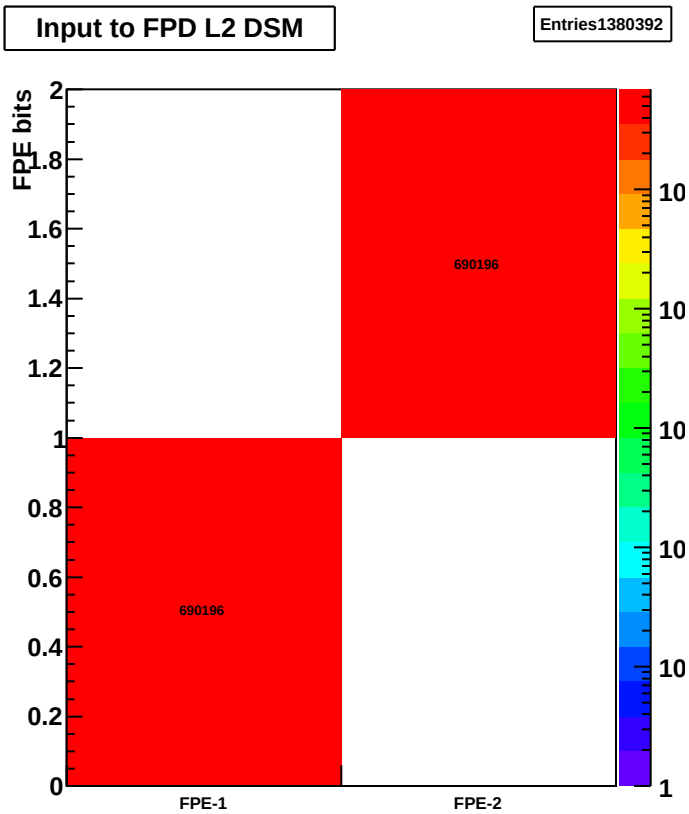

Input to FE002 QT board
Entries 2.208627e+07
ADC
channel

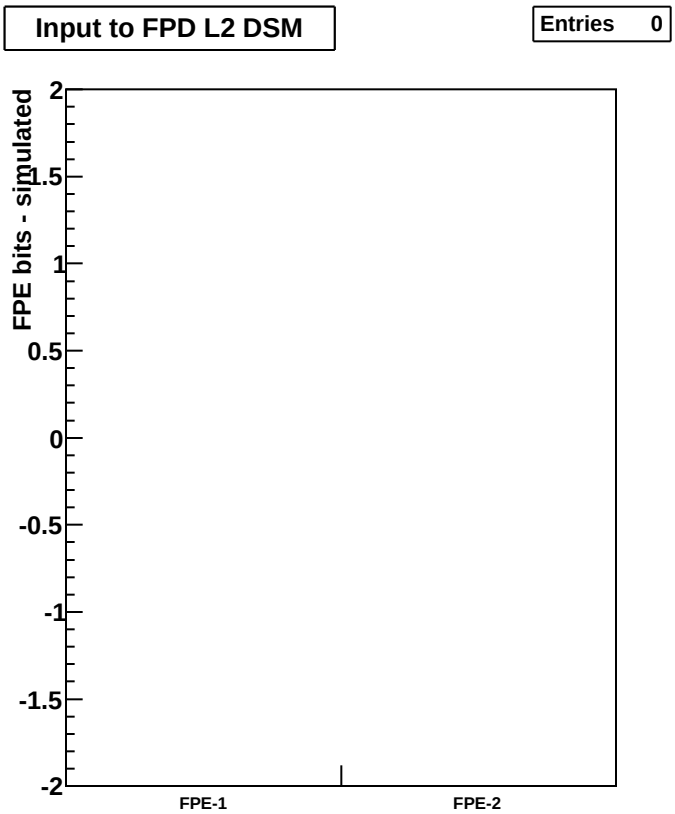

Input to FE003 QT board
Entries 2.208627e+07
ADC
channel


Input to FE004 QT board
Entries 2.208627e+07
ADC
channel


Input to FPE L1 DSM
Entries 2760784
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries2018627
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1380392
FPE bits
690196
690196
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2