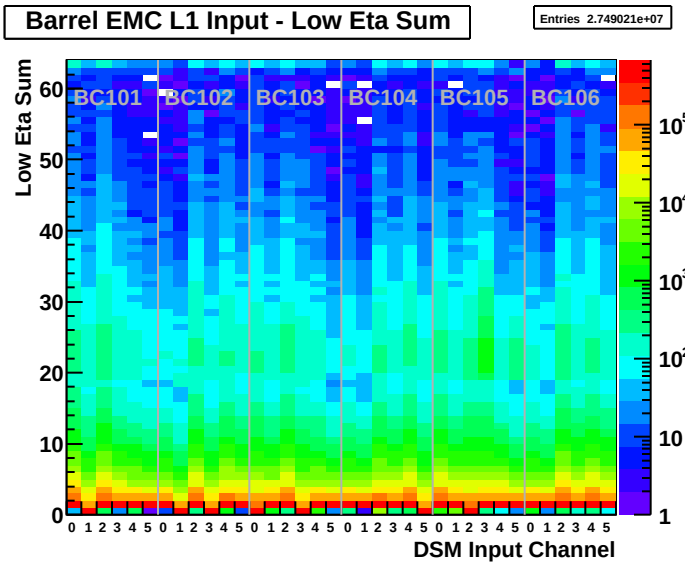
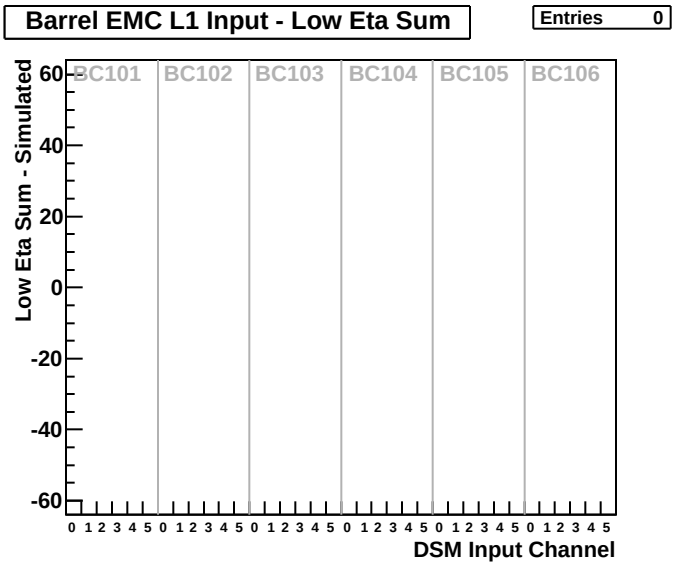
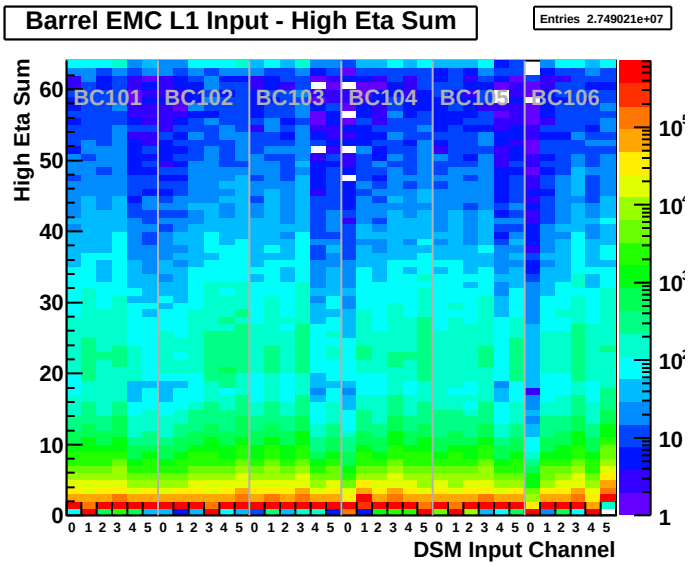
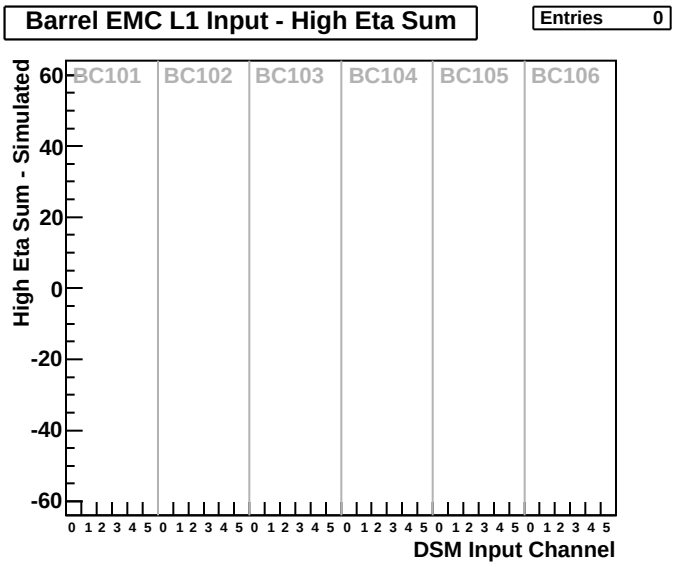
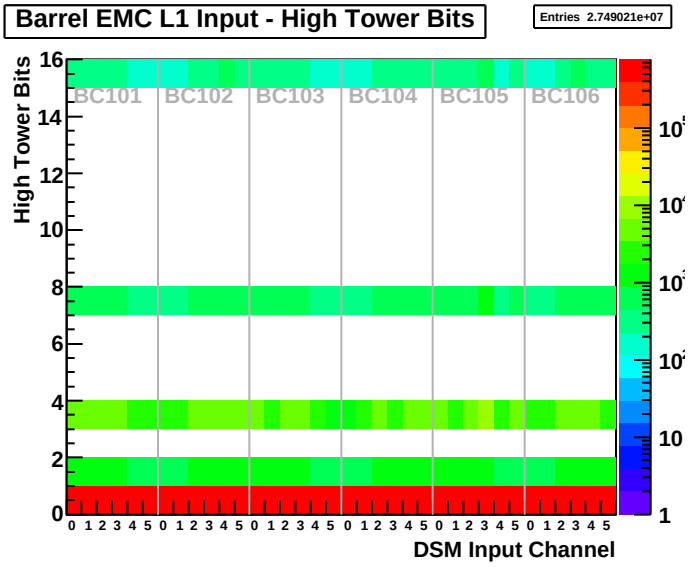
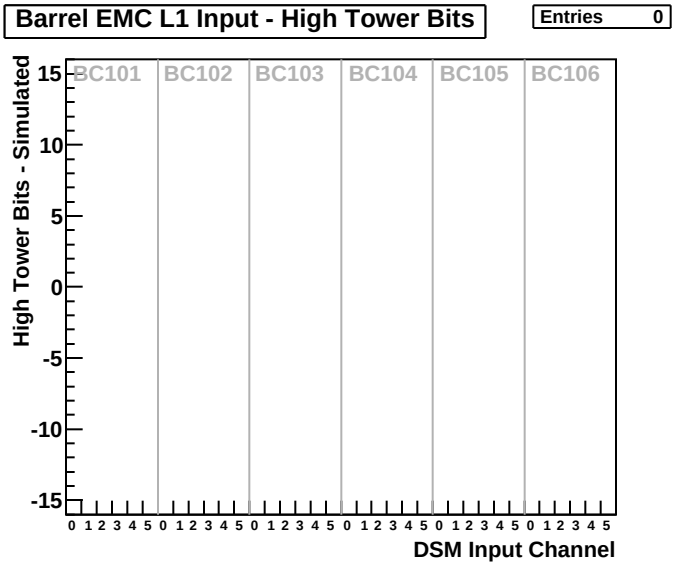

Barrel EMC L1 Input - Low Eta Sum
Entries 2.749021e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

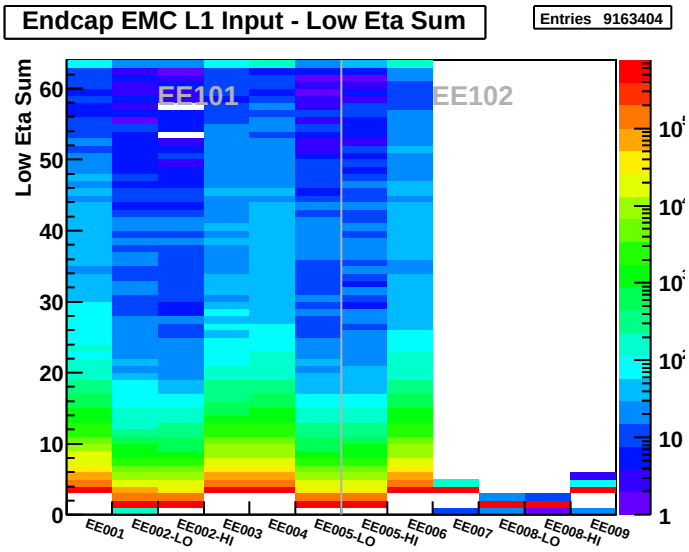

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

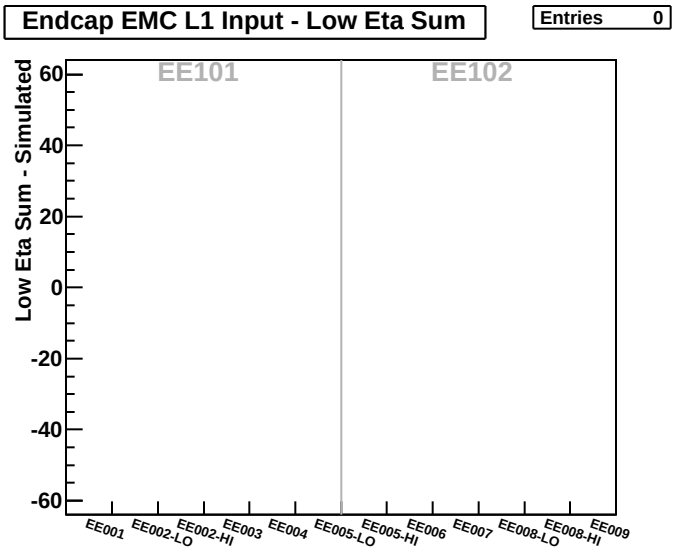

Barrel EMC L1 Input - High Eta Sum
Entries 2.749021e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

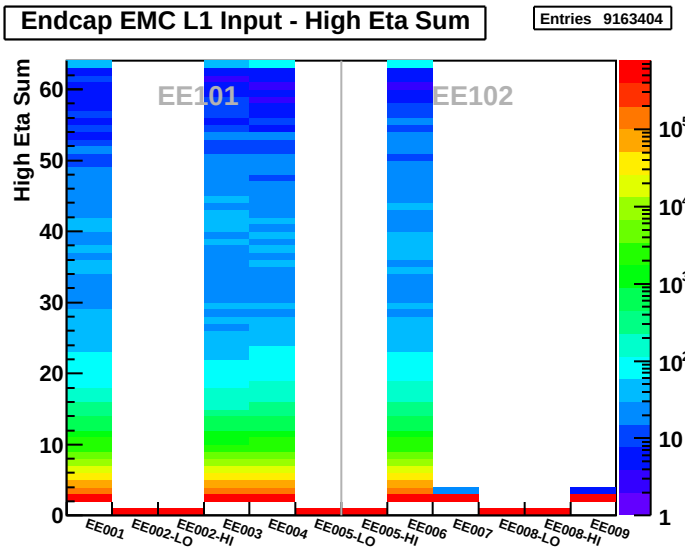

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

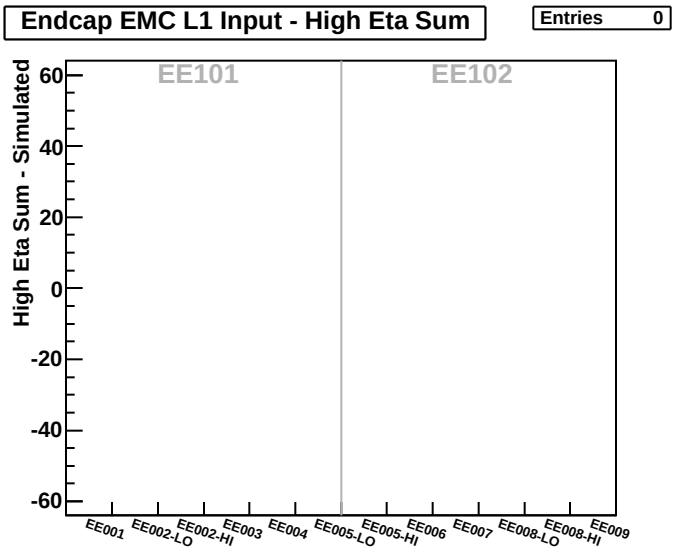

Barrel EMC L1 Input - High Tower Bits
Entries 2.749021e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

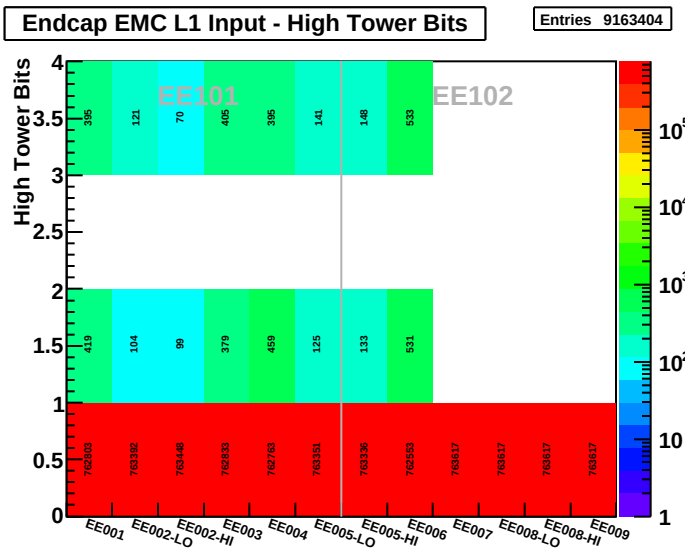

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

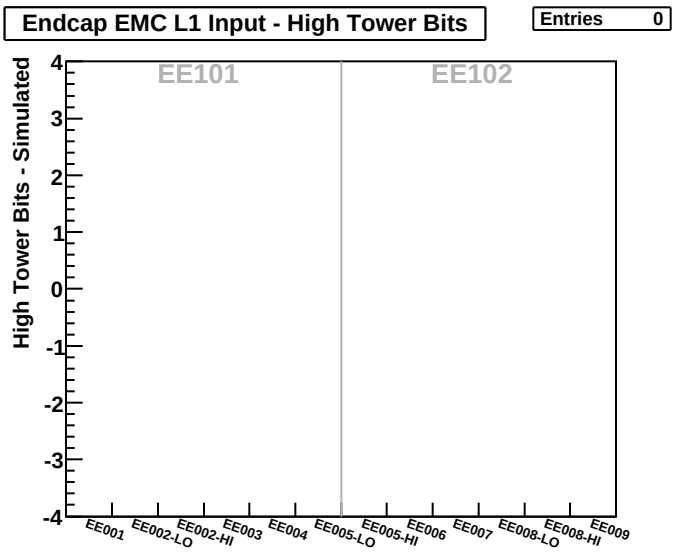

Endcap EMC L1 Input - Low Eta Sum
Entries 9163404
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

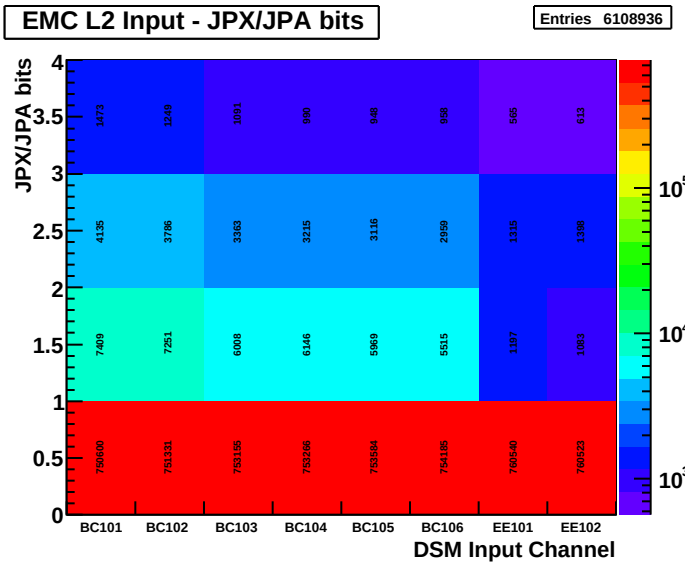

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

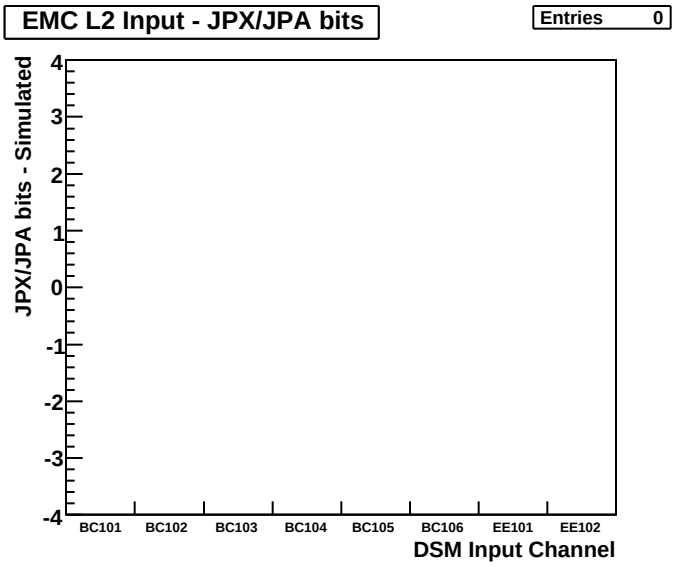

Endcap EMC L1 Input - High Eta Sum
Entries 9163404
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

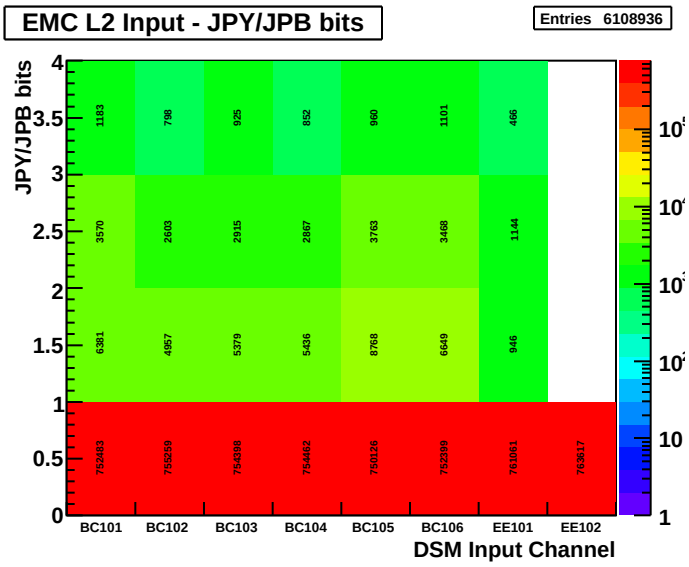

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

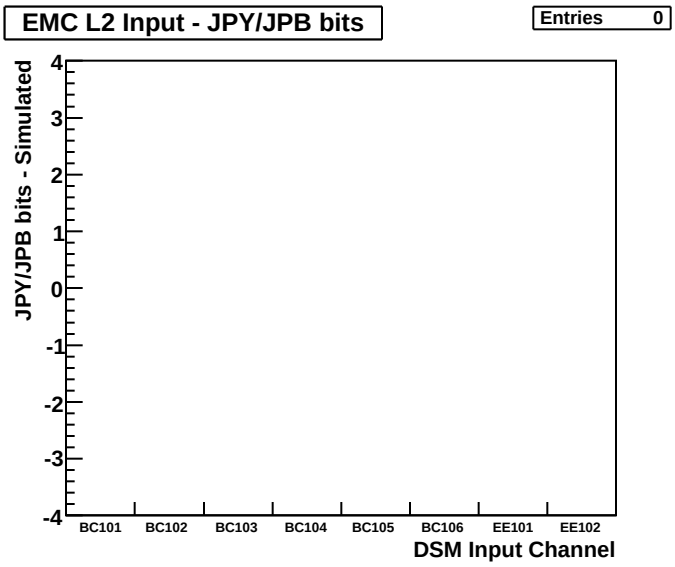

Endcap EMC L1 Input - High Tower Bits
Entries 9163404
High Tower Bits
EE101
EE102
395 121 70 405 395 141 148 533
419 104 99 379 459 125 133 531
762803 763392 763448 762833 762763 763351 763336 762553 763617 763617 763617 763617
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

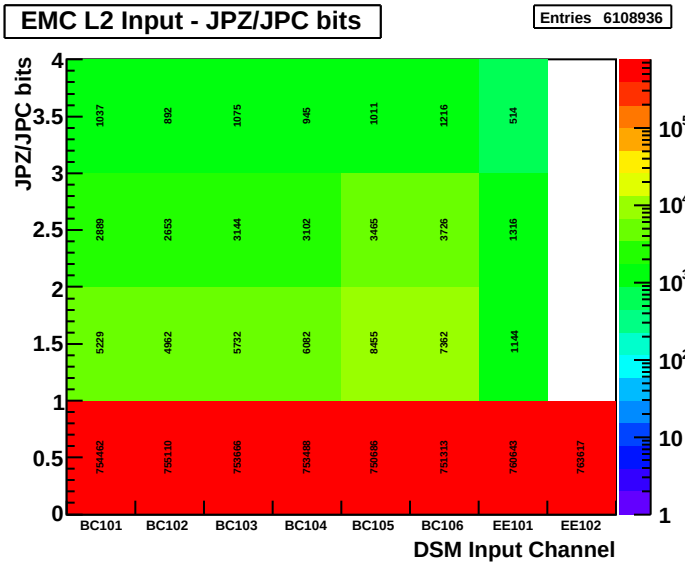

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

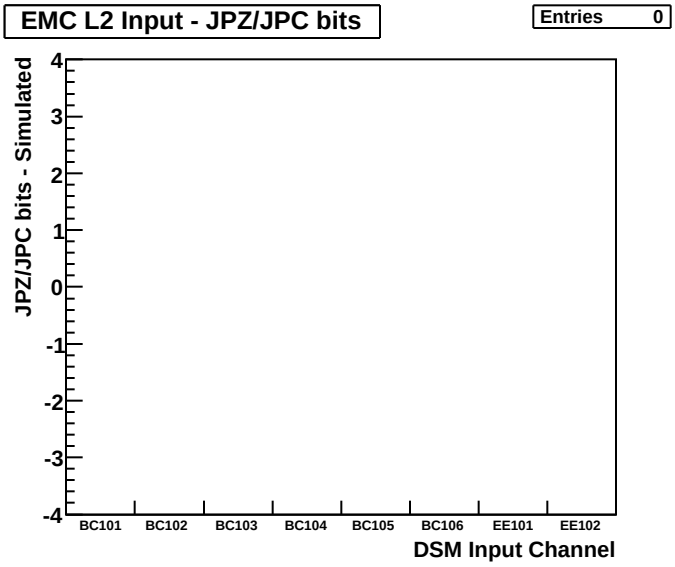

EMC L2 Input - JPX/JPA bits
Entries 6108936
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1473 1249 1091 990 948 958 565 613
4135 3786 3363 3215 3116 2959 1315 1398
7409 7251 6008 6146 5969 5515 1197 1083
750600 751331 753155 753266 753584 754185 760540 760523

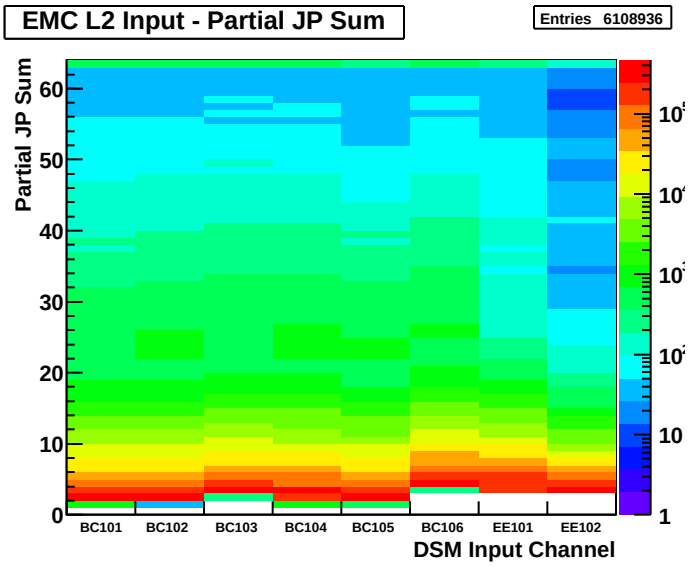

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

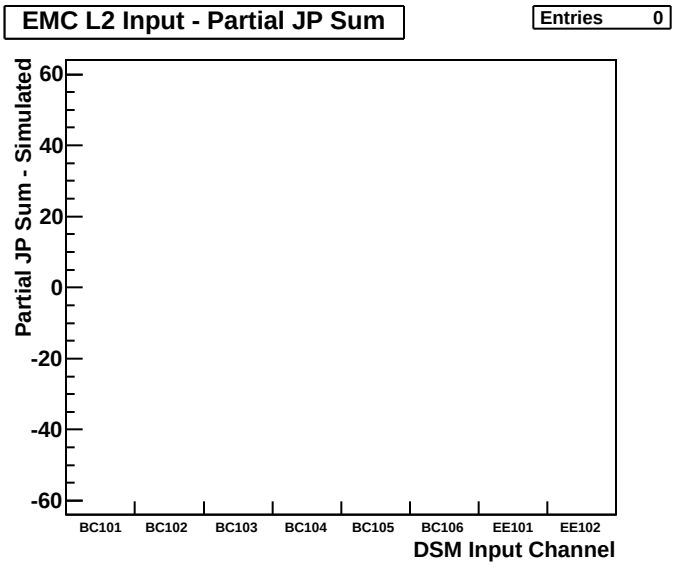

EMC L2 Input - JPY/JPB bits
Entries 6108936
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1183 798 925 852 960 1101 466
3570 2603 2915 2867 3763 3468 1144
6381 4957 5379 5436 8768 6649 946
752483 752259 754398 754462 750126 752399 761061 763617

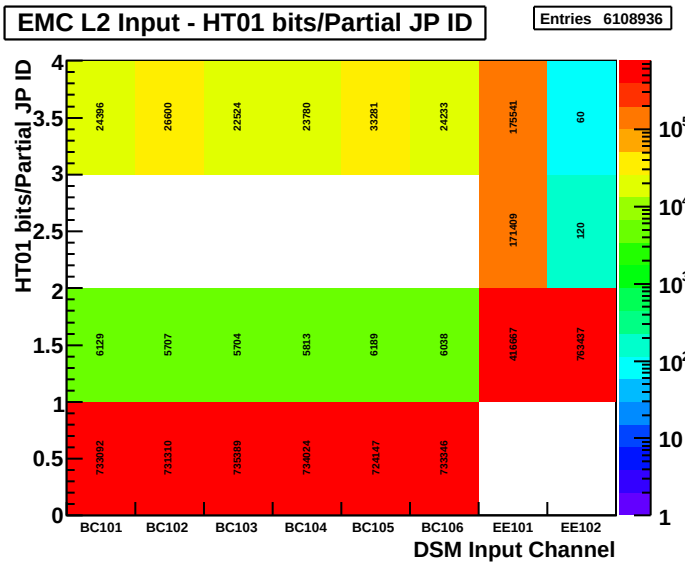

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

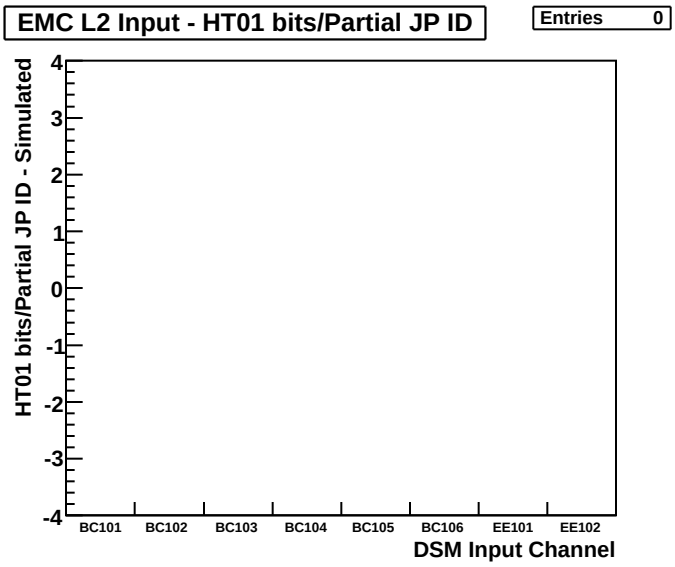

EMC L2 Input - JPZ/JPC bits
Entries 6108936
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1037 892 1075 945 1011 1216 514
2889 2653 3144 3102 3465 3726 1316
5229 4962 5732 6082 8455 7362 1144
754462 755110 753566 753488 750686 751333 760643 763617

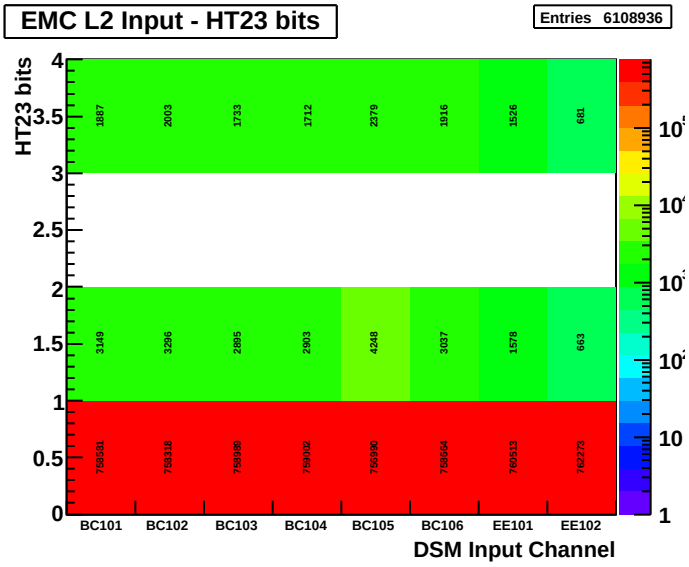

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

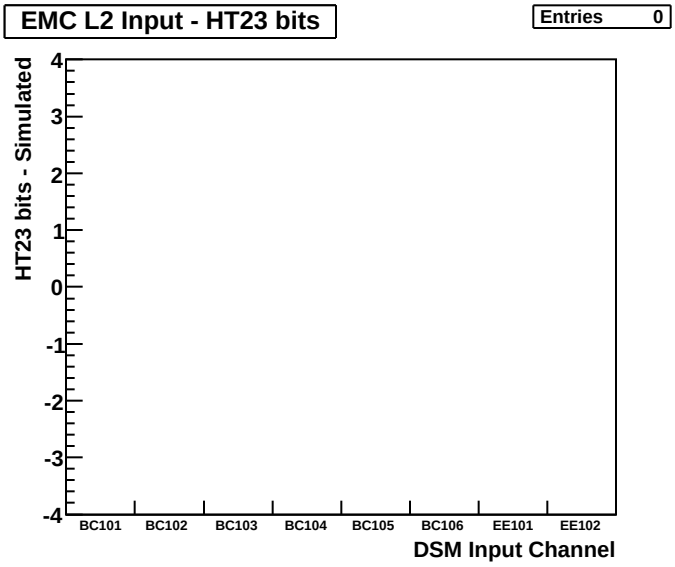

EMC L2 Input - Partial JP Sum
Entries 6108936
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

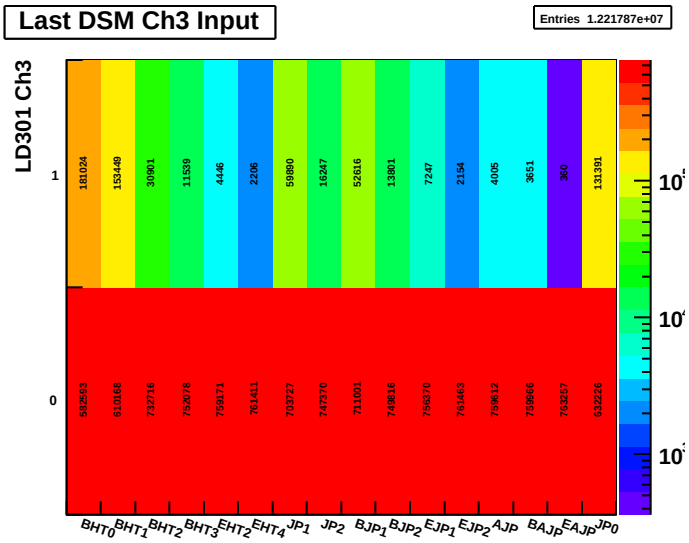

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

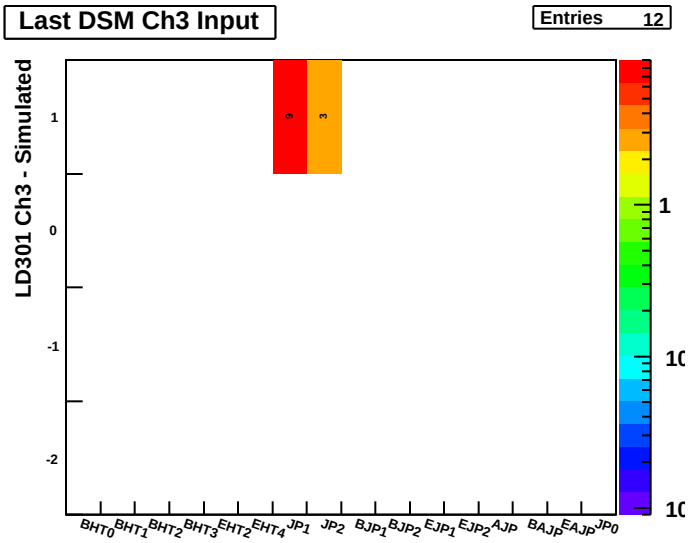

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6108936
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

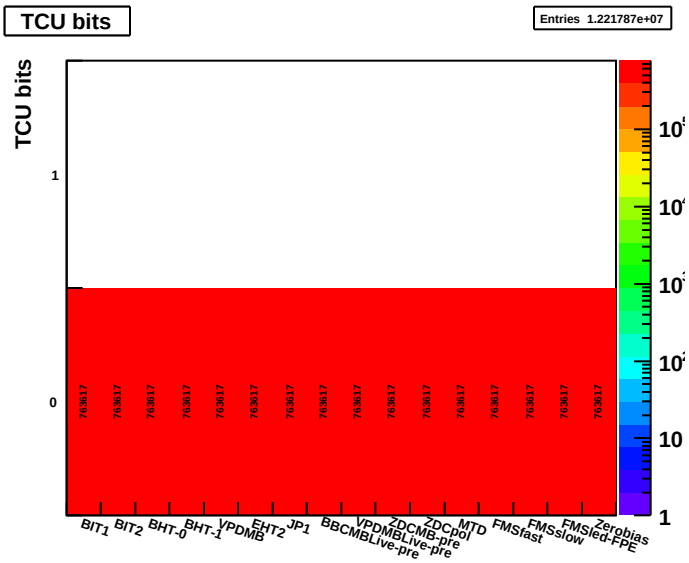

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

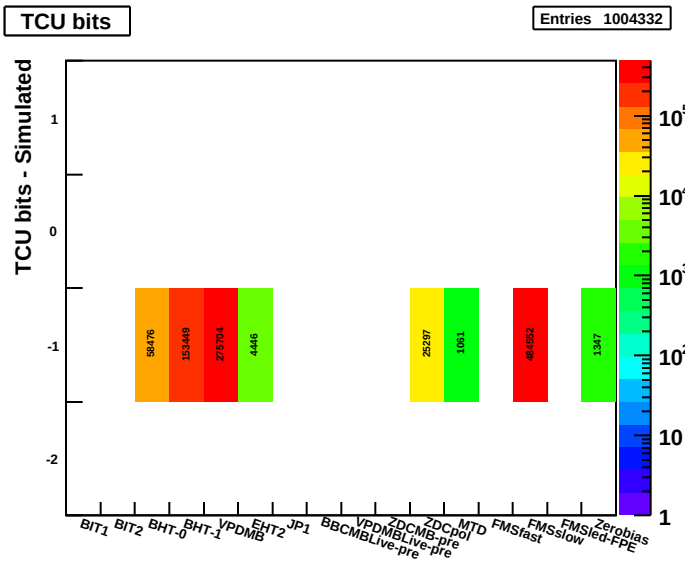

EMC L2 Input - HT23 bits
Entries 6108936
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

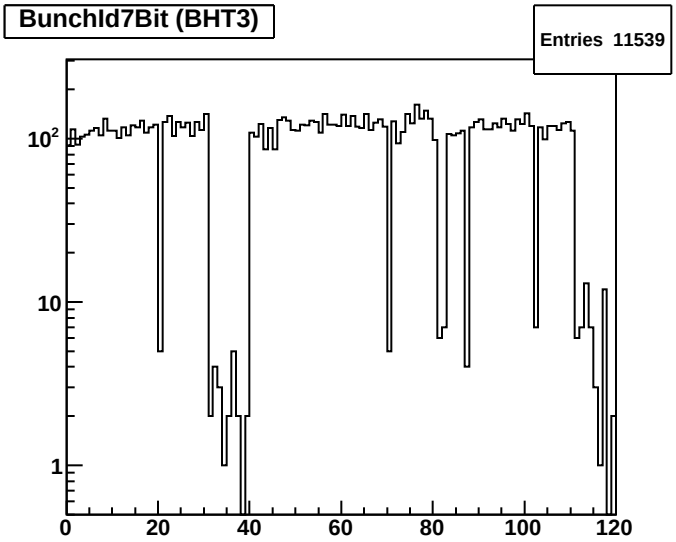

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

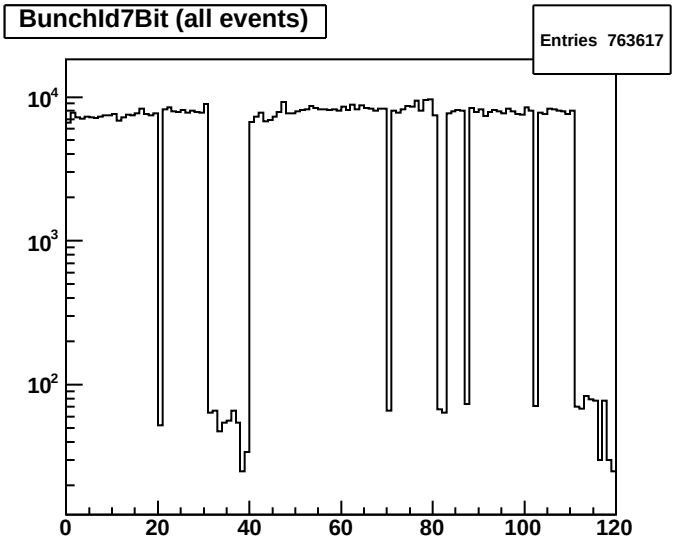

Last DSM Ch3 Input
Entries 1.221787e+07
LD301 Ch3
1
0
181024 153449 30901 11539 4446 2206 59990 16247 52616 13801 7247 2154 4005 3651 360 131391
582593 610168 732716 752078 759171 761411 703727 747370 711001 748816 756370 761463 759612 759866 762257 632226
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

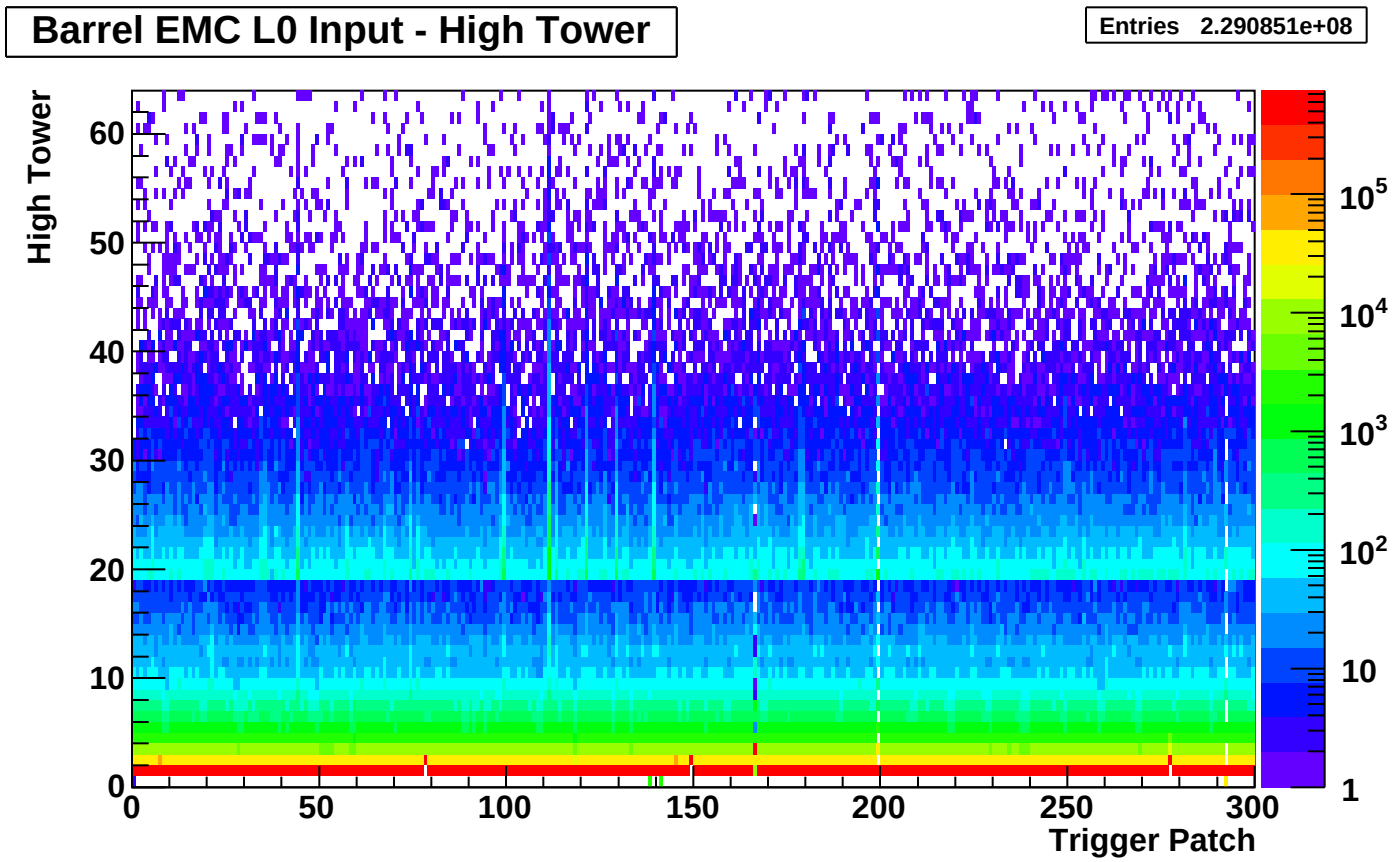

Last DSM Ch3 Input
Entries 12
LD301 Ch3 - Simulated
1
0
-1
-2
9
3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

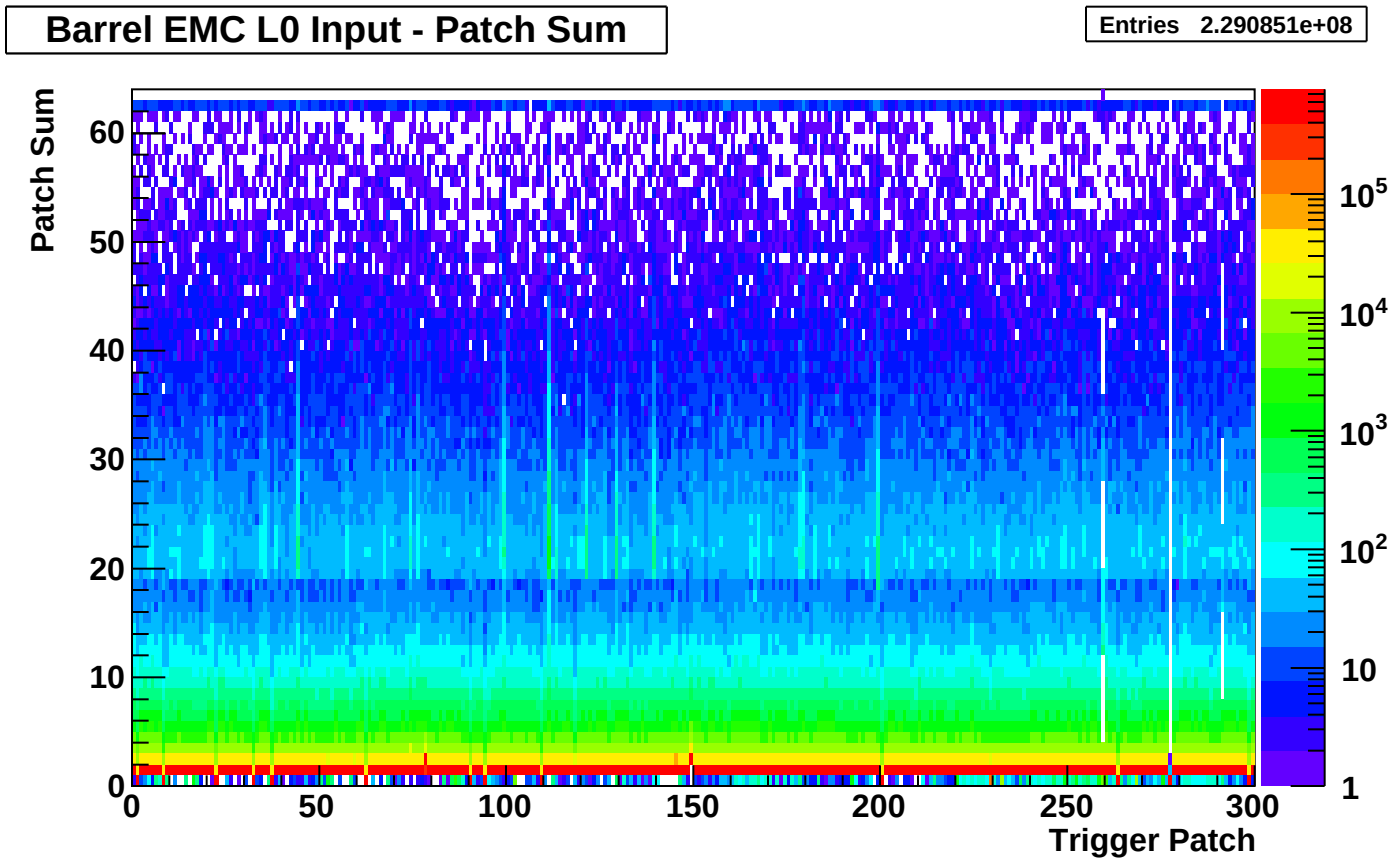

TCU bits
Entries 1.221787e+07
TCU bits
1
0
763617
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

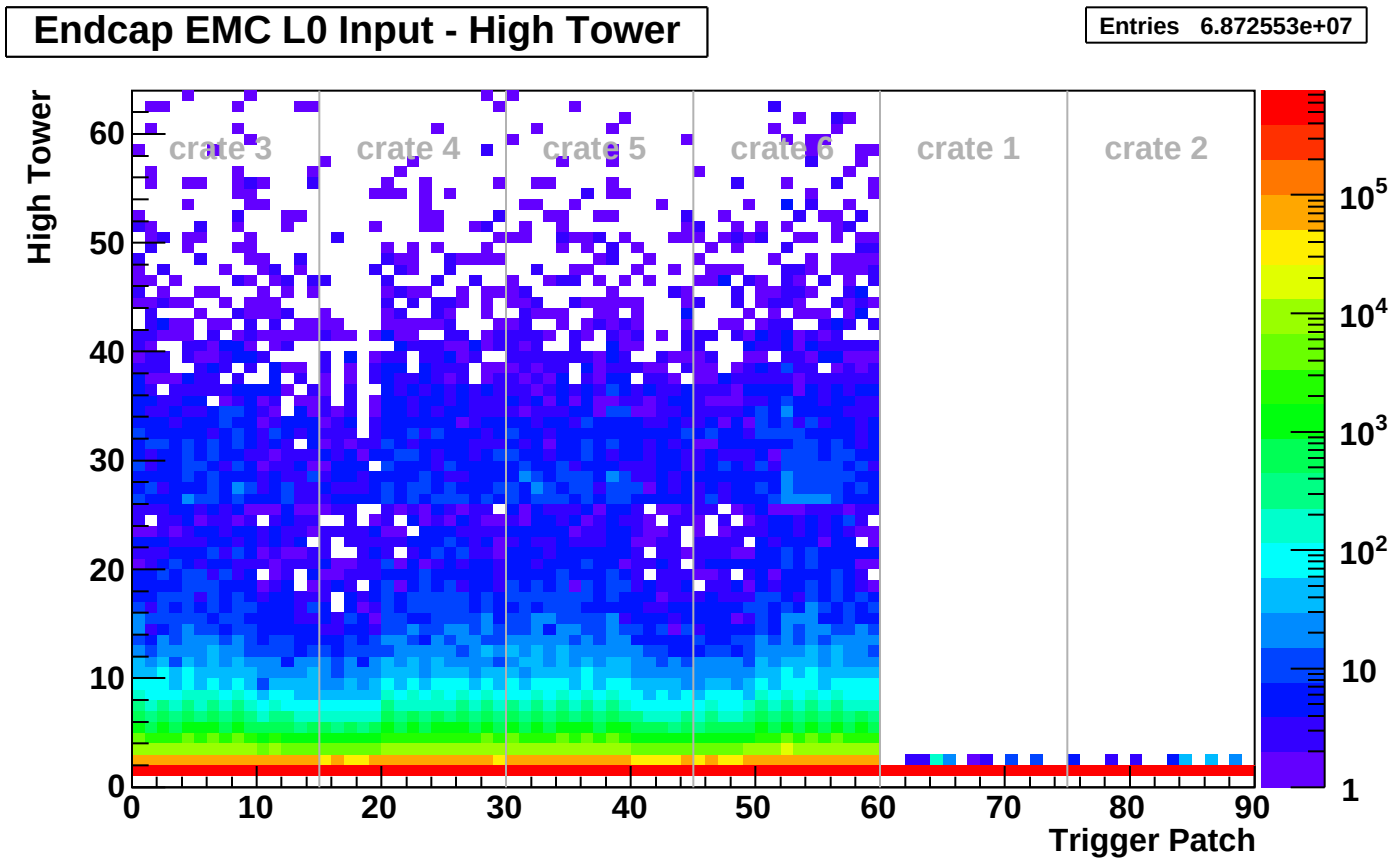

TCU bits
Entries 1004332
TCU bits - Simulated
1
0
-1
-2
58476 153449 275704 4446 25297 1061 484552 1347
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

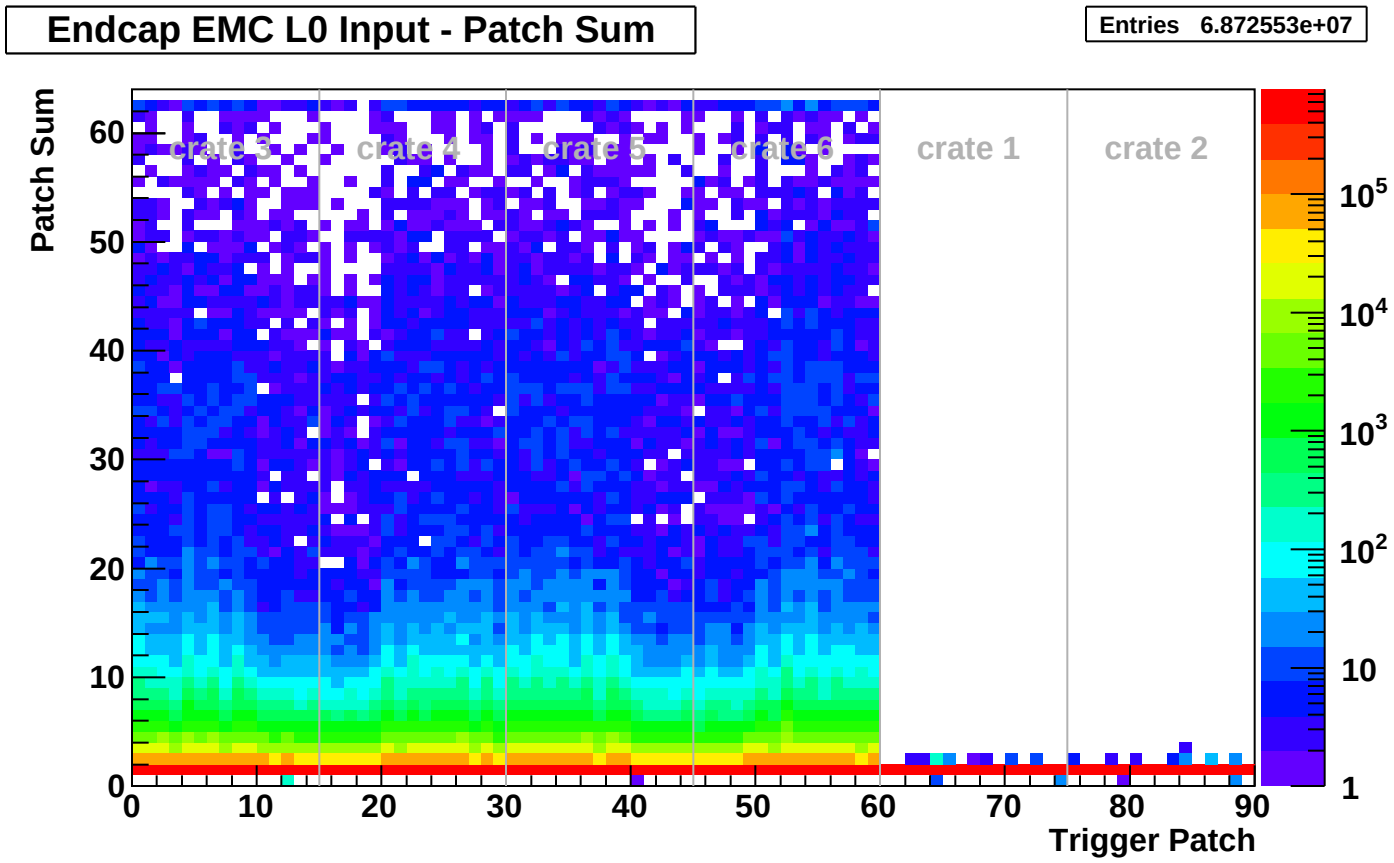

BunchId7Bit (BHT3)
Entries 11539
10²
10
1
0 20 40 60 80 100 120

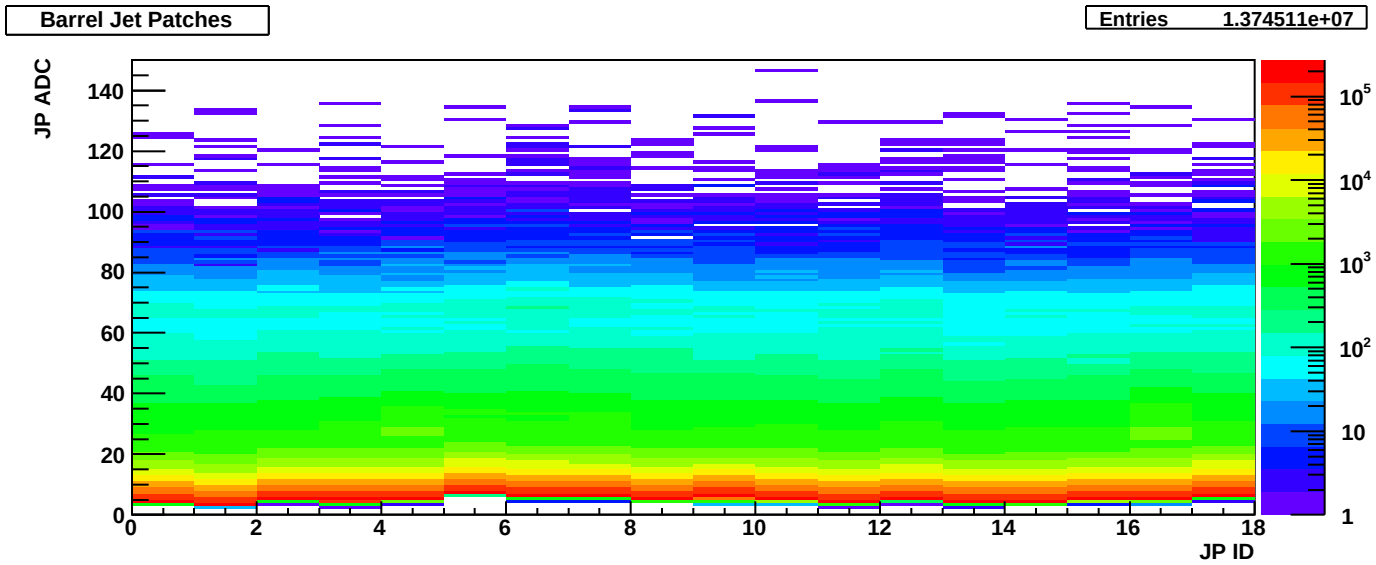

BunchId7Bit (all events)
Entries 763617
10⁴
10³
10²
0 20 40 60 80 100 120

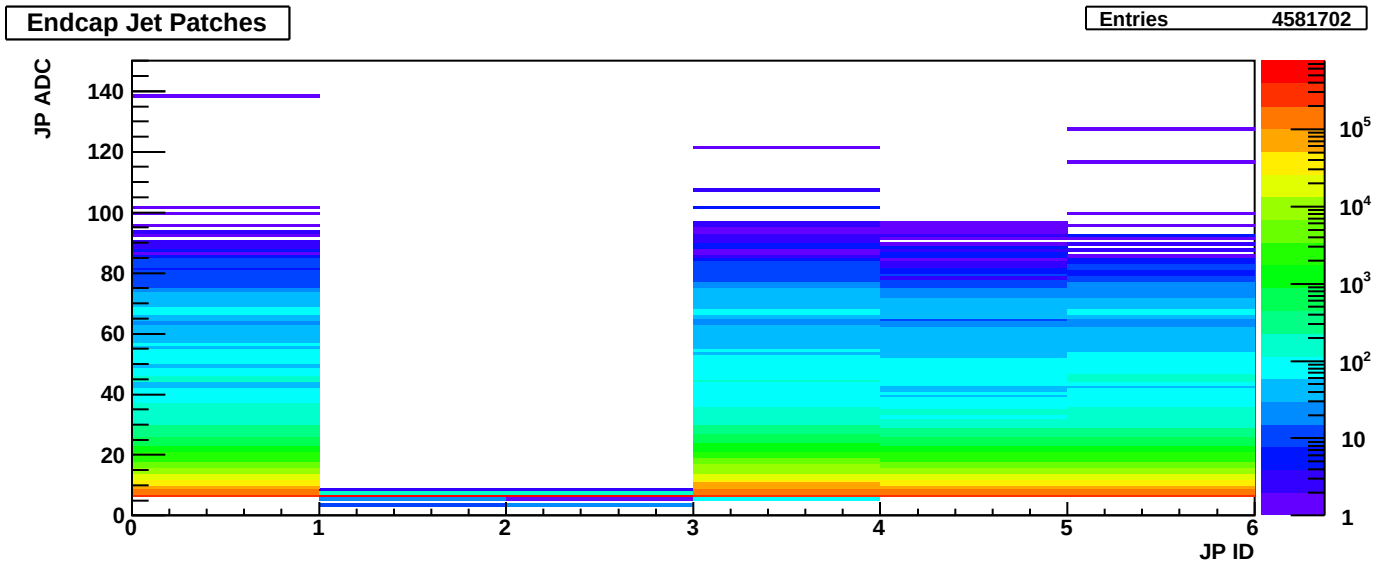

Barrel EMC L0 Input - High Tower
Entries 2.290851e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

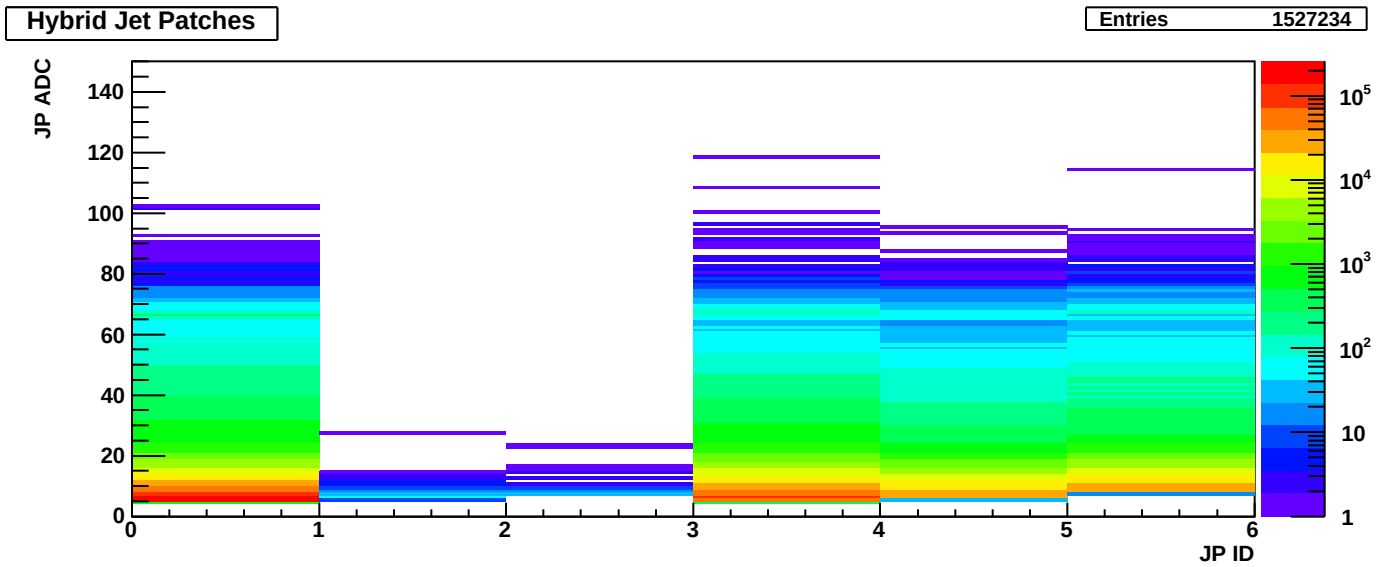

Barrel EMC L0 Input - Patch Sum
Entries 2.290851e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 6.872553e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Endcap EMC L0 Input - Patch Sum
Entries 6.872553e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

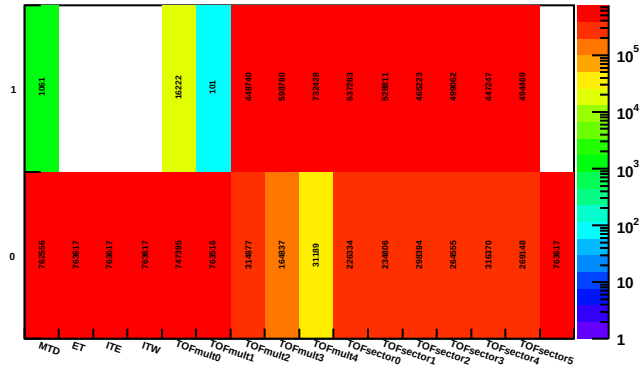

Barrel Jet Patches
Entries 1.374511e+07
JP ADC
JP ID


Endcap Jet Patches
Entries 4581702
JP ADC
JP ID


Hybrid Jet Patches
Entries 1527234
JP ADC
JP ID

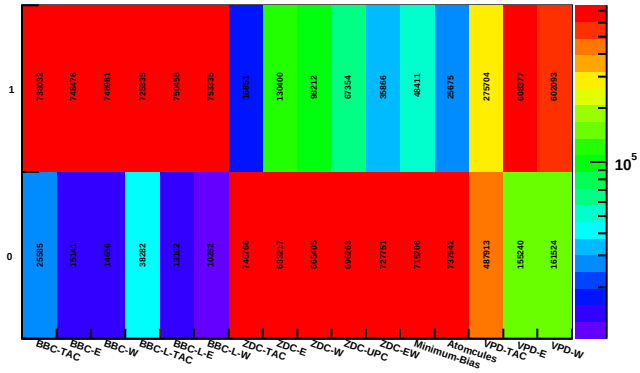
TF201 0-15 (ch0)

Entries 1.221787e+07



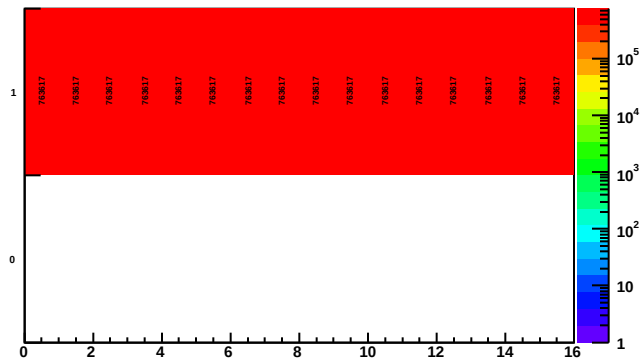
VT201 0-15 (ch1)

Entries 1.221787e+07



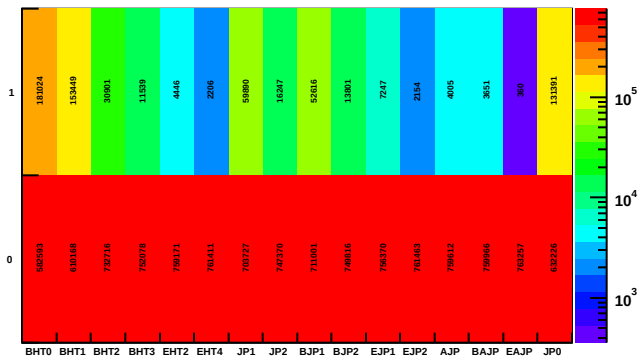
Unused (ch2)

Entries 1.221787e+07



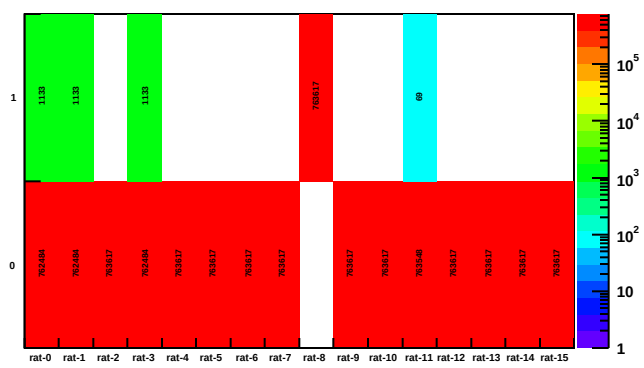
EM201 0-15 (ch3)

Entries 1.221787e+07



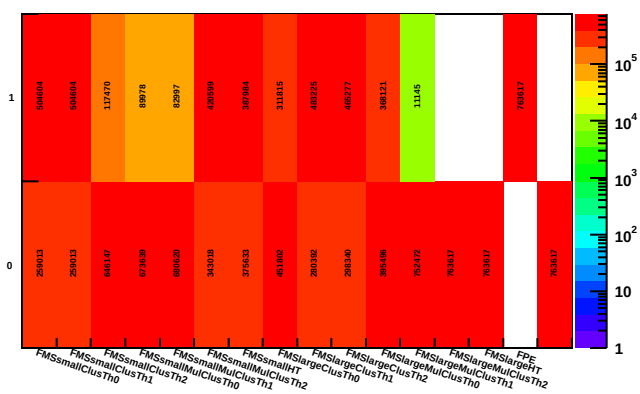
RAT board (ch4)

Entries 1.221787e+07



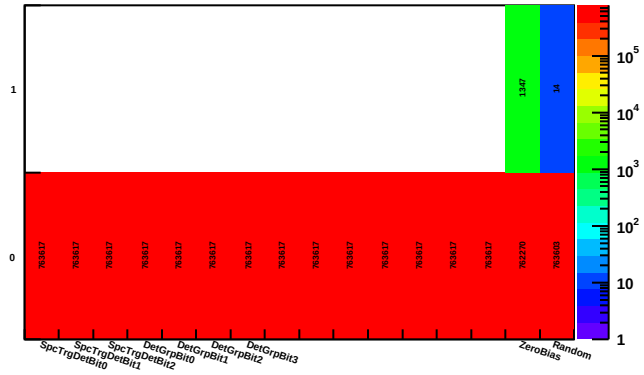
FP201 0-15 (ch5)

Entries 1.221787e+07



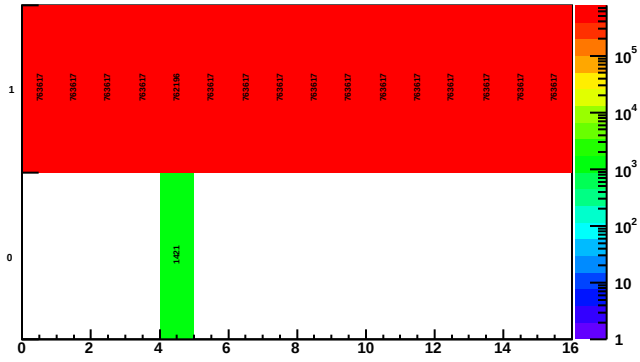
ST201 0-15 (ch6)

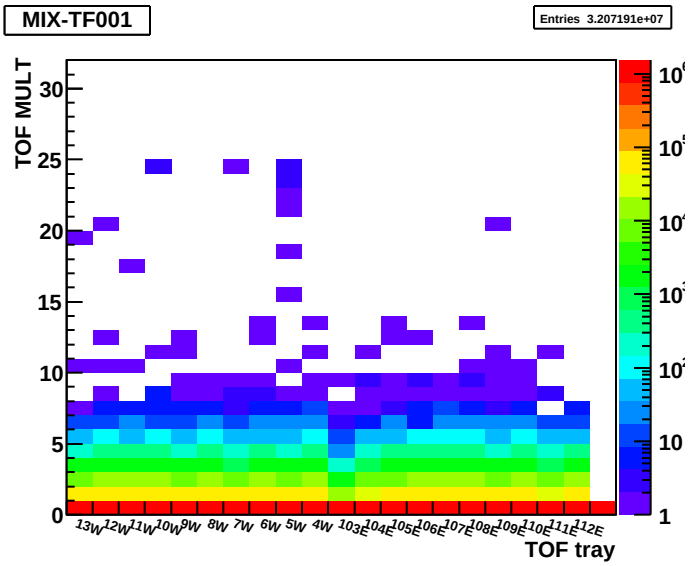
Entries 1.221787e+07

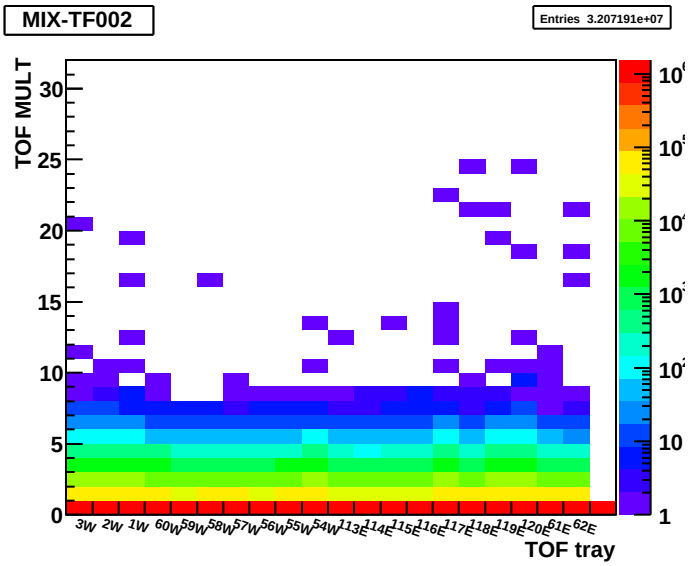


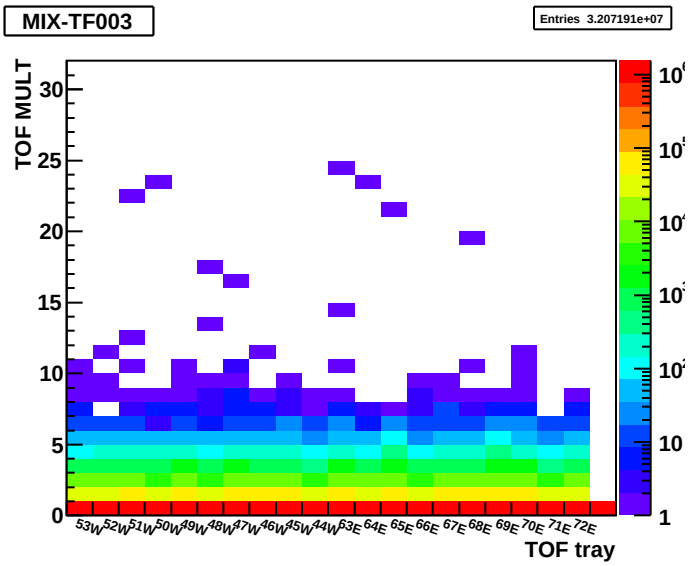
Unused (ch7)

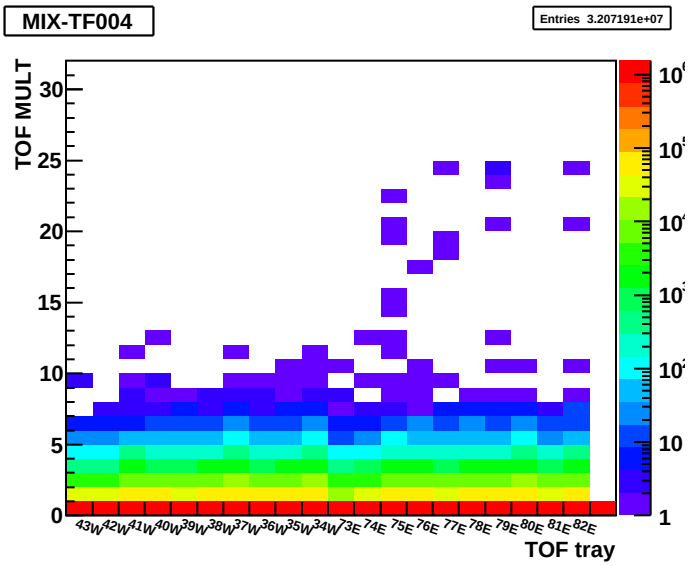
Entries 1.221787e+07

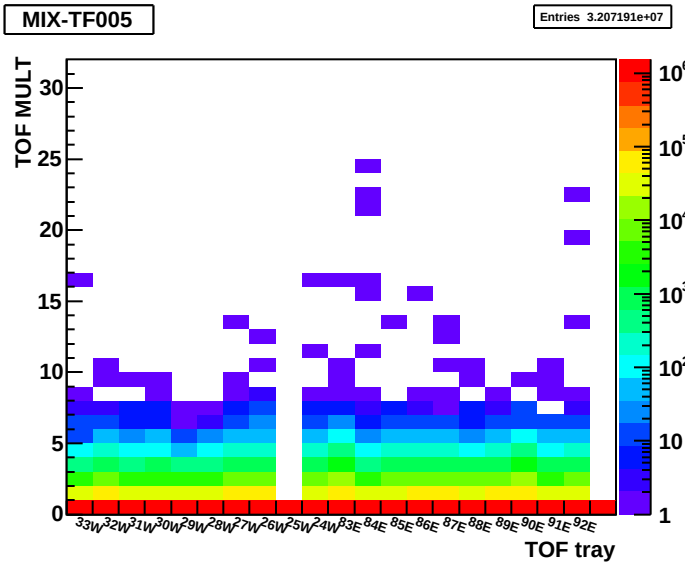


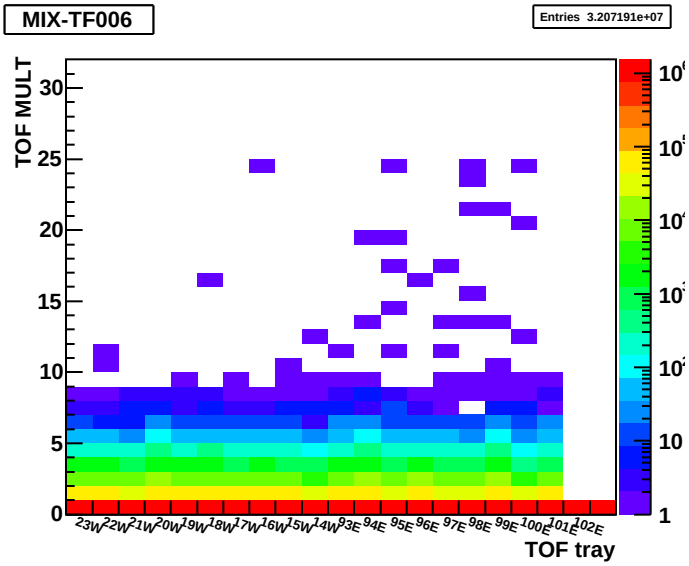

MIX-TF001
Entries 3.207191e+07
TOF MULT
TOF tray

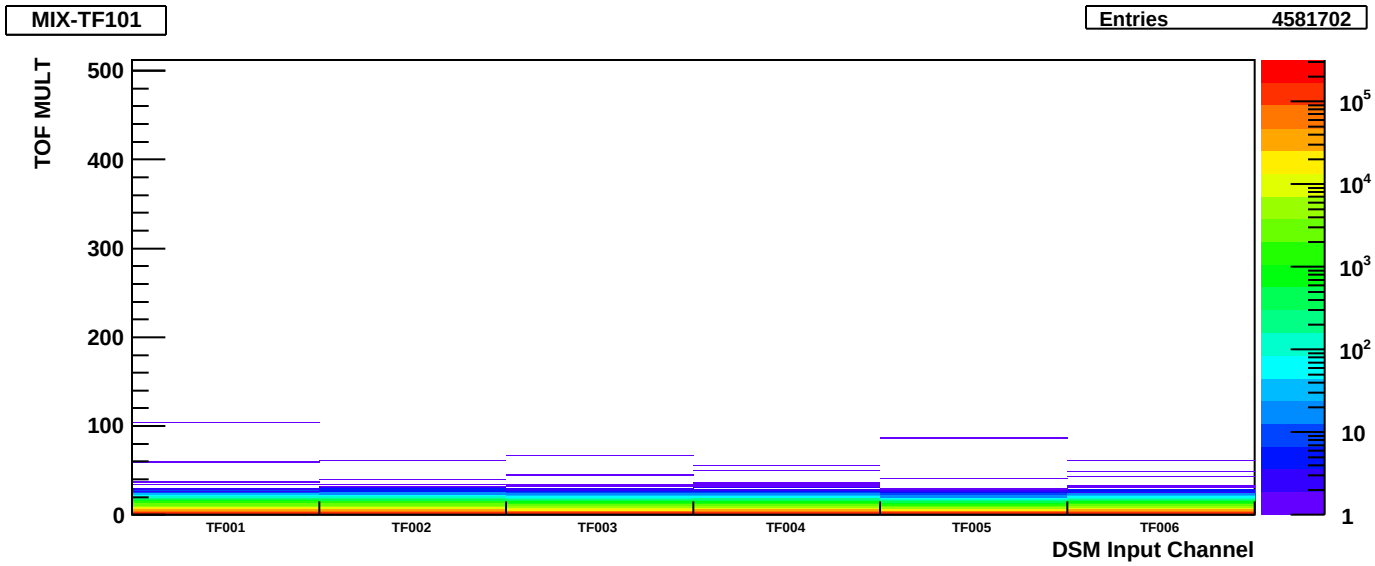

MIX-TF002
Entries 3.207191e+07
TOF MULT
TOF tray

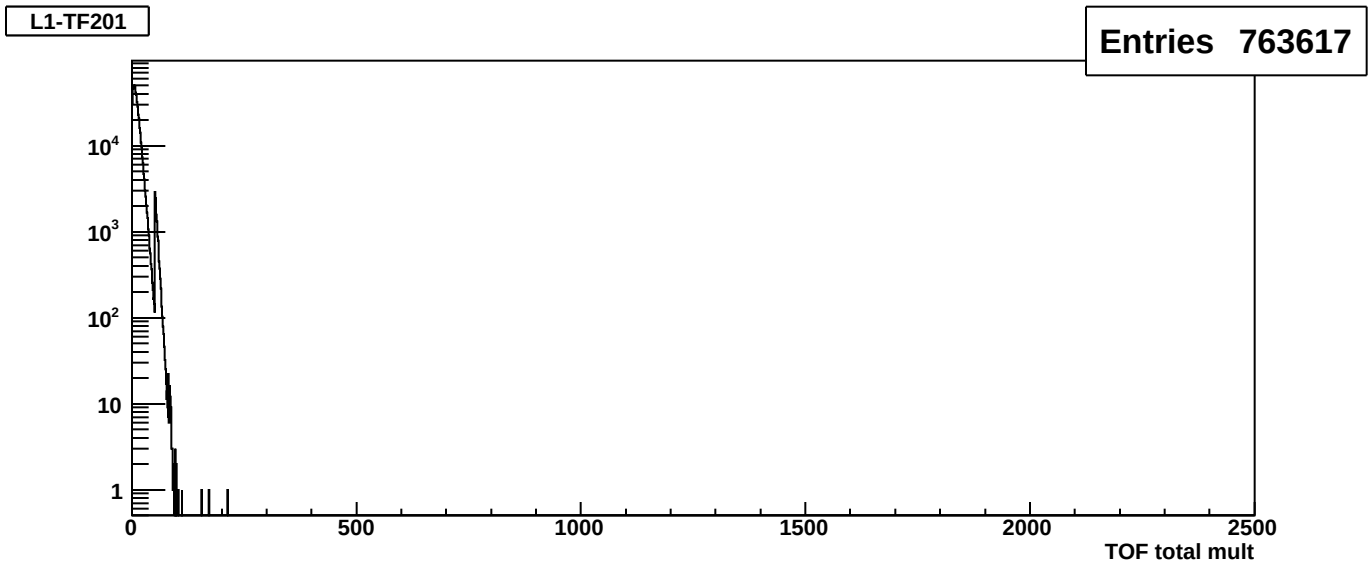

MIX-TF003
Entries 3.207191e+07
TOF MULT
TOF tray

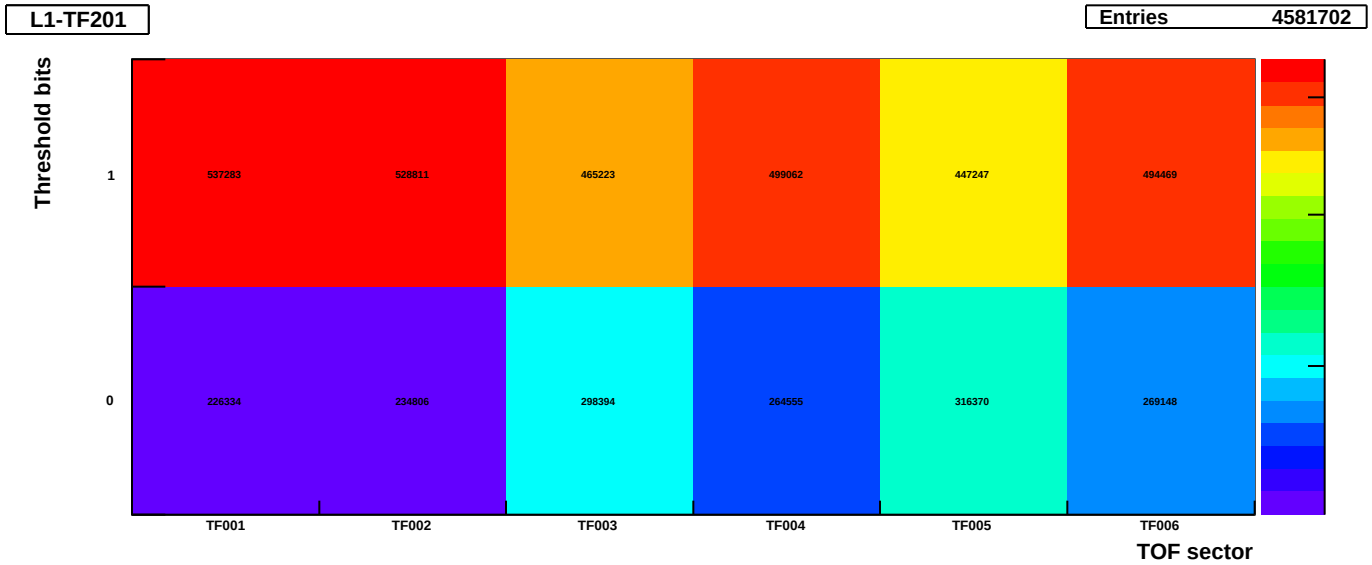

MIX-TF004
Entries 3.207191e+07
TOF MULT
TOF tray

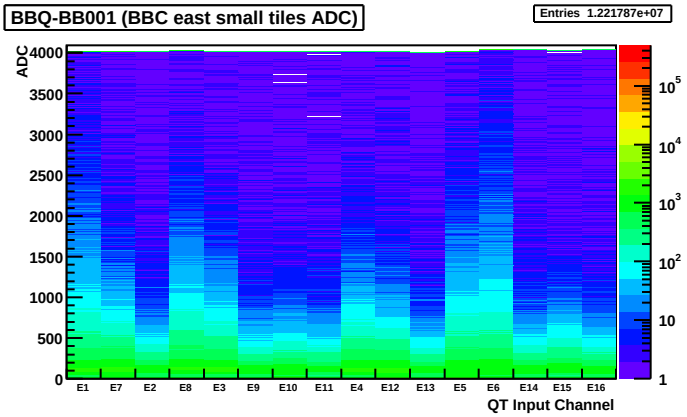

MIX-TF005
Entries 3.207191e+07
TOF MULT
TOF tray

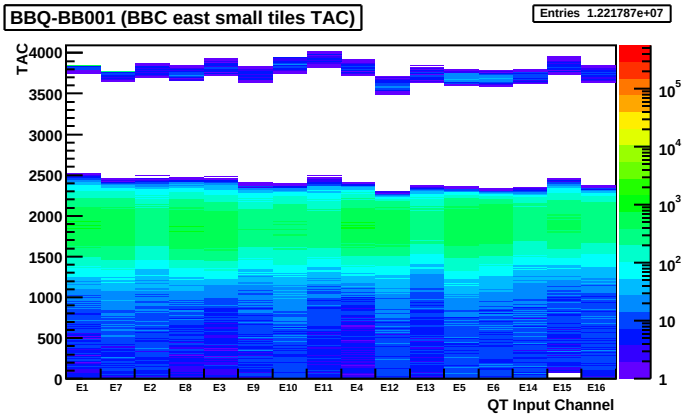

MIX-TF006
Entries 3.207191e+07
TOF MULT
TOF tray

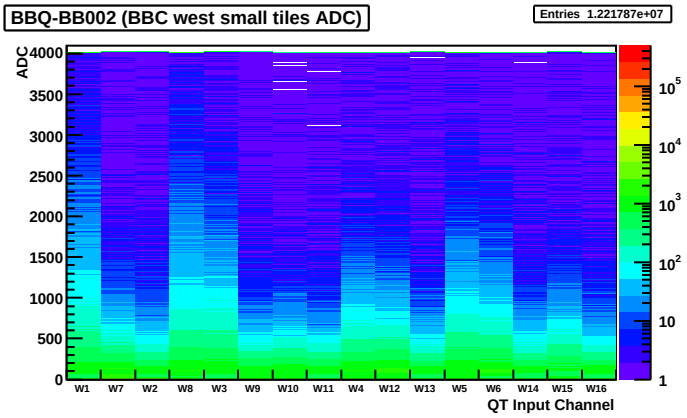

MIX-TF101
Entries 4581702
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

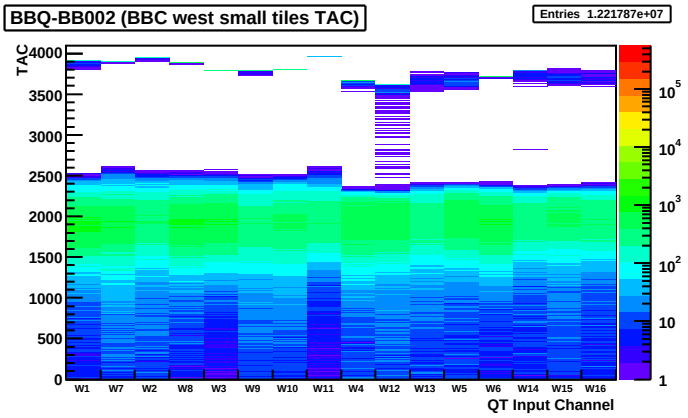

L1-TF201
Entries 763617
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

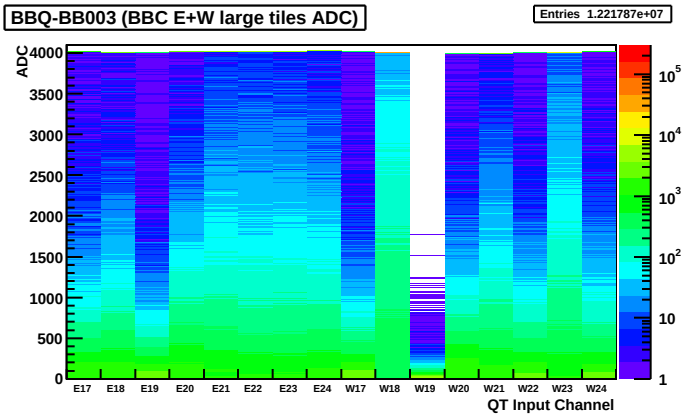

L1-TF201
Entries 4581702
Threshold bits
1
0
537283
528811
465223
499062
447247
494469
226334
234806
298394
264555
316370
269148
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

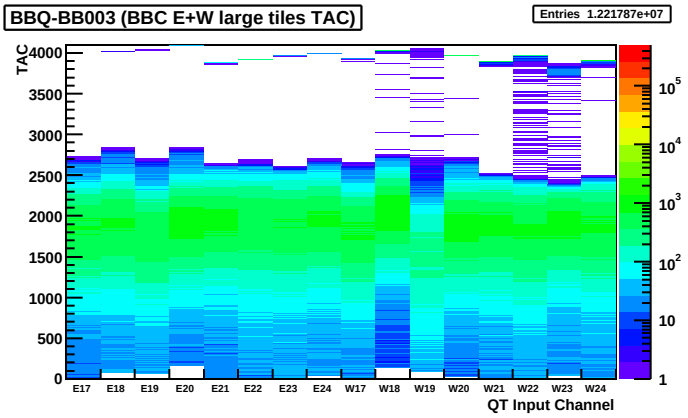

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.221787e+07
ADC
QT Input Channel

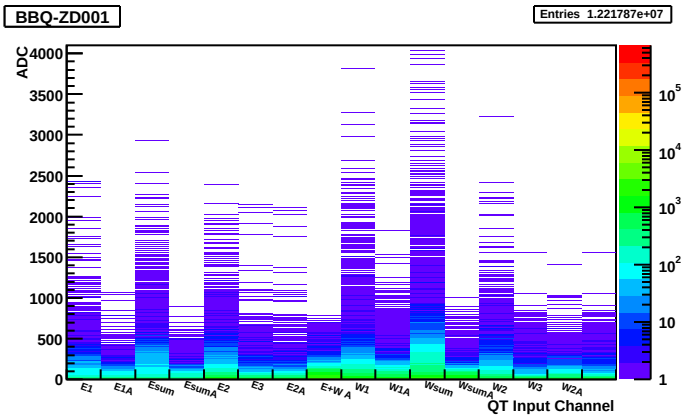

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.221787e+07
TAC
QT Input Channel

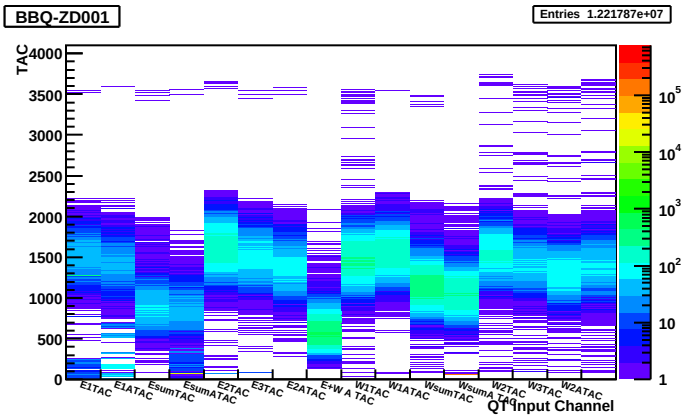

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.221787e+07
ADC
QT Input Channel

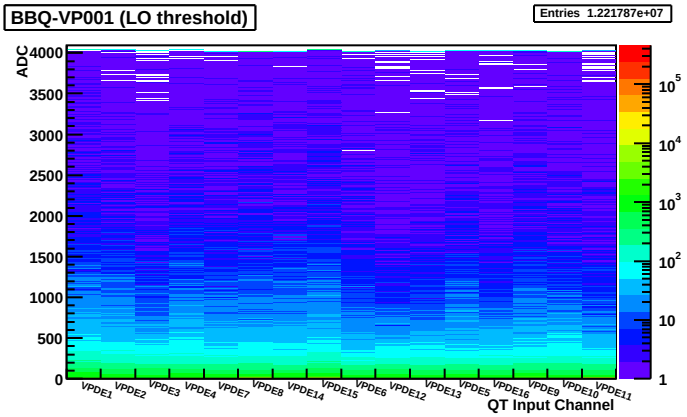

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.221787e+07
TAC
QT Input Channel

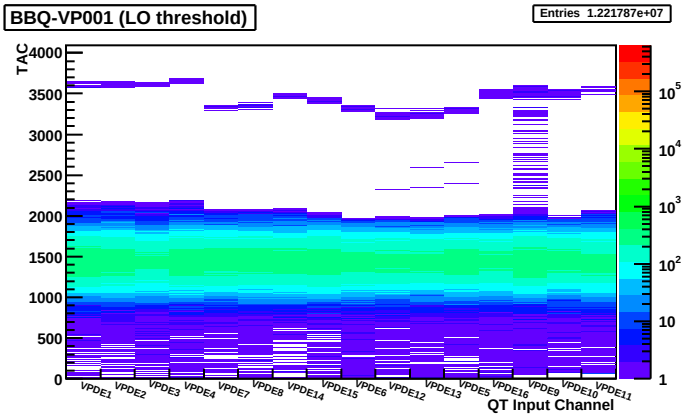

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.221787e+07
ADC
QT Input Channel

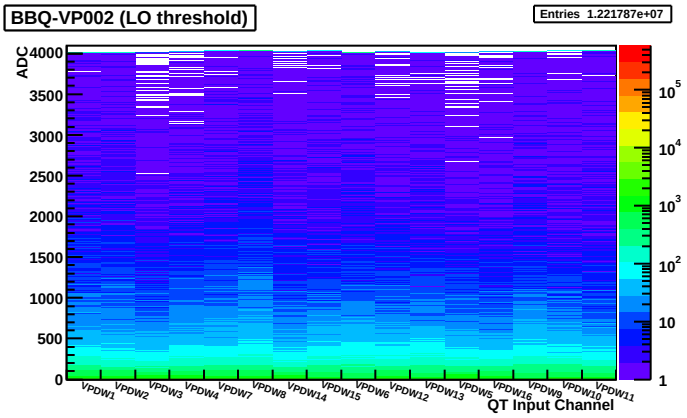

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.221787e+07
TAC
QT Input Channel

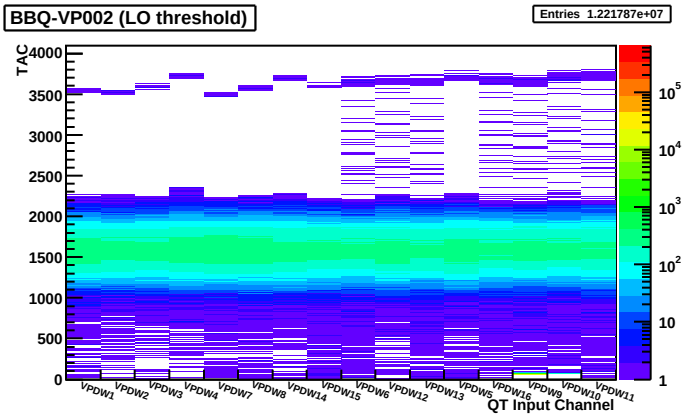

BBQ-ZD001
Entries 1.221787e+07
ADC
QT Input Channel

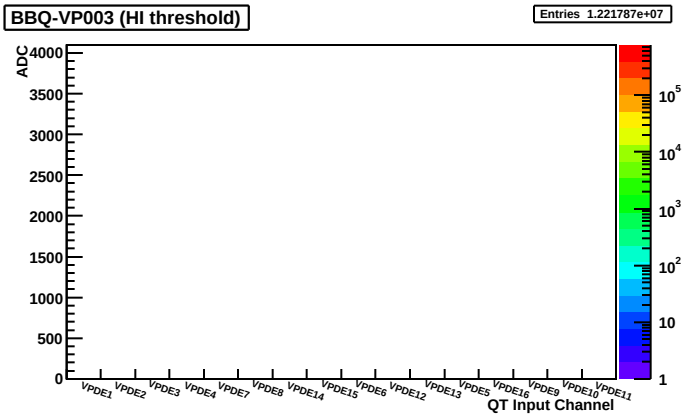

BBQ-ZD001
Entries 1.221787e+07
TAC
QT Input Channel

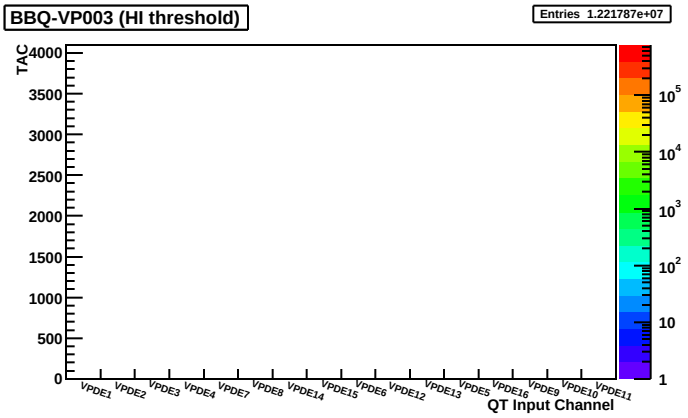

BBQ-VP001 (LO threshold)
Entries 1.221787e+07
ADC
QT Input Channel

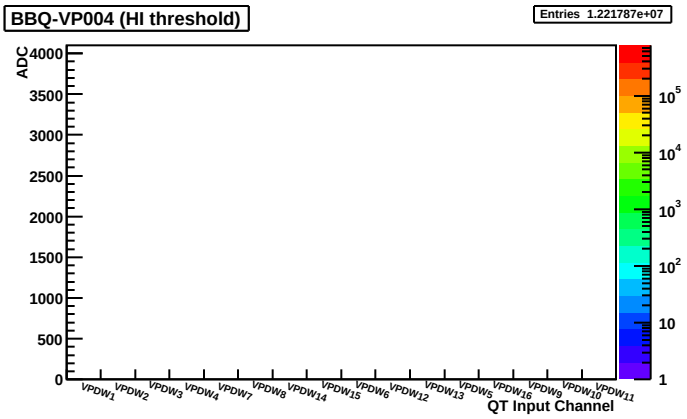

BBQ-VP001 (LO threshold)
Entries 1.221787e+07
TAC
QT Input Channel

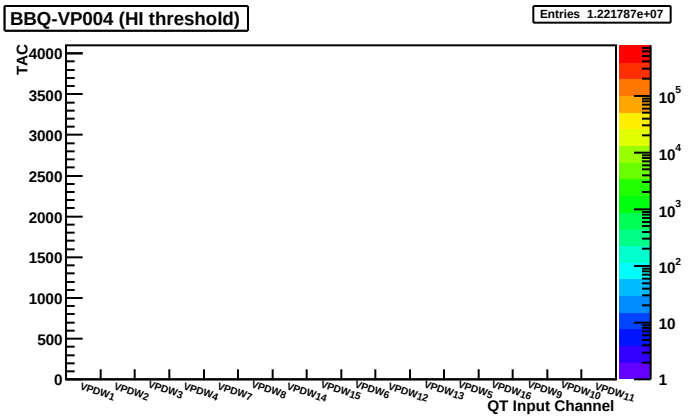

BBQ-VP002 (LO threshold)
Entries 1.221787e+07
ADC
QT Input Channel

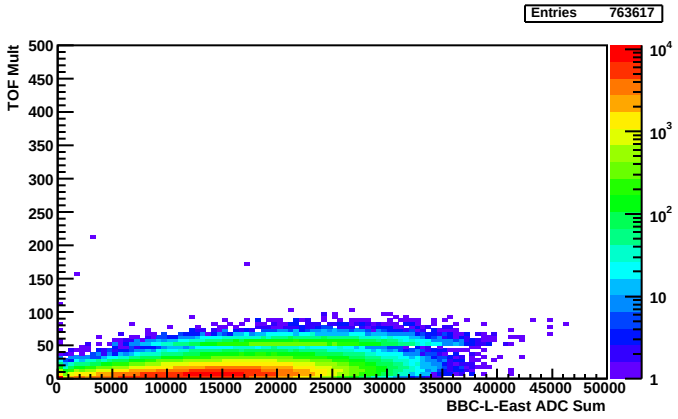

BBQ-VP002 (LO threshold)
Entries 1.221787e+07
TAC
QT Input Channel

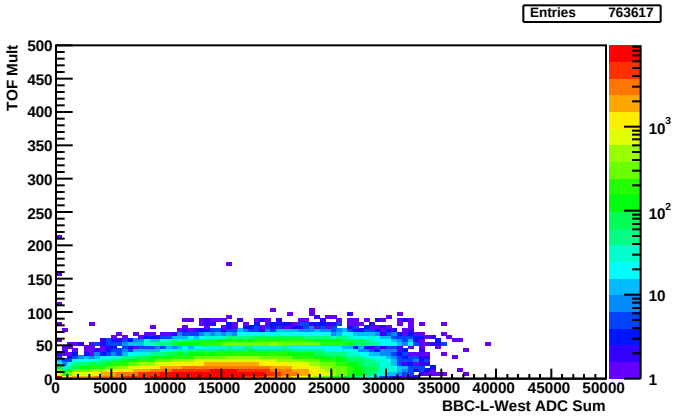

BBQ-VP003 (HI threshold)
Entries 1.221787e+07
ADC
QT Input Channel

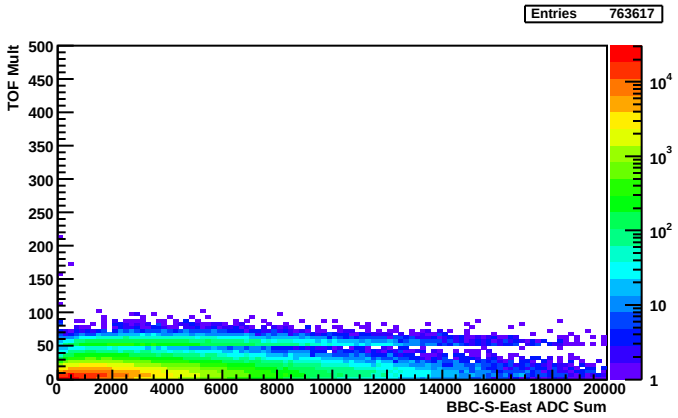

BBQ-VP003 (HI threshold)
Entries 1.221787e+07
TAC
QT Input Channel

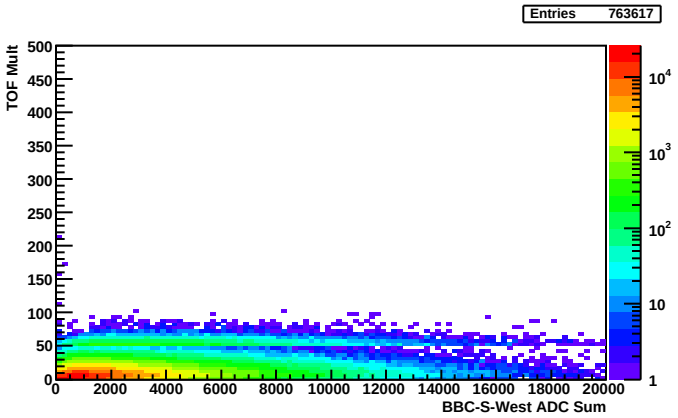

BBQ-VP004 (HI threshold)
Entries 1.221787e+07
ADC
QT Input Channel

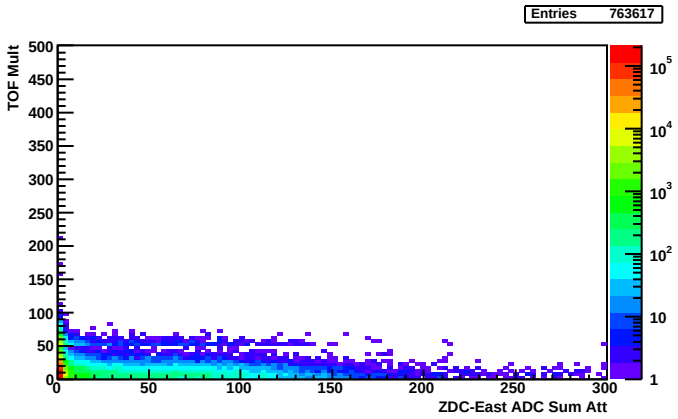

BBQ-VP004 (HI threshold)
Entries 1.221787e+07
TAC
QT Input Channel

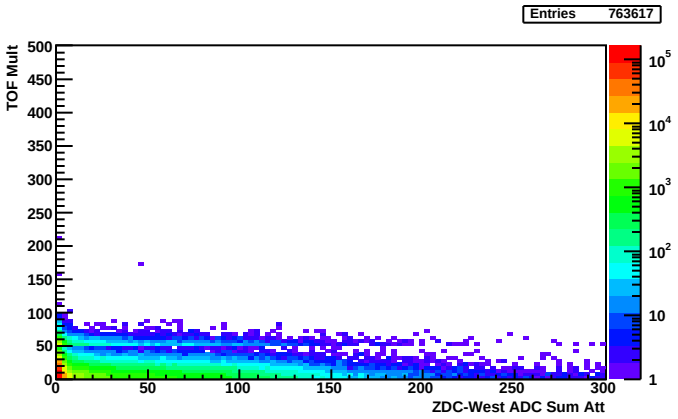

Entries 763617
TOF Mult
BBC-L-East ADC Sum

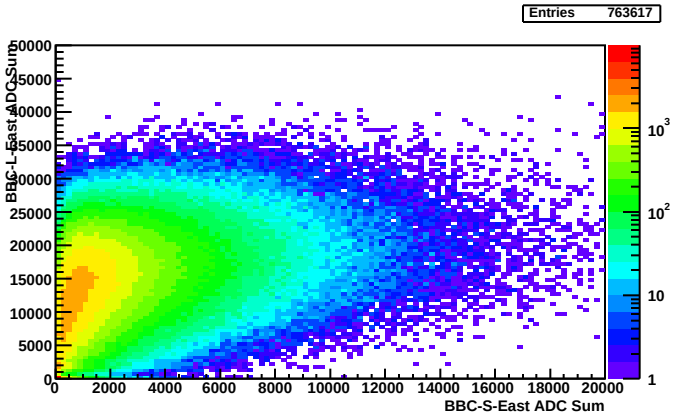

Entries 763617
TOF Mult
BBC-L-West ADC Sum

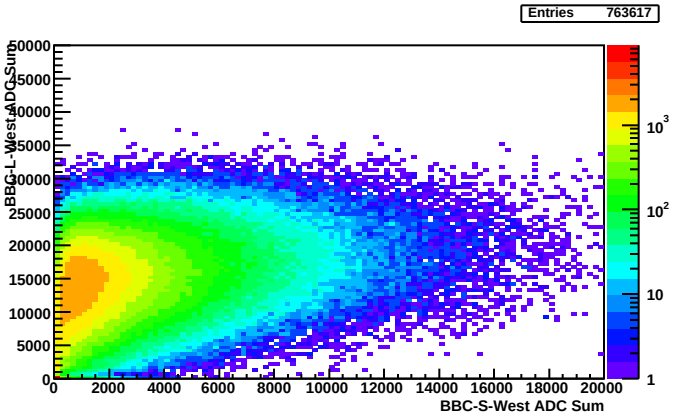

Entries 763617
TOF Mult
BBC-S-East ADC Sum

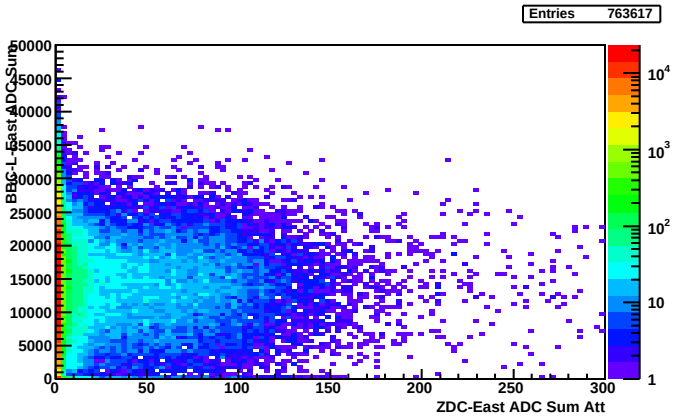

Entries 763617
TOF Mult
BBC-S-West ADC Sum

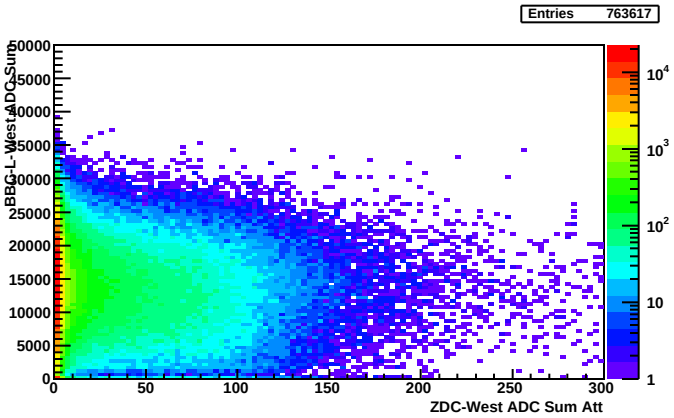

Entries 763617
TOF Mult
ZDC-East ADC Sum Att

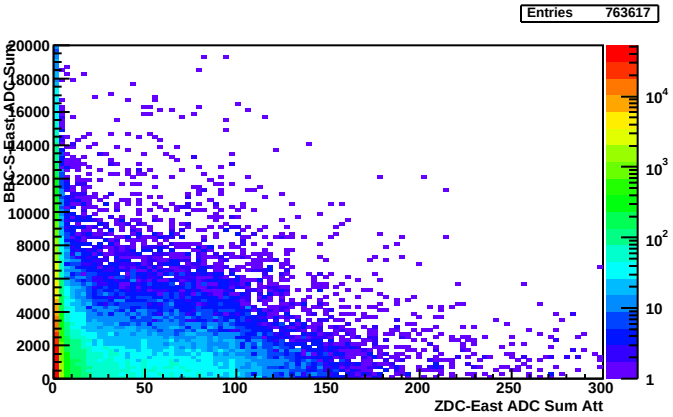

Entries 763617
TOF Mult
ZDC-West ADC Sum Att

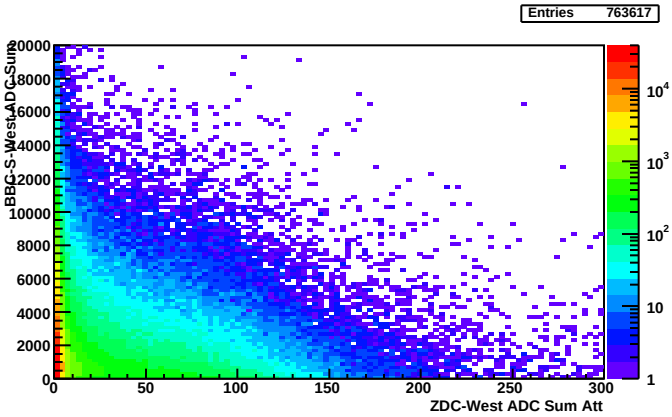

Entries 763617
BBC-L-East ADC Sum
BBC-S-East ADC Sum

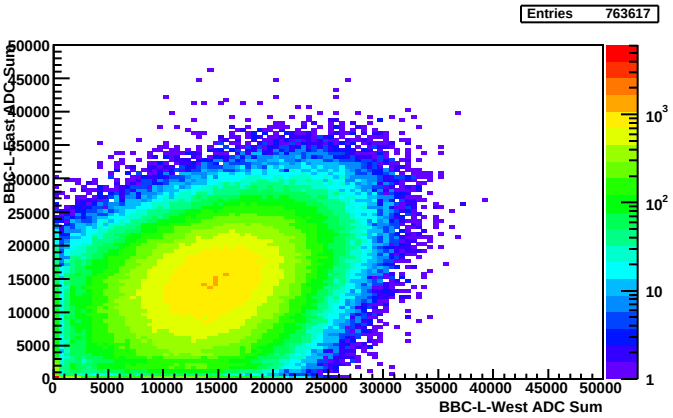

Entries 763617
BBC-L-West ADC Sum
BBC-S-West ADC Sum

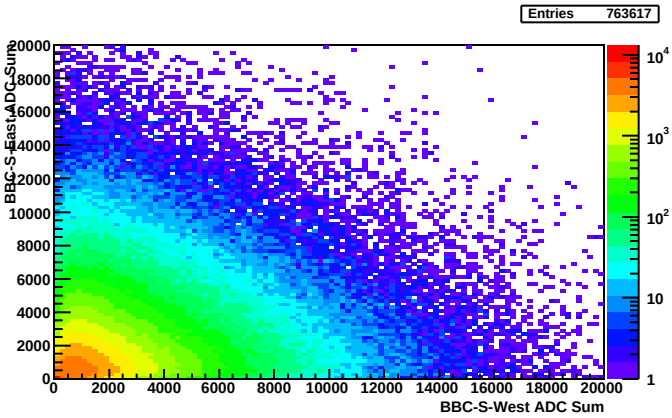

Entries 763617
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

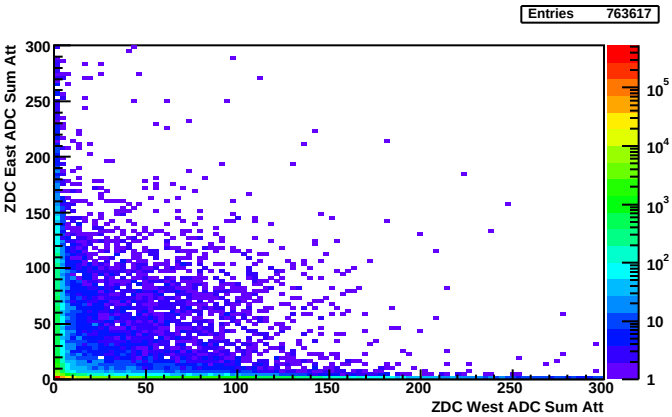

Entries 763617
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

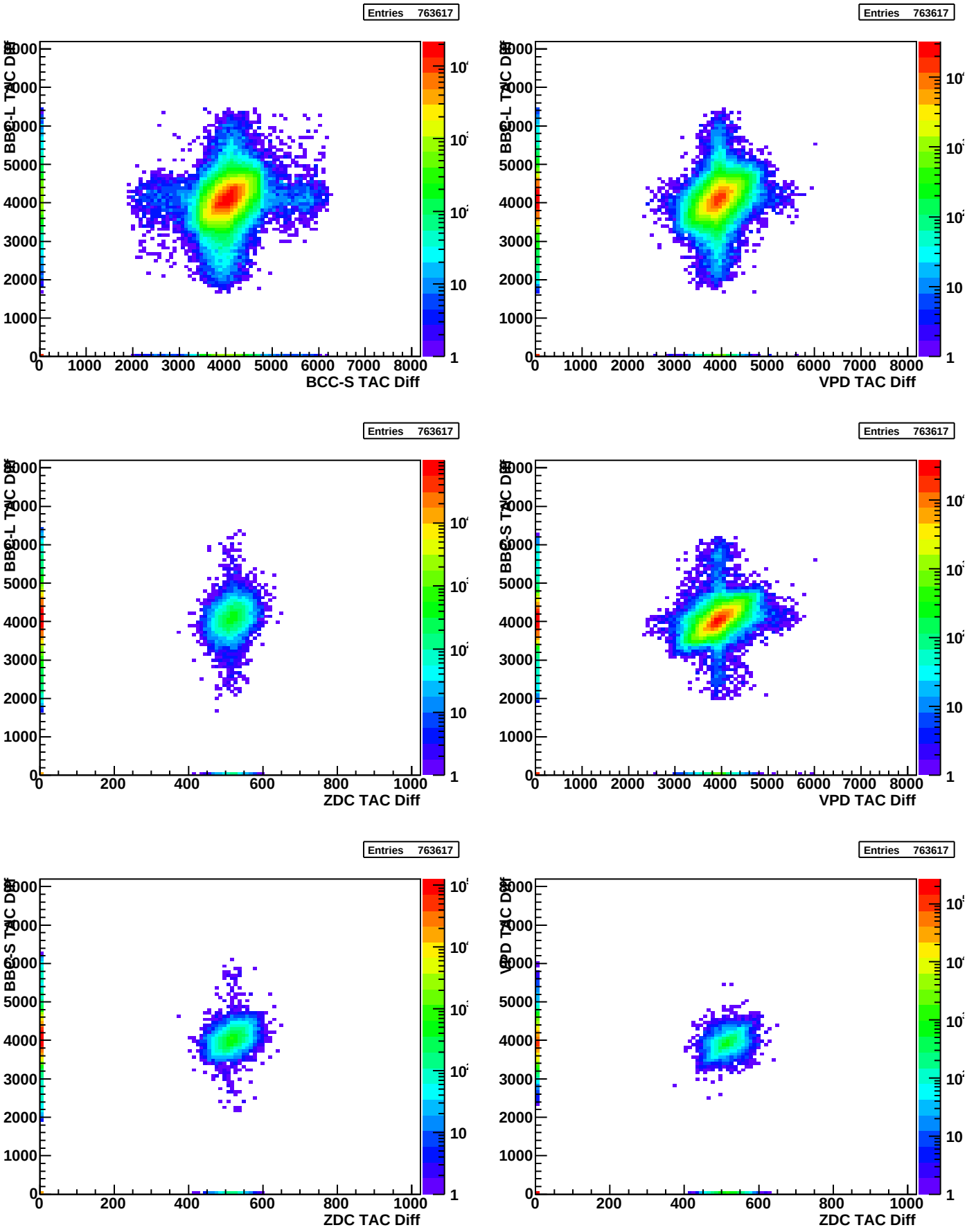

Entries 763617
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

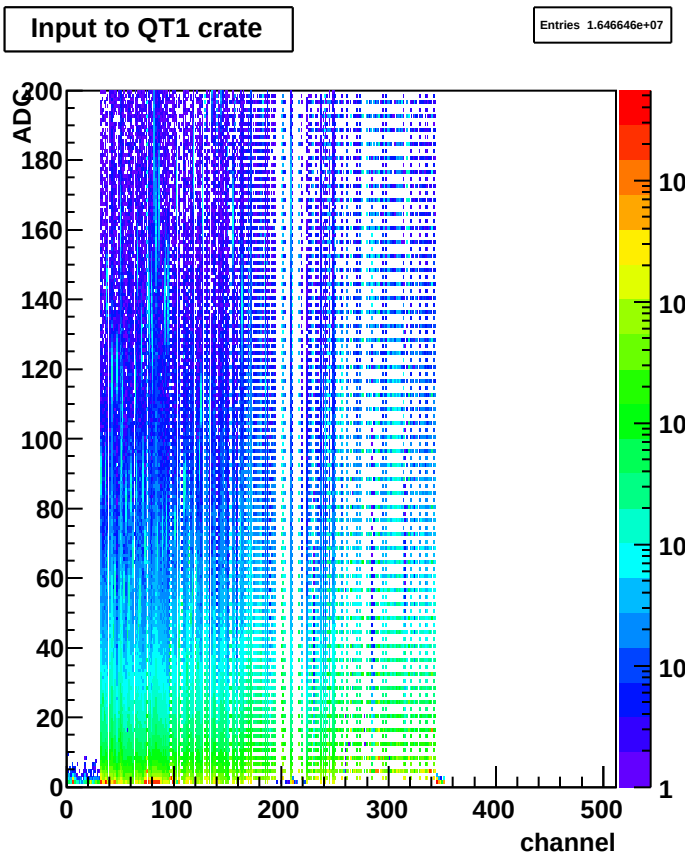

Entries 763617
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

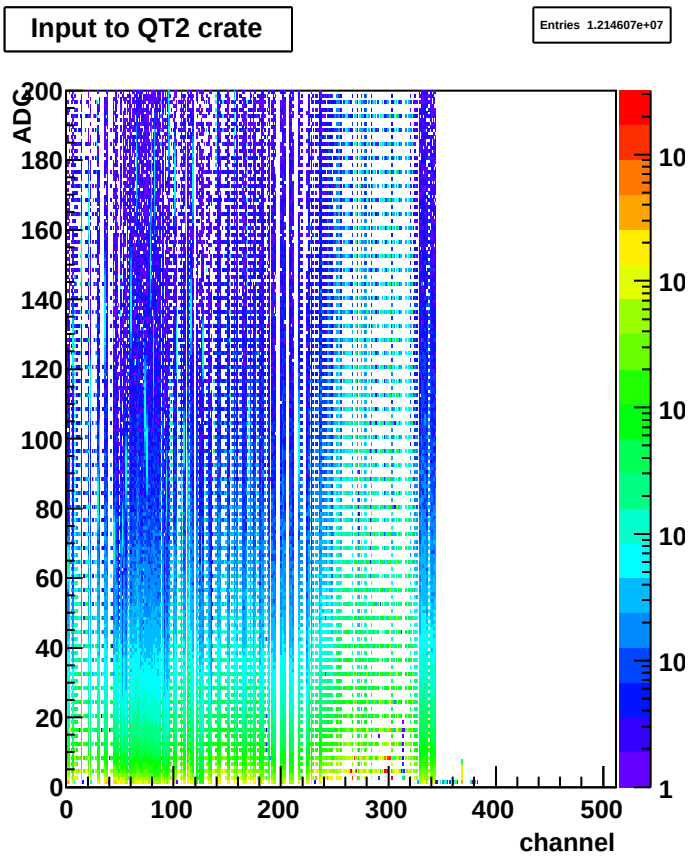

Entries 763617
BBC-L-East ADC Sum
BBC-L-West ADC Sum

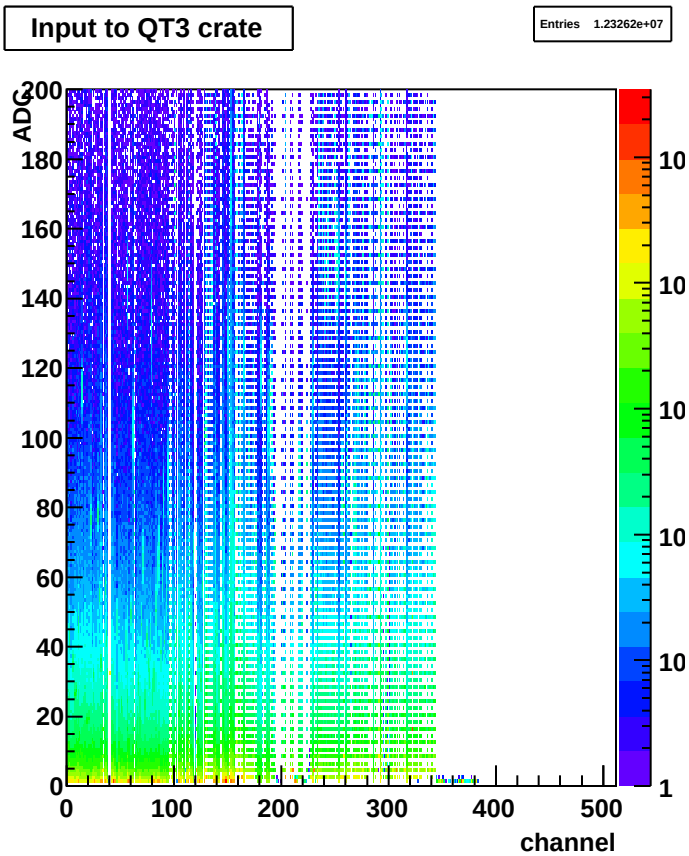

Entries 763617
BBC-S-East ADC Sum
BBC-S-West ADC Sum

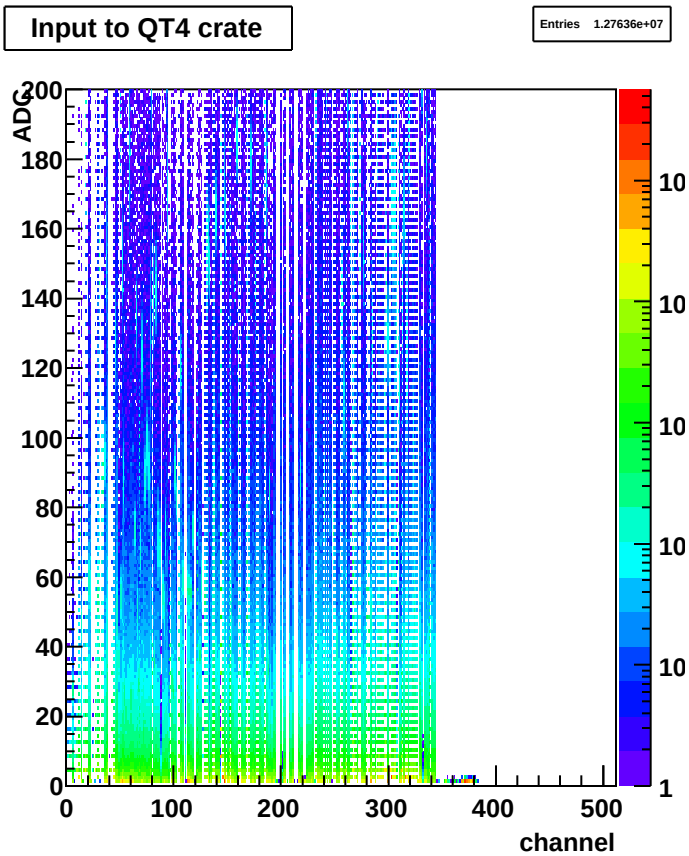

Entries 763617
ZDC East ADC Sum Att
ZDC West ADC Sum Att

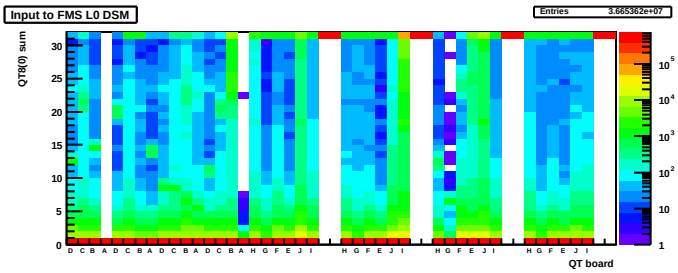

Entries 763617
BBC-L TAC Diff
BCC-S TAC Diff
Entries 763617
BBC-L TAC Diff
VPD TAC Diff
Entries 763617
BBC-L TAC Diff
ZDC TAC Diff
Entries 763617
BBC-S TAC Diff
VPD TAC Diff
Entries 763617
BBC-S TAC Diff
ZDC TAC Diff
Entries 763617
VPD TAC Diff
ZDC TAC Diff

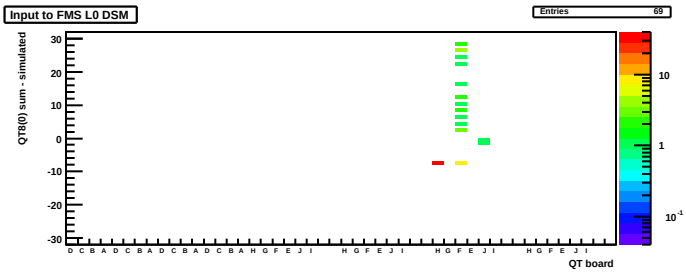

Input to QT1 crate
Entries 1.646646e+07
ADC
channel

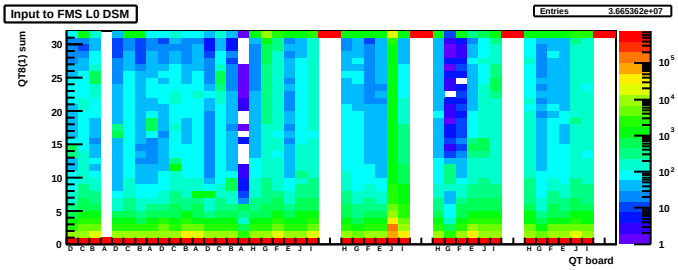

Input to QT2 crate
Entries 1.214607e+07
ADC
channel

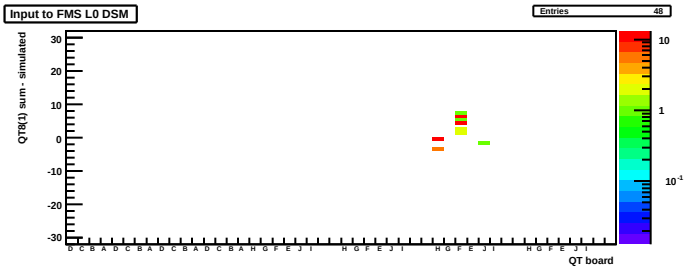

Input to QT3 crate
Entries 1.23262e+07
ADC
channel

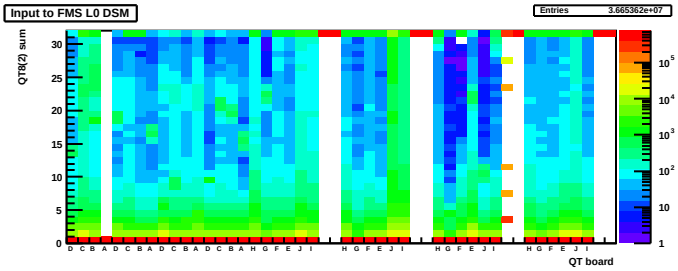

Input to QT4 crate
Entries 1.27636e+07
ADC
channel

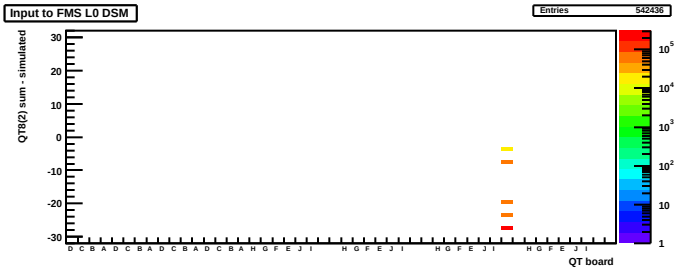

Input to FMS L0 DSM
Entries 3.665362e+07
QT8(0) sum
QT board

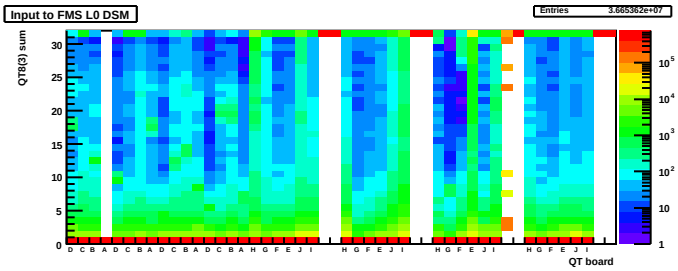

Input to FMS L0 DSM
Entries 69
QT8(0) sum - simulated
QT board

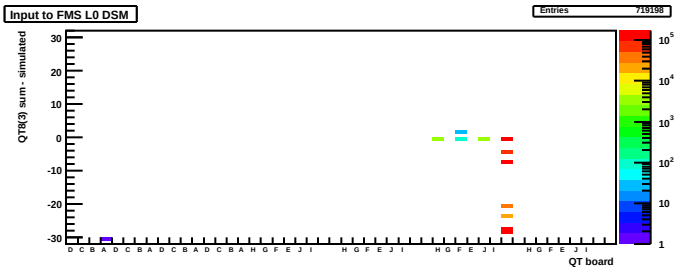

Input to FMS L0 DSM
Entries 3.665362e+07
QT8(1) sum
QT board

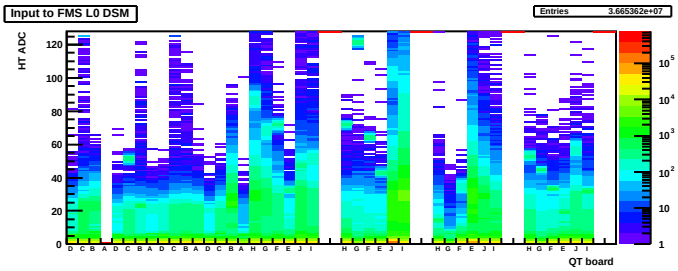

Input to FMS L0 DSM
Entries 48
QT8(1) sum - simulated
QT board

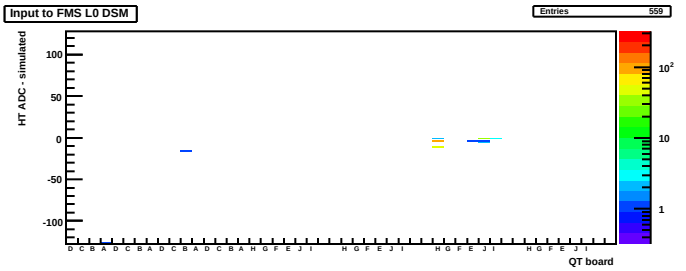

Input to FMS L0 DSM
Entries 3.665362e+07
QT8(2) sum
QT board

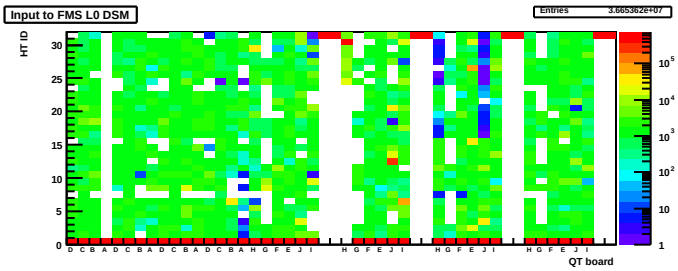

Input to FMS L0 DSM
Entries 542436
QT8(2) sum - simulated
QT board

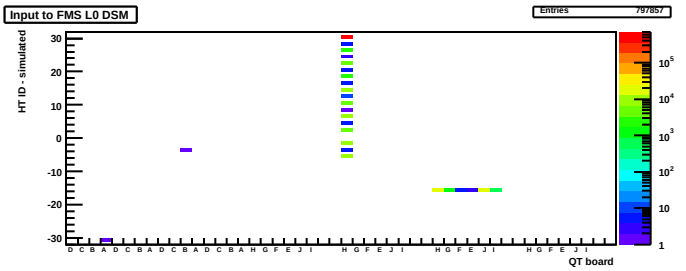

Input to FMS L0 DSM
Entries 3.665362e+07
QT8(3) sum
QT board

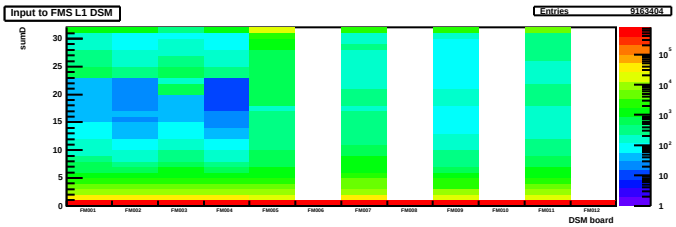

Input to FMS L0 DSM
Entries 719198
QT8(3) sum - simulated
QT board

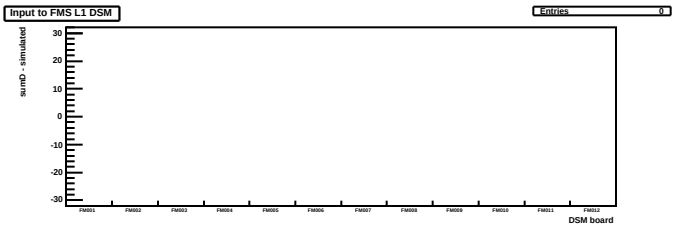

Input to FMS L0 DSM
Entries 3.665362e+07
HT ADC
QT board

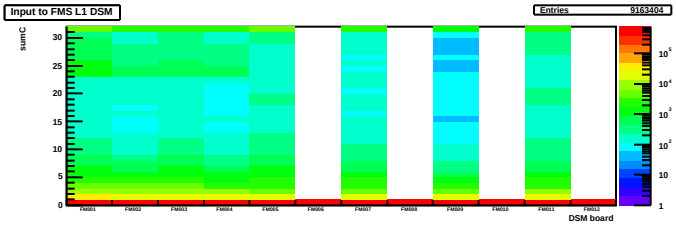

Input to FMS L0 DSM
Entries 559
HT ADC - simulated
QT board

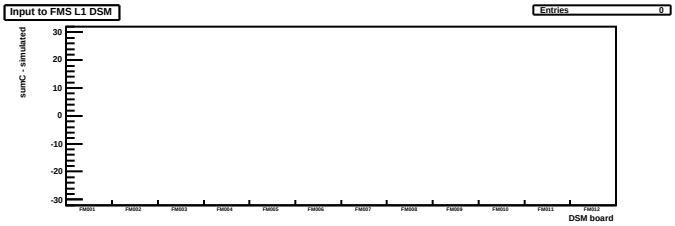

Input to FMS L0 DSM
Entries 3.665362e+07
HT ID
QT board

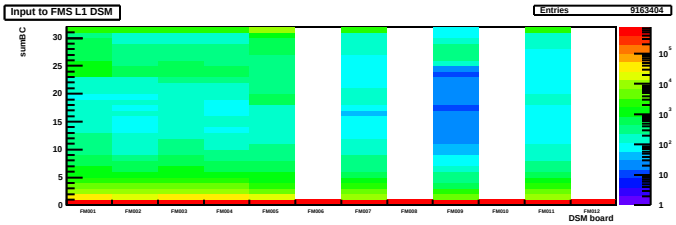

Input to FMS L0 DSM
Entries 797857
HT ID - simulated
QT board

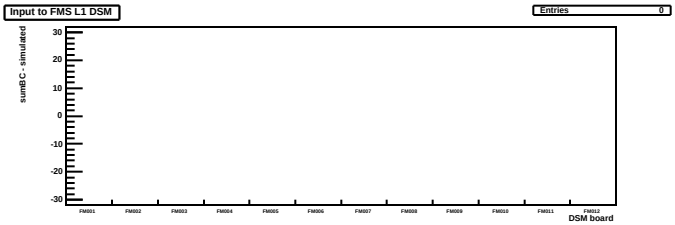

Input to FMS L1 DSM
Entries 9163404
sumD
DSM board

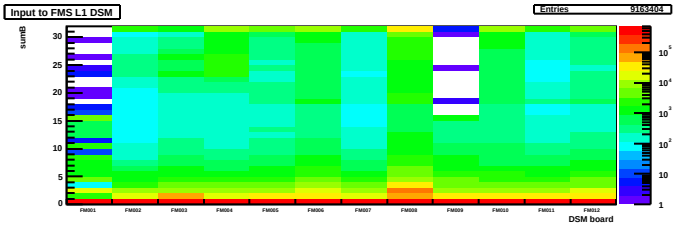

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

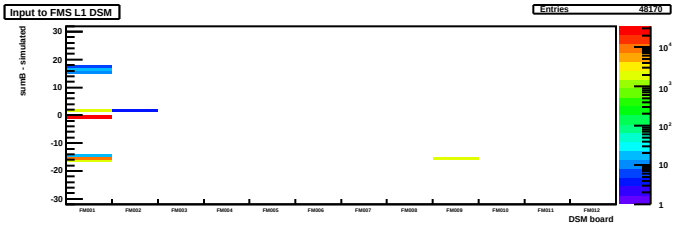

Input to FMS L1 DSM
Entries 9163404
sumC
DSM board

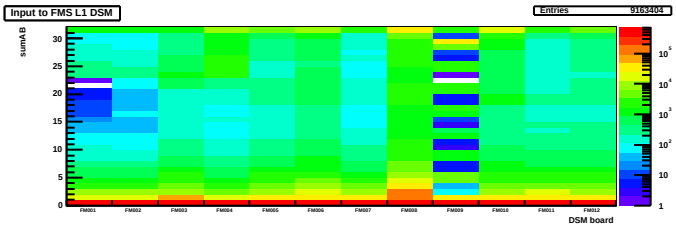

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

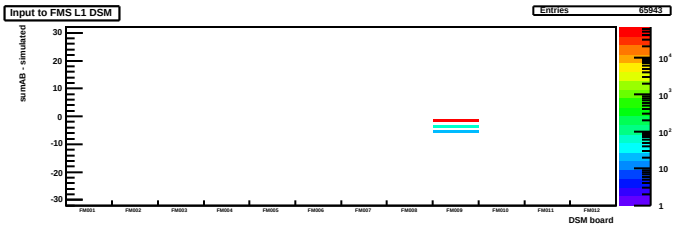

Input to FMS L1 DSM
Entries 9163404
sumBC
DSM board

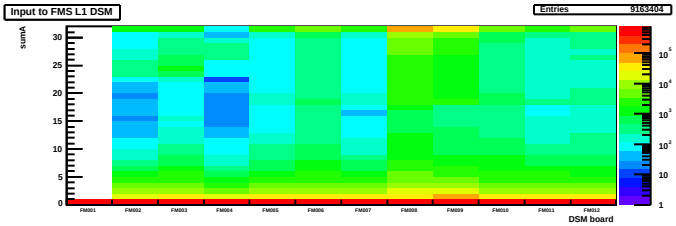

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

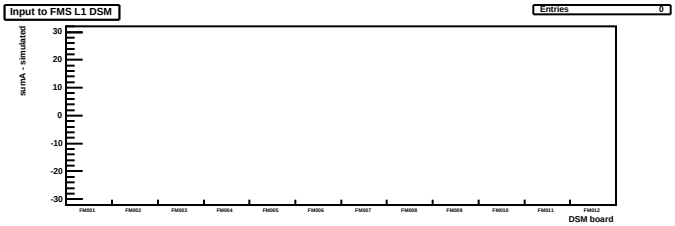

Input to FMS L1 DSM
Entries 9163404
sumB
DSM board

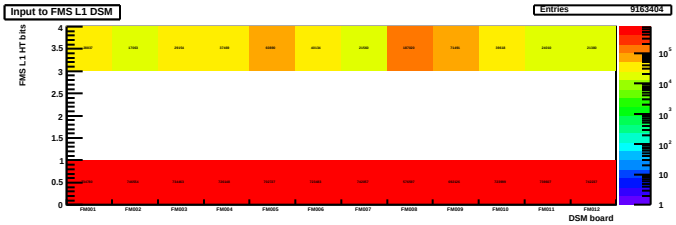

Input to FMS L1 DSM
Entries 48170
sumB - simulated
DSM board

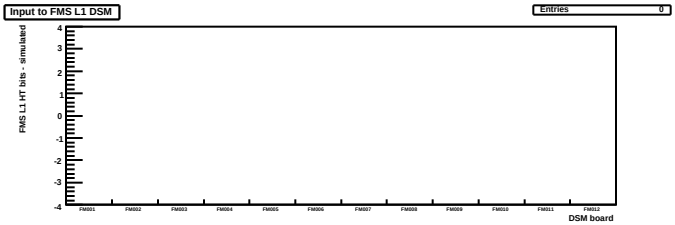

Input to FMS L1 DSM
Entries 9163404
sumAB
DSM board

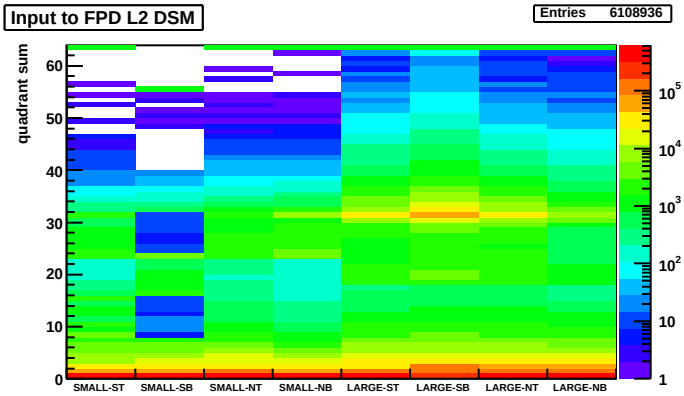

Input to FMS L1 DSM
Entries 65943
sumAB - simulated
DSM board

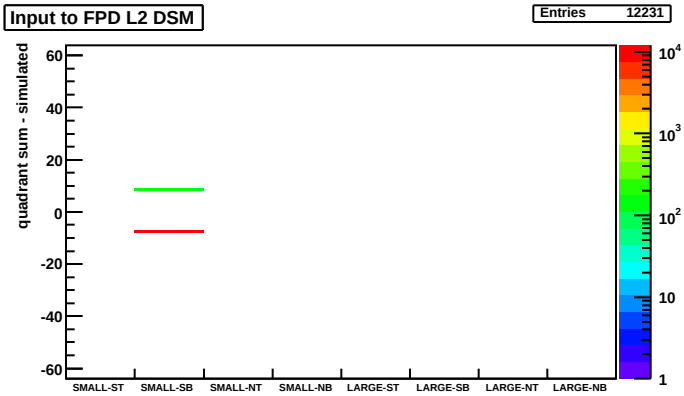

Input to FMS L1 DSM
Entries 9163404
sumA
DSM board

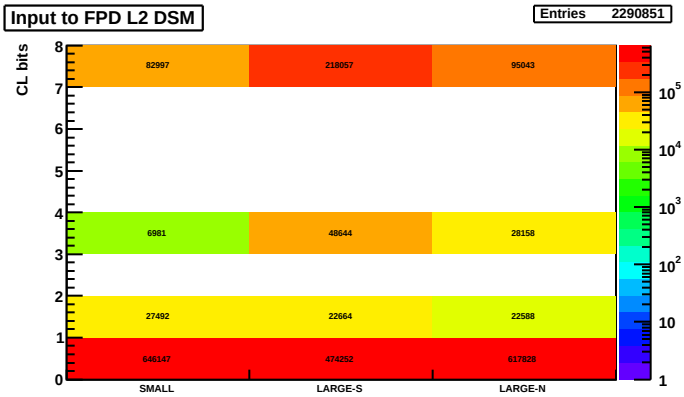

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

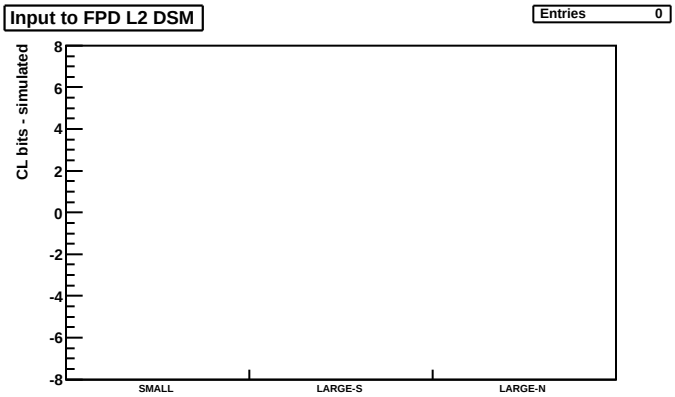

Input to FMS L1 DSM
Entries 9163404
FMS L1 HT bits
DSM board

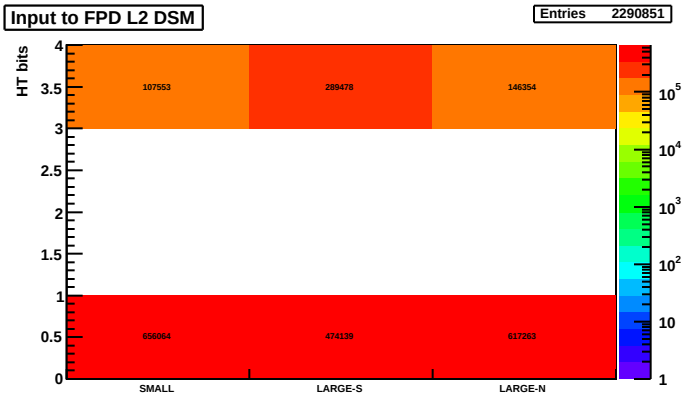

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

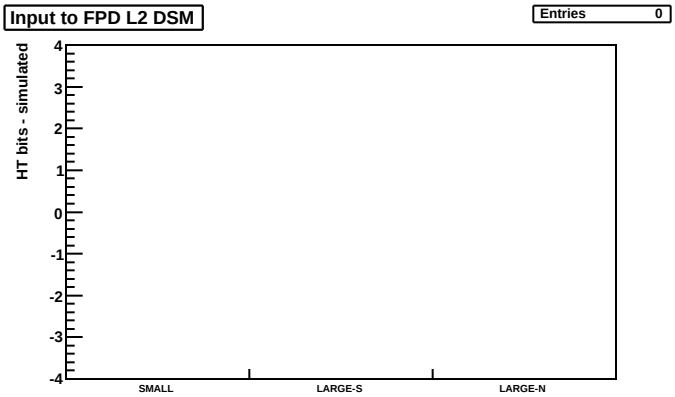

Input to FPD L2 DSM
Entries 6108936
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

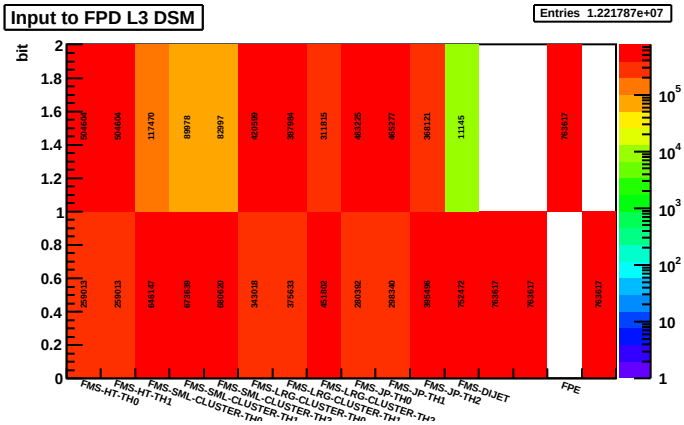

Input to FPD L2 DSM
Entries 12231
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

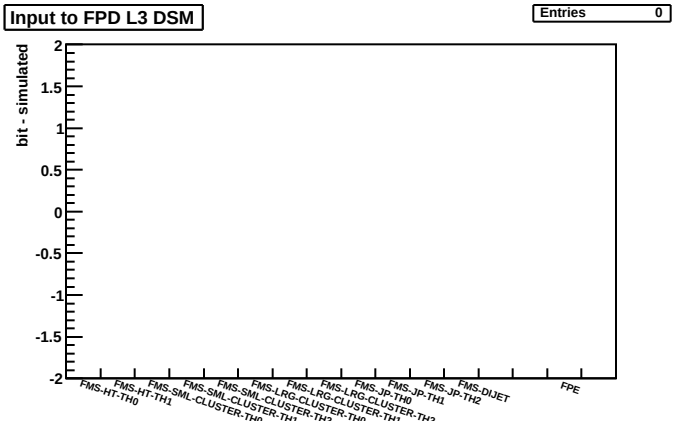

Input to FPD L2 DSM
Entries 2290851
CL bits
82997
218057
95043
6981
48644
28158
27492
22664
22588
646147
474252
617828
SMALL
LARGE-S
LARGE-N

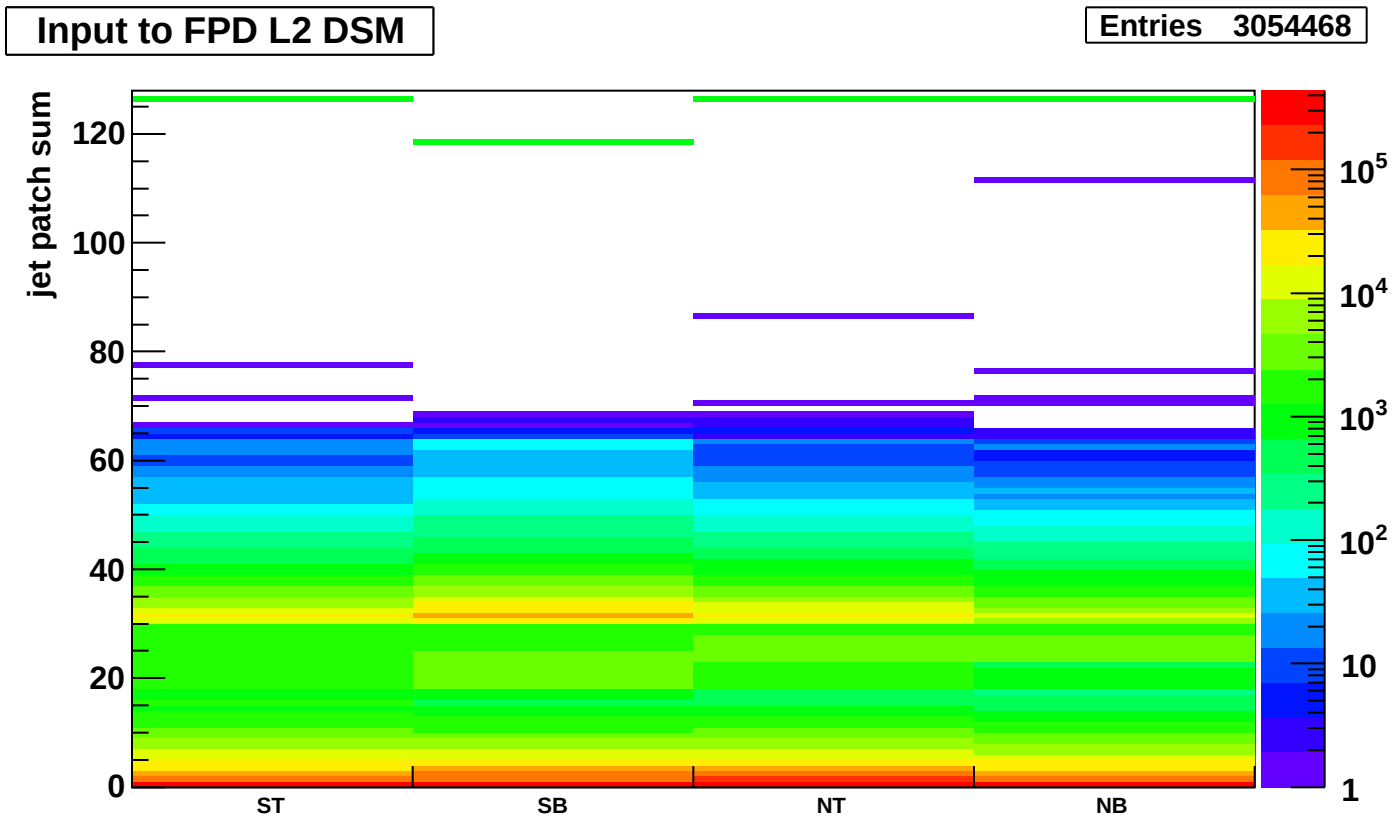

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

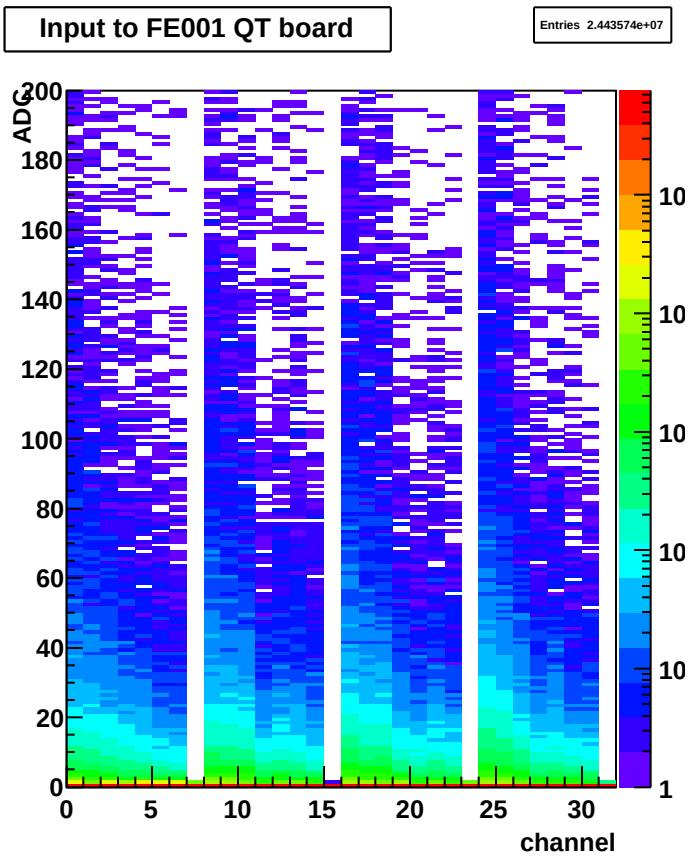

Input to FPD L2 DSM
Entries 2290851
HT bits
107553
289478
146354
656064
474139
617263
SMALL
LARGE-S
LARGE-N

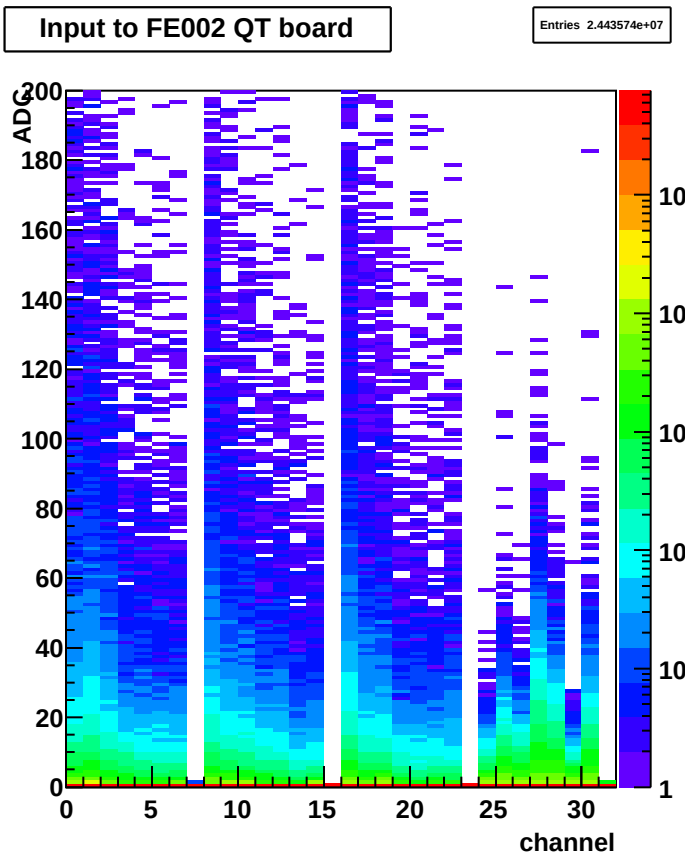

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

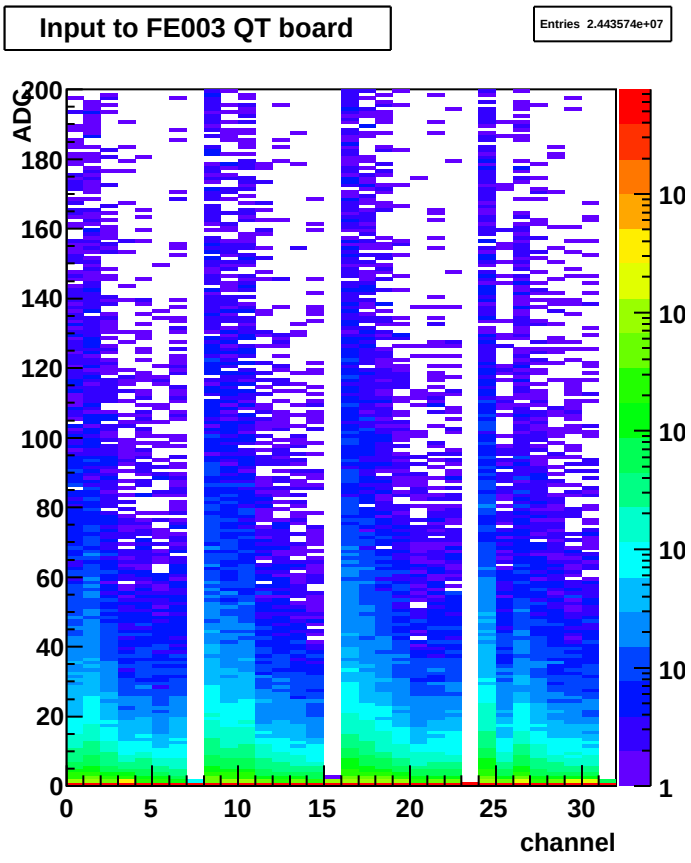

Input to FPD L3 DSM
Entries 1.221787e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

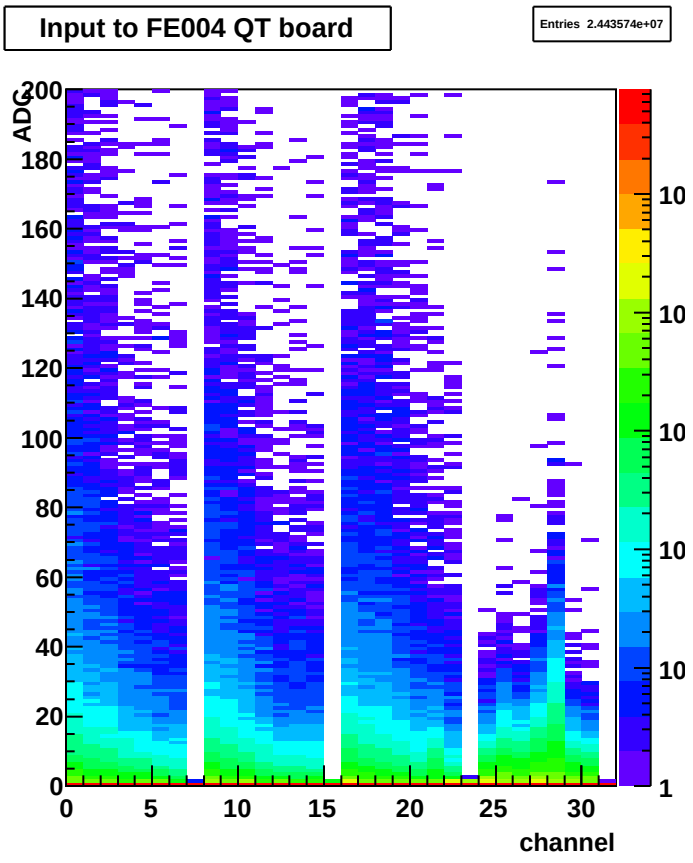

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

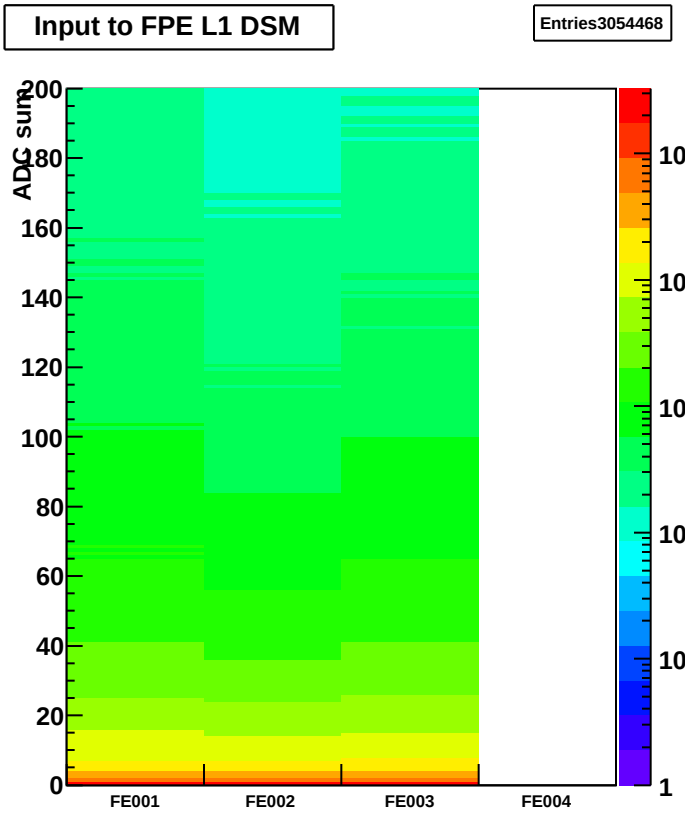

Input to FPD L2 DSM
Entries 3054468
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

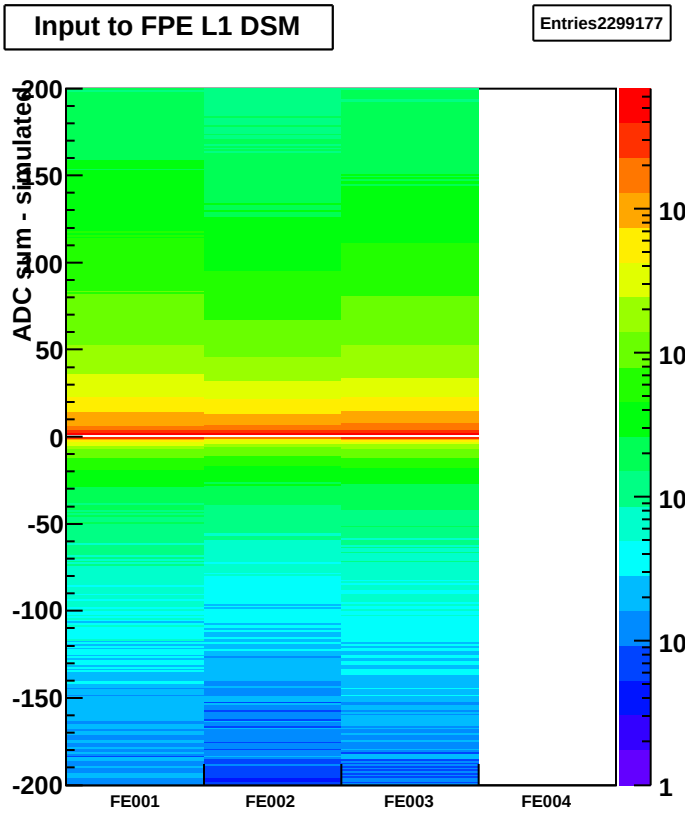

Input to FE001 QT board
Entries 2.443574e+07
ADC
channel

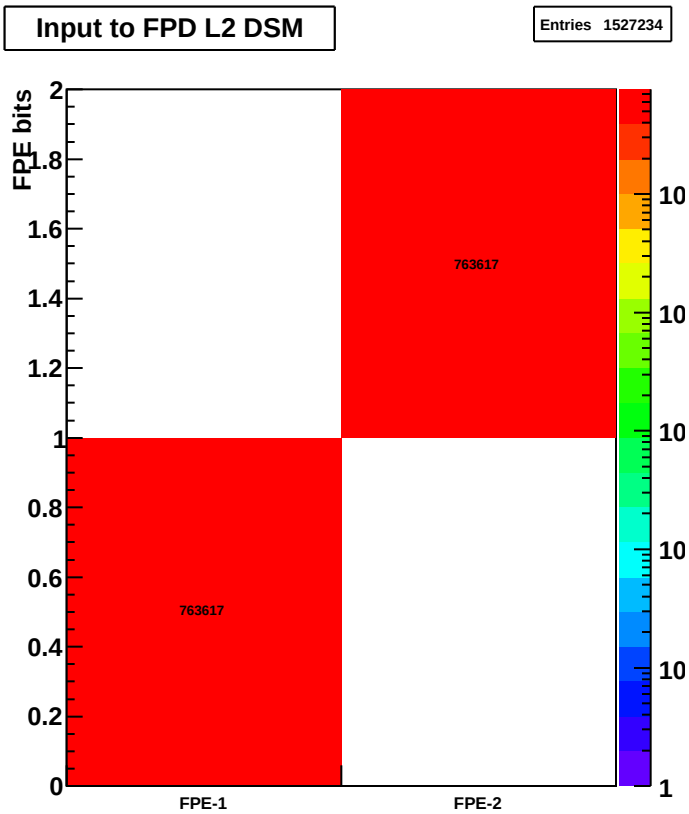

Input to FE002 QT board
Entries 2.443574e+07
ADC
channel

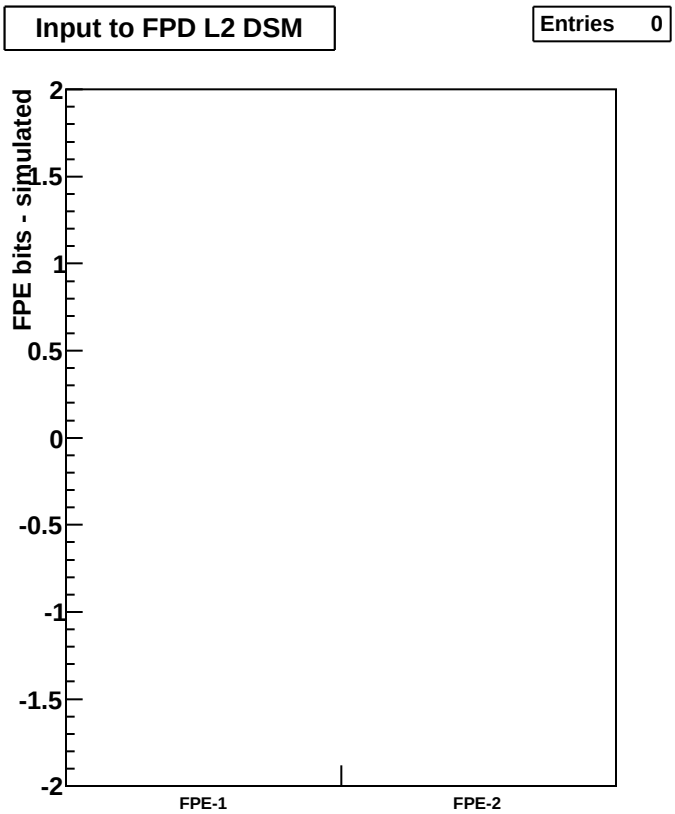

Input to FE003 QT board
Entries 2.443574e+07
ADC
channel


Input to FE004 QT board
Entries 2.443574e+07
ADC
channel


Input to FPE L1 DSM
Entries3054468
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries2299177
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1527234
FPE bits
763617
763617
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2