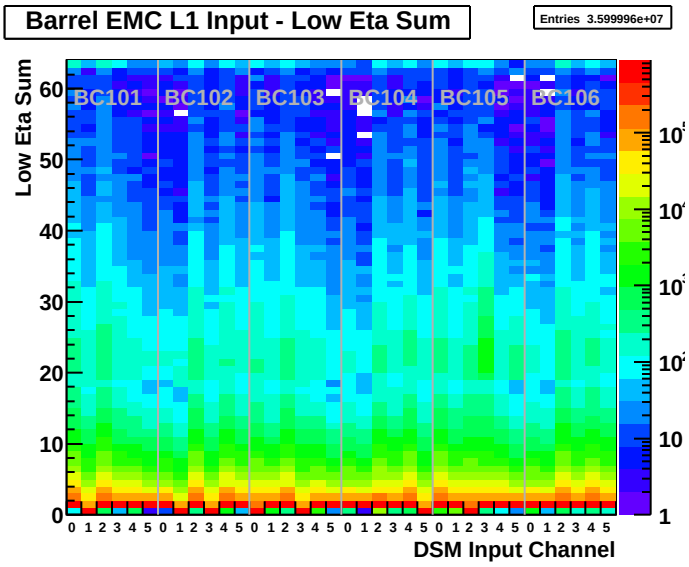
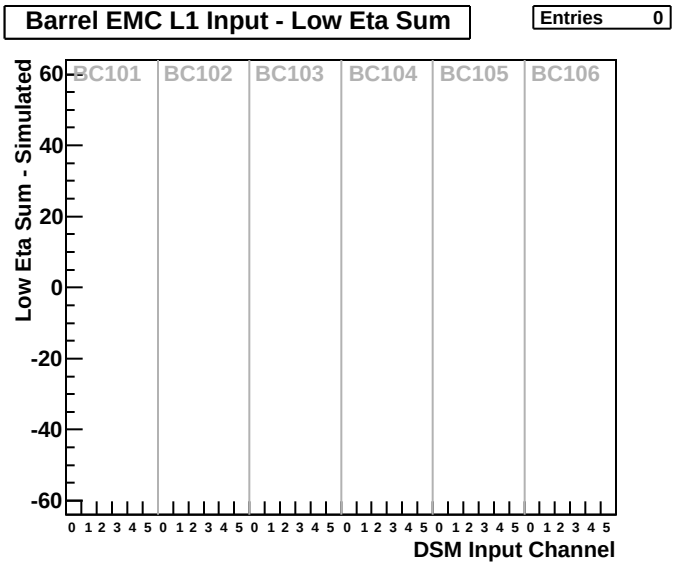
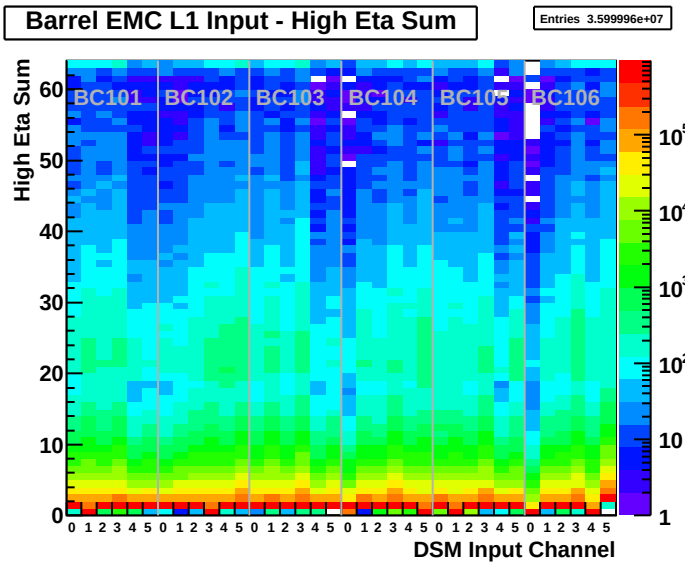
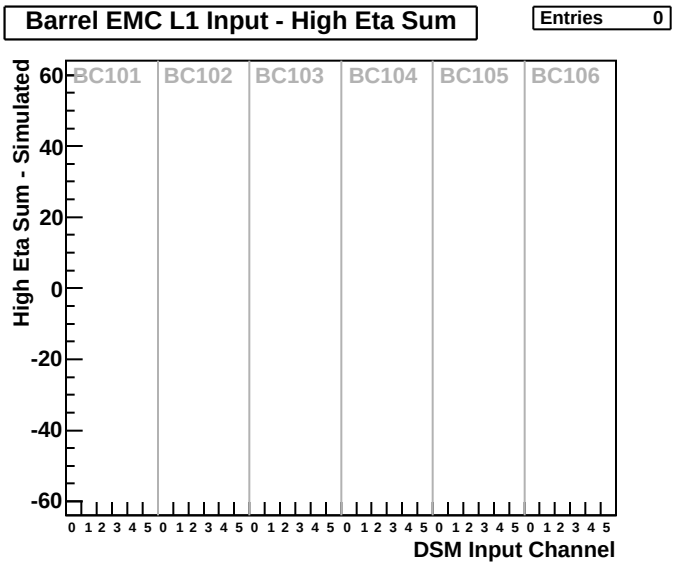
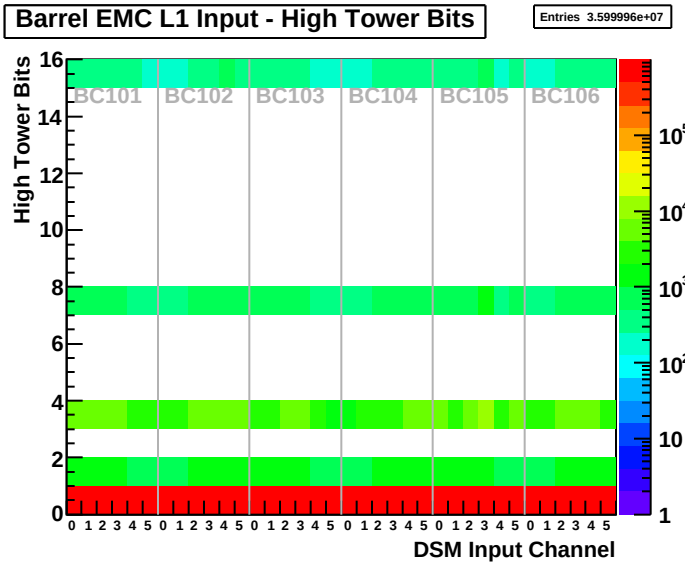
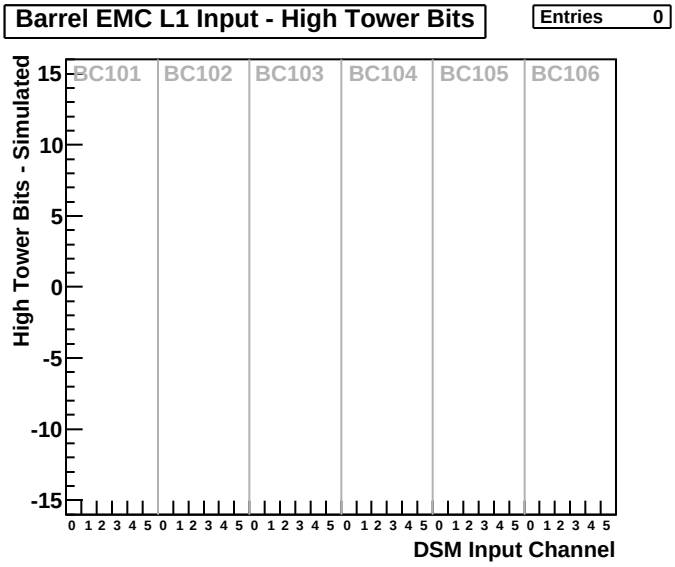

Barrel EMC L1 Input - Low Eta Sum
Entries 3.599996e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

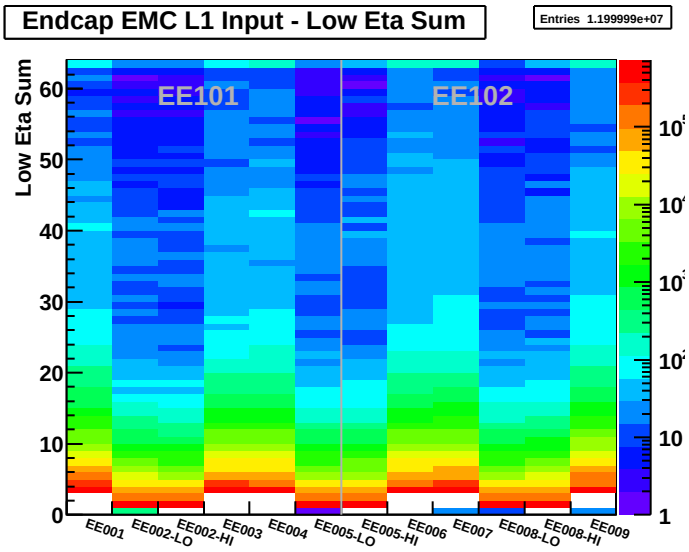

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

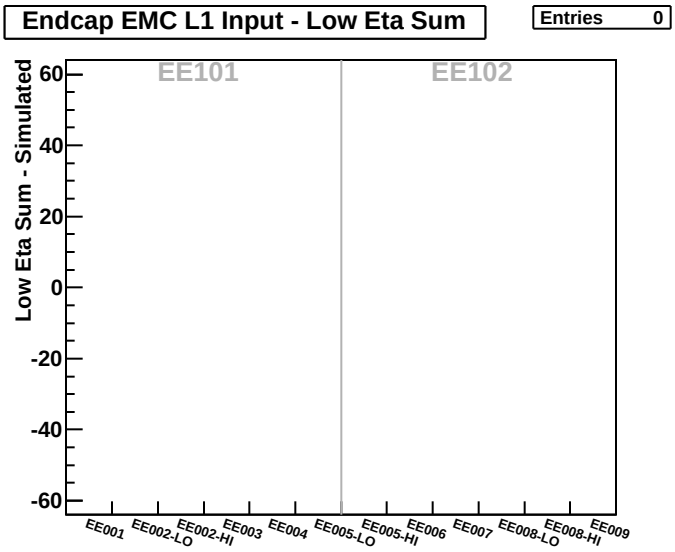

Barrel EMC L1 Input - High Eta Sum
Entries 3.599996e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

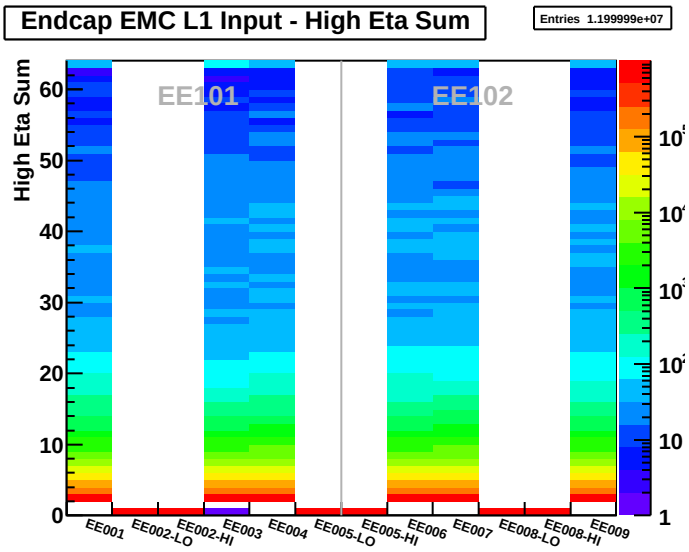

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

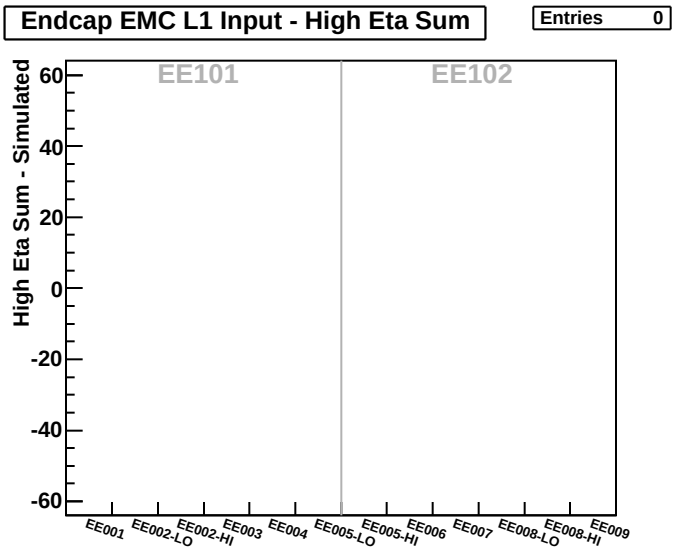

Barrel EMC L1 Input - High Tower Bits
Entries 3.599996e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

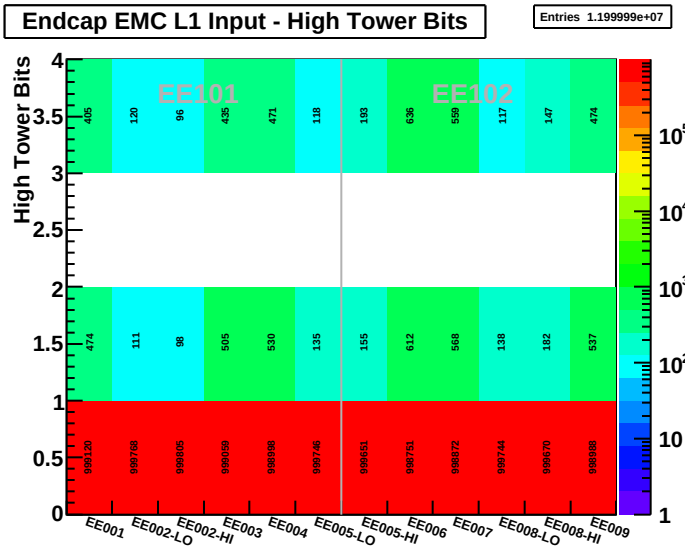

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

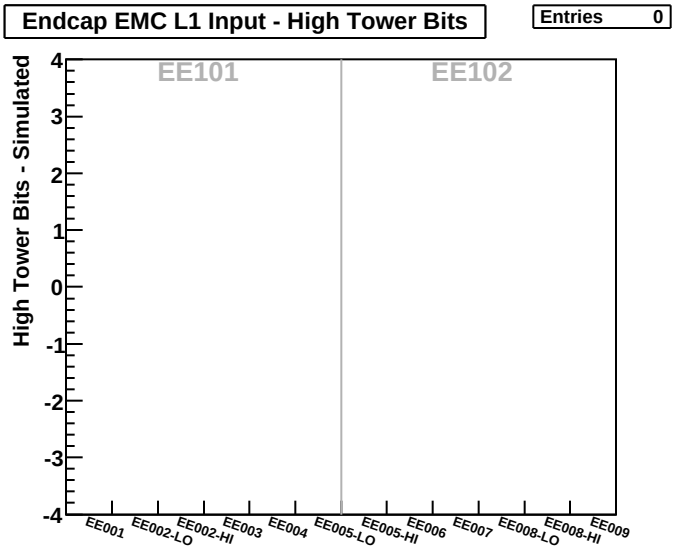

Endcap EMC L1 Input - Low Eta Sum
Entries 1.199999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

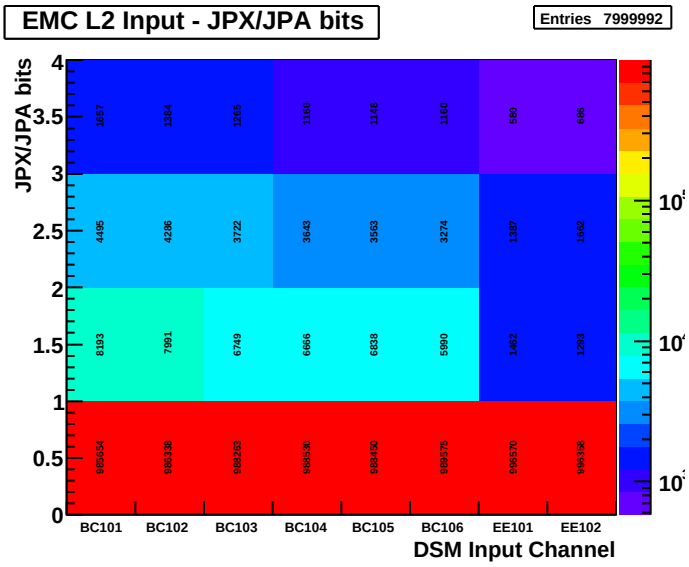

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

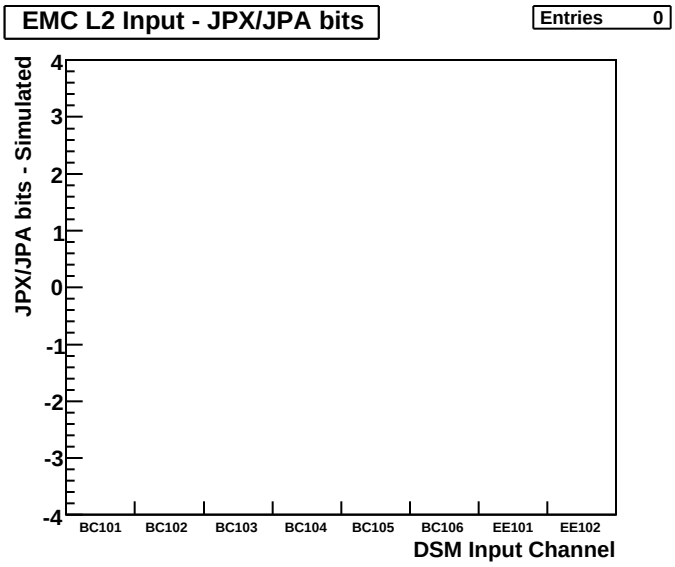

Endcap EMC L1 Input - High Eta Sum
Entries 1.199999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

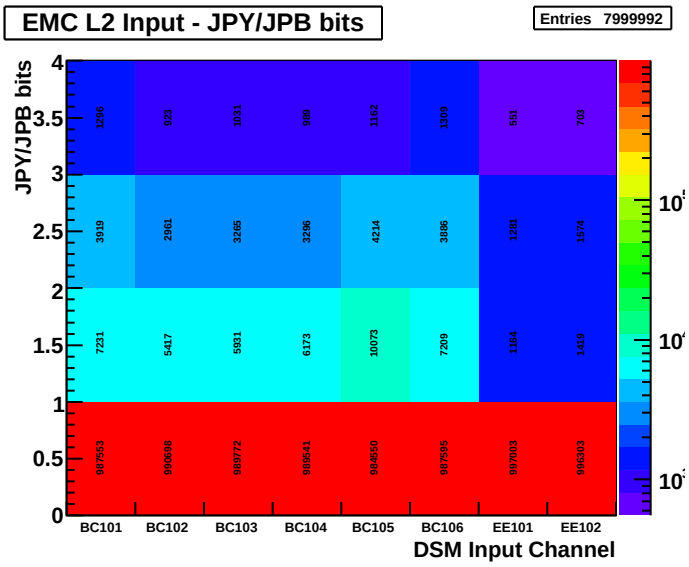

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

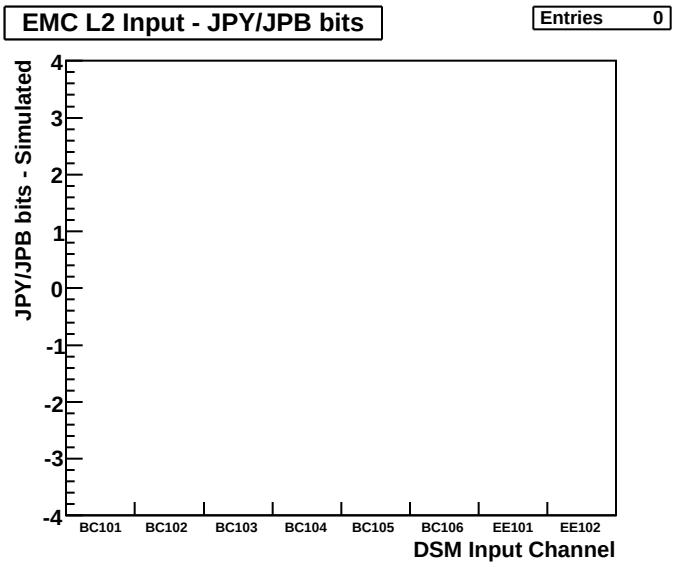

Endcap EMC L1 Input - High Tower Bits
Entries 1.199999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

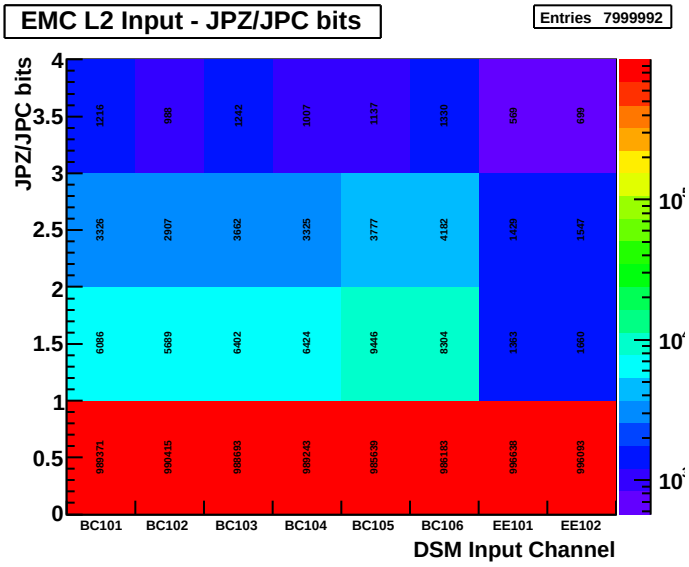

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

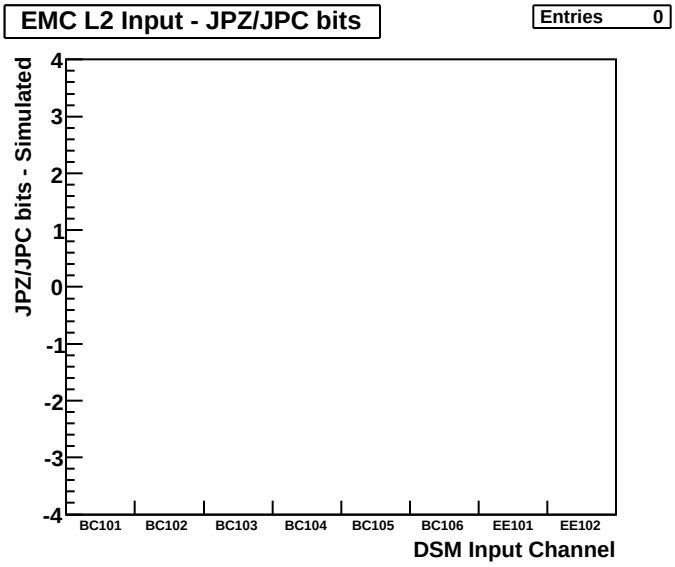

EMC L2 Input - JPX/JPA bits
Entries 7999992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1657 1384 1265 1160 1148 1160 580 686
4495 4286 3722 3643 3563 3274 1387 1662
8193 7991 6749 6666 6838 5990 1462 1293
985654 988338 988263 988530 988450 989575 999570 996338

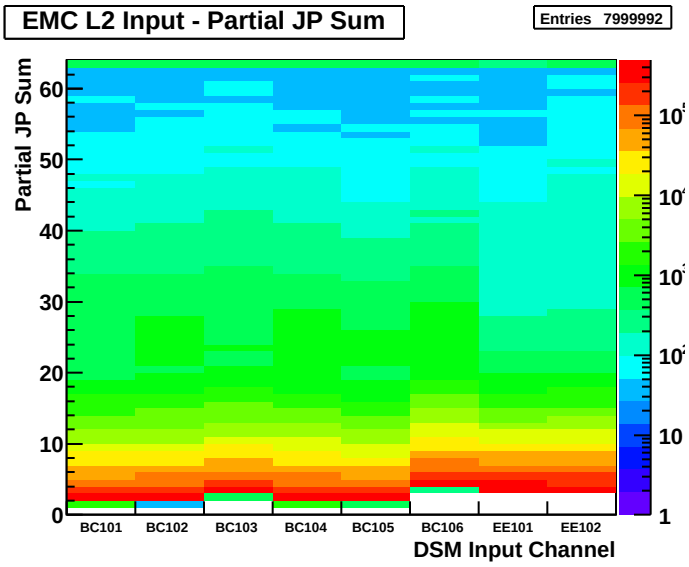

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

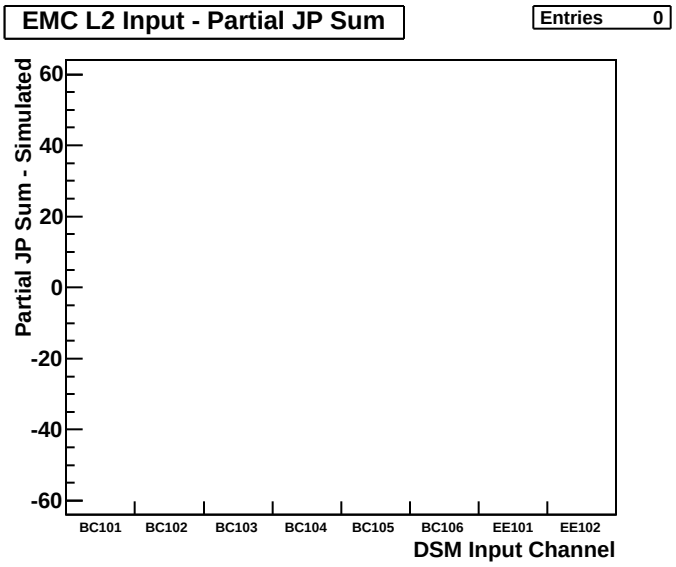

EMC L2 Input - JPY/JPB bits
Entries 7999992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1296 922 1031 988 1162 1309 551 703
3919 2961 3265 3296 4214 3886 1281 1574
7231 5417 5931 6173 10073 7209 1164 1419
987553 990698 989772 989541 984550 987595 997003 996303

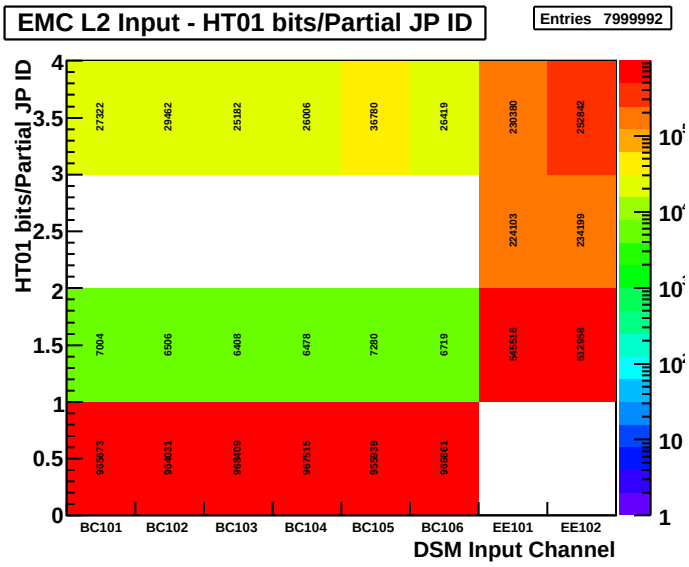

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

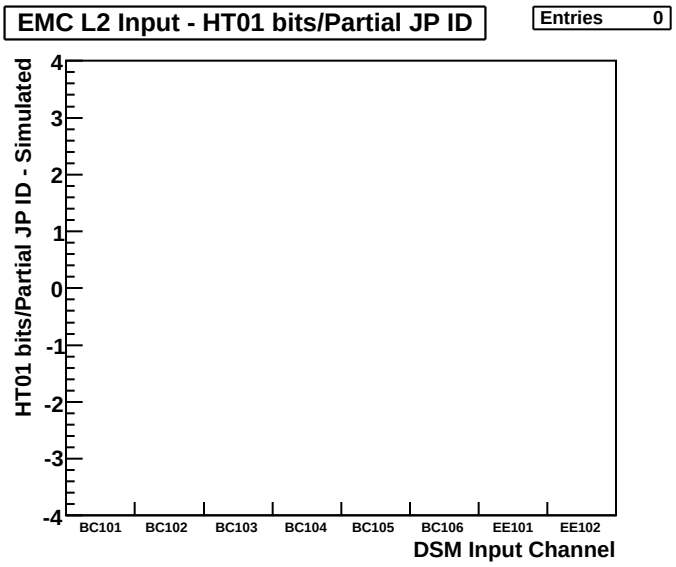

EMC L2 Input - JPZ/JPC bits
Entries 7999992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1216 988 1242 1007 1137 1339 569 699
3326 2907 3662 3325 3777 4182 1429 1547
6086 5689 6402 6424 9446 8304 1383 1660
989371 990415 988693 989243 985639 986183 996538 996093

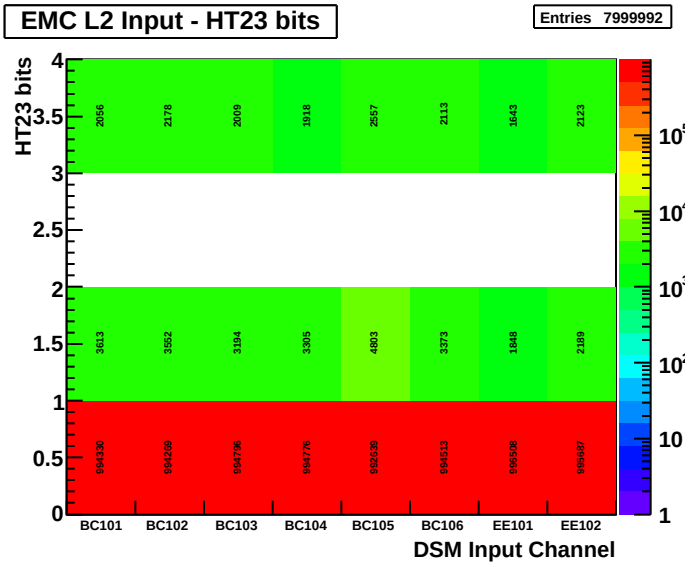

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

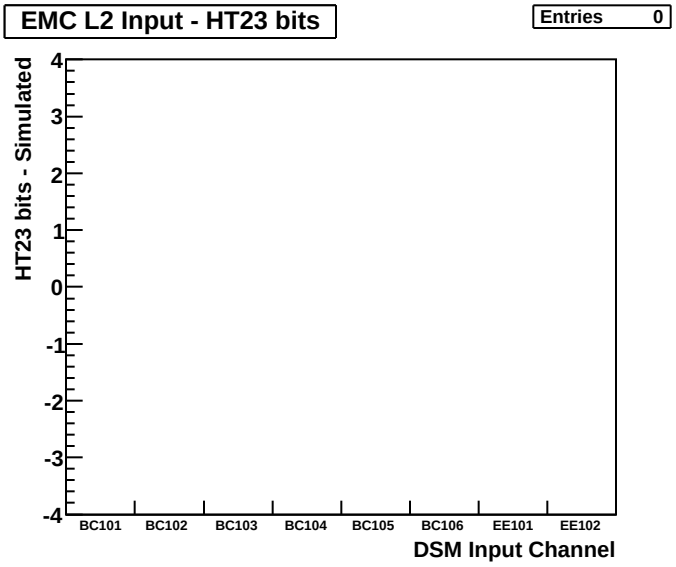

EMC L2 Input - Partial JP Sum
Entries 7999992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

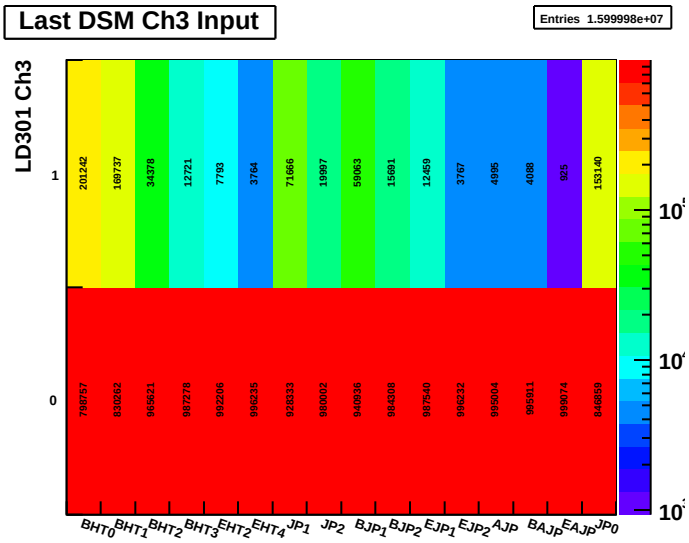

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

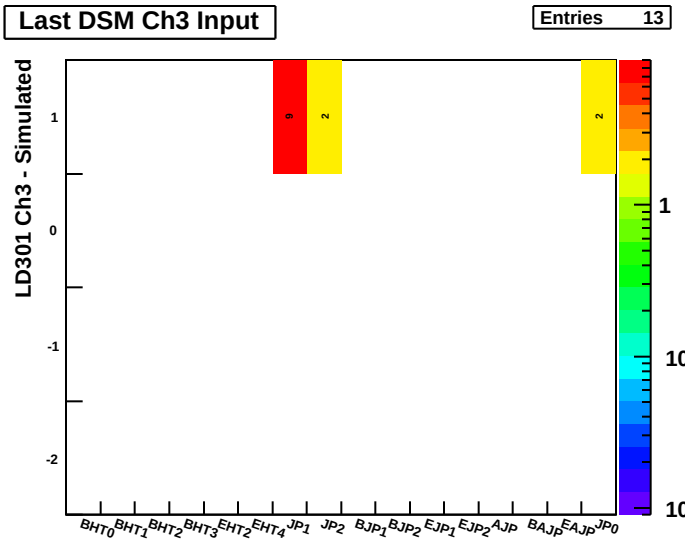

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7999992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

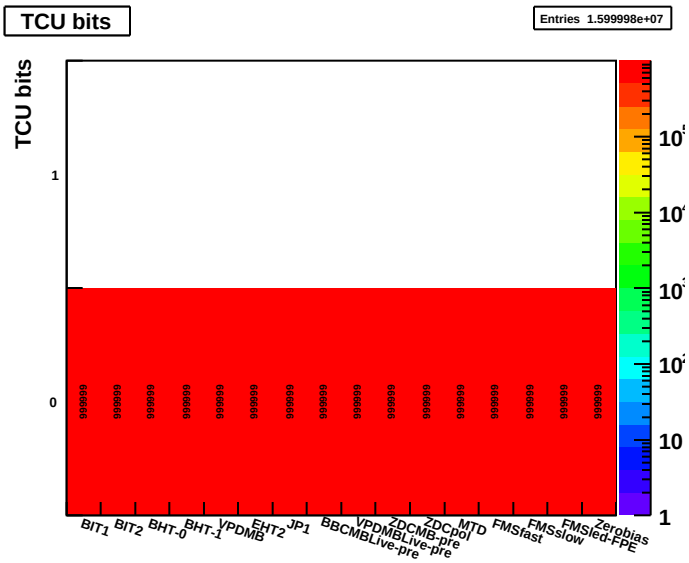

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

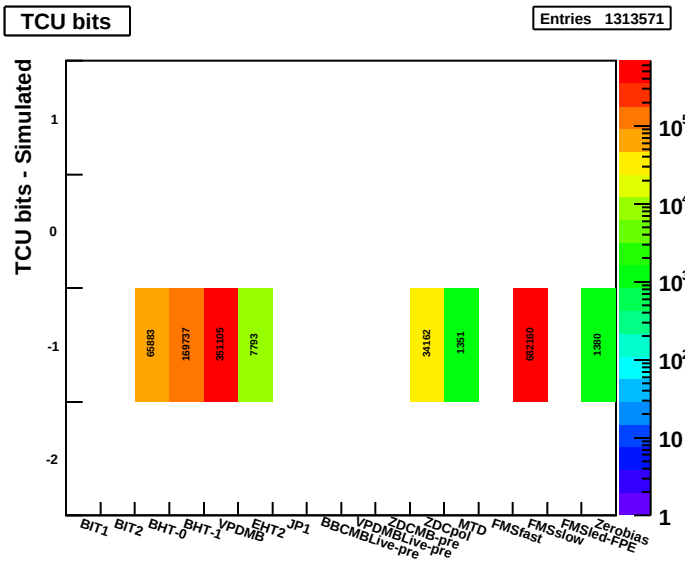

EMC L2 Input - HT23 bits
Entries 7999992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

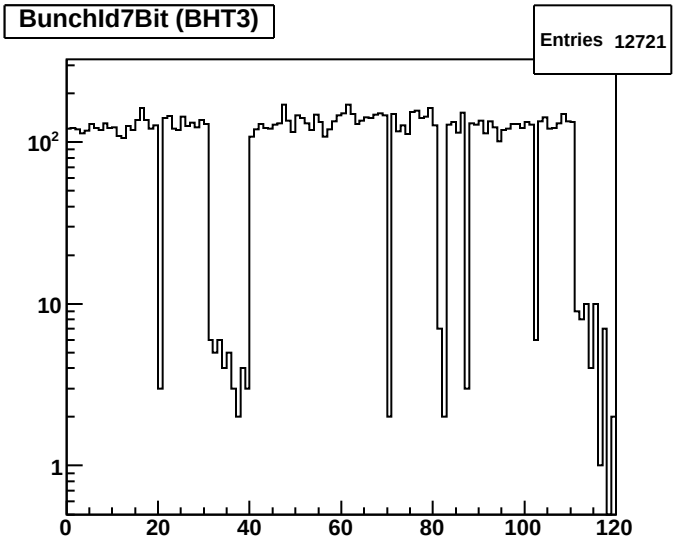

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

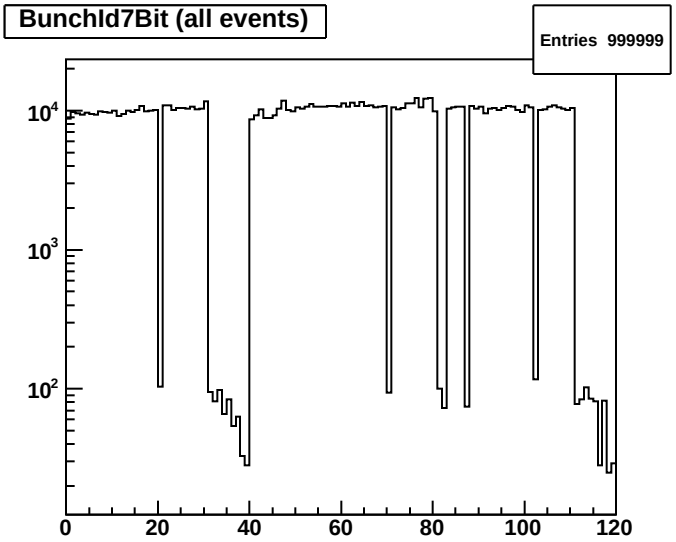

Last DSM Ch3 Input
Entries 1.599998e+07
LD301 Ch3
201242 169737 34378 12721 7793 3764 71666 19997 59063 15691 12459 3767 4995 4088 925 153140
798757 830262 965621 987278 992206 996235 928333 980002 940936 984308 987540 996232 995004 995911 999074 846859
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

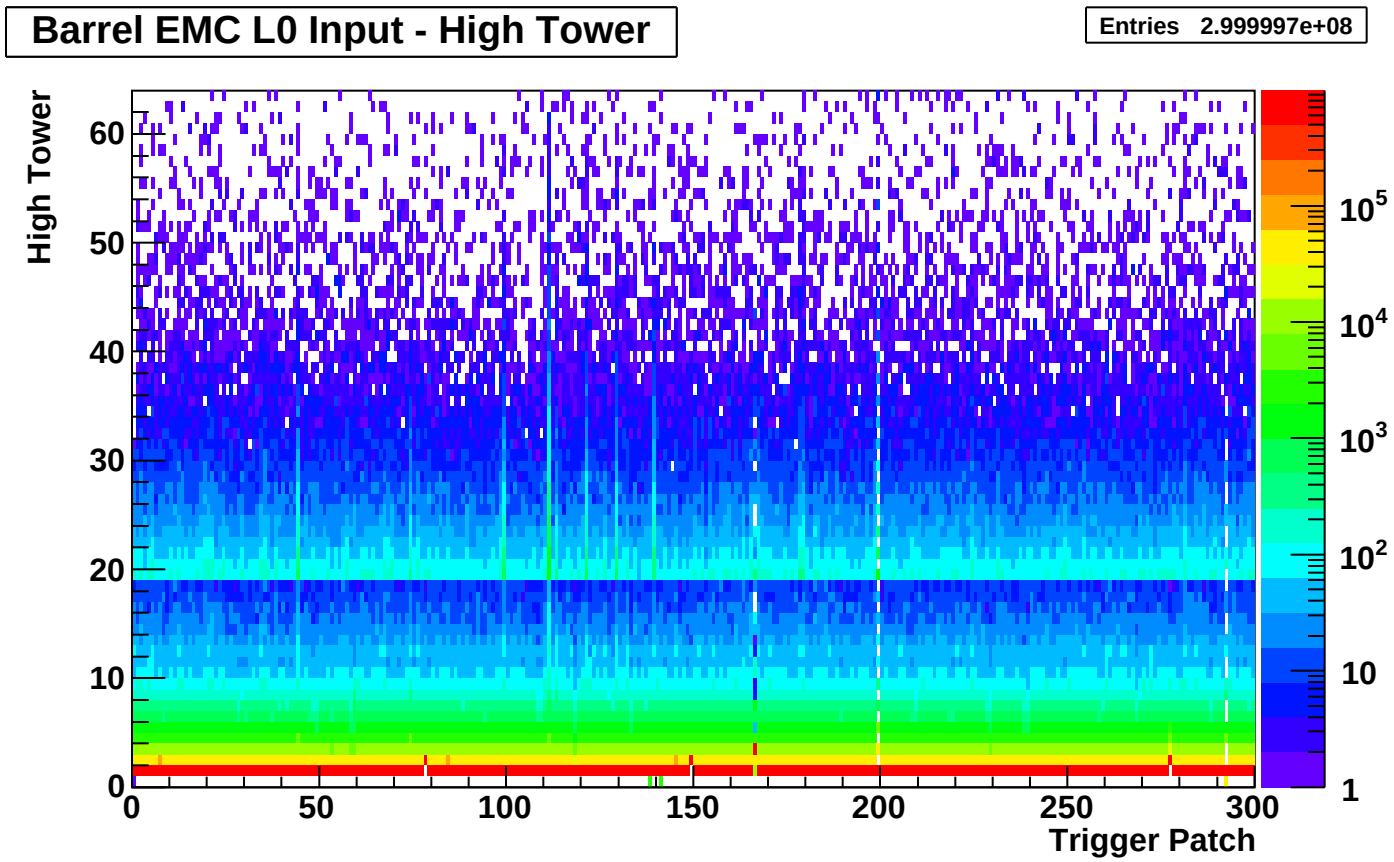

Last DSM Ch3 Input
Entries 13
LD301 Ch3 - Simulated
9 2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

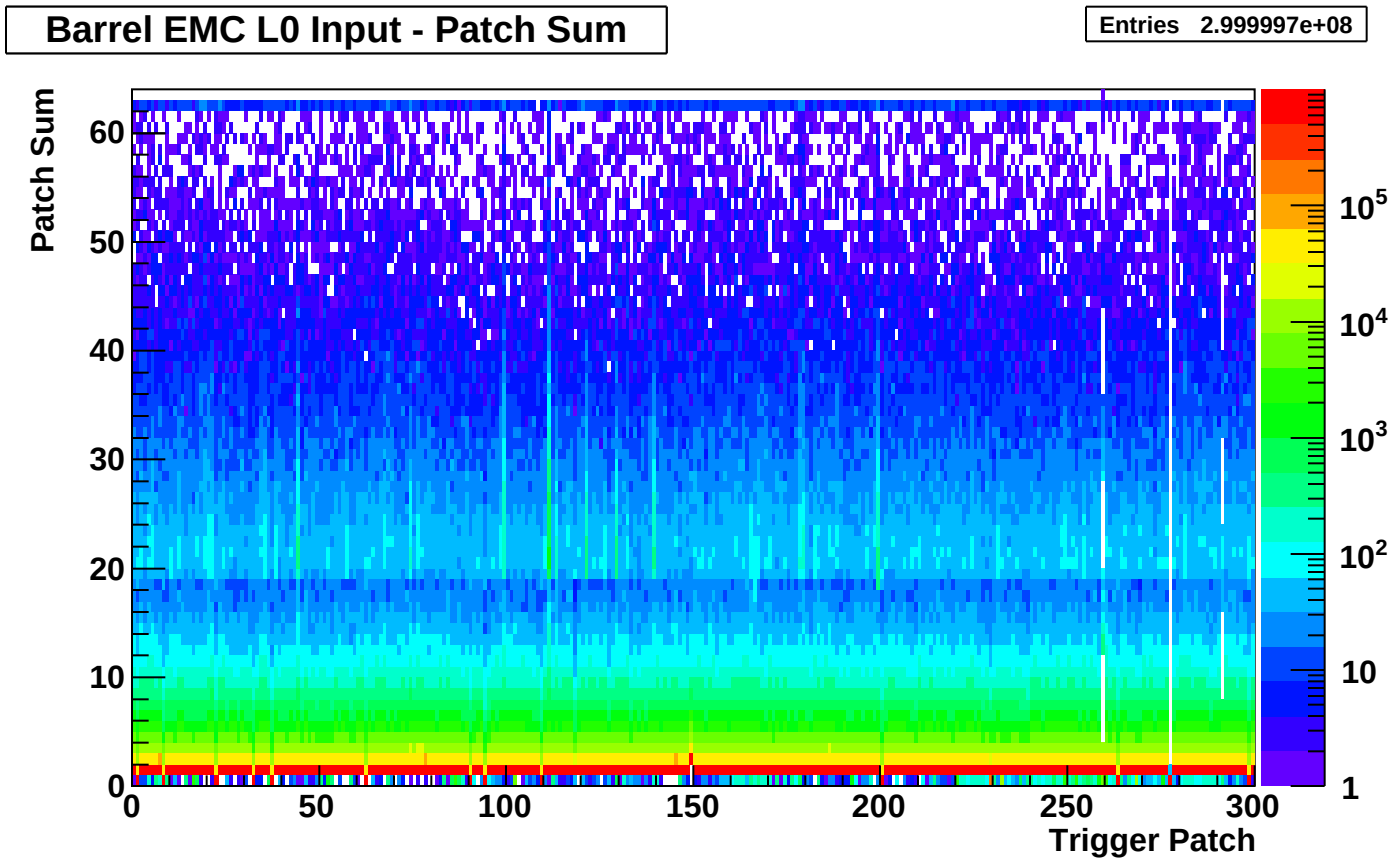

TCU bits
Entries 1.599998e+07
TCU bits
999999
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

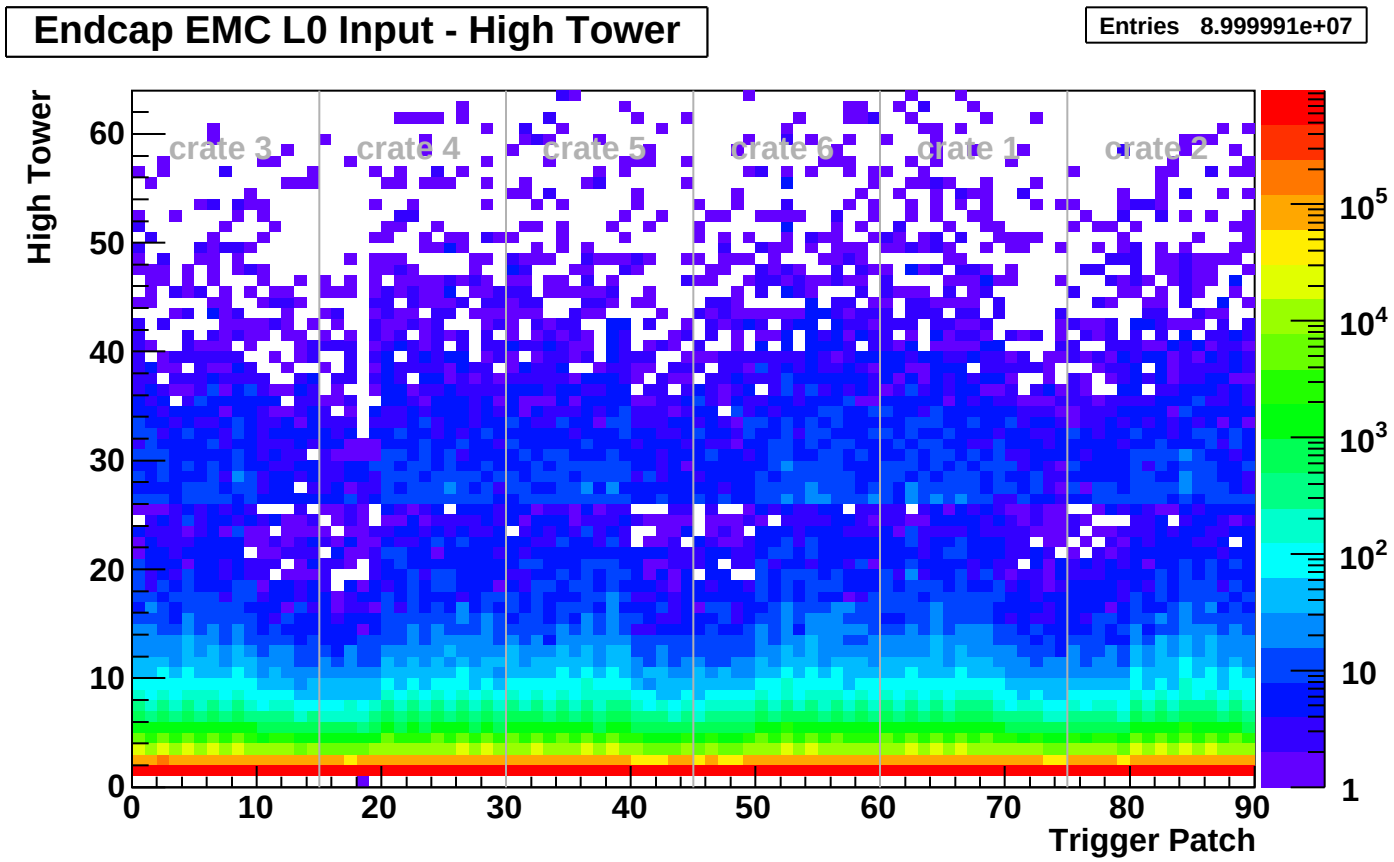

TCU bits
Entries 1313571
TCU bits - Simulated
65883 169737 351105 7793 34162 1351 682160 1380
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

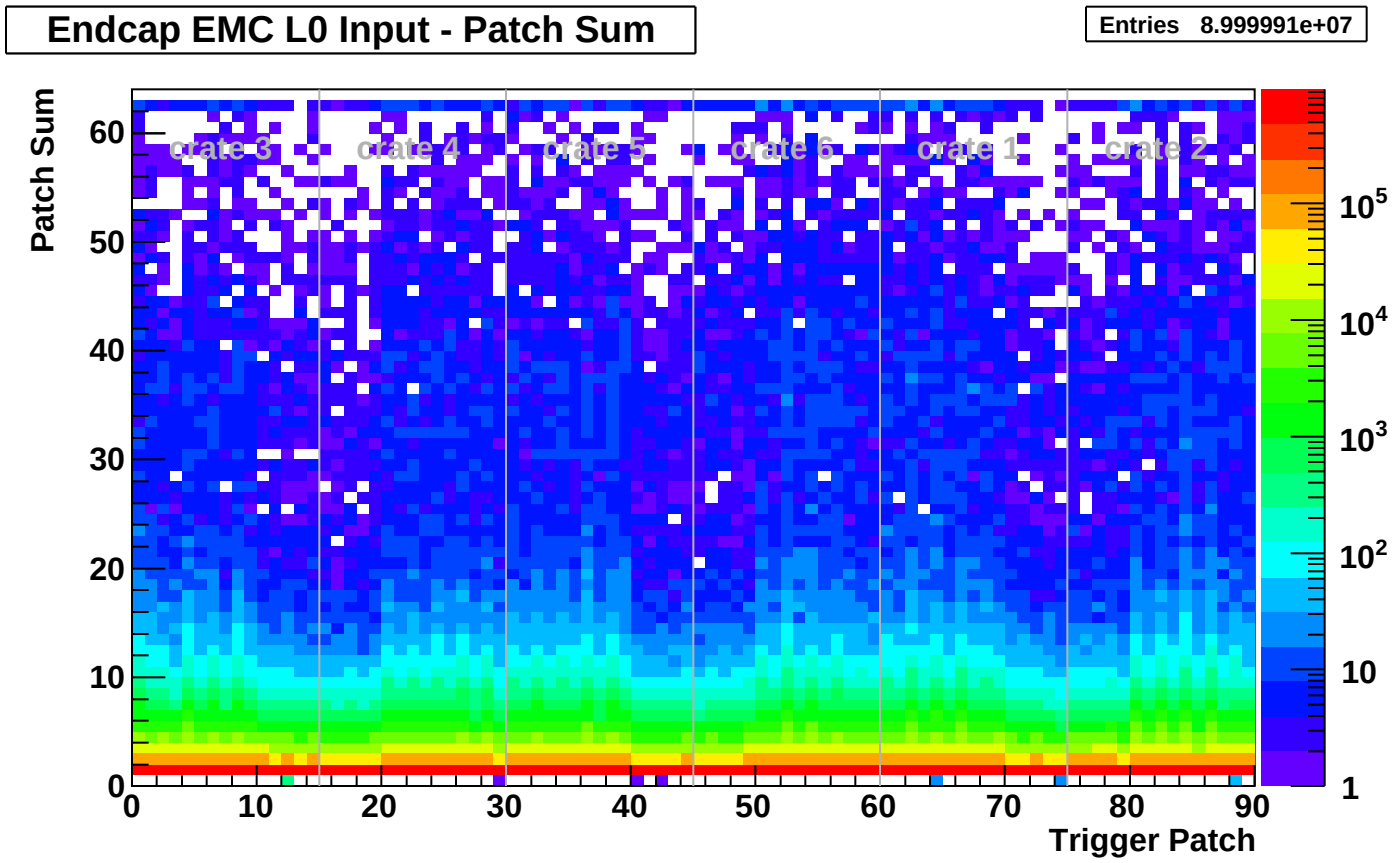

BunchId7Bit (BHT3)
Entries 12721

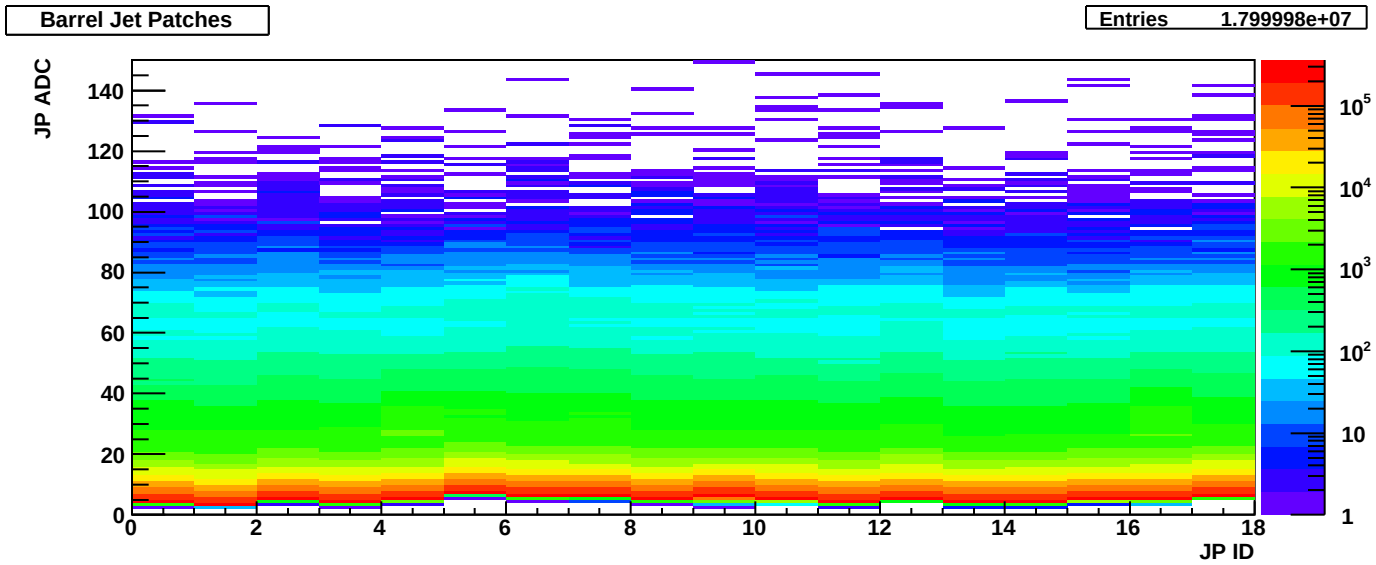

BunchId7Bit (all events)
Entries 999999

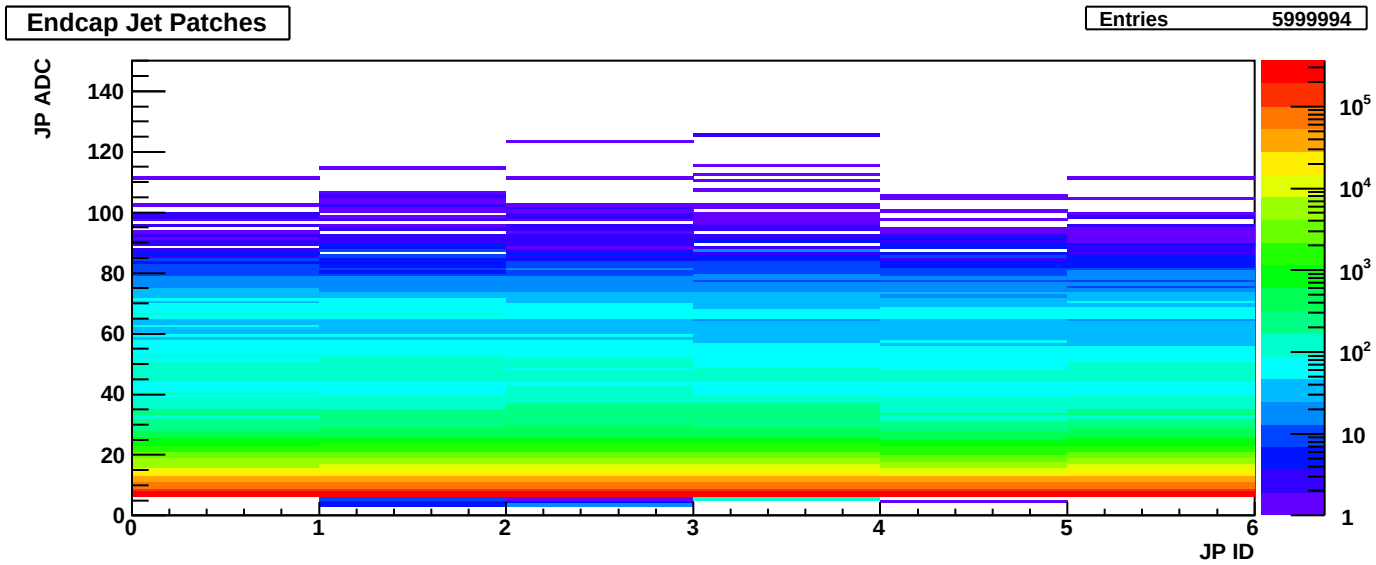

Barrel EMC L0 Input - High Tower
Entries 2.999997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

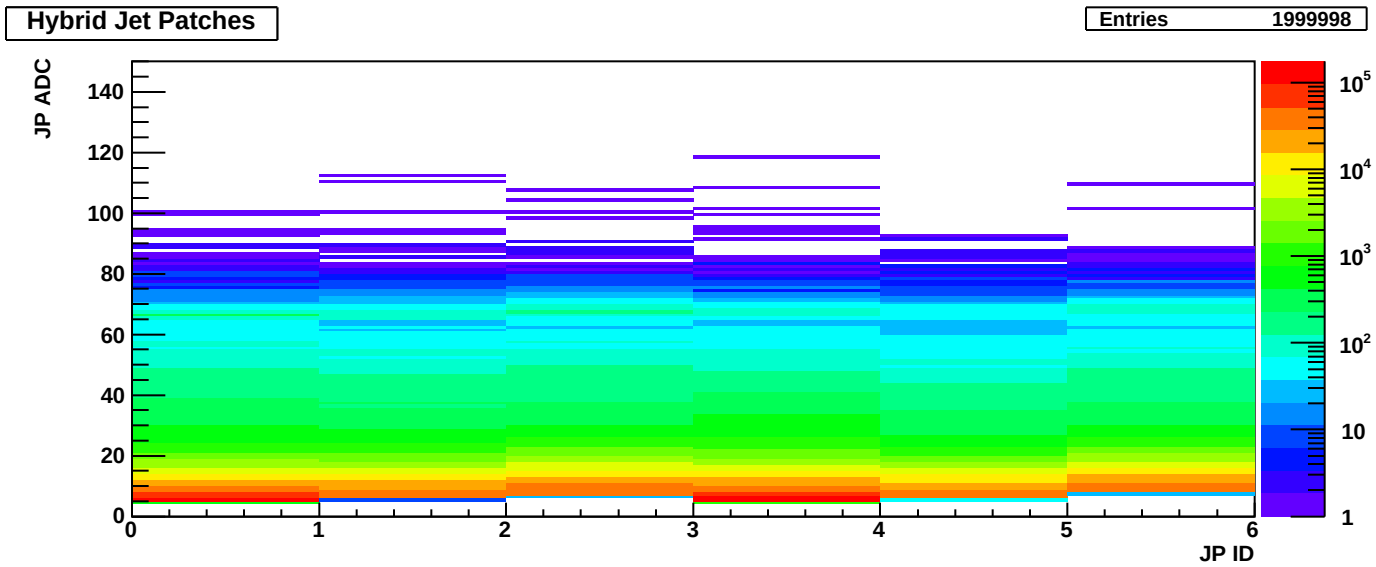

Barrel EMC L0 Input - Patch Sum
Entries 2.999997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

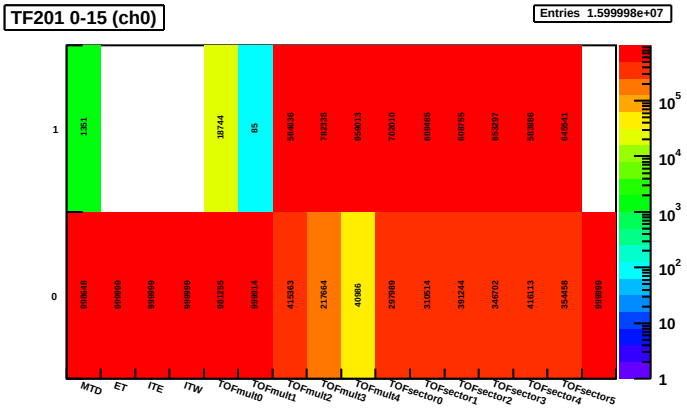

Endcap EMC L0 Input - High Tower
Entries 8.999991e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

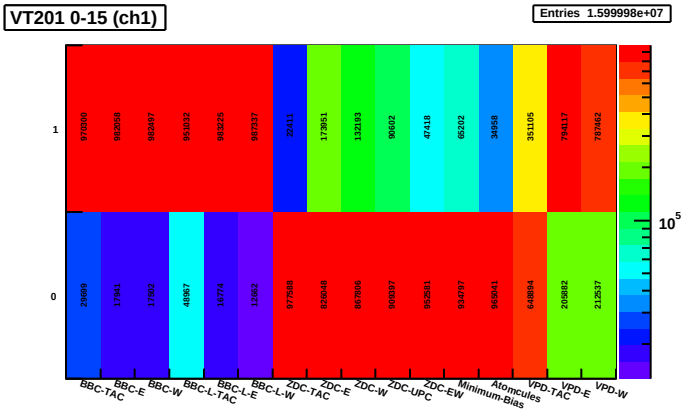

Endcap EMC L0 Input - Patch Sum
Entries 8.999991e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

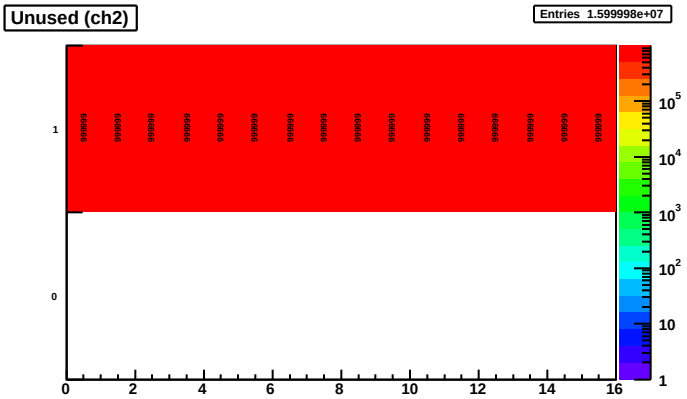

Barrel Jet Patches
Entries 1.799998e+07
JP ADC
JP ID

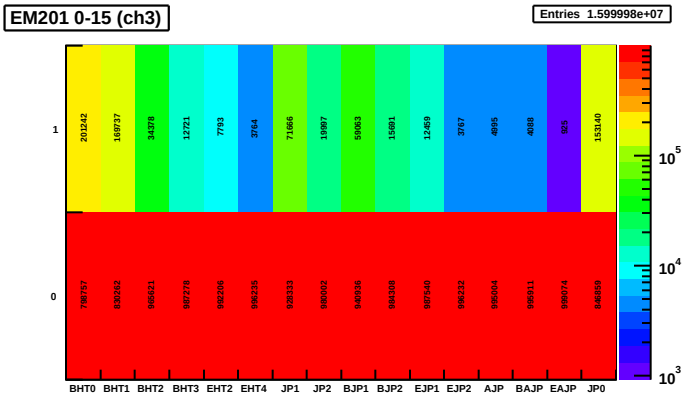

Endcap Jet Patches
Entries 5999994
JP ADC
JP ID

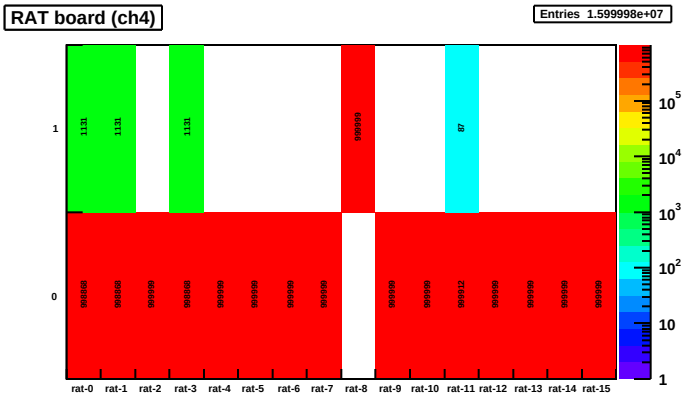

Hybrid Jet Patches
Entries 1999998
JP ADC
JP ID

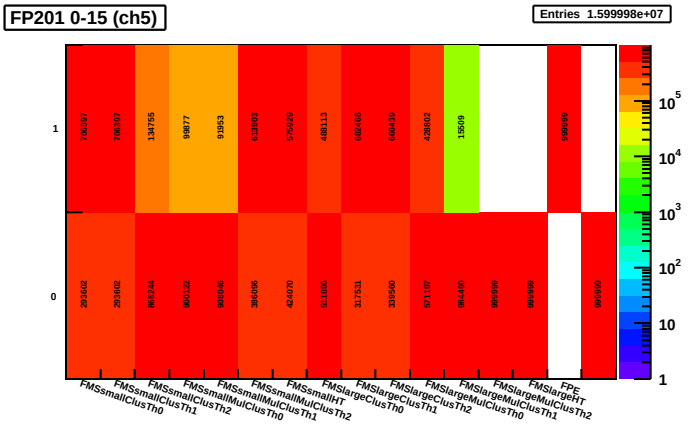

TF201 0-15 (ch0)
Entries 1.599998e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

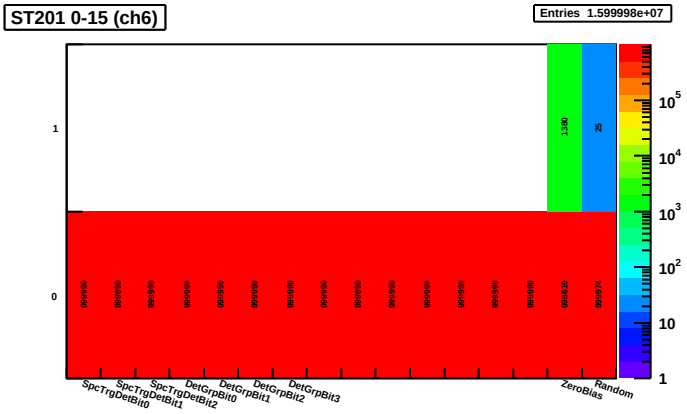

VT201 0-15 (ch1)
Entries 1.599998e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

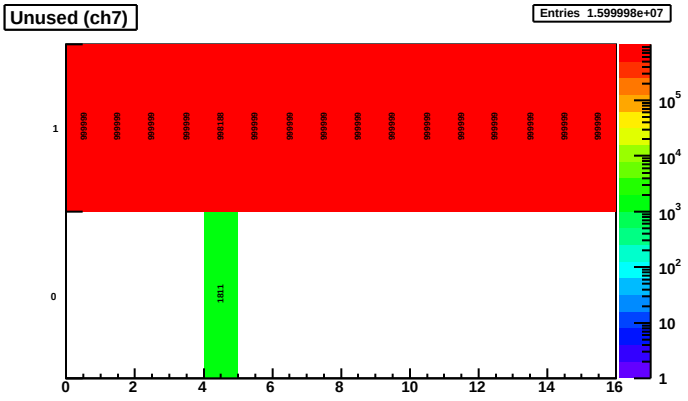

Unused (ch2)
Entries 1.599998e+07

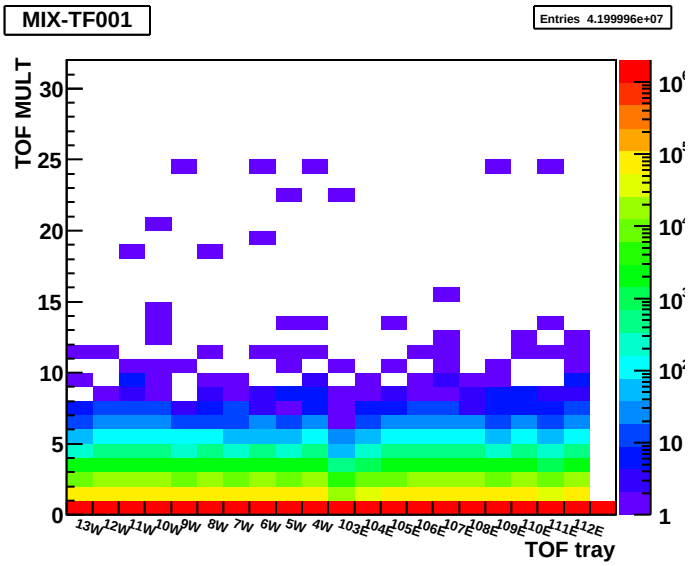

EM201 0-15 (ch3)
Entries 1.599998e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

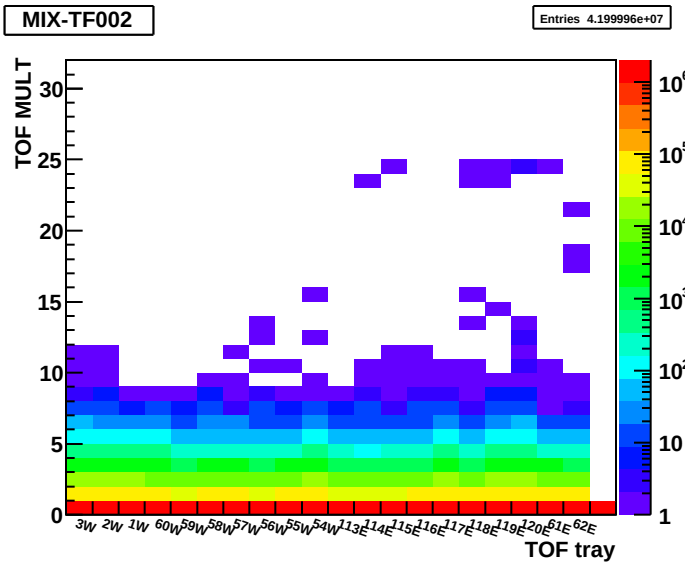

RAT board (ch4)
Entries 1.599998e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

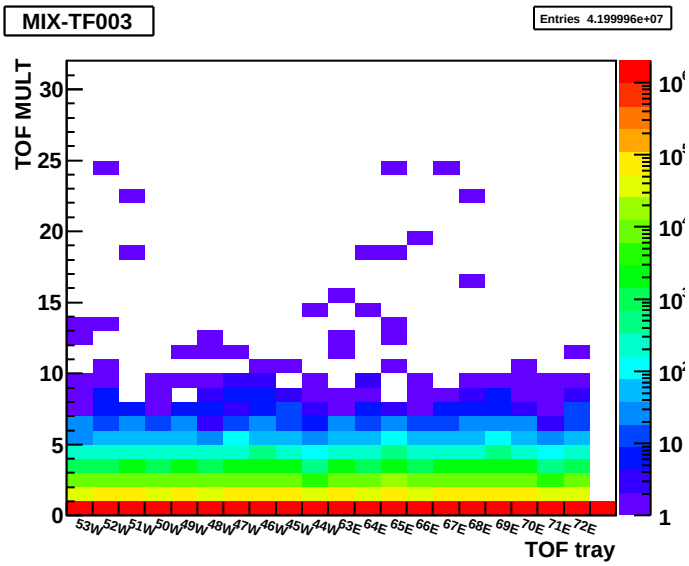

FP201 0-15 (ch5)
Entries 1.599998e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

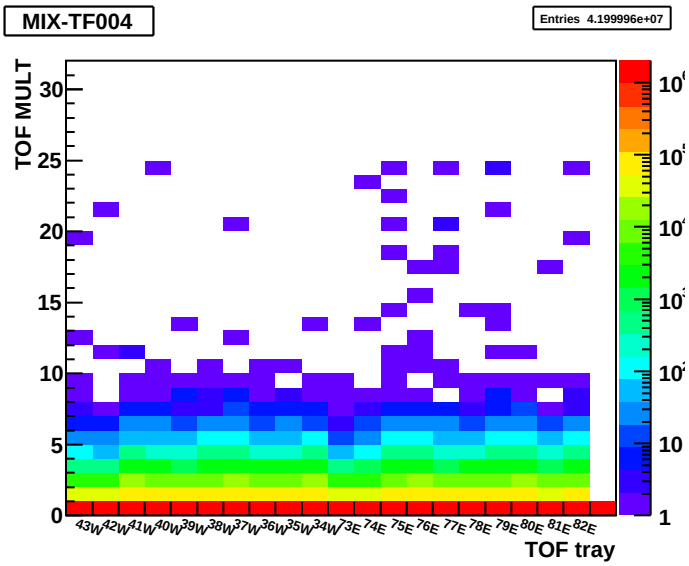

ST201 0-15 (ch6)
Entries 1.599998e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

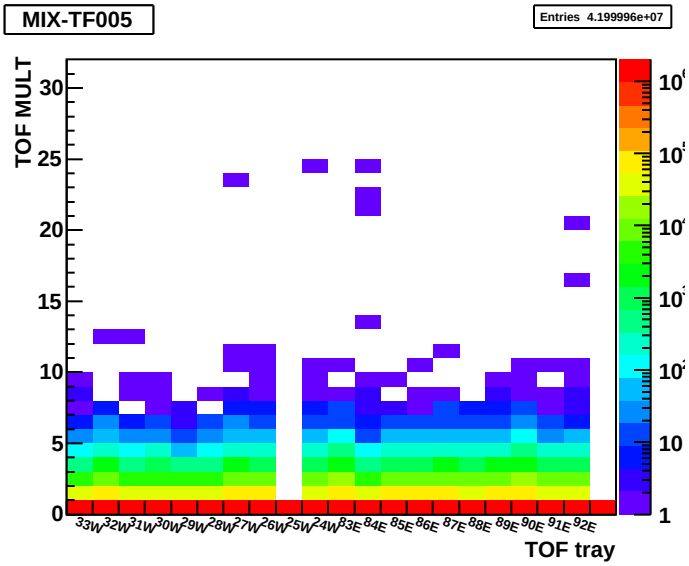

Unused (ch7)
Entries 1.599998e+07

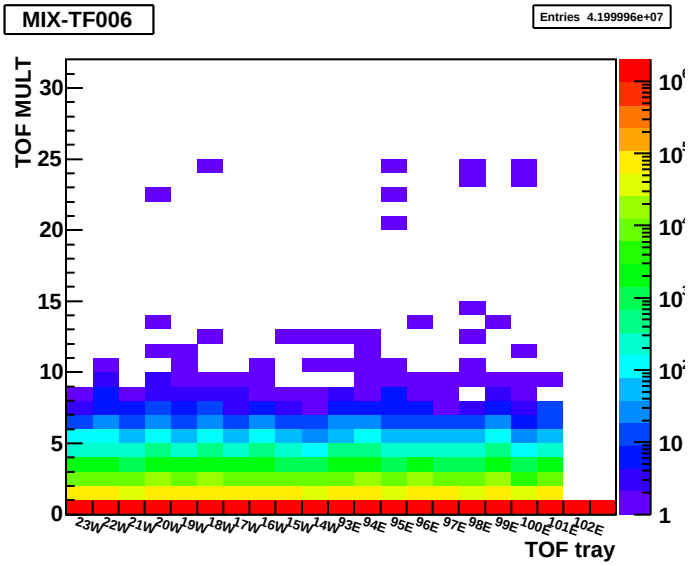

MIX-TF001
Entries 4.199996e+07
TOF MULT
TOF tray

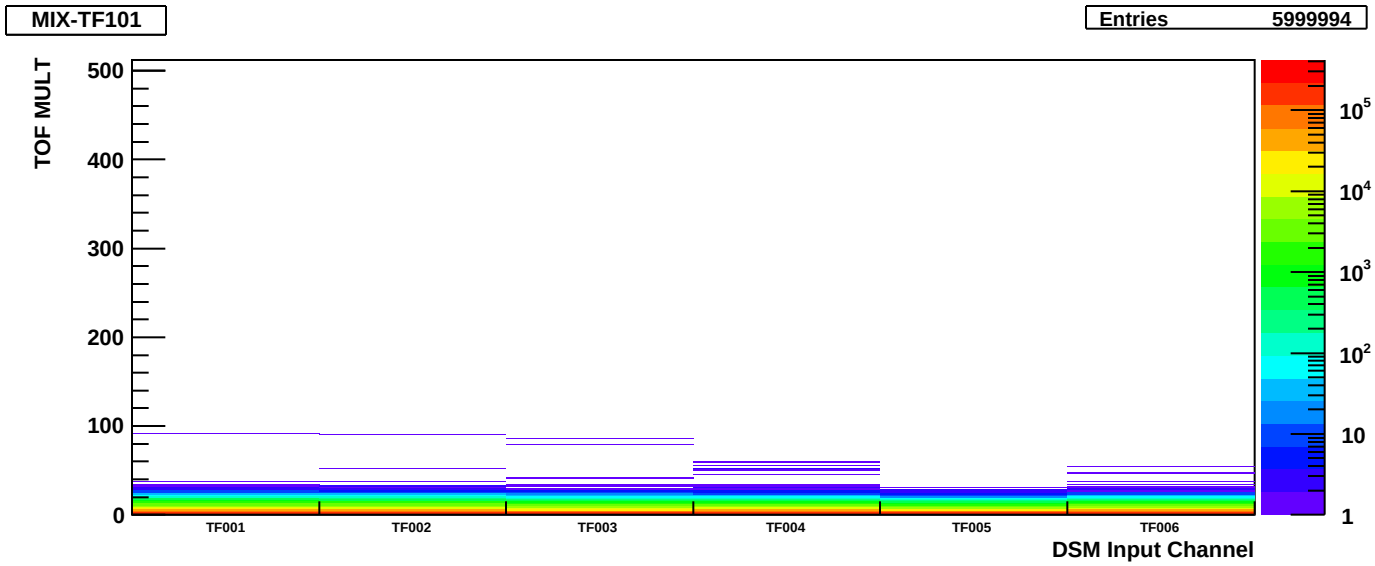

MIX-TF002
Entries 4.199996e+07
TOF MULT
TOF tray

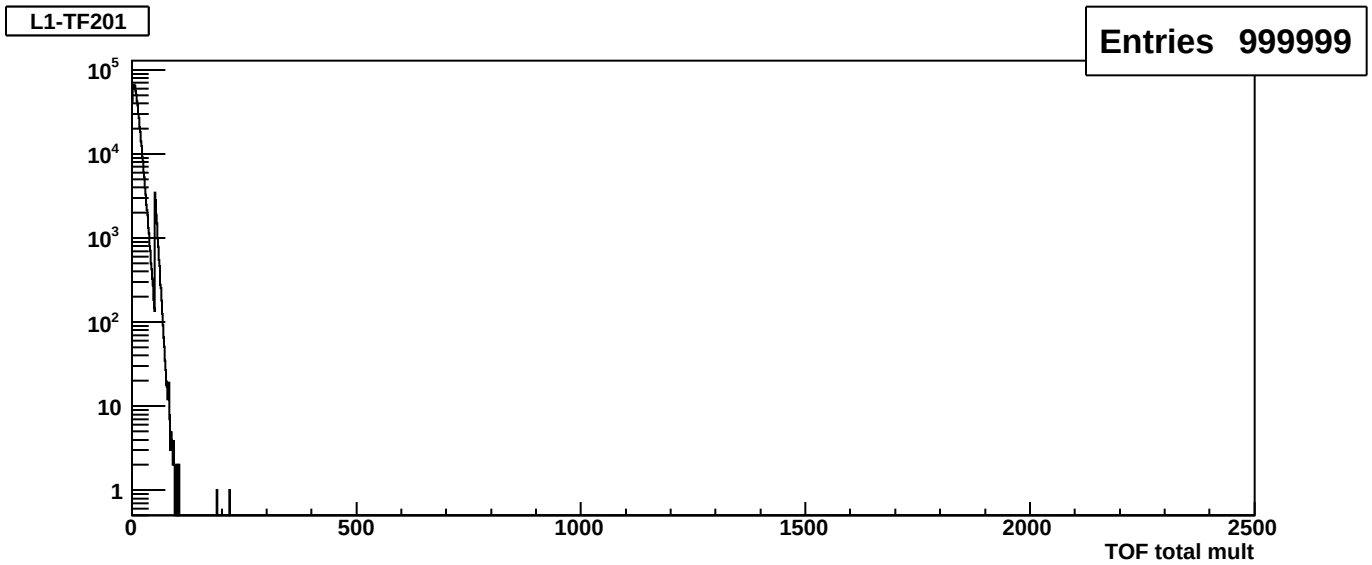

MIX-TF003
Entries 4.199996e+07
TOF MULT
TOF tray

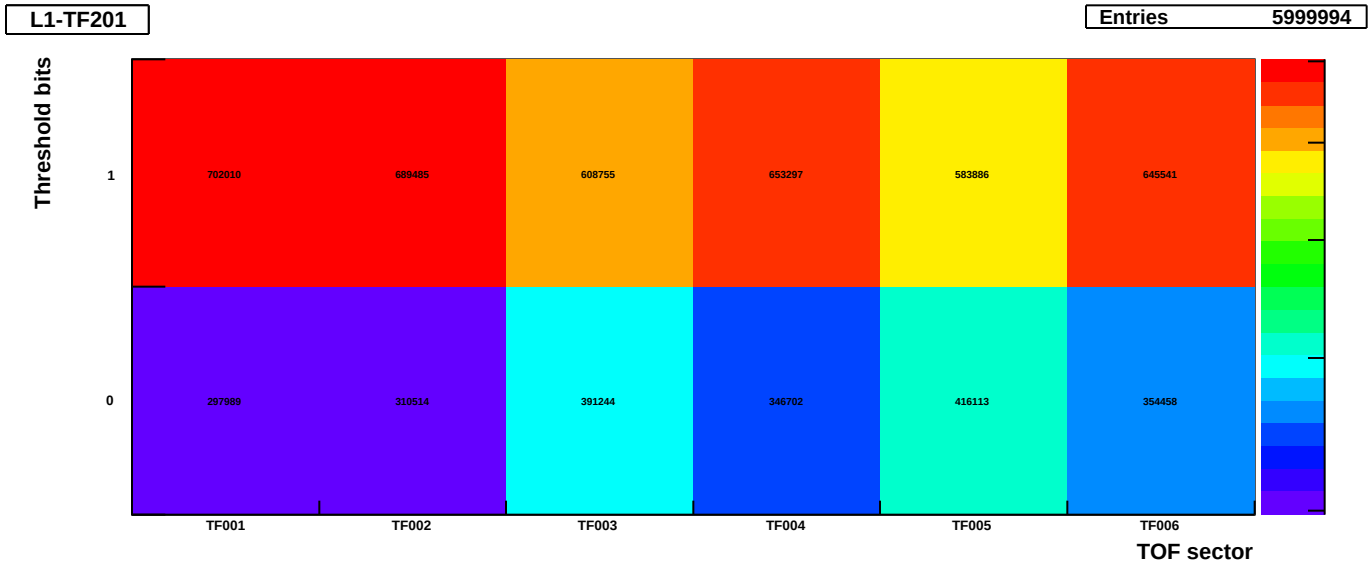

MIX-TF004
Entries 4.199996e+07
TOF MULT
TOF tray

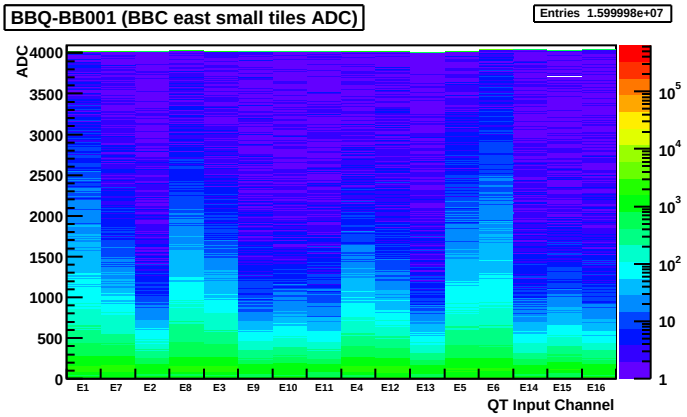

MIX-TF005
Entries 4.199996e+07
TOF MULT
TOF tray

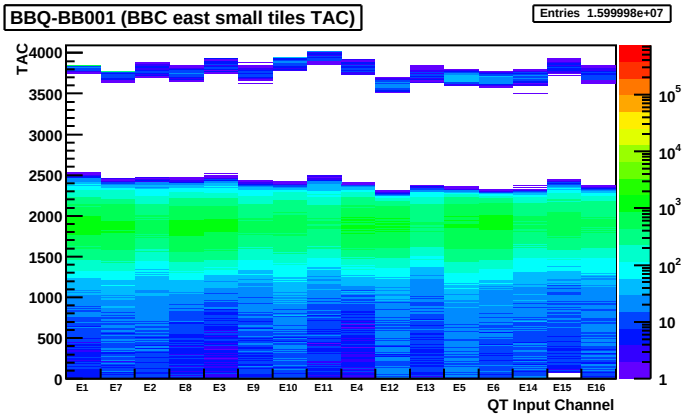

MIX-TF006
Entries 4.199996e+07
TOF MULT
TOF tray

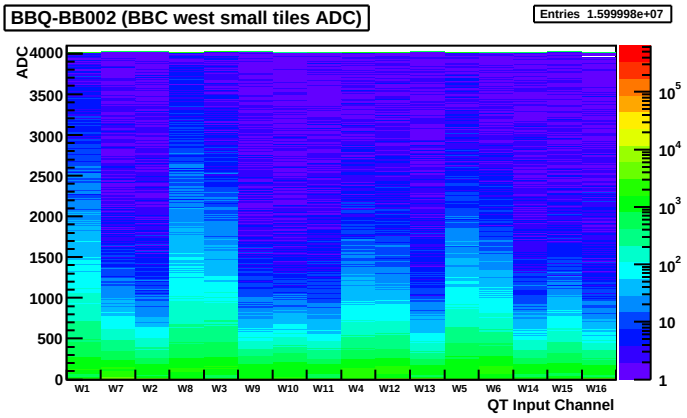

MIX-TF101
Entries 5999994
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

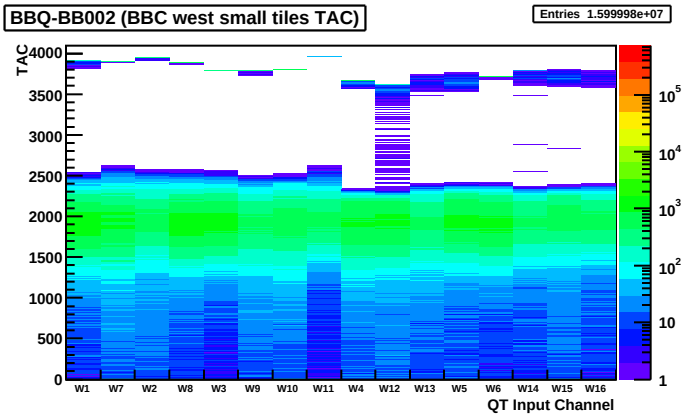

L1-TF201
Entries 999999
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

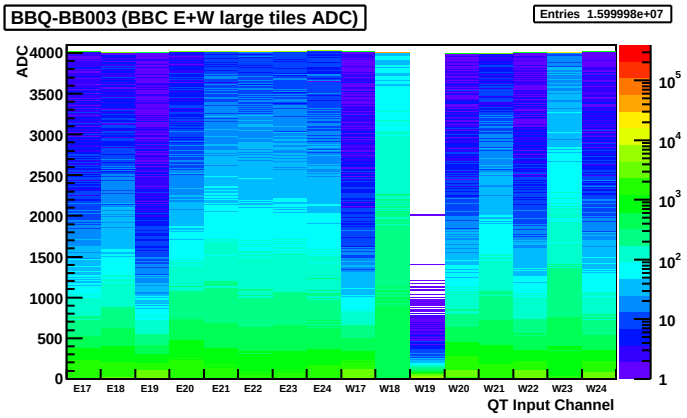

L1-TF201
Entries 5999994
Threshold bits
1
0
702010
689485
608755
653297
583886
645541
297989
310514
391244
346702
416113
354458
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

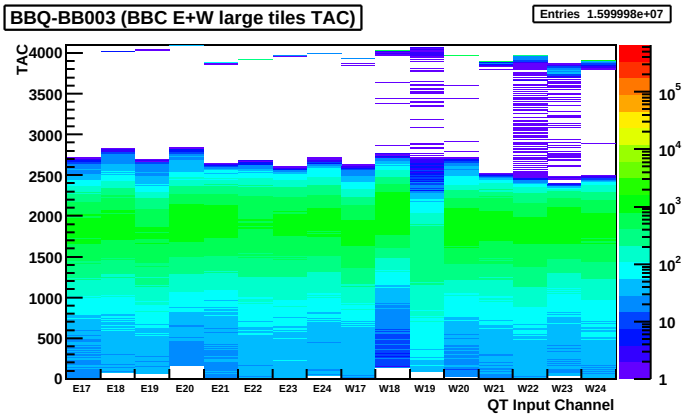

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

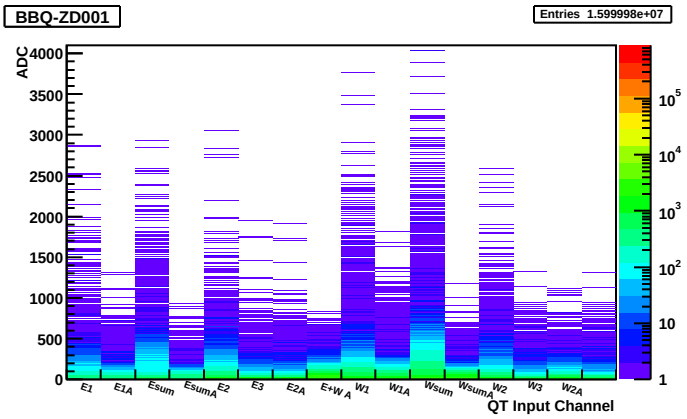

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

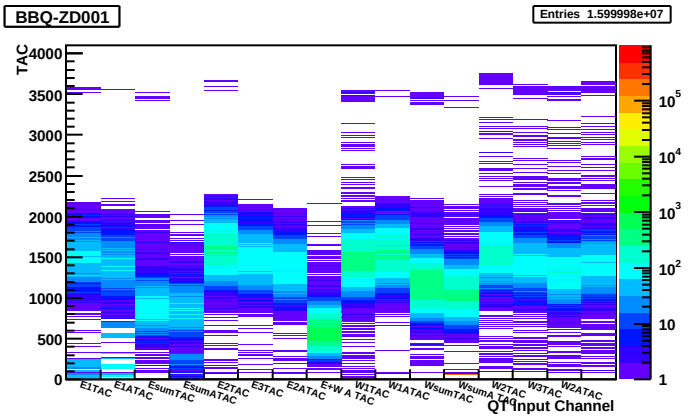

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

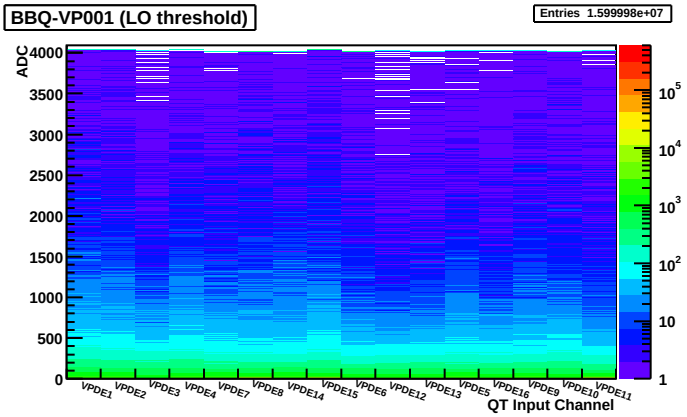

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

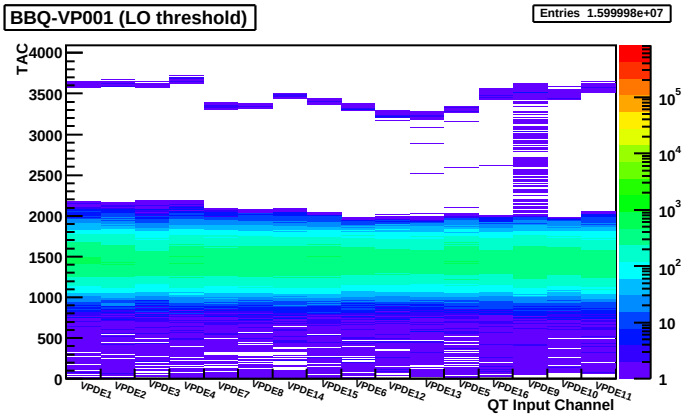

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

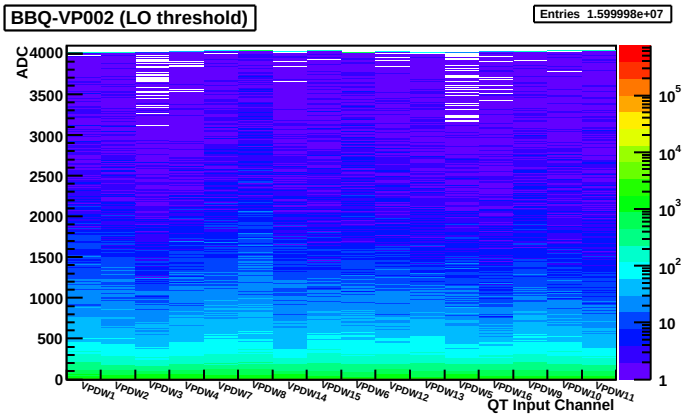

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

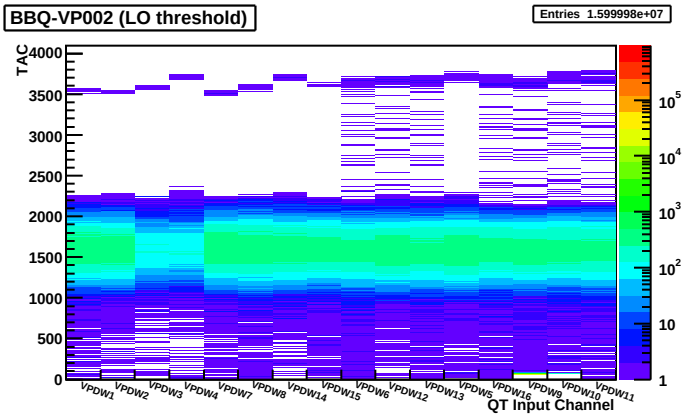

BBQ-ZD001
Entries 1.599998e+07
ADC
QT Input Channel

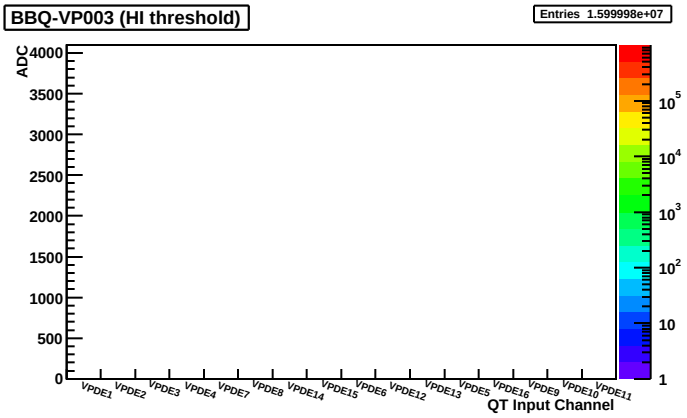

BBQ-ZD001
Entries 1.599998e+07
TAC
QT Input Channel

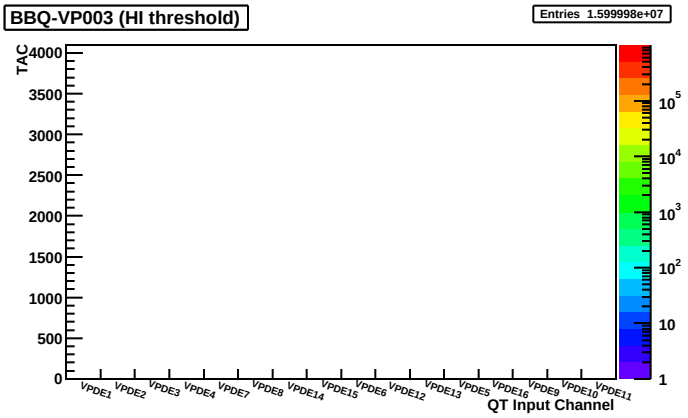

BBQ-VP001 (LO threshold)
Entries 1.599998e+07
ADC
QT Input Channel

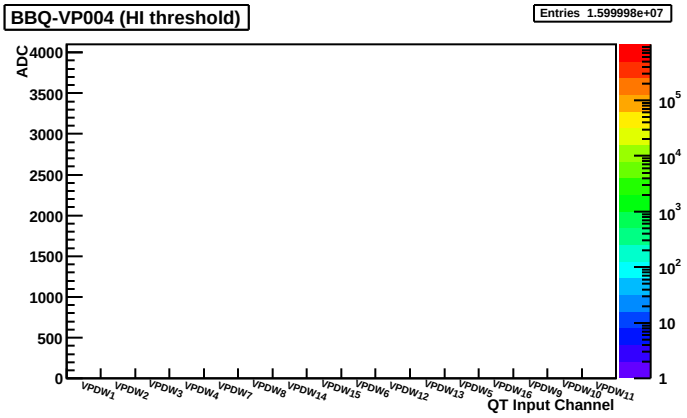

BBQ-VP001 (LO threshold)
Entries 1.599998e+07
TAC
QT Input Channel

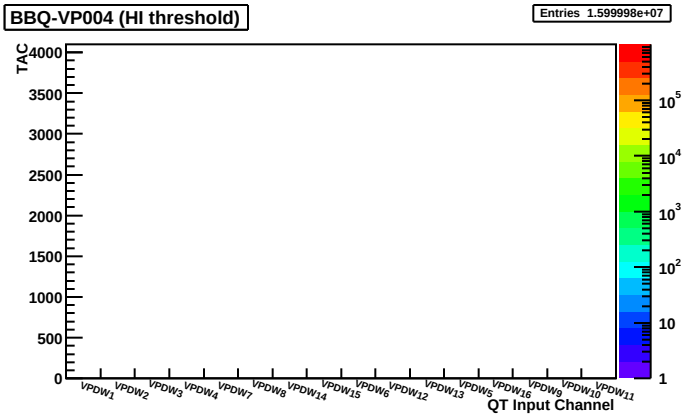

BBQ-VP002 (LO threshold)
Entries 1.599998e+07
ADC
QT Input Channel

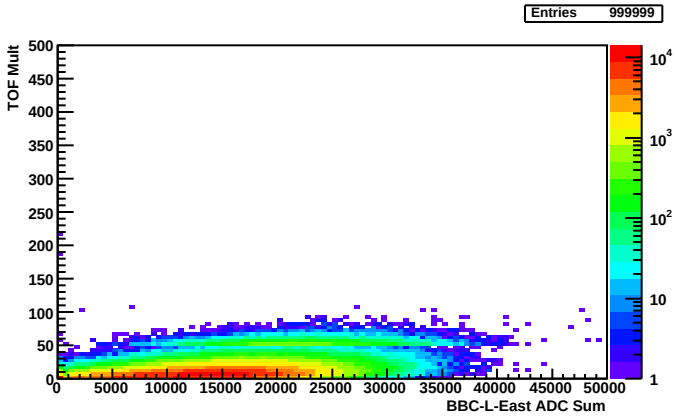

BBQ-VP002 (LO threshold)
Entries 1.599998e+07
TAC
QT Input Channel

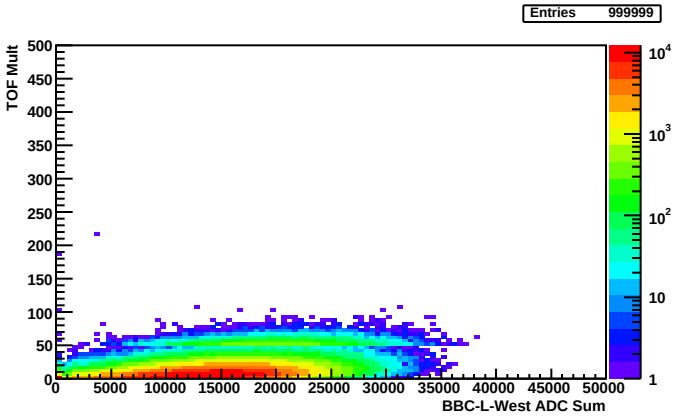

BBQ-VP003 (HI threshold)
Entries 1.599998e+07
ADC
QT Input Channel

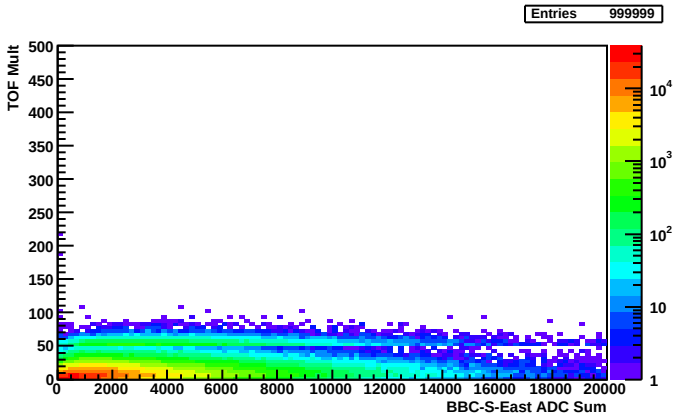

BBQ-VP003 (HI threshold)
Entries 1.599998e+07
TAC
QT Input Channel

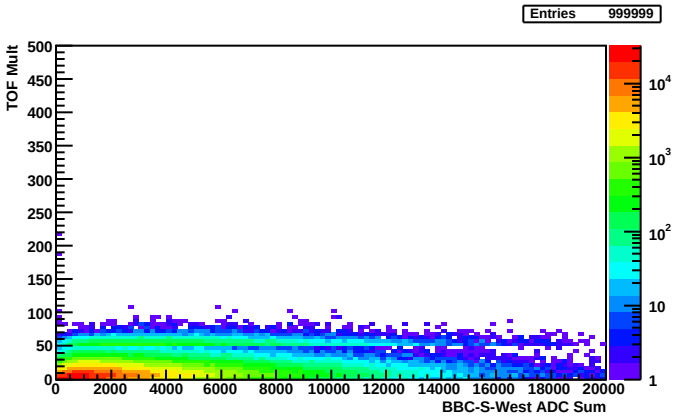

BBQ-VP004 (HI threshold)
Entries 1.599998e+07
ADC
QT Input Channel

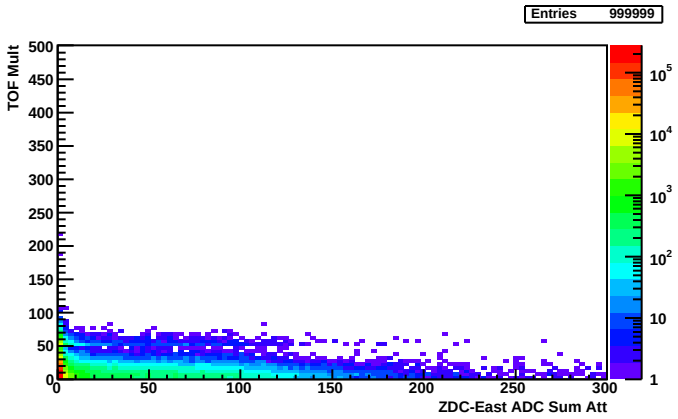

BBQ-VP004 (HI threshold)
Entries 1.599998e+07
TAC
QT Input Channel

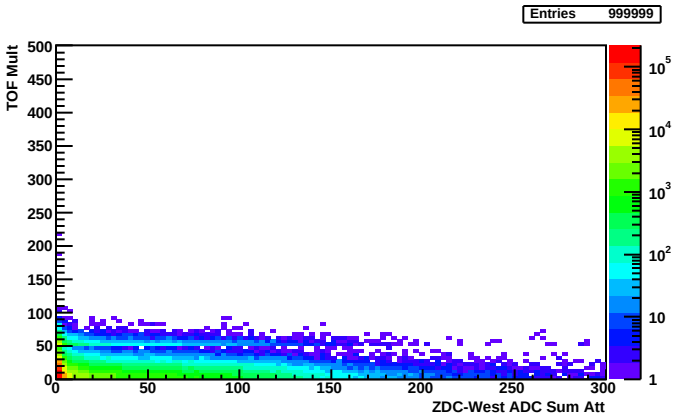

Entries 999999
TOF Mult
BBC-L-East ADC Sum

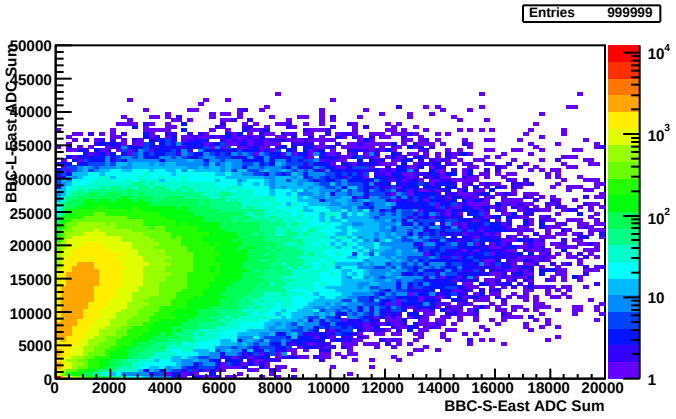

Entries 999999
TOF Mult
BBC-L-West ADC Sum

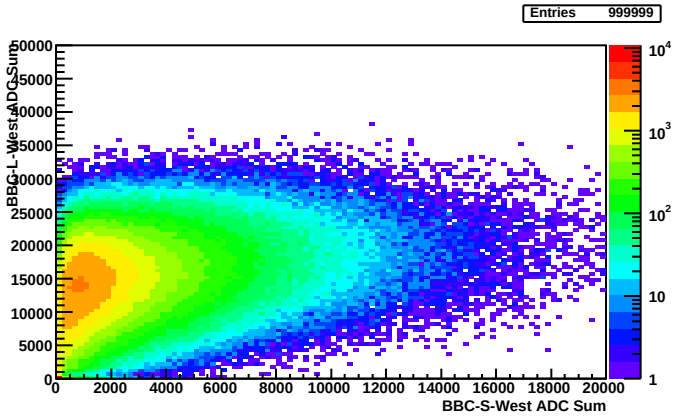

Entries 999999
TOF Mult
BBC-S-East ADC Sum

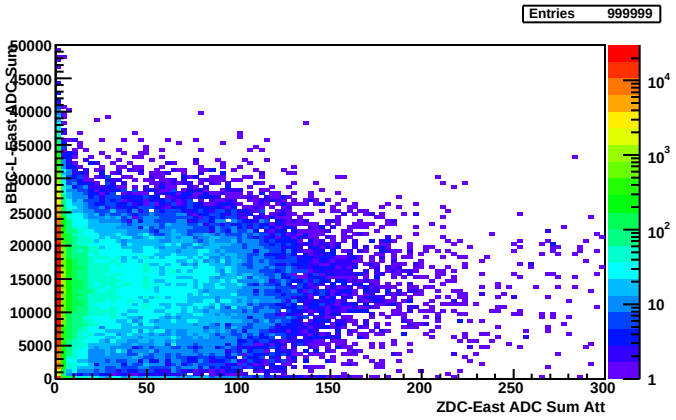

Entries 999999
TOF Mult
BBC-S-West ADC Sum

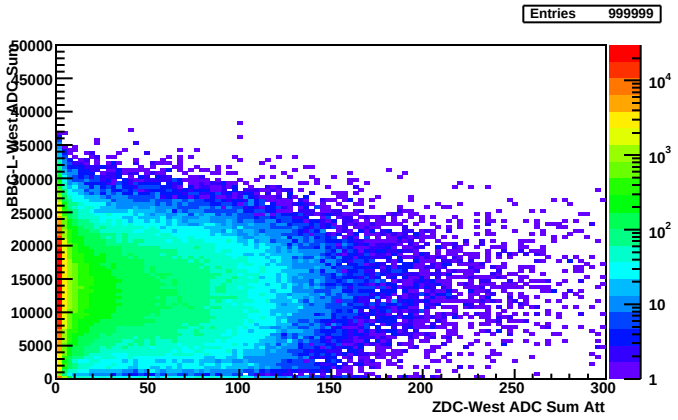

Entries 999999
TOF Mult
ZDC-East ADC Sum Att

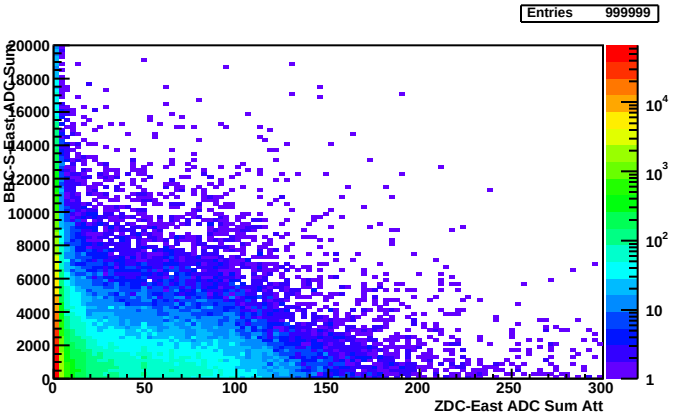

Entries 999999
TOF Mult
ZDC-West ADC Sum Att

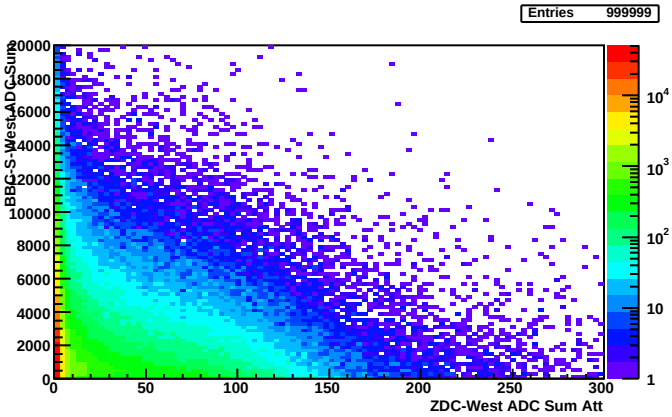

Entries 999999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

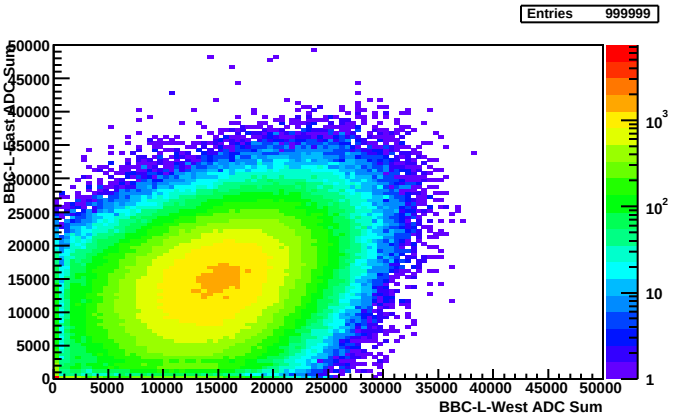

Entries 999999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

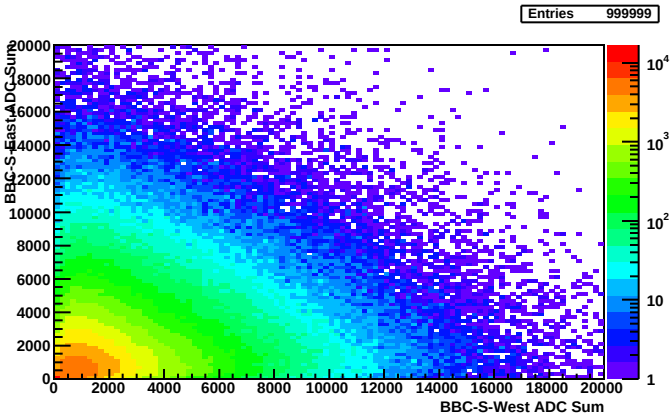

Entries 999999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

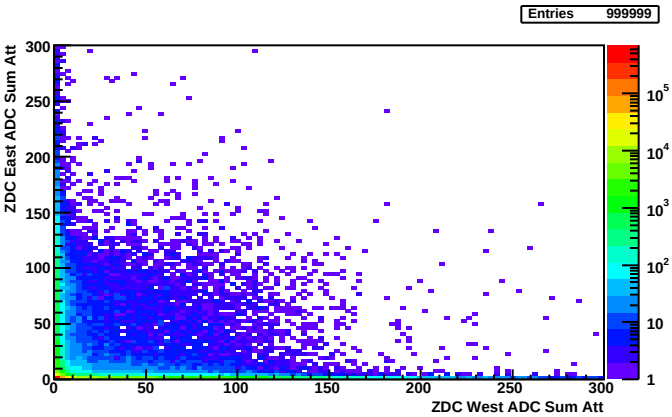

Entries 999999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

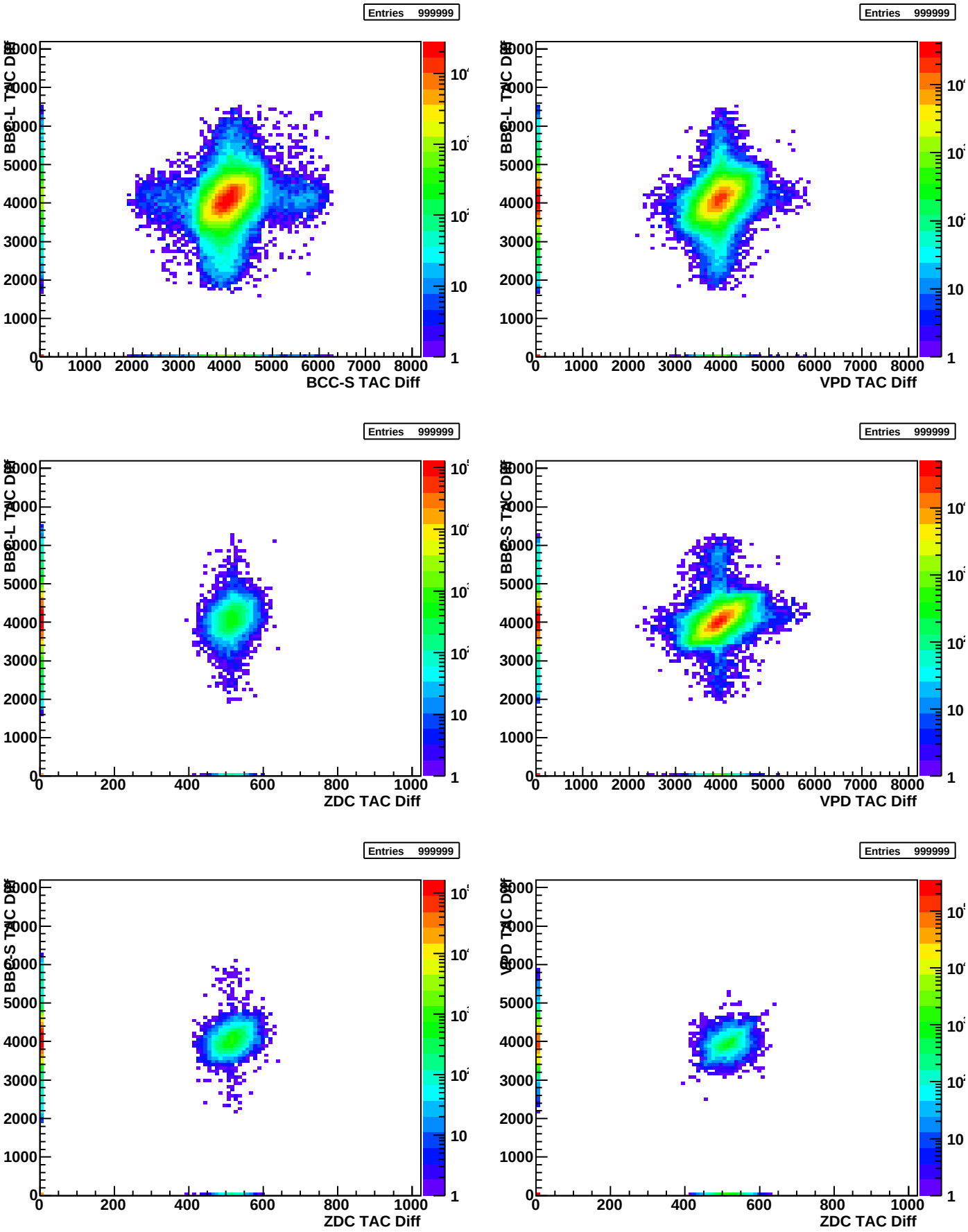

Entries 999999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

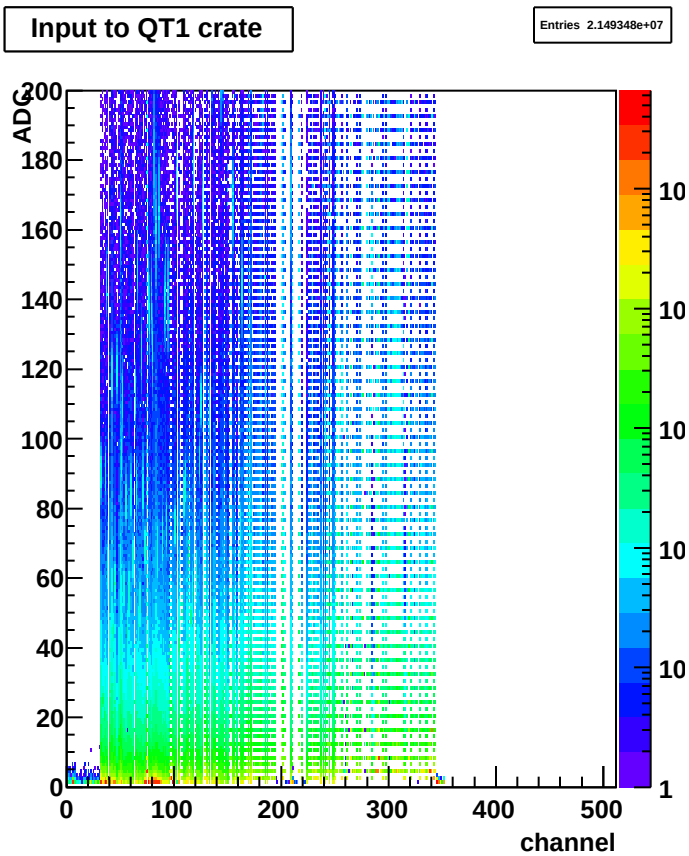

Entries 999999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

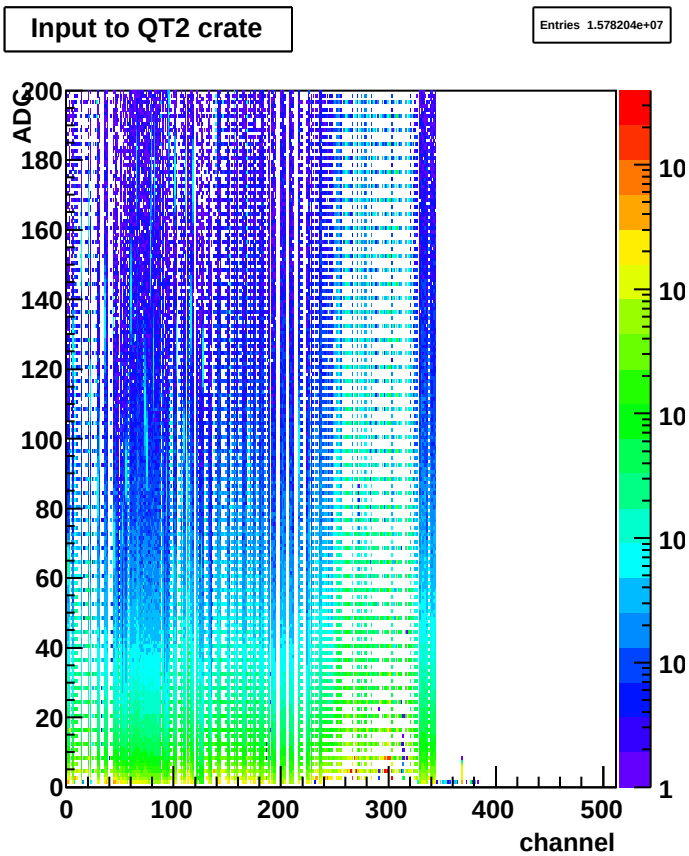

Entries 999999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

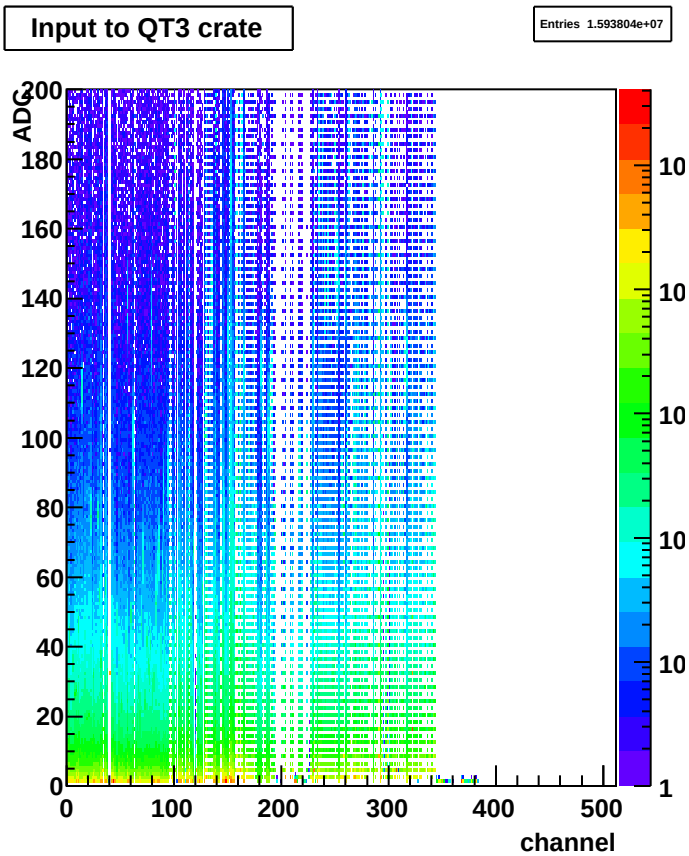

Entries 999999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

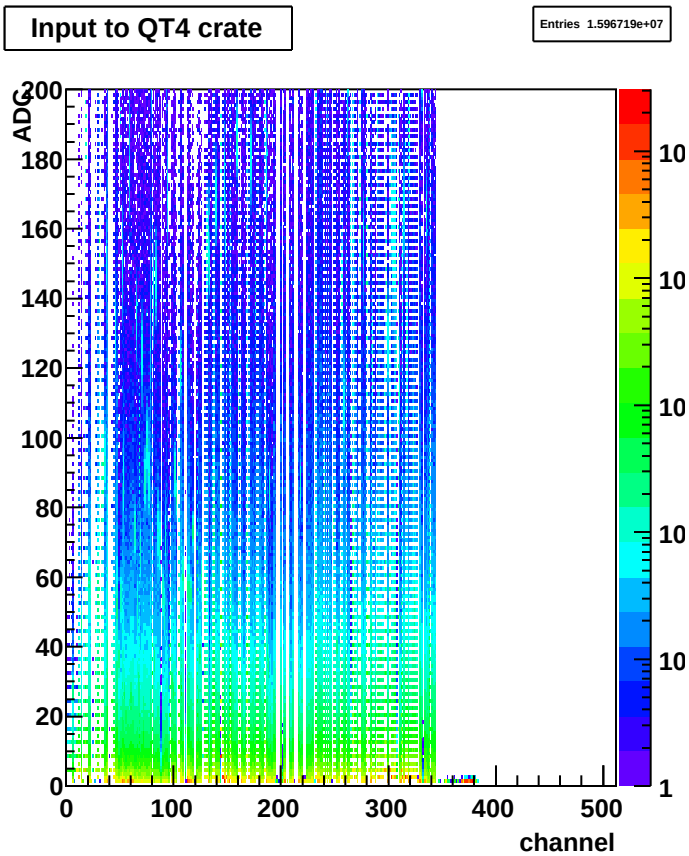

Entries 999999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

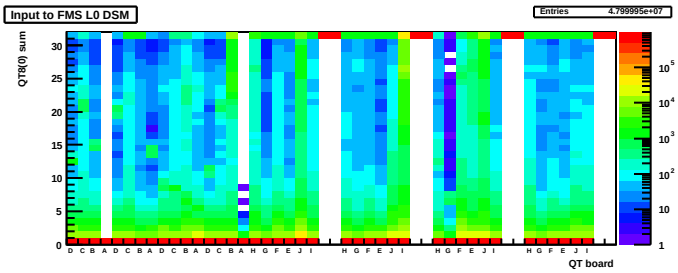

Entries 999999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 999999
BBC-L TAC Diff
VPD TAC Diff
Entries 999999
BBC-L TAC Diff
ZDC TAC Diff
Entries 999999
BBC-S TAC Diff
VPD TAC Diff
Entries 999999
BBC-S TAC Diff
ZDC TAC Diff
Entries 999999
VPD TAC Diff
ZDC TAC Diff

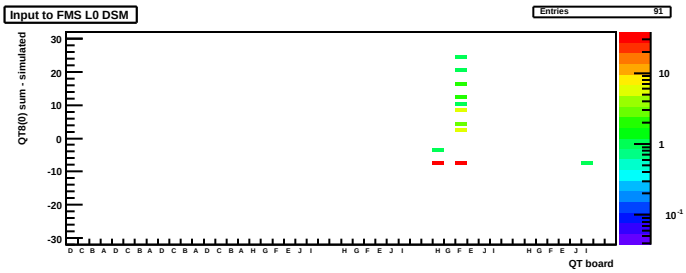

Input to QT1 crate
Entries 2.149348e+07
ADC
channel

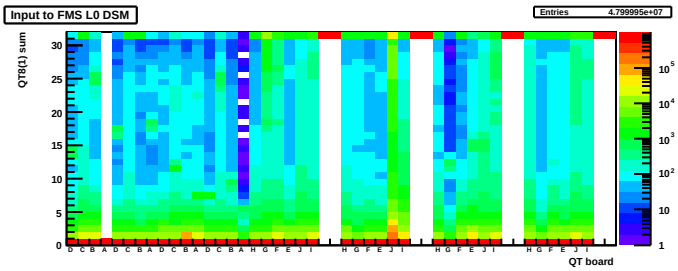

Input to QT2 crate
Entries 1.578204e+07
ADC
channel

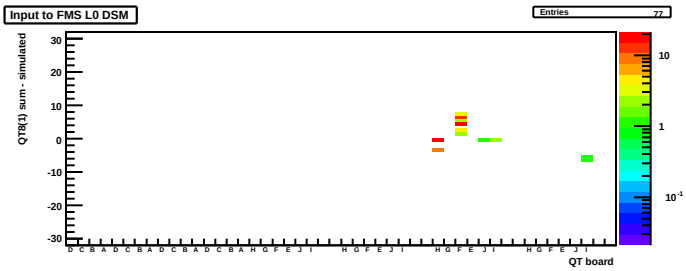

Input to QT3 crate
Entries 1.593804e+07
ADC
channel

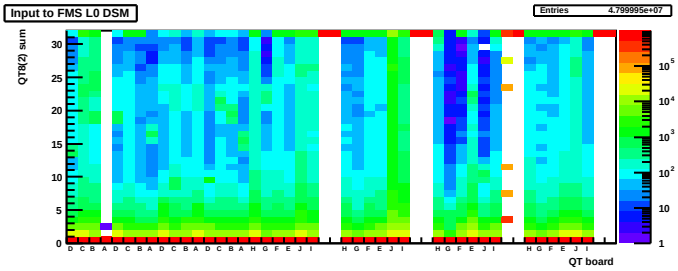

Input to QT4 crate
Entries 1.596719e+07
ADC
channel

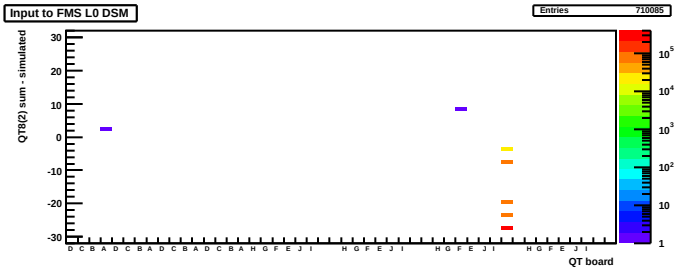

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(0) sum
QT board

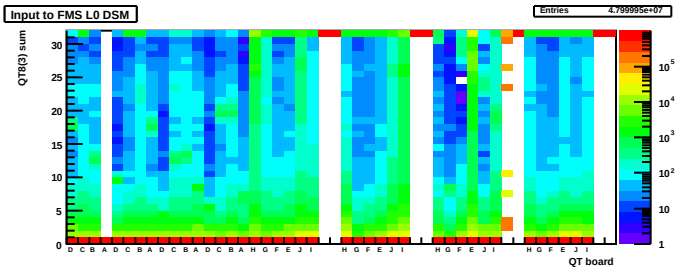

Input to FMS L0 DSM
Entries 91
QT8(0) sum - simulated
QT board

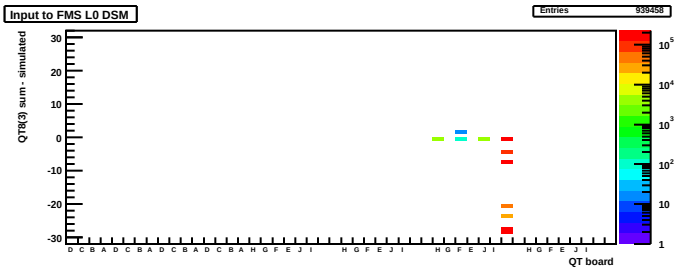

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(1) sum
QT board

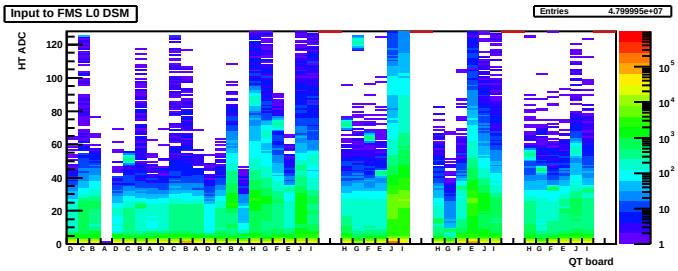

Input to FMS L0 DSM
Entries 77
QT8(1) sum - simulated
QT board

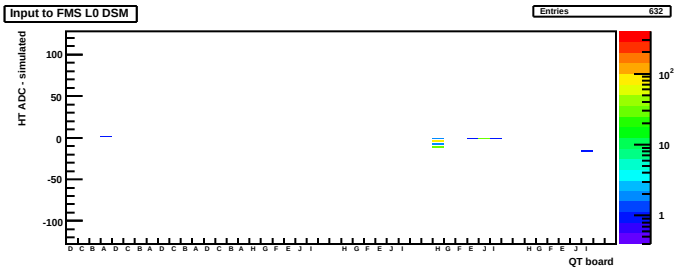

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(2) sum
QT board

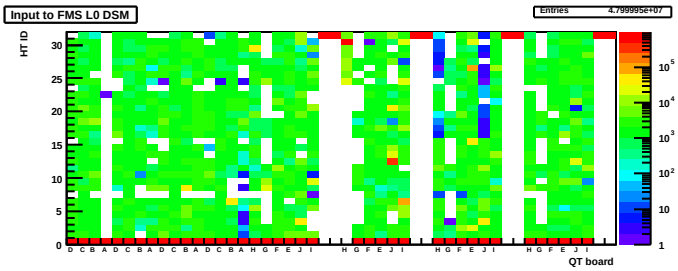

Input to FMS L0 DSM
Entries 710085
QT8(2) sum - simulated
QT board

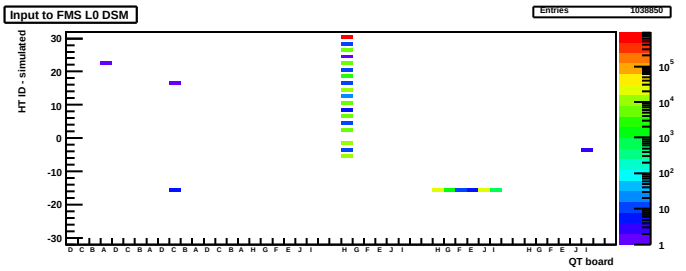

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(3) sum
QT board

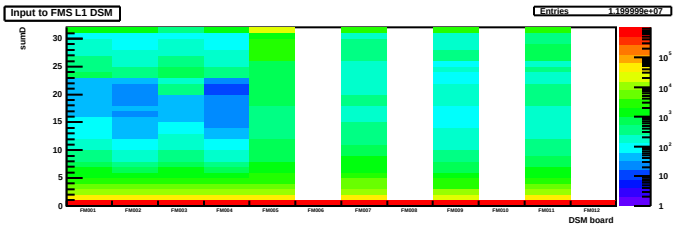

Input to FMS L0 DSM
Entries 939458
QT8(3) sum - simulated
QT board

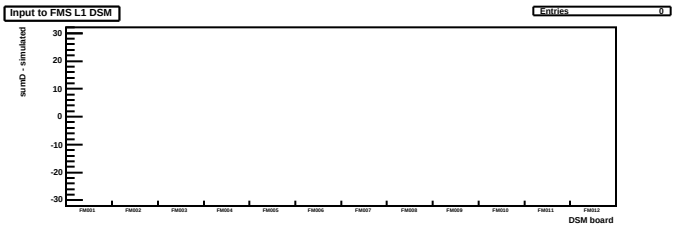

Input to FMS L0 DSM
Entries 4.799995e+07
HT ADC
QT board

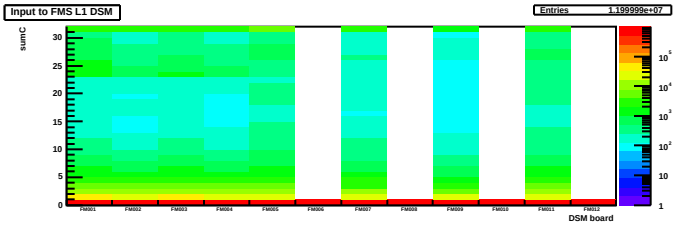

Input to FMS L0 DSM
Entries 632
HT ADC - simulated
QT board

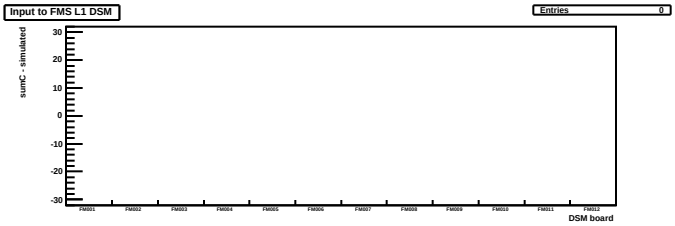

Input to FMS L0 DSM
Entries 4.799995e+07
HT ID
QT board

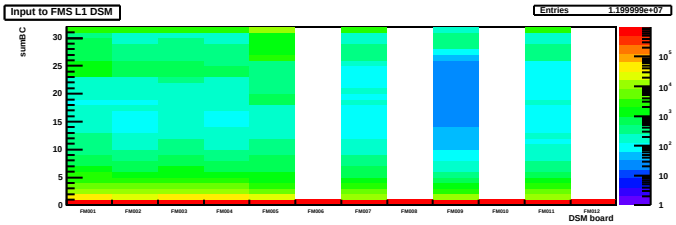

Input to FMS L0 DSM
Entries 1038850
HT ID - simulated
QT board

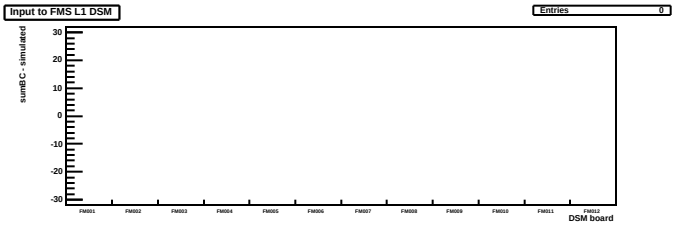

Input to FMS L1 DSM
Entries 1.199999e+07
sumD
DSM board

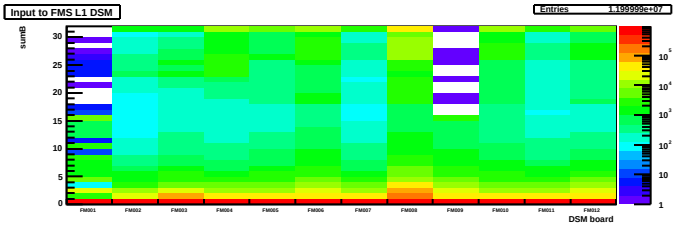

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

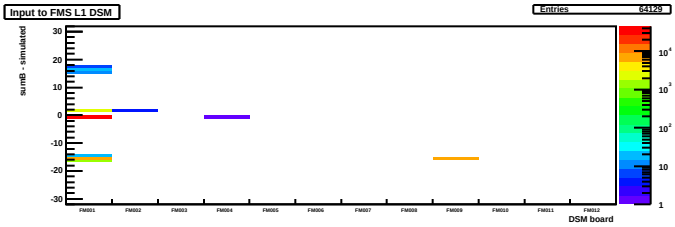

Input to FMS L1 DSM
Entries 1.199999e+07
sumC
DSM board

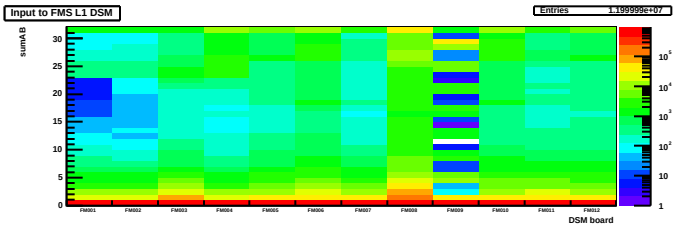

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

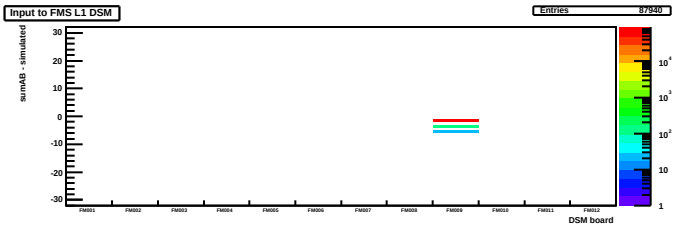

Input to FMS L1 DSM
Entries 1.199999e+07
sumBC
DSM board

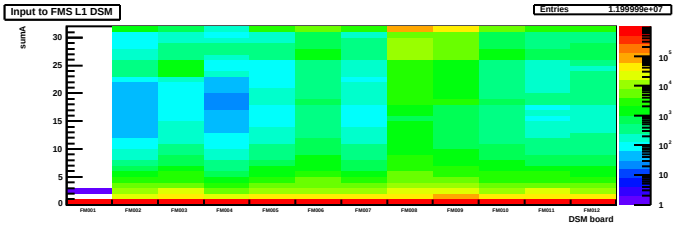

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

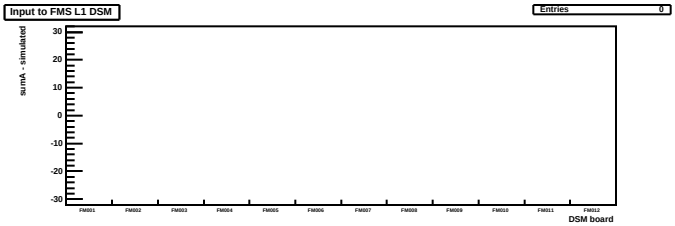

Input to FMS L1 DSM
Entries 1.199999e+07
sumB
DSM board

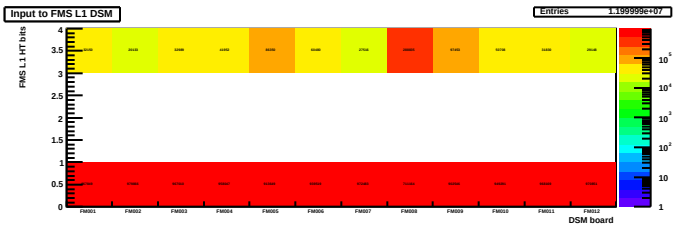

Input to FMS L1 DSM
Entries 64129
sumB - simulated
DSM board

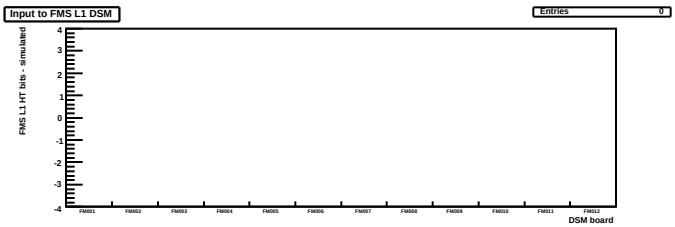

Input to FMS L1 DSM
Entries 1.199999e+07
sumAB
DSM board

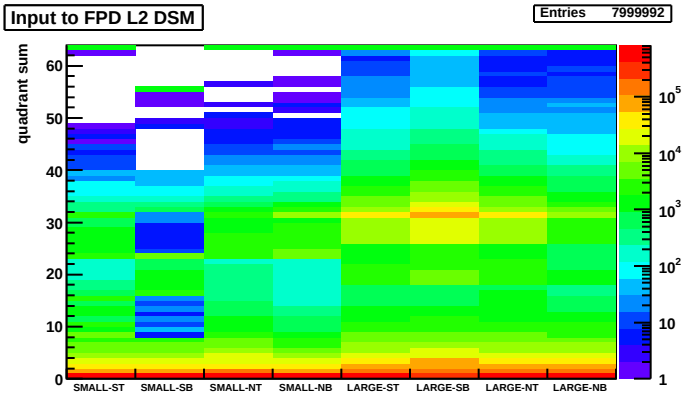

Input to FMS L1 DSM
Entries 87940
sumAB - simulated
DSM board

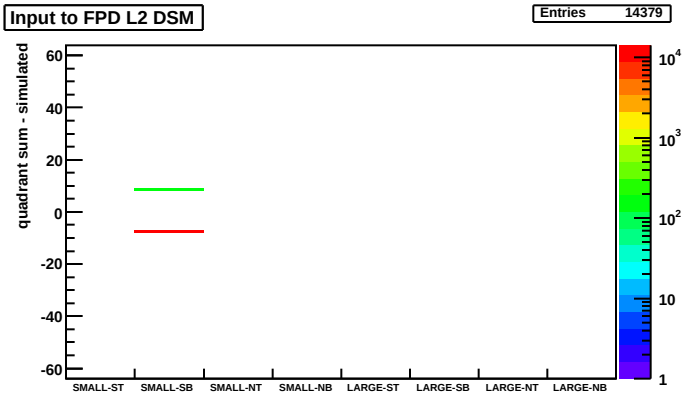

Input to FMS L1 DSM
Entries 1.199999e+07
sumA
DSM board

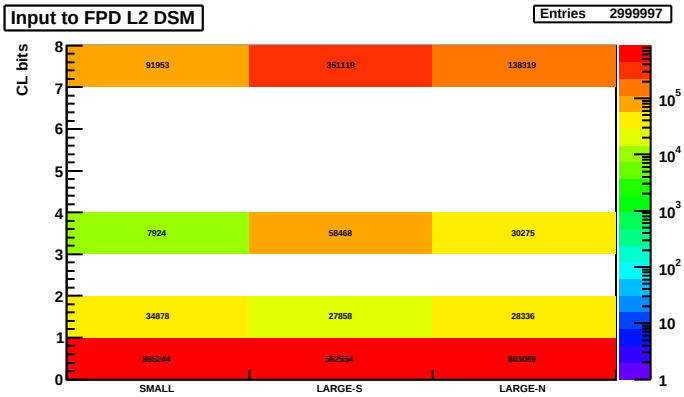

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

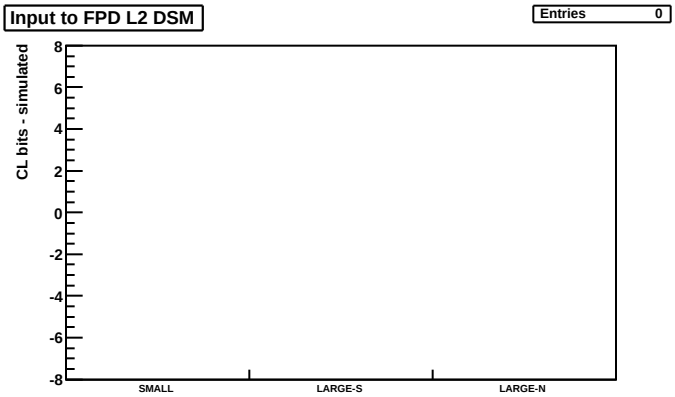

Input to FMS L1 DSM
Entries 1.199999e+07
FMS L1 HT bits
DSM board

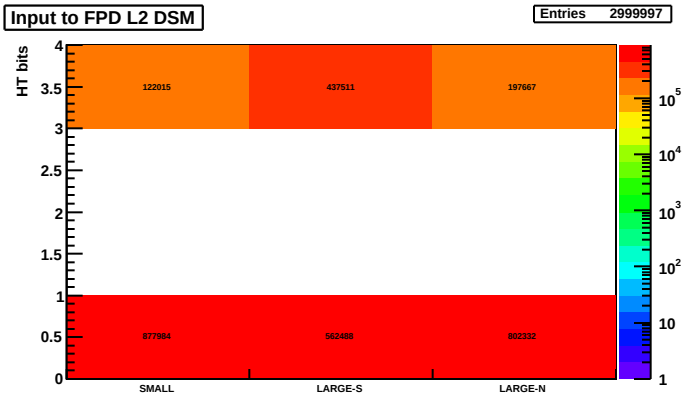

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

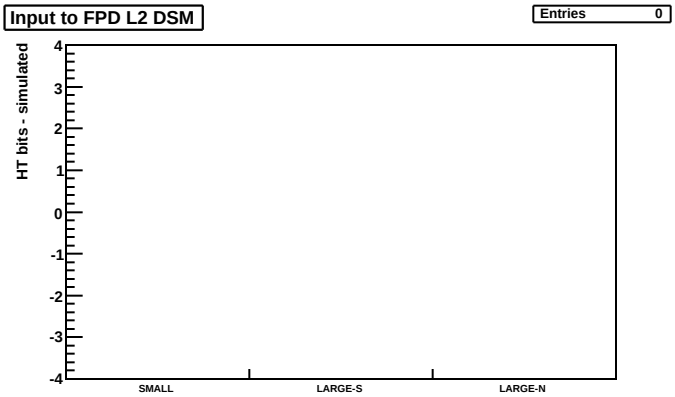

Input to FPD L2 DSM
Entries 7999992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

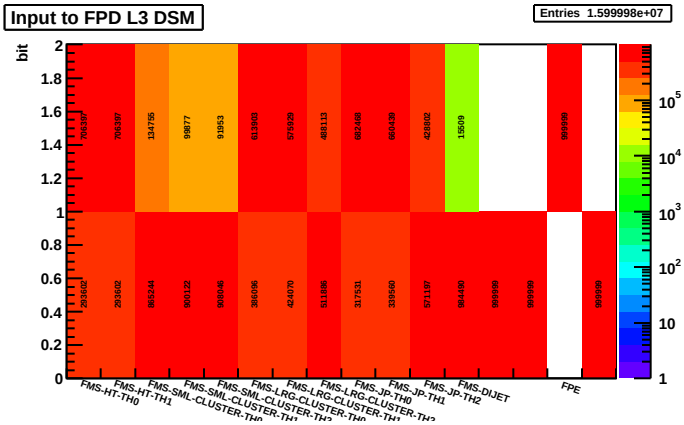

Input to FPD L2 DSM
Entries 14379
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

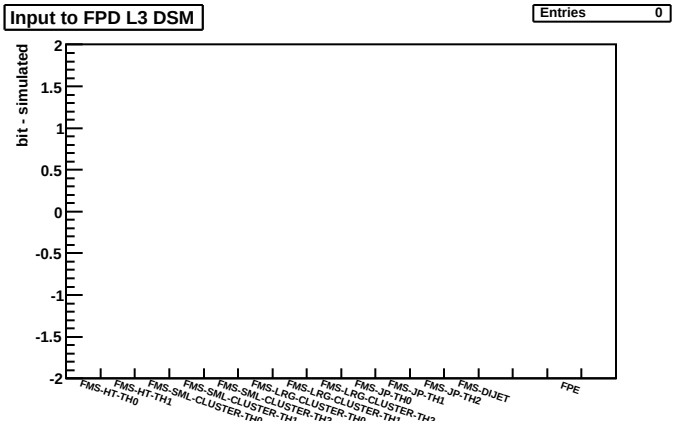

Input to FPD L2 DSM
Entries 2999997
CL bits
91953
351119
138319
7924
58468
30275
34878
27858
28336
865244
562554
803069
SMALL
LARGE-S
LARGE-N

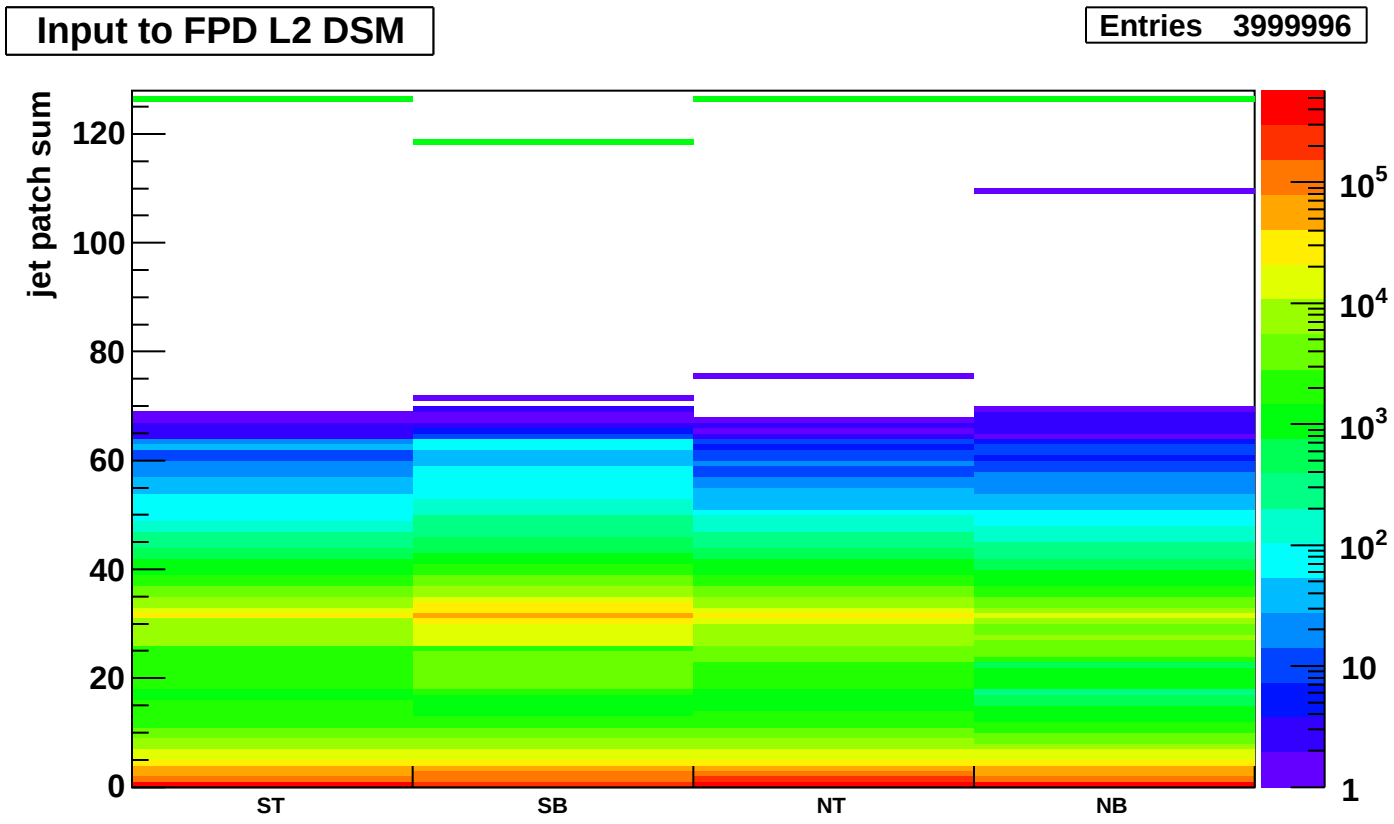

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

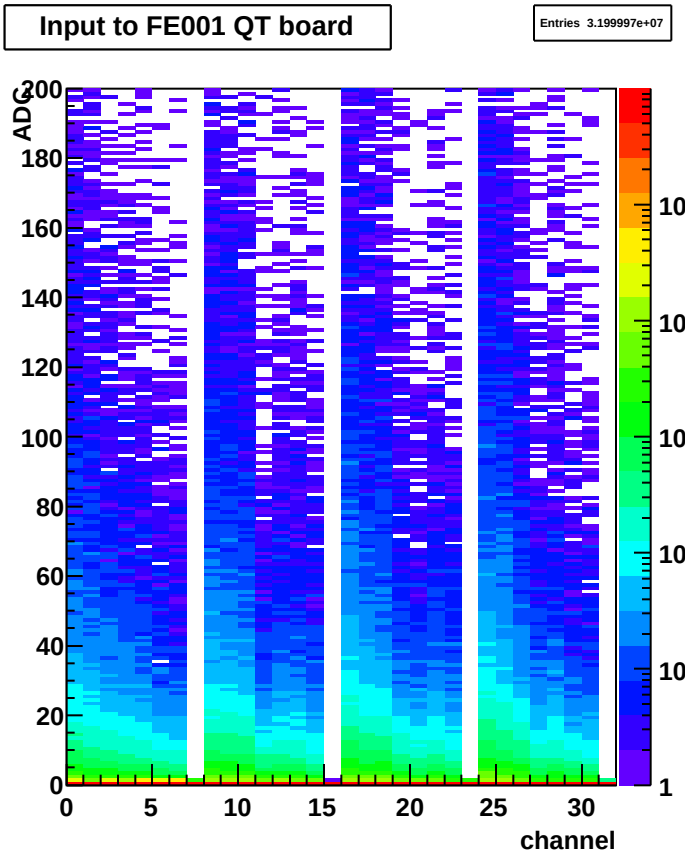

Input to FPD L2 DSM
Entries 2999997
HT bits
122015
437511
197667
877984
562488
802332
SMALL
LARGE-S
LARGE-N

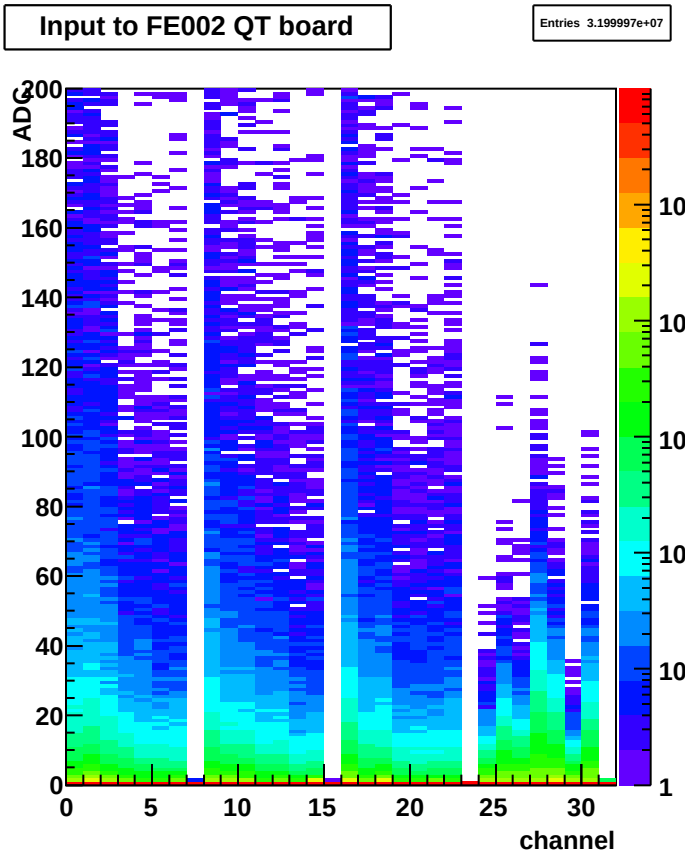

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

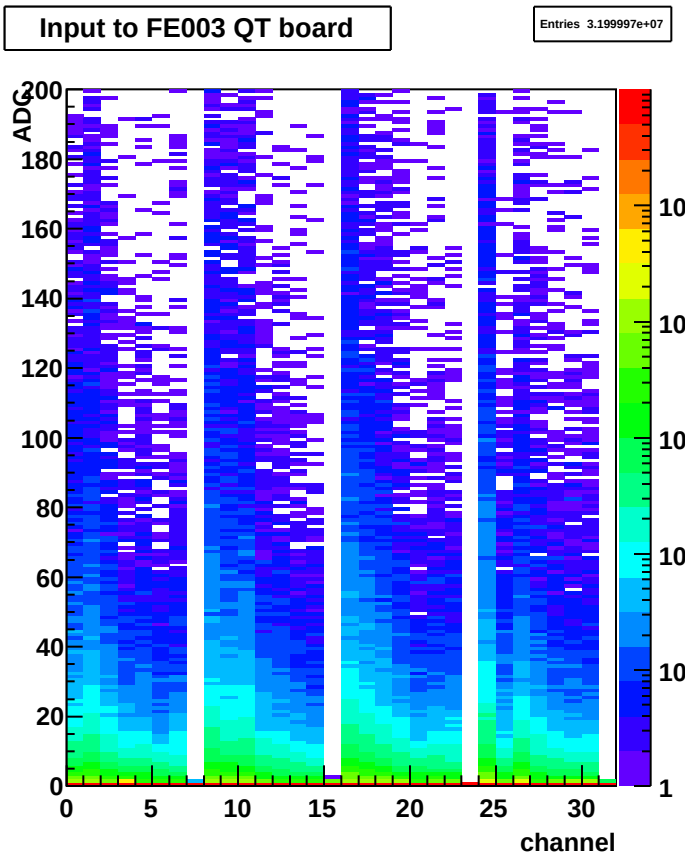

Input to FPD L3 DSM
Entries 1.599998e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

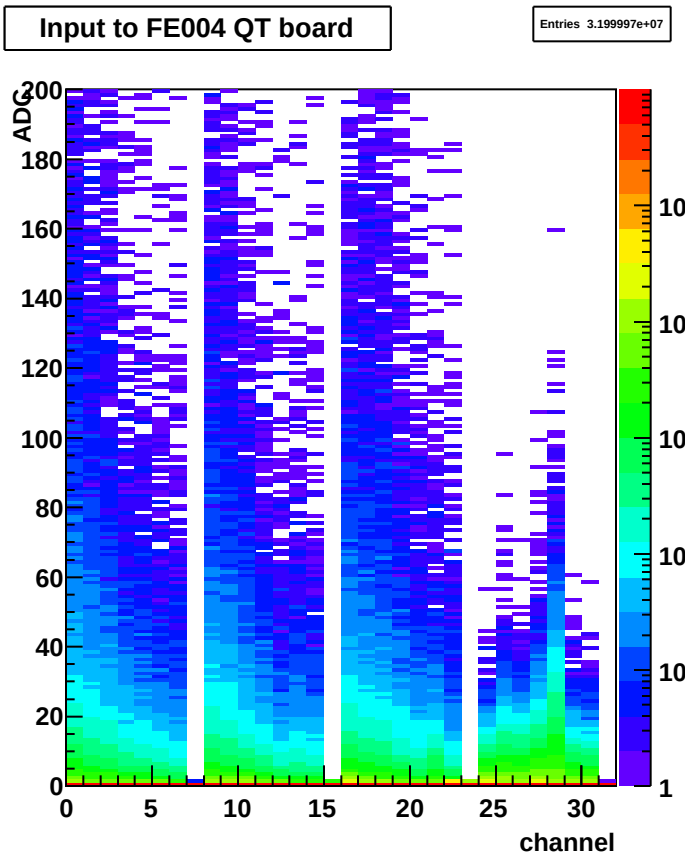

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

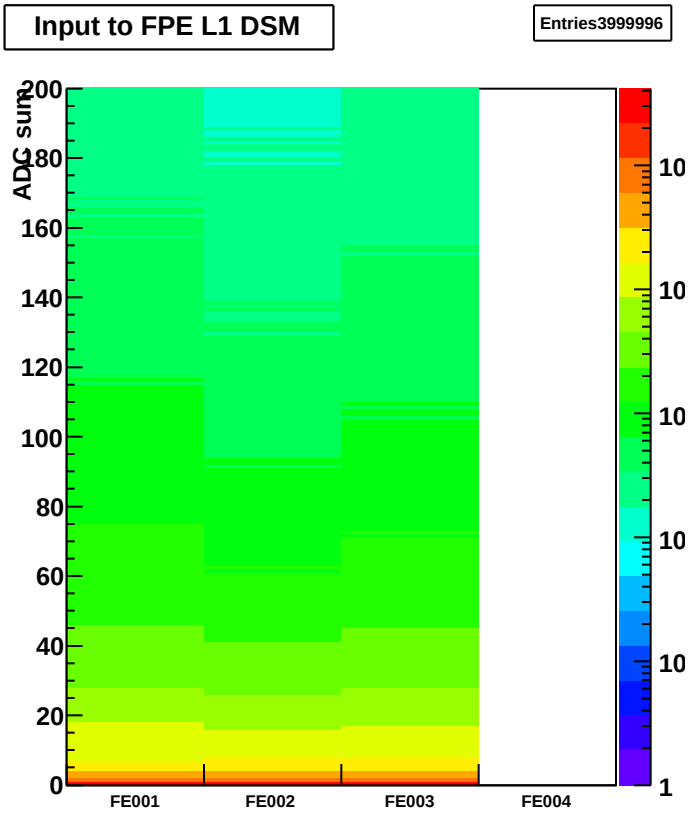

Input to FPD L2 DSM
Entries 3999996
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

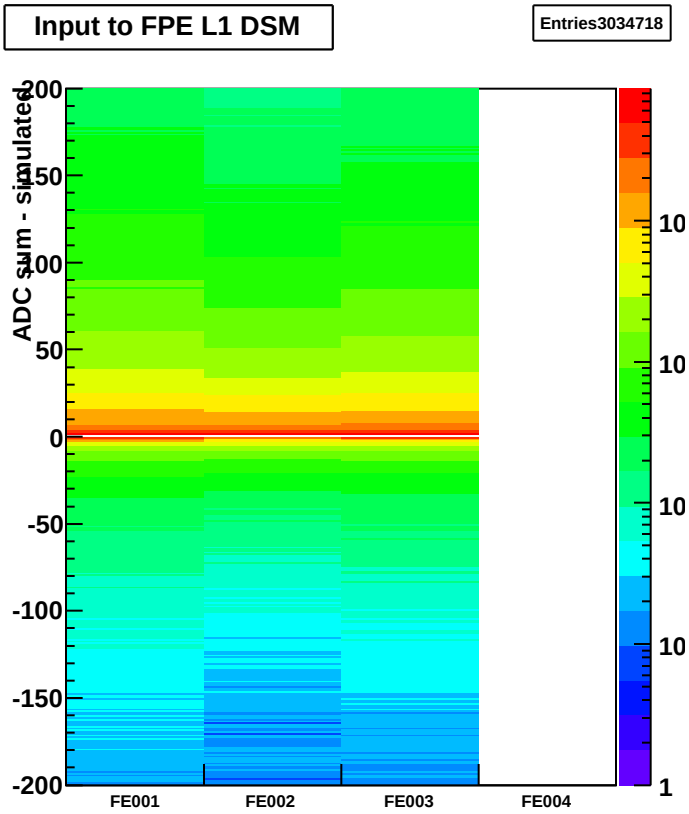

Input to FE001 QT board
Entries 3.199997e+07
ADC
channel

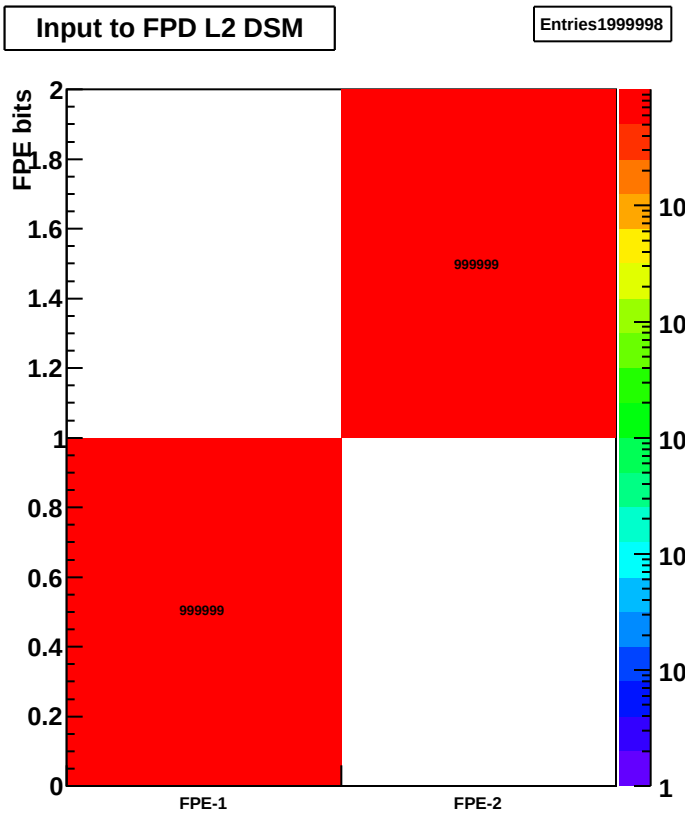

Input to FE002 QT board
Entries 3.199997e+07
ADC
channel

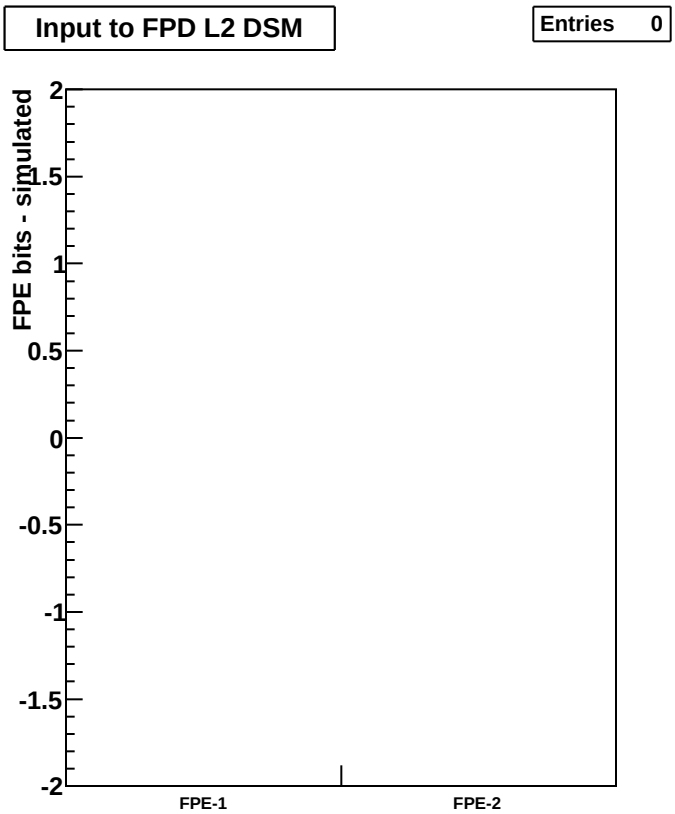

Input to FE003 QT board
Entries 3.199997e+07
ADC
channel


Input to FE004 QT board
Entries 3.199997e+07
ADC
channel


Input to FPE L1 DSM
Entries3999996
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries3034718
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1999998
FPE bits
999999
999999
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2