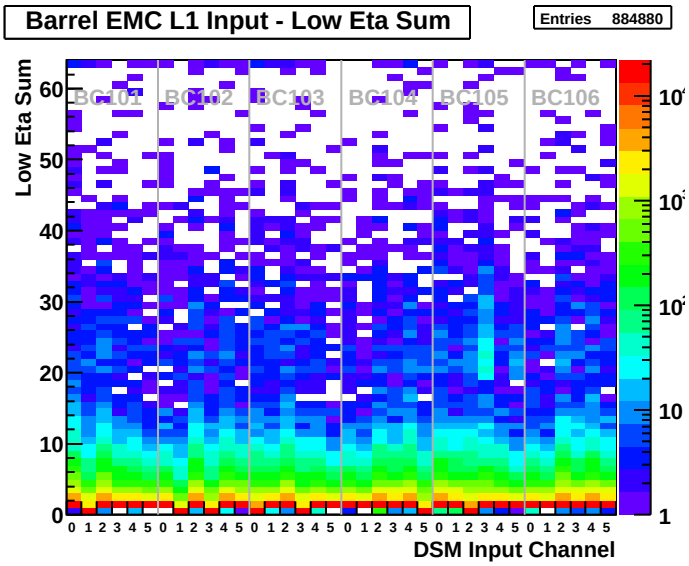
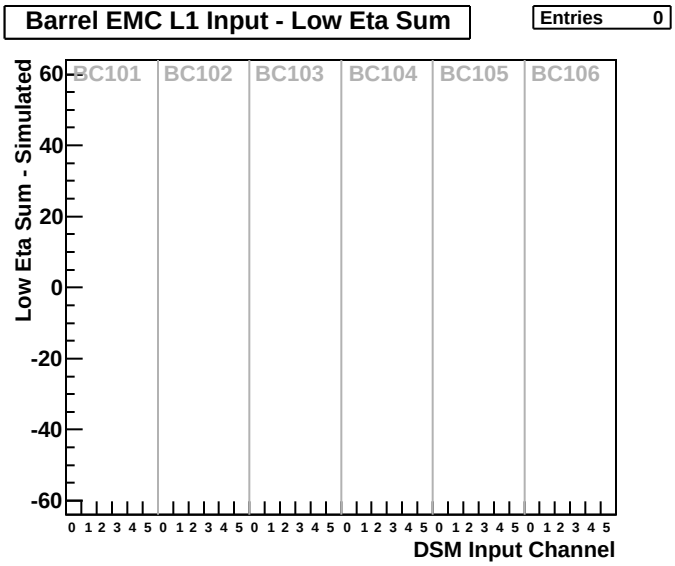
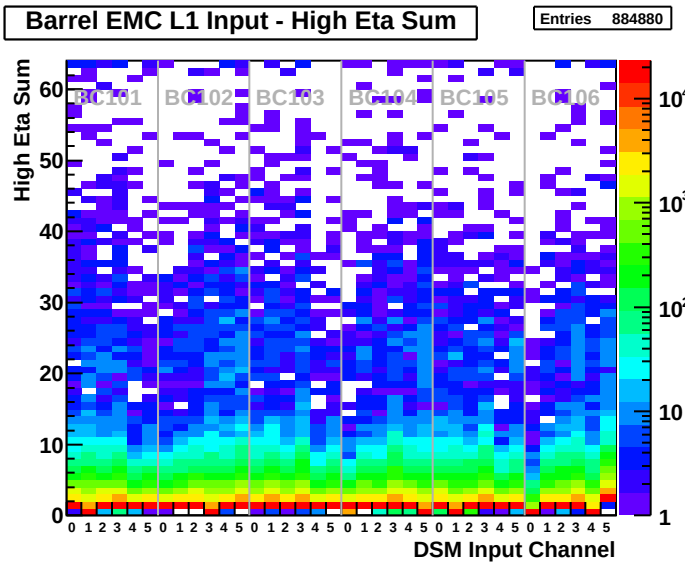
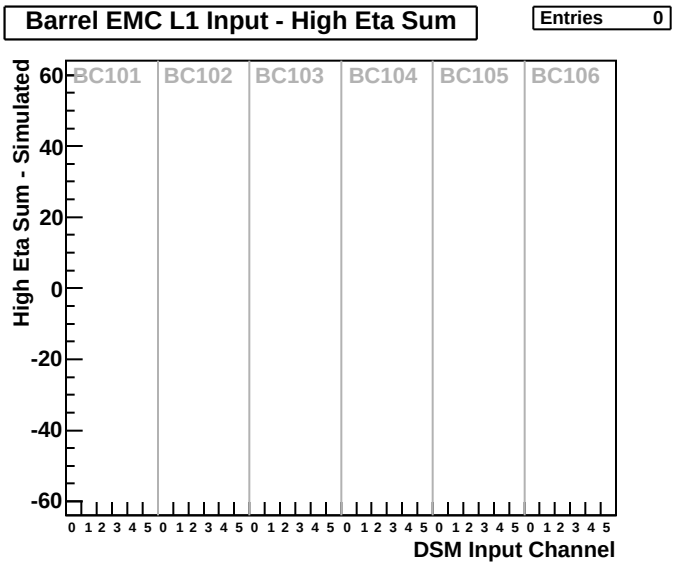
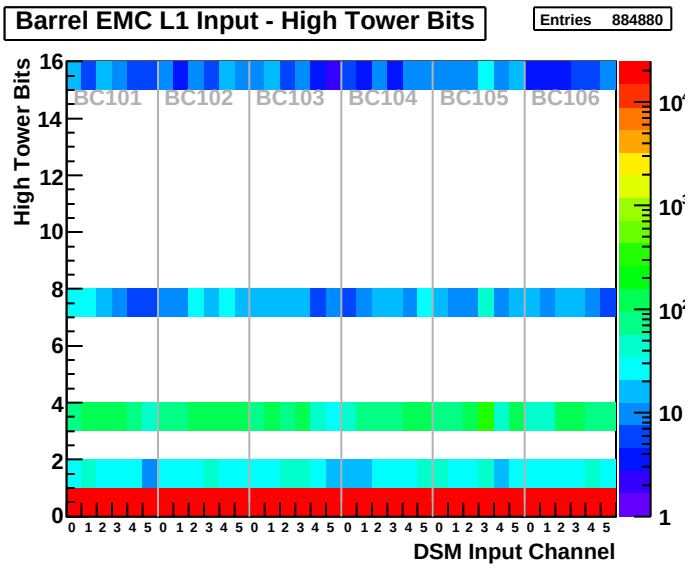
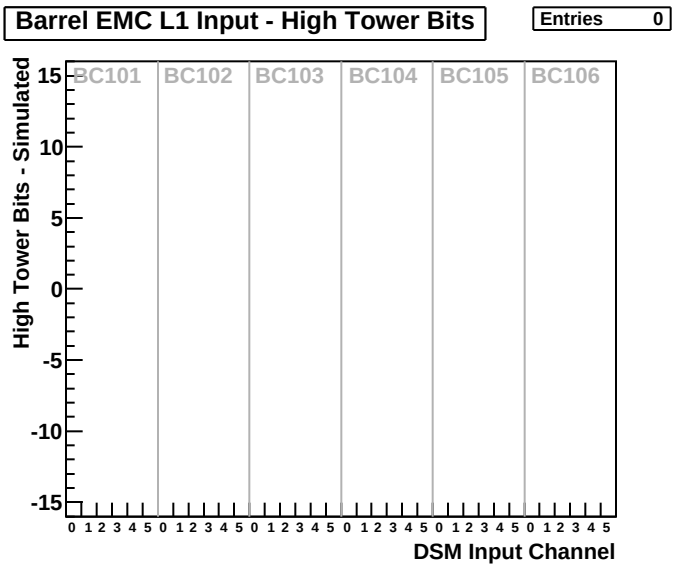

Barrel EMC L1 Input - Low Eta Sum
Entries 884880
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

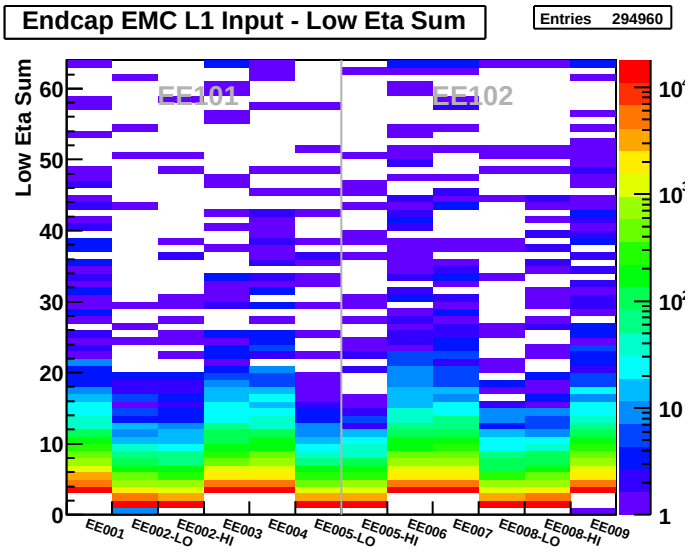

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

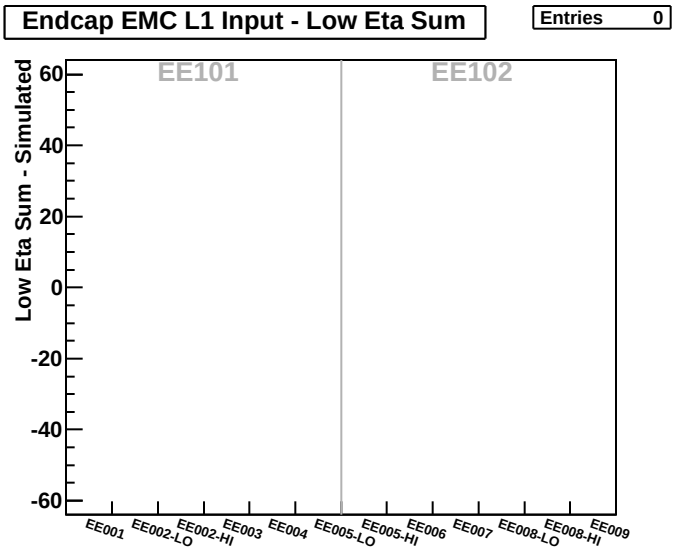

Barrel EMC L1 Input - High Eta Sum
Entries 884880
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

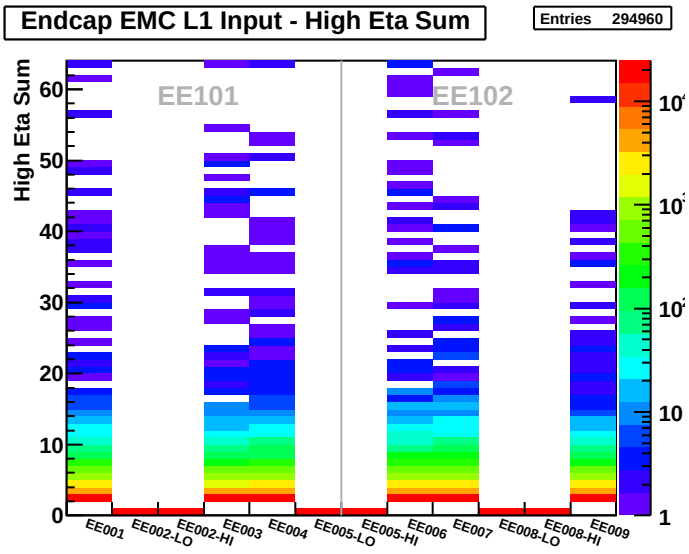

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

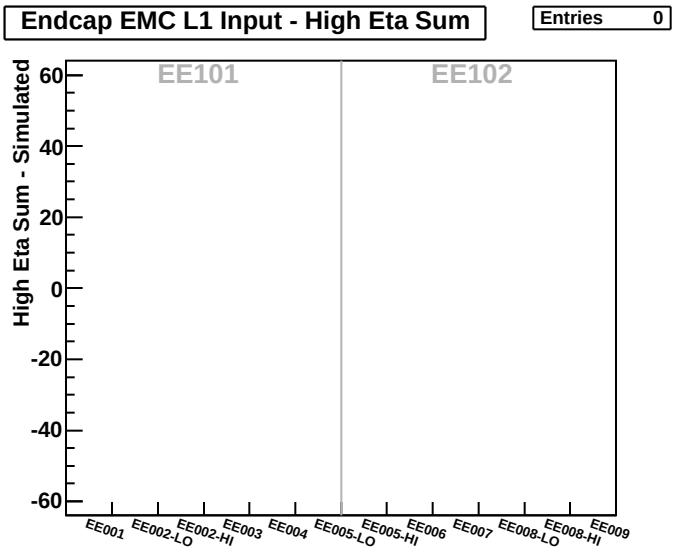

Barrel EMC L1 Input - High Tower Bits
Entries 884880
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

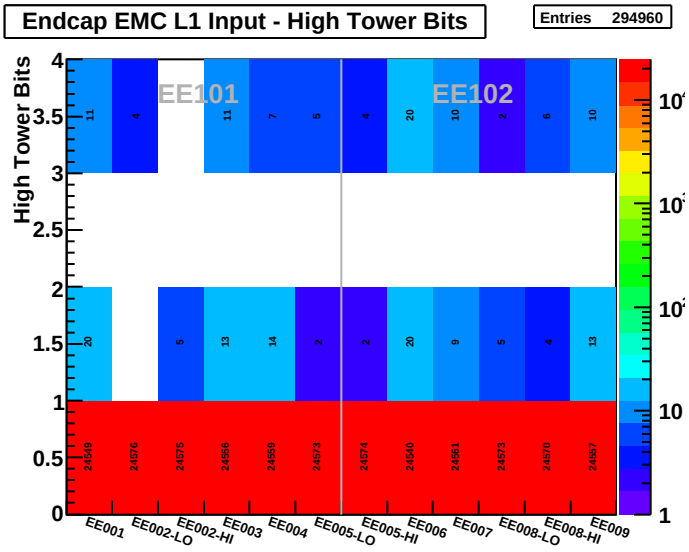

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

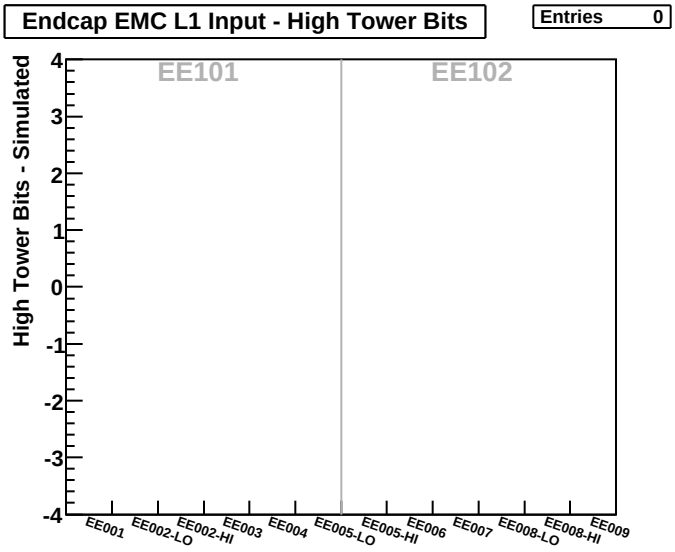

Endcap EMC L1 Input - Low Eta Sum
Entries 294960
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

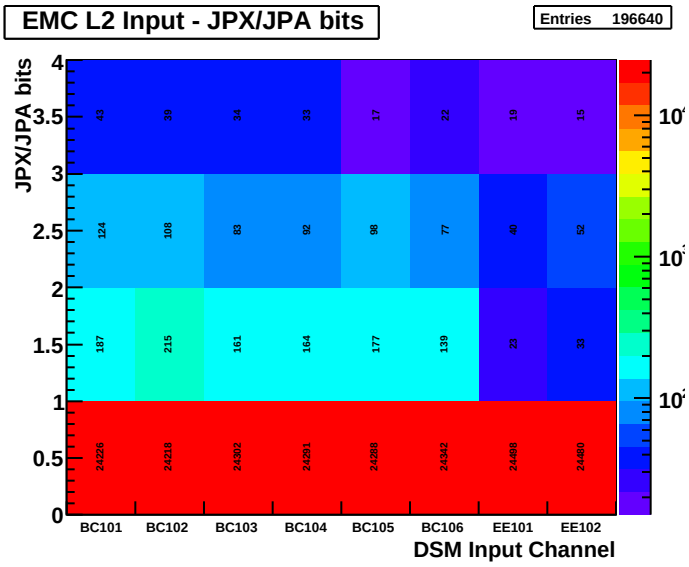

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

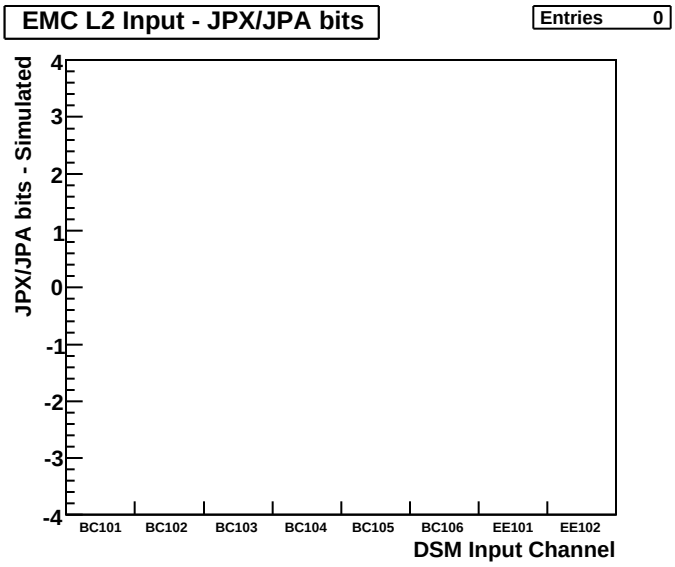

Endcap EMC L1 Input - High Eta Sum
Entries 294960
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

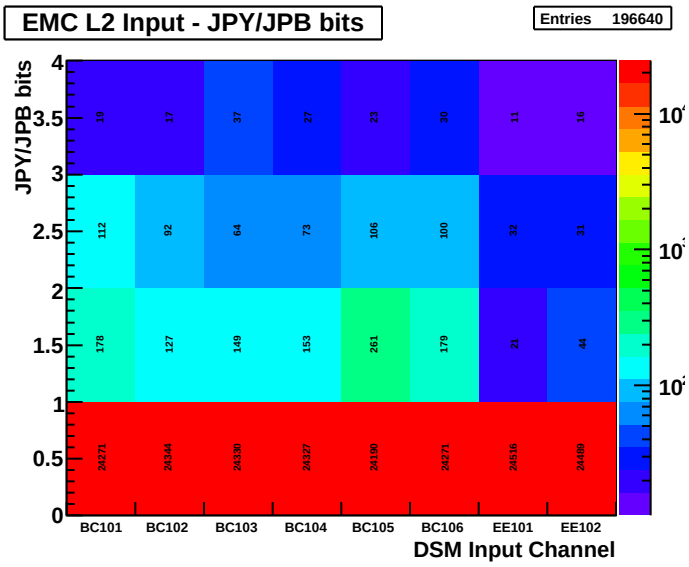

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

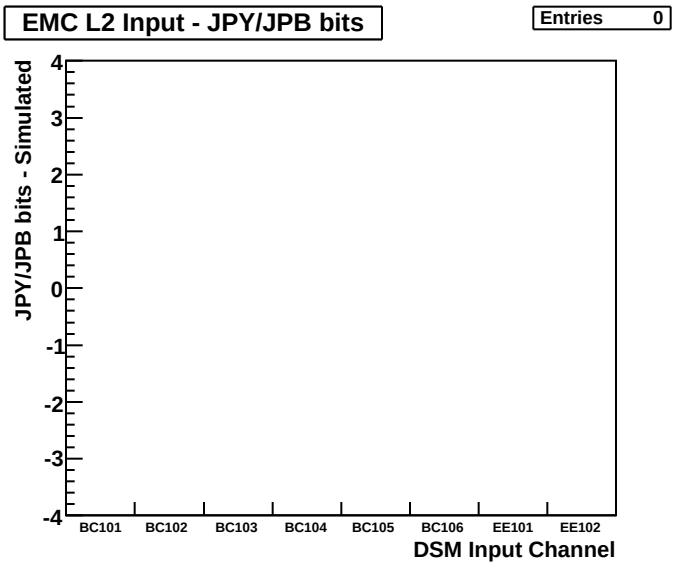

Endcap EMC L1 Input - High Tower Bits
Entries 294960
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

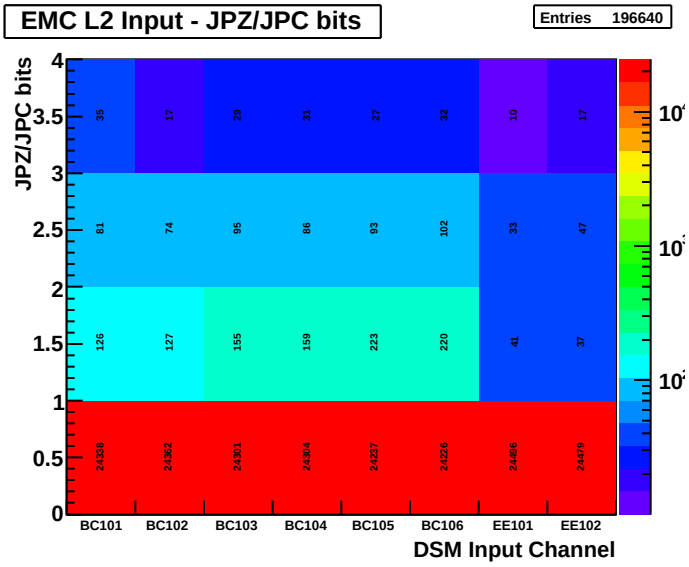

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

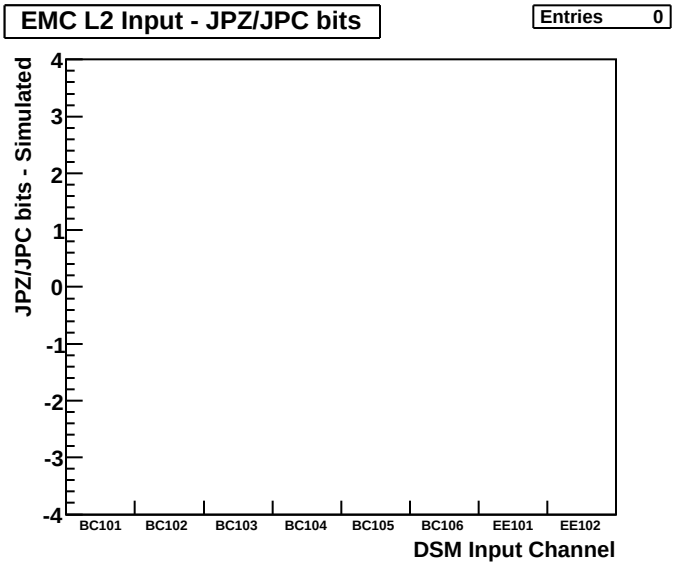

EMC L2 Input - JPX/JPA bits
Entries 196640
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
43 39 34 33 17 22 19 15
124 108 83 92 98 77 40 52
187 215 161 164 177 139 23 33
24226 24218 24302 24291 24288 24342 24498 24480

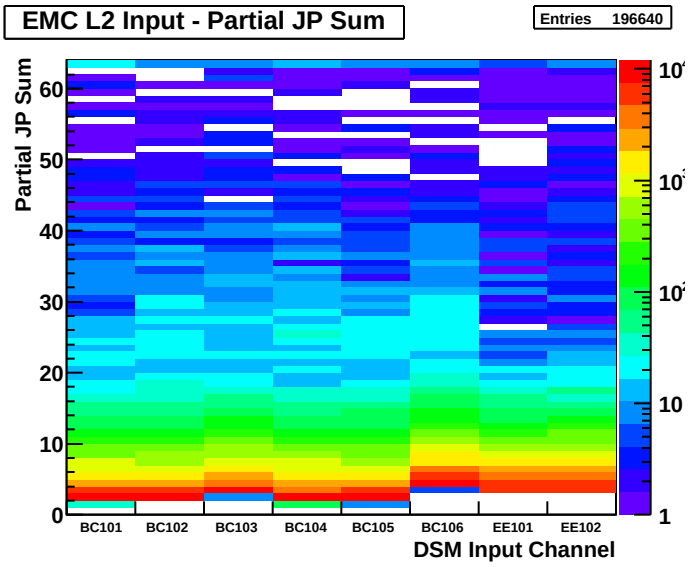

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

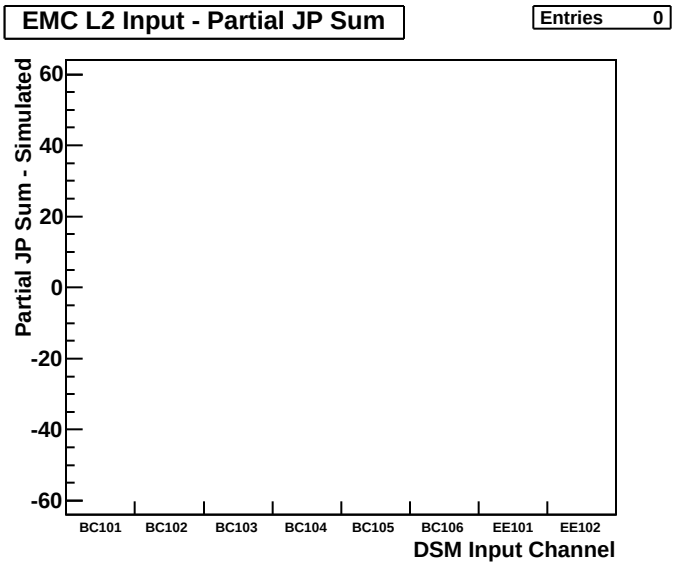

EMC L2 Input - JPY/JPB bits
Entries 196640
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
19 17 37 27 23 30 11 16
112 92 64 73 106 100 32 31
178 127 149 153 261 179 21 44
24271 24344 24330 24327 24190 24271 24516 24489

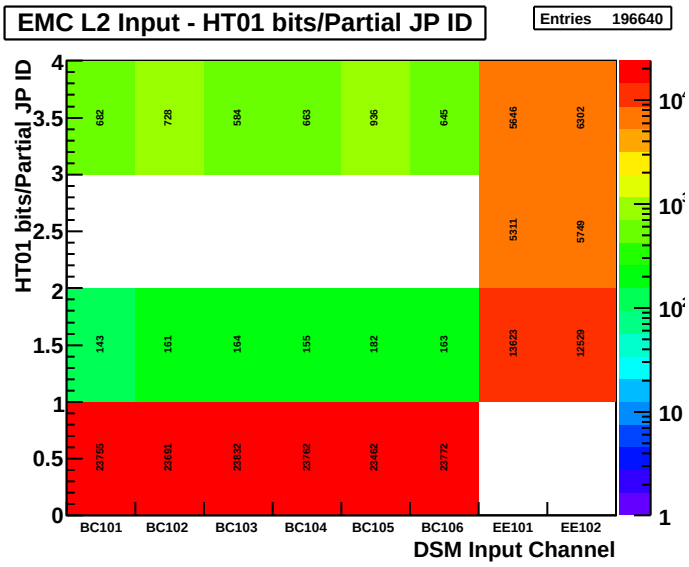

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

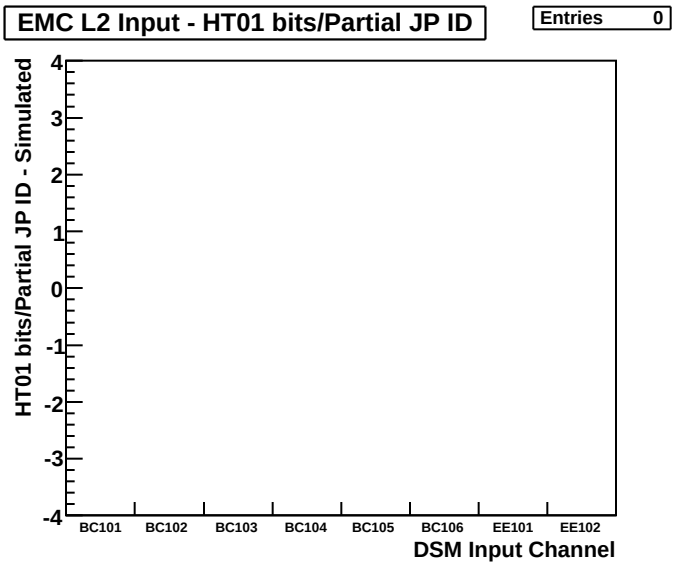

EMC L2 Input - JPZ/JPC bits
Entries 196640
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
35 17 29 31 27 32 10 17
81 74 95 86 93 102 33 47
126 127 155 159 223 220 41 37
24338 24362 24301 24304 24237 24226 24486 24479

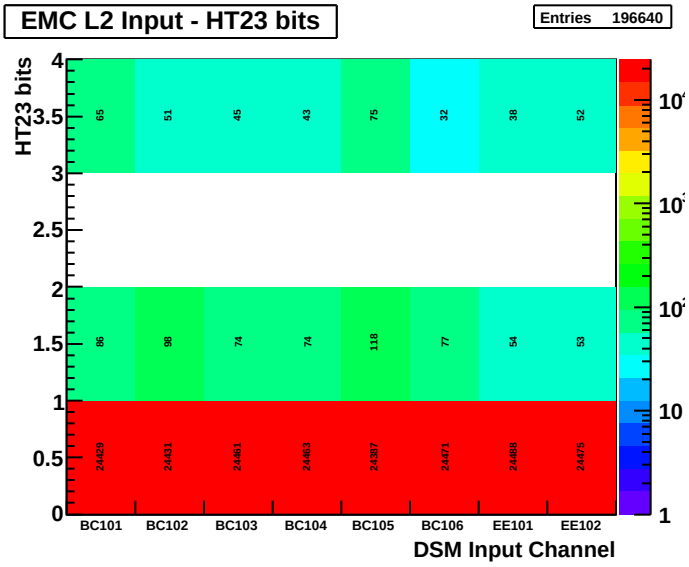

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

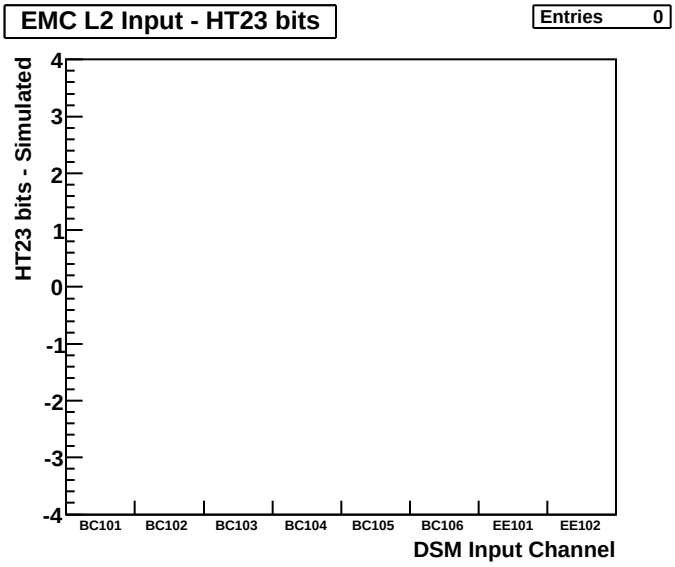

EMC L2 Input - Partial JP Sum
Entries 196640
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

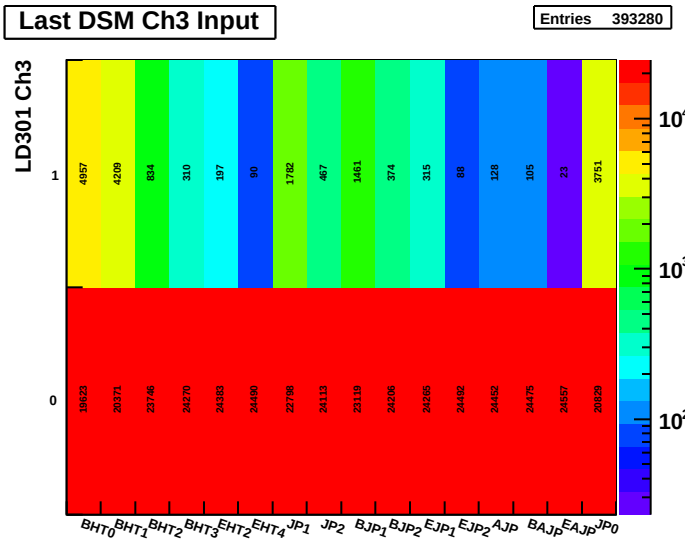

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

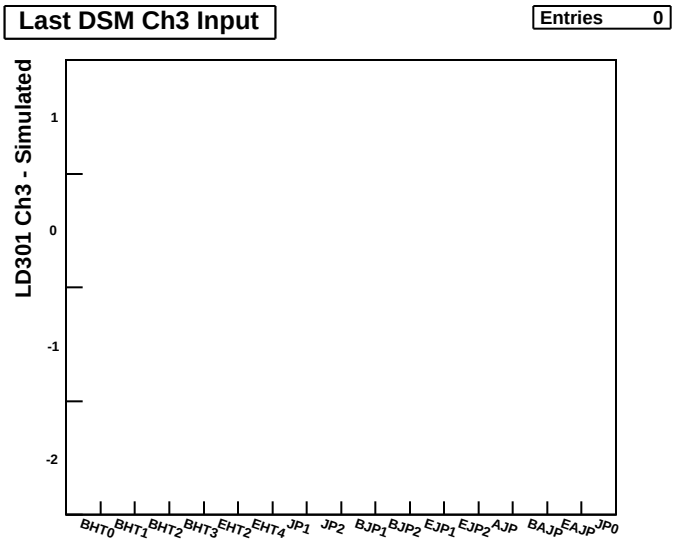

EMC L2 Input - HT01 bits/Partial JP ID
Entries 196640
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

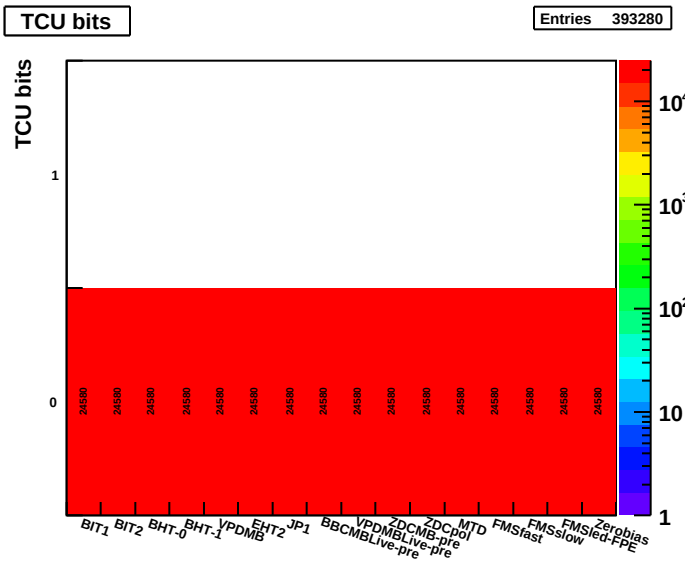

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

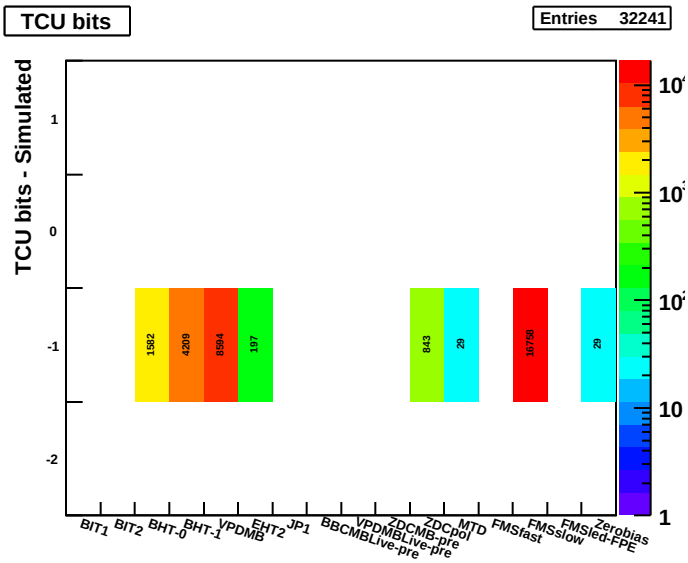

EMC L2 Input - HT23 bits
Entries 196640
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

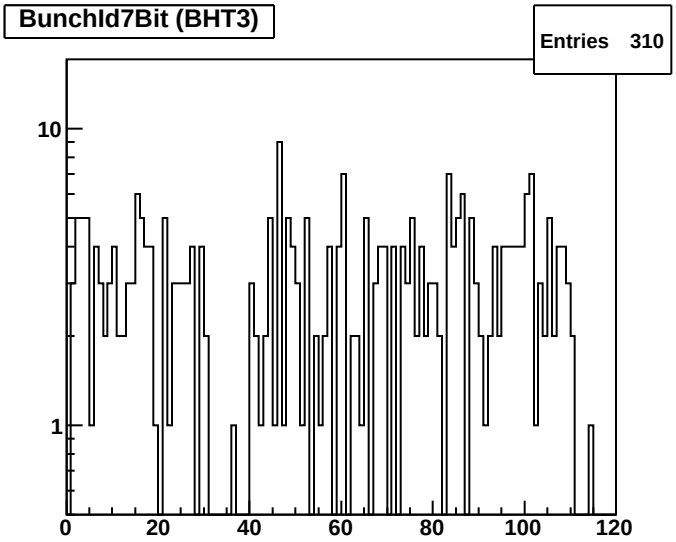

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

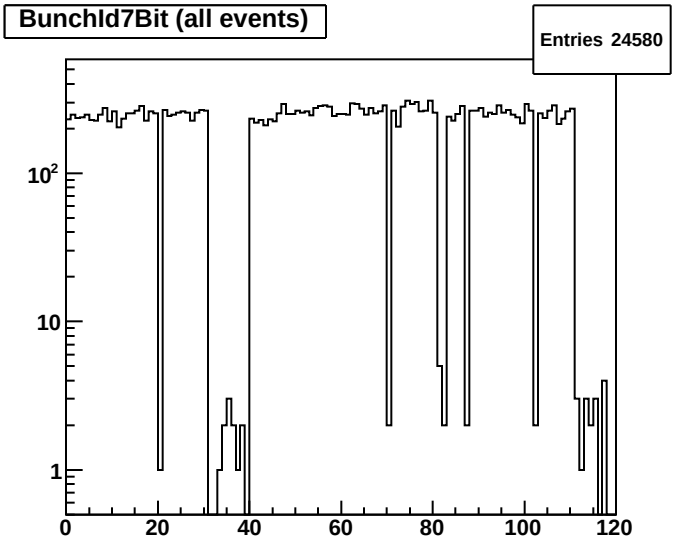

Last DSM Ch3 Input
Entries 393280
LD301 Ch3
4957 4209 834 310 197 90 1782 467 1461 374 315 88 128 105 23 3751
19623 20371 23746 24270 24383 24490 22798 24113 23119 24206 24265 24492 24452 24475 24557 20829
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

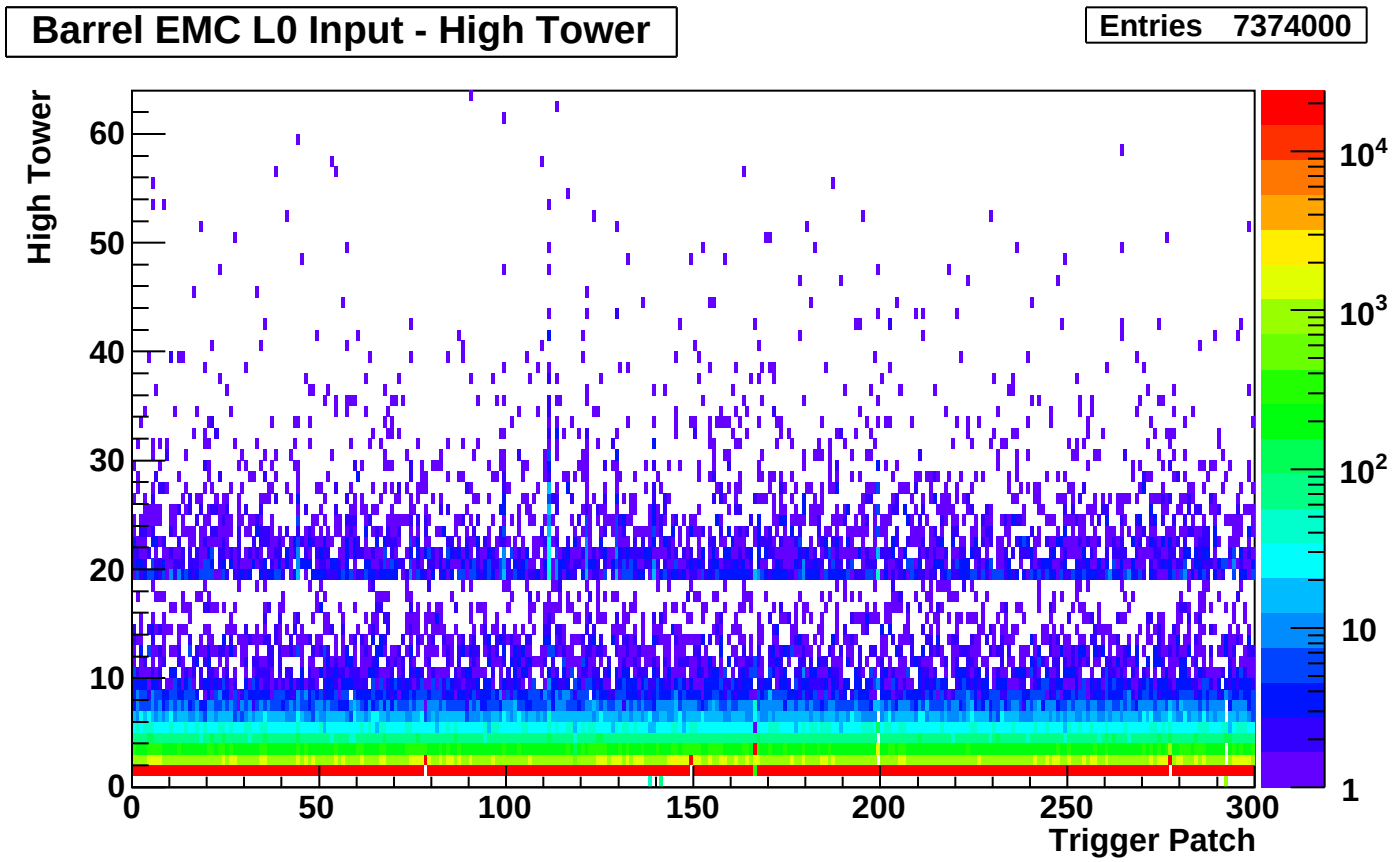

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

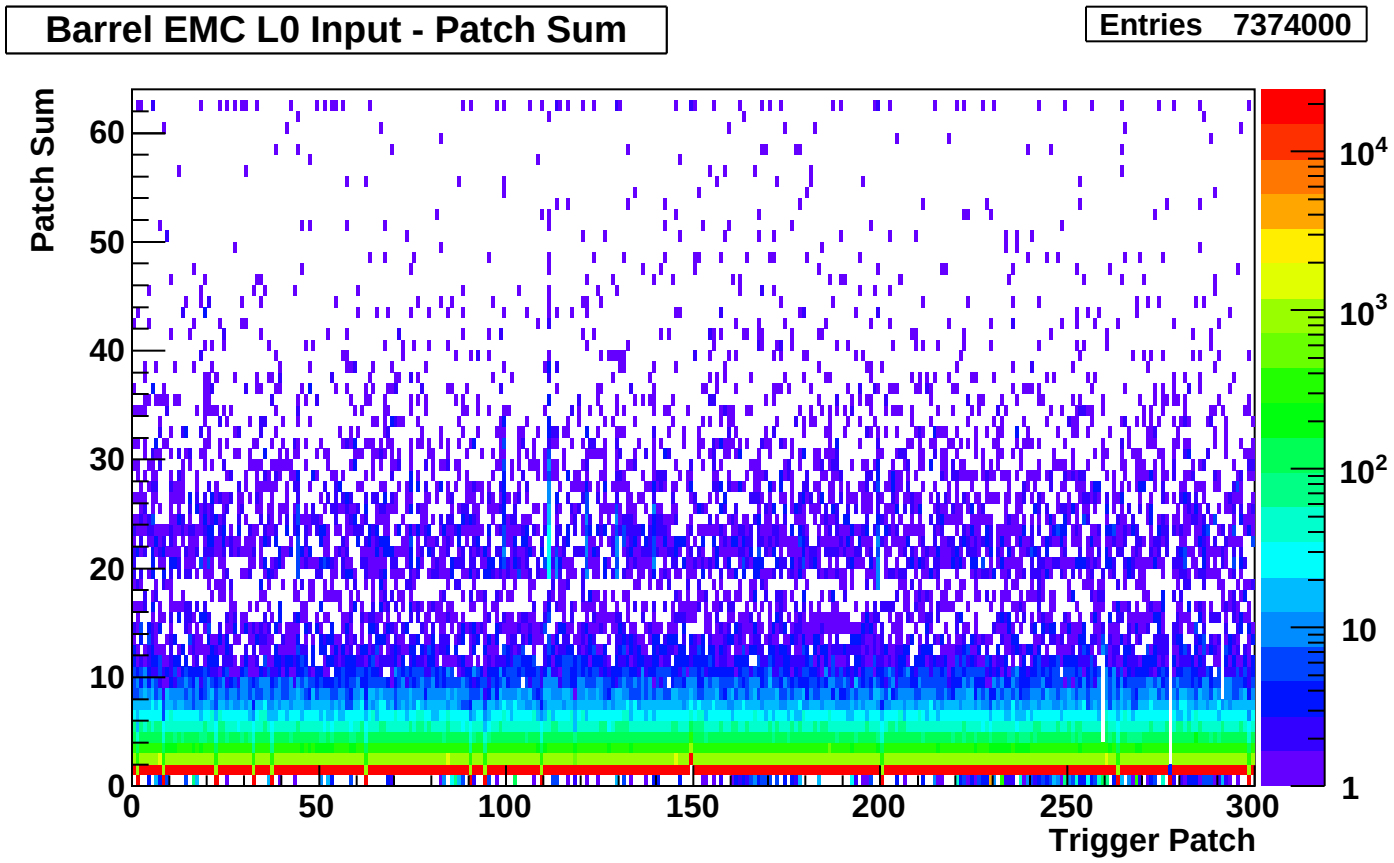

TCU bits
Entries 393280
TCU bits
24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580 24580
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

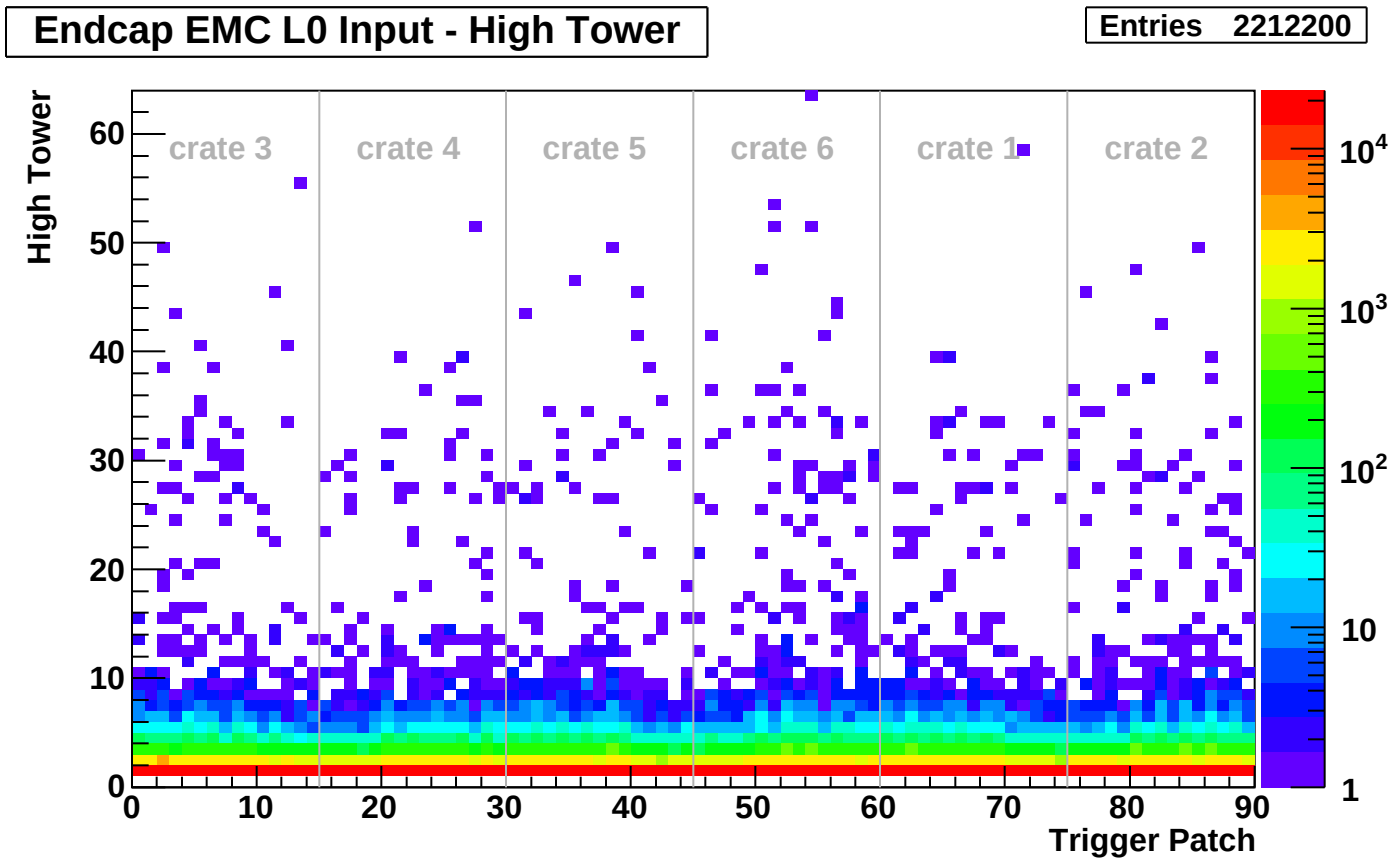

TCU bits
Entries 32241
TCU bits - Simulated
1582 4209 8594 197 843 29 16758 29
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

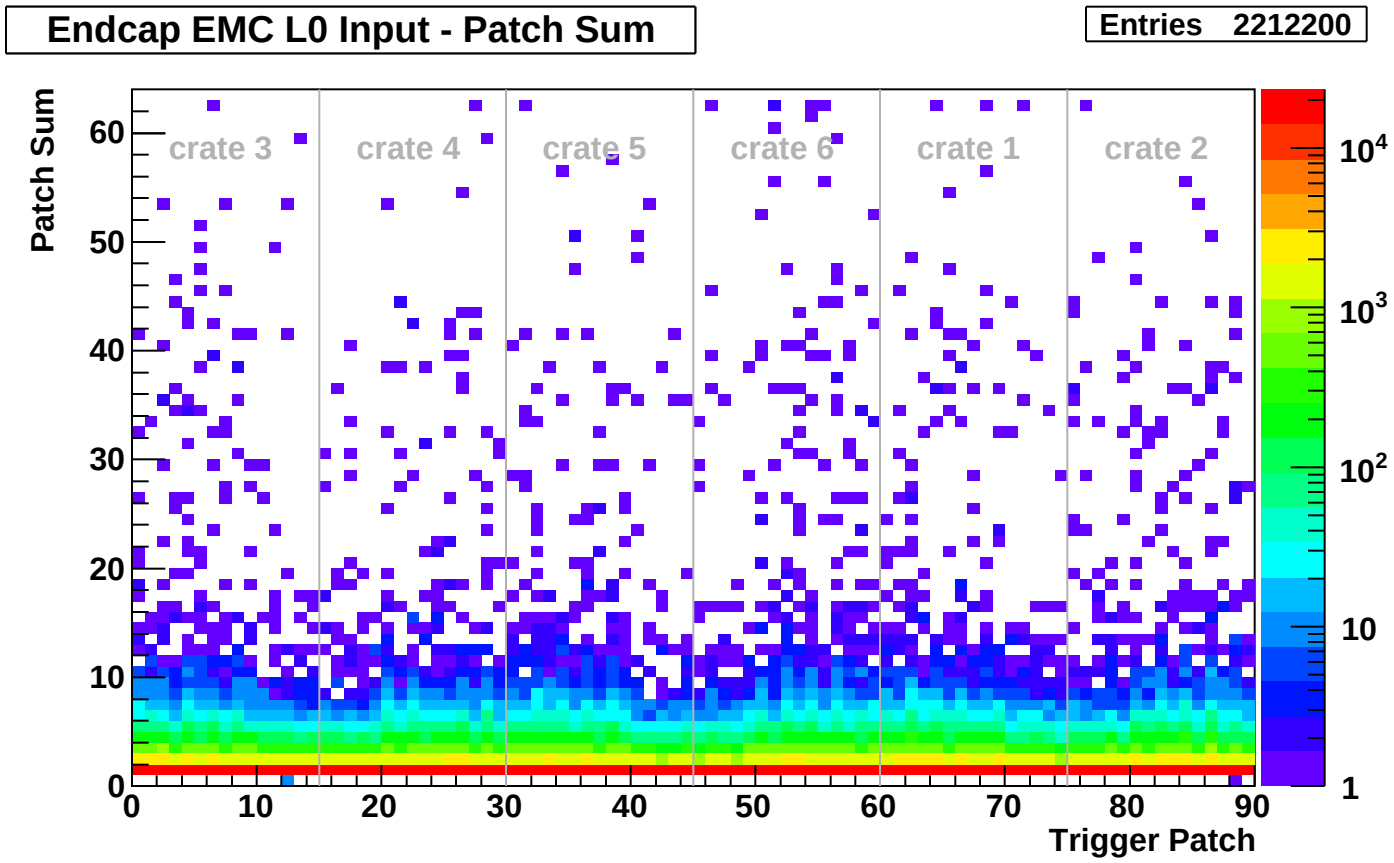

BunchId7Bit (BHT3)
Entries 310

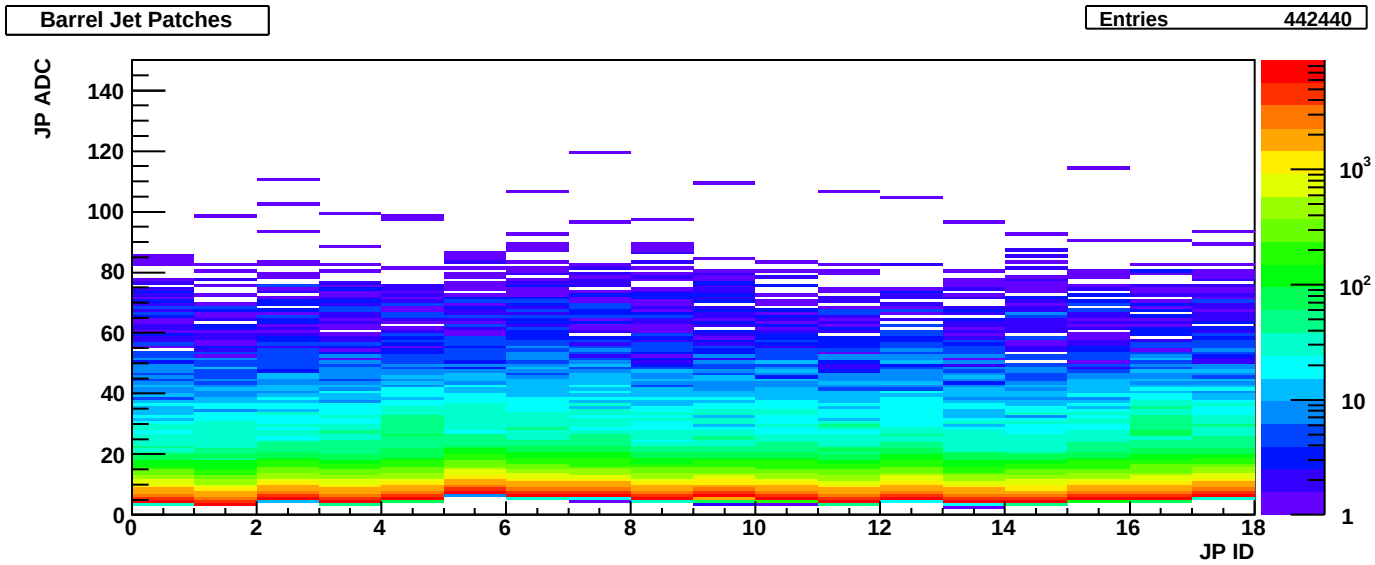

BunchId7Bit (all events)
Entries 24580

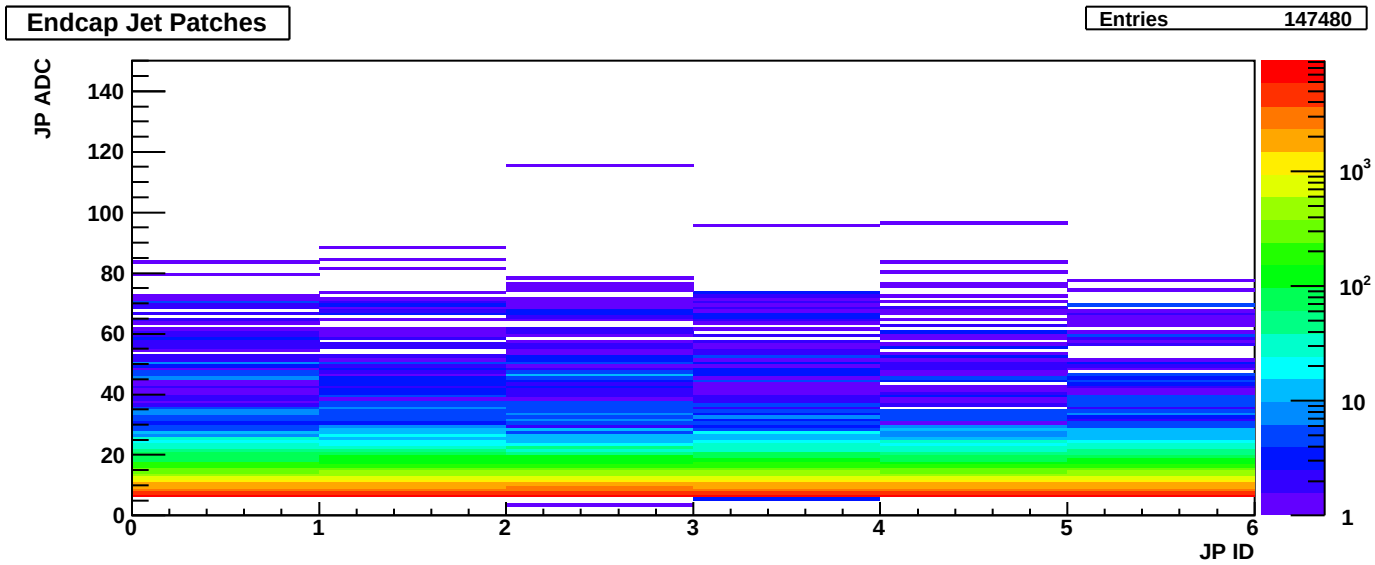

Barrel EMC L0 Input - High Tower
Entries 7374000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

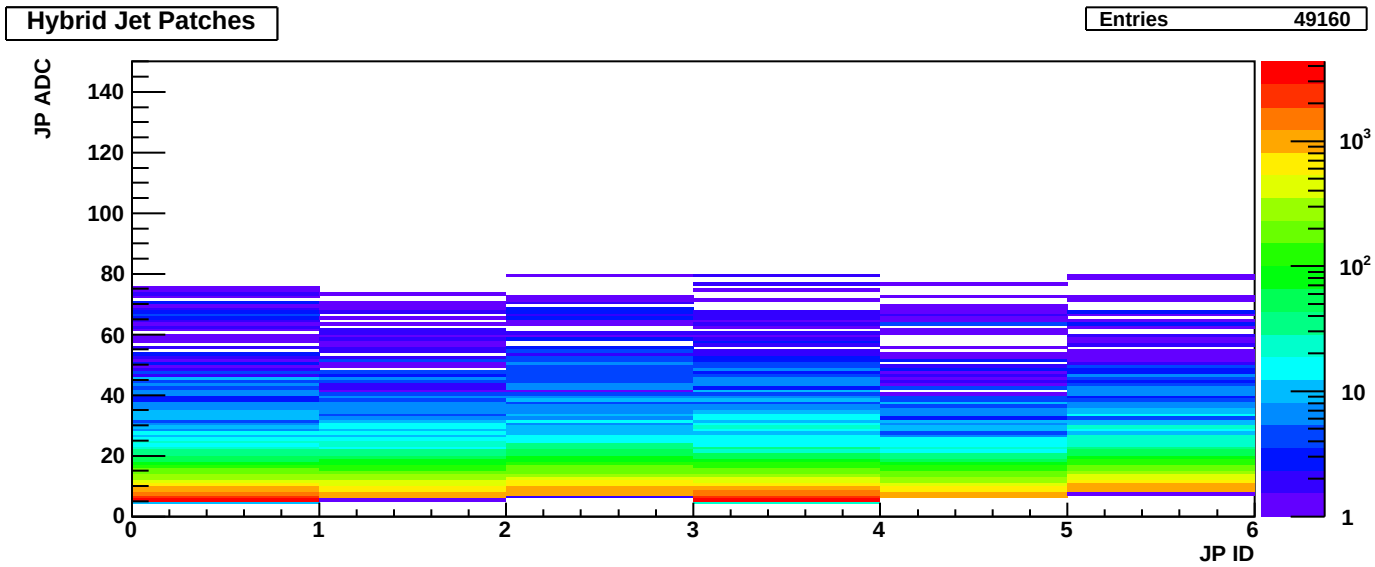

Barrel EMC L0 Input - Patch Sum
Entries 7374000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4


Endcap EMC L0 Input - High Tower
Entries 2212200
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

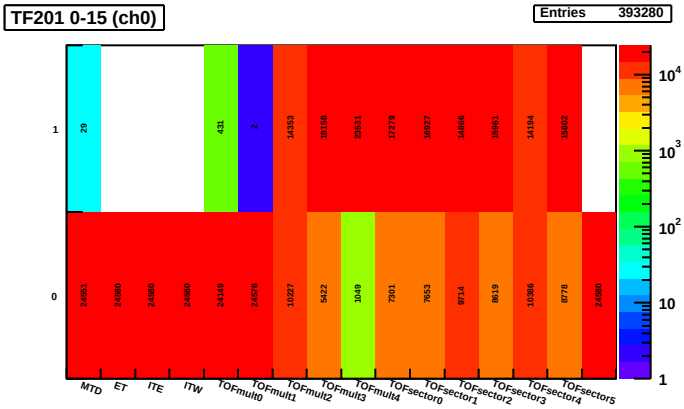

Endcap EMC L0 Input - Patch Sum
Entries 2212200
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 442440
JP ADC
JP ID

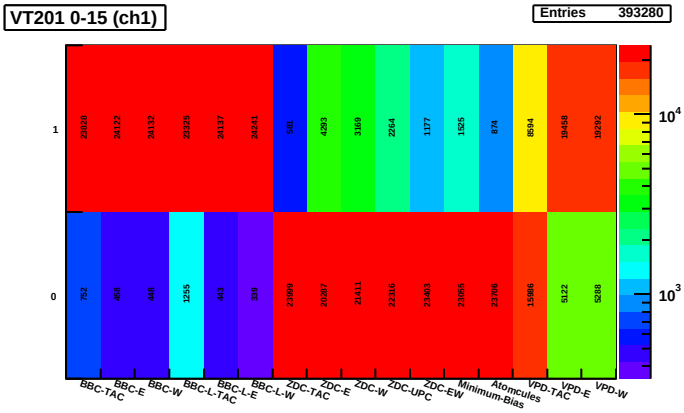

Endcap Jet Patches
Entries 147480
JP ADC
JP ID


Hybrid Jet Patches
Entries 49160
JP ADC
JP ID

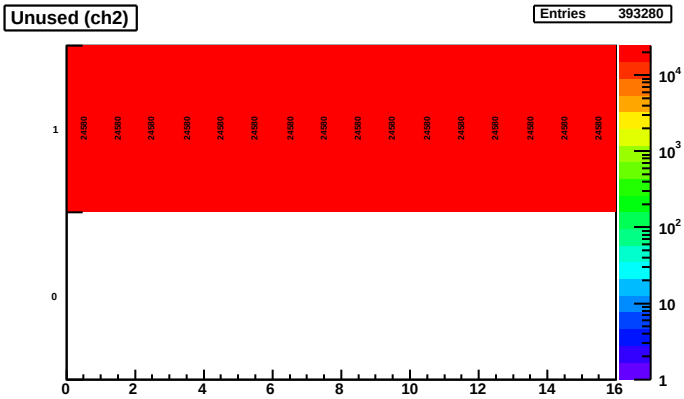
TF201 0-15 (ch0)



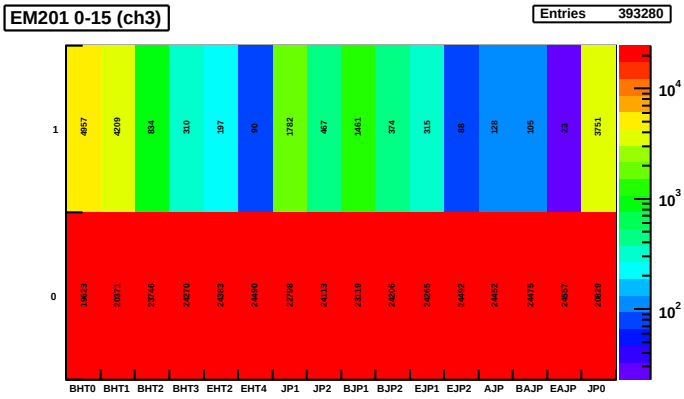
VT201 0-15 (ch1)



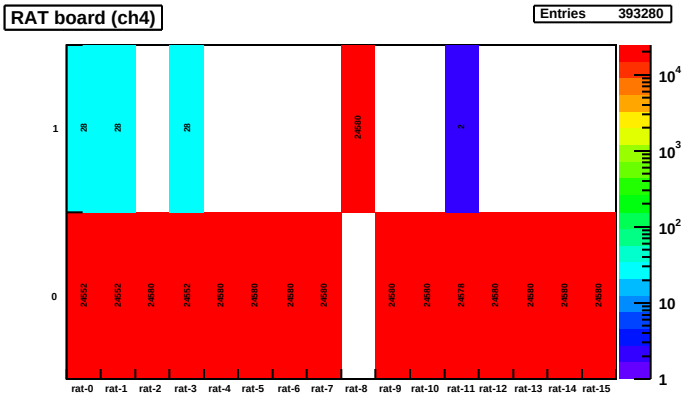
Unused (ch2)



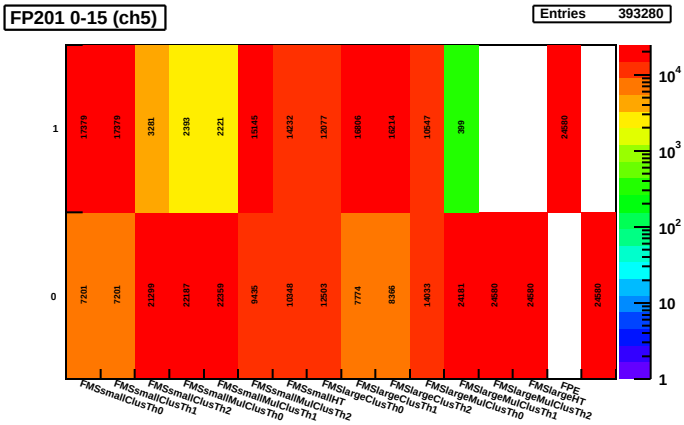
EM201 0-15 (ch3)



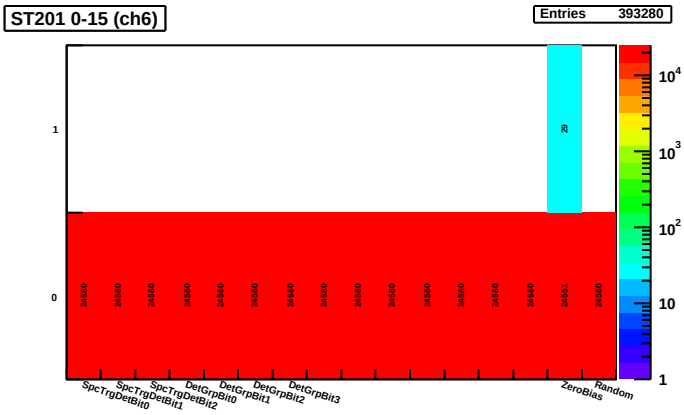
RAT board (ch4)



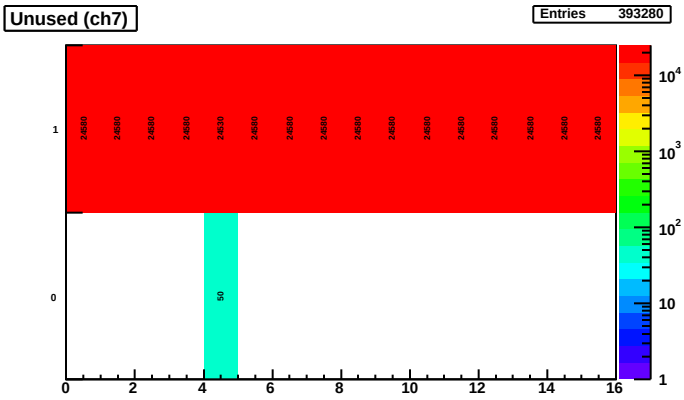
FP201 0-15 (ch5)

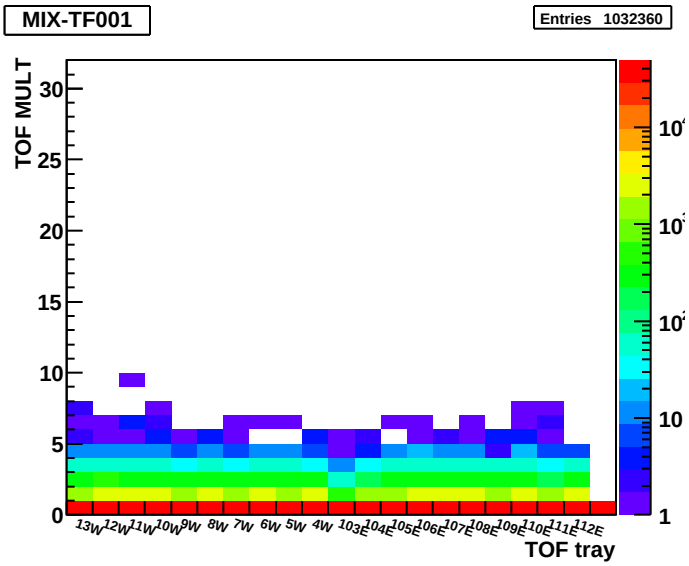


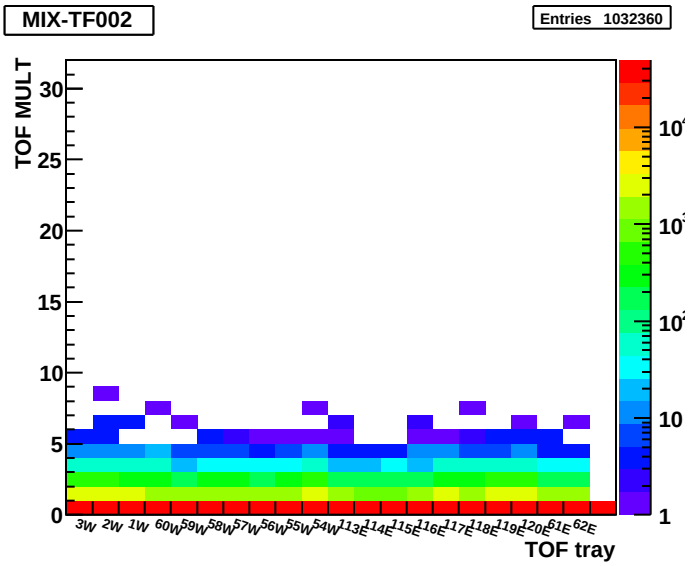
ST201 0-15 (ch6)

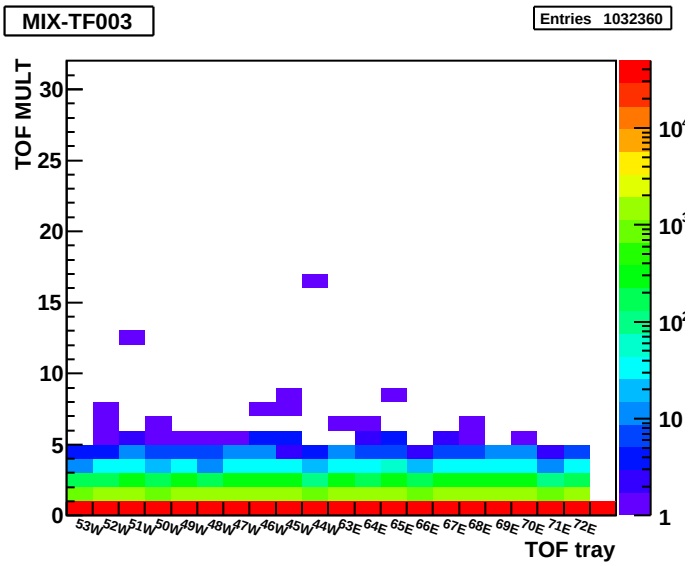


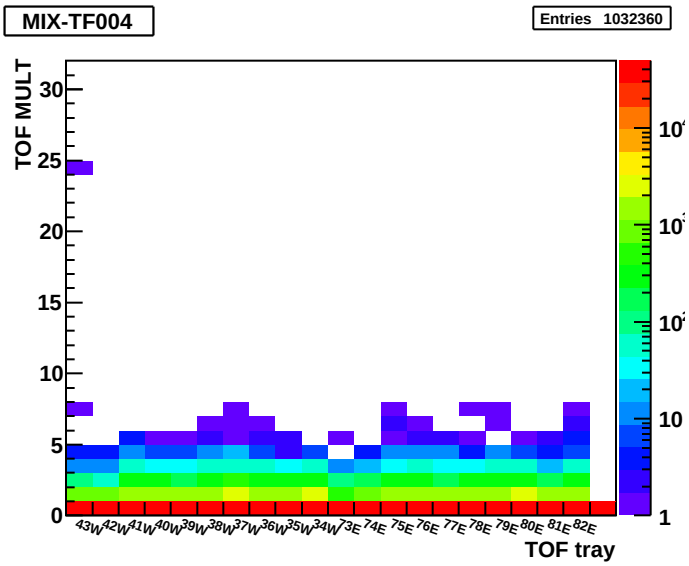
Unused (ch7)

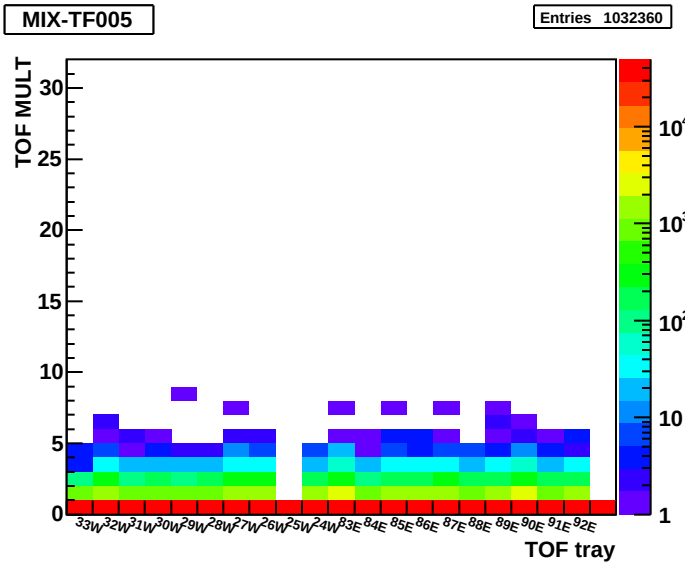


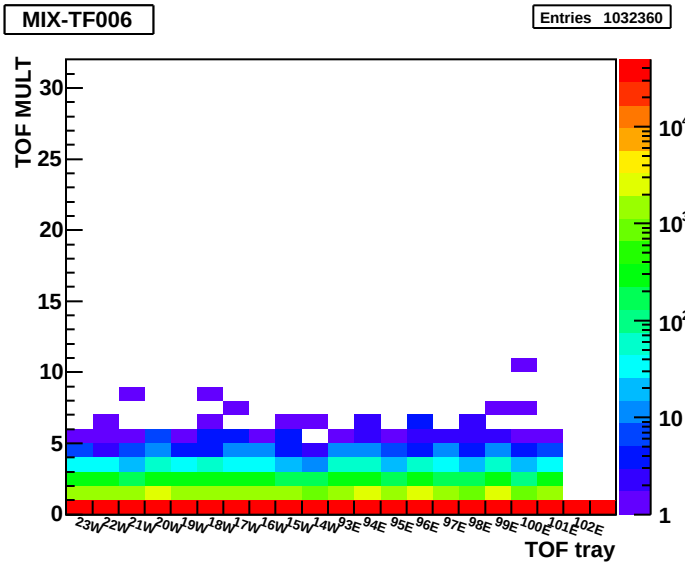

MIX-TF001
Entries 1032360
TOF MULT
TOF tray

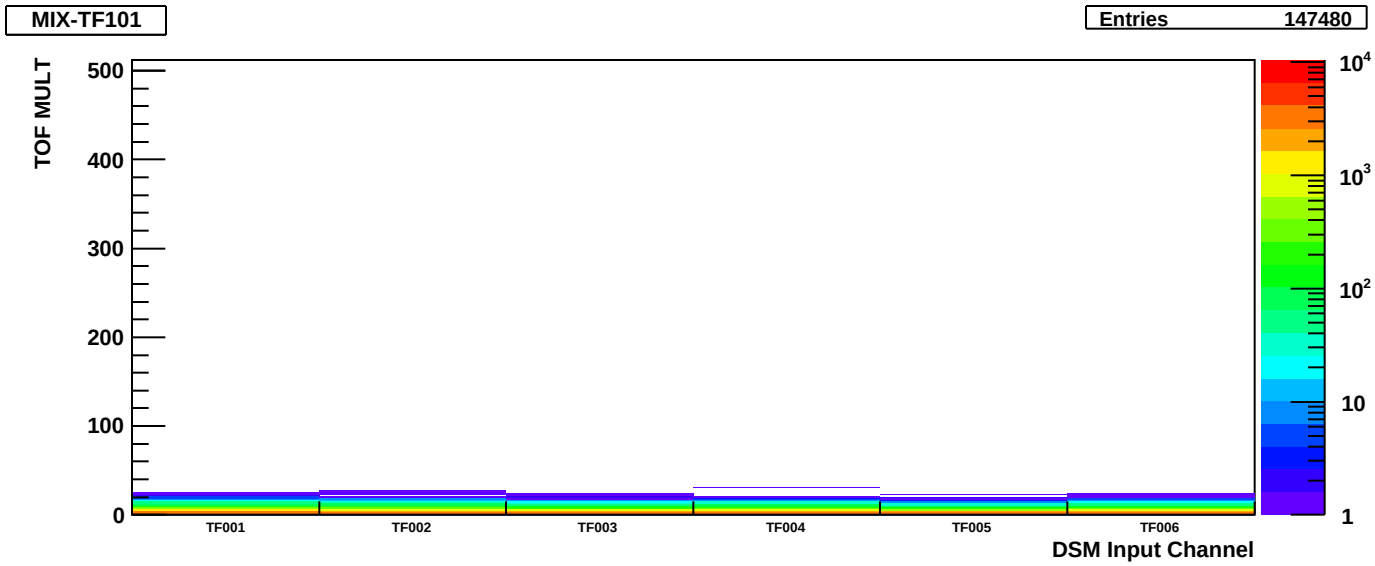

MIX-TF002
Entries 1032360
TOF MULT
TOF tray

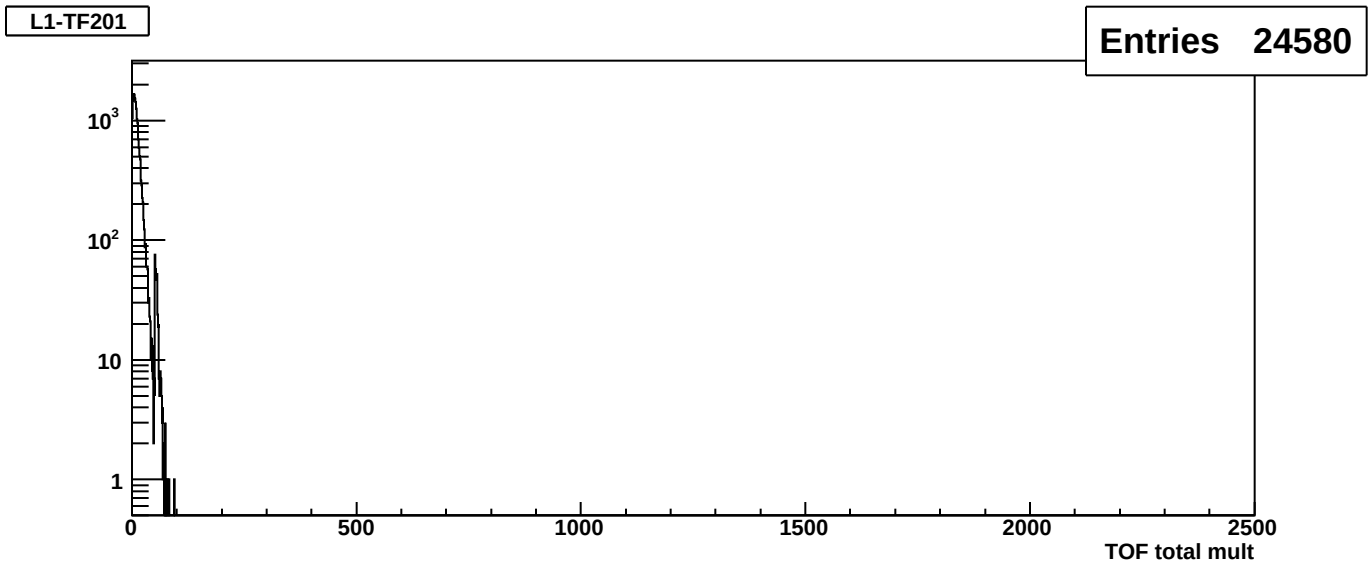

MIX-TF003
Entries 1032360
TOF MULT
TOF tray

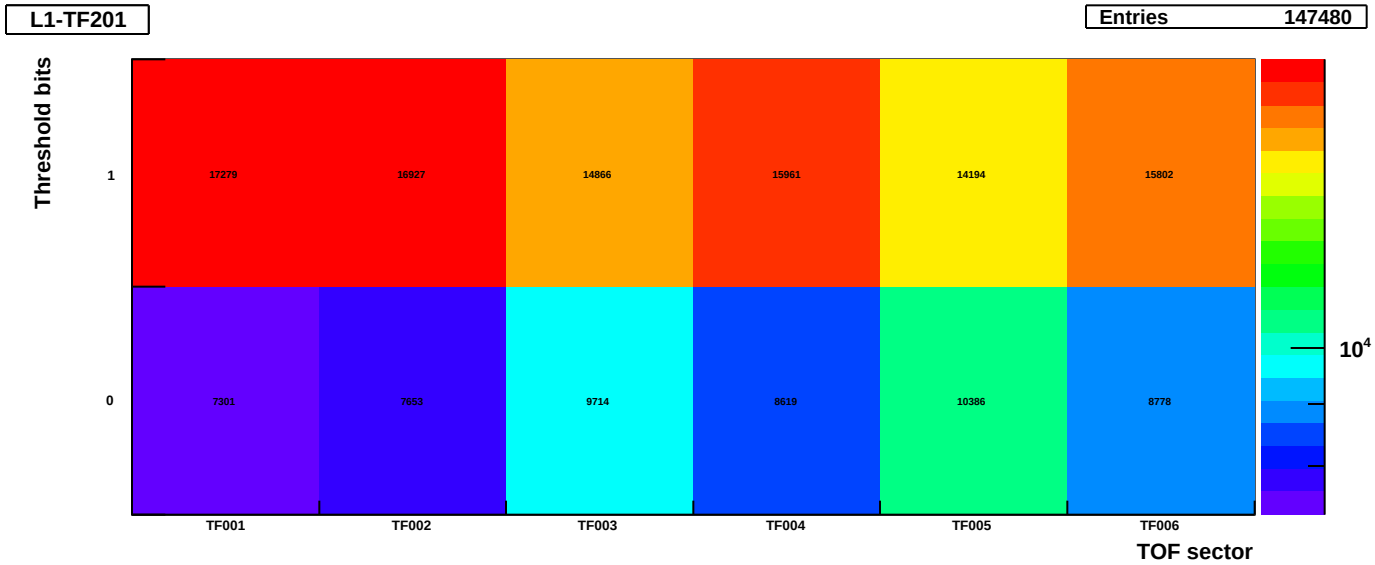

MIX-TF004
Entries 1032360
TOF MULT
TOF tray

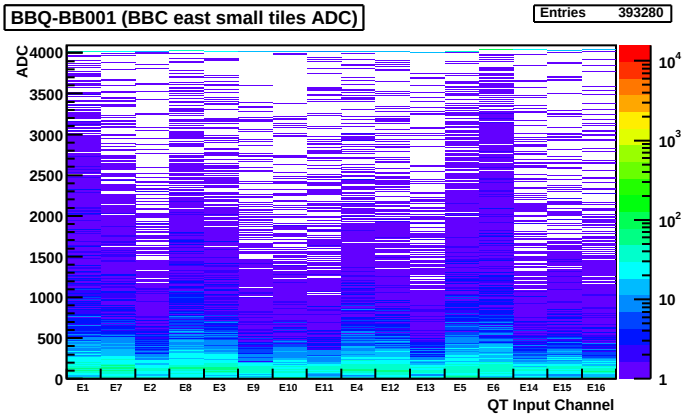

MIX-TF005
Entries 1032360
TOF MULT
TOF tray

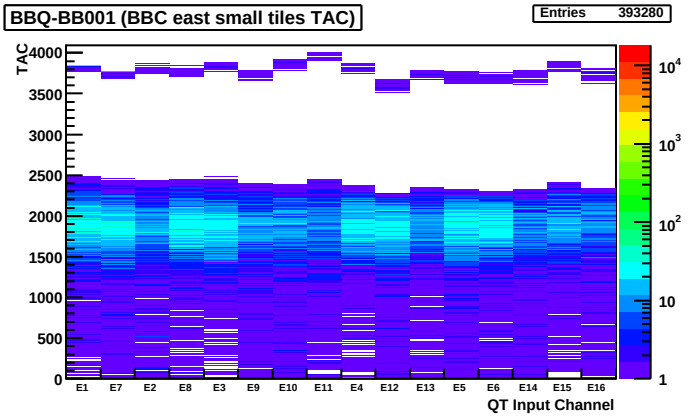

MIX-TF006
Entries 1032360
TOF MULT
TOF tray

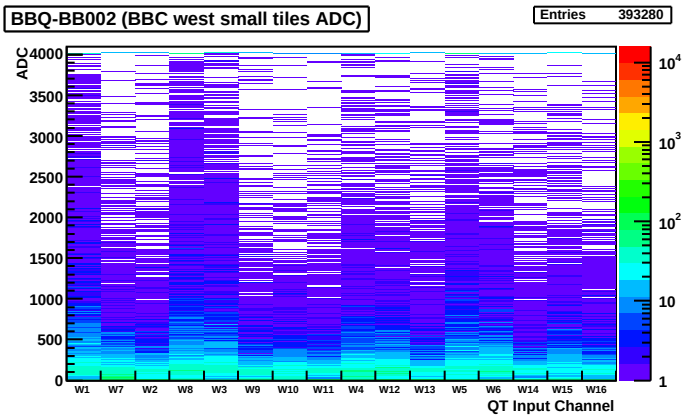

MIX-TF101
Entries 147480
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

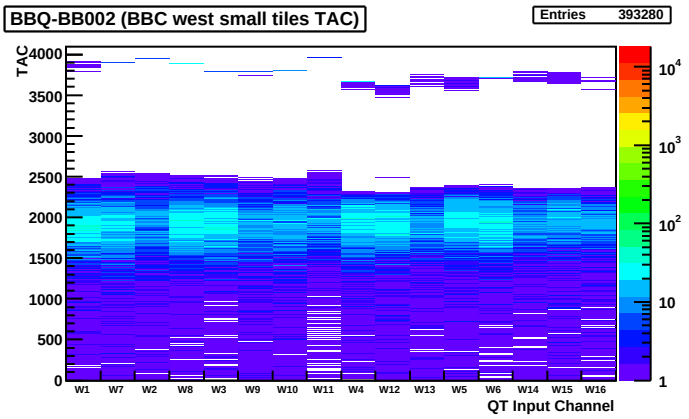

L1-TF201
Entries 24580
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

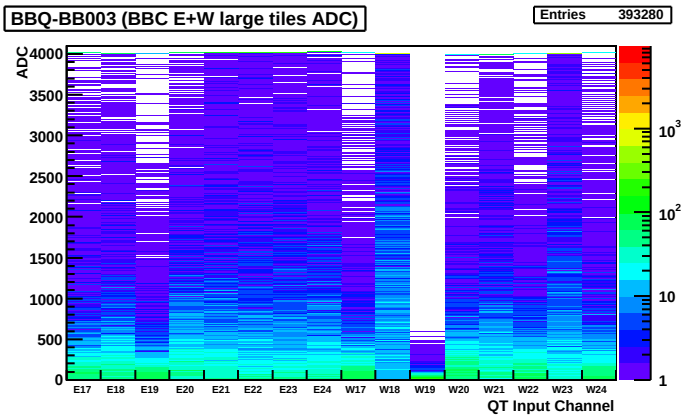

L1-TF201
Entries 147480
Threshold bits
1
0
17279
16927
14866
15961
14194
15802
7301
7653
9714
8619
10386
8778
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

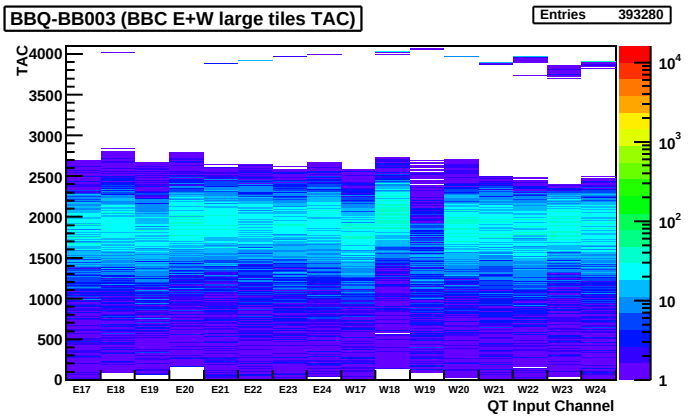

BBQ-BB001 (BBC east small tiles ADC)
Entries 393280
ADC
QT Input Channel

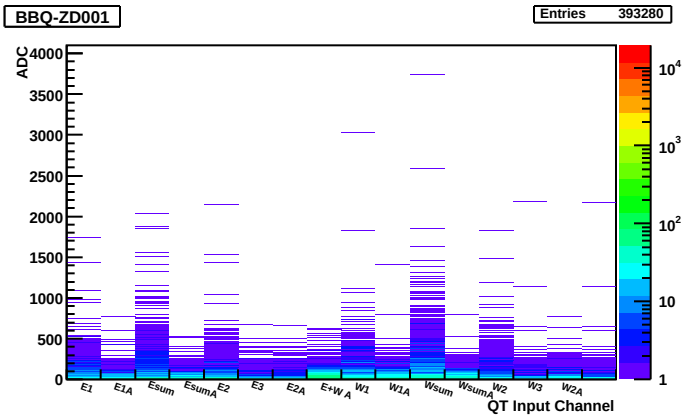

BBQ-BB001 (BBC east small tiles TAC)
Entries 393280
TAC
QT Input Channel

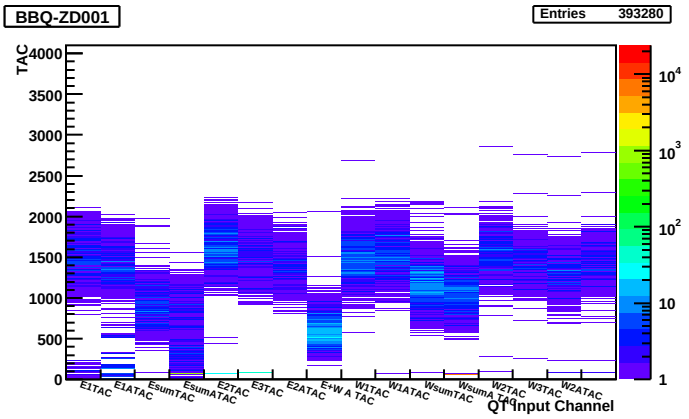

BBQ-BB002 (BBC west small tiles ADC)
Entries 393280
ADC
QT Input Channel

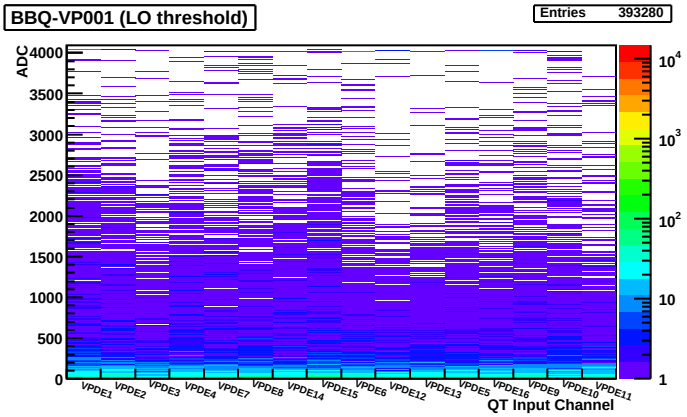

BBQ-BB002 (BBC west small tiles TAC)
Entries 393280
TAC
QT Input Channel

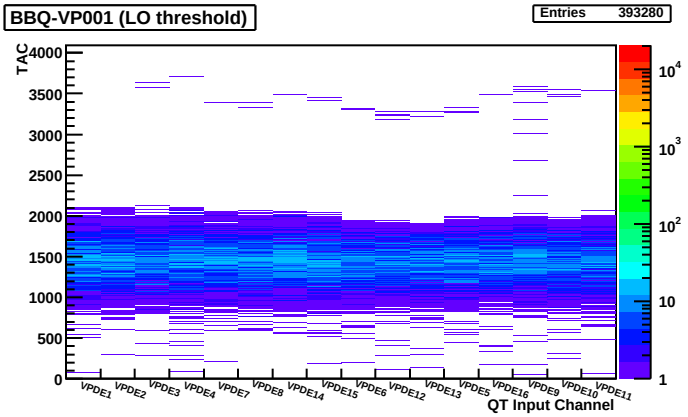

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 393280
ADC
QT Input Channel

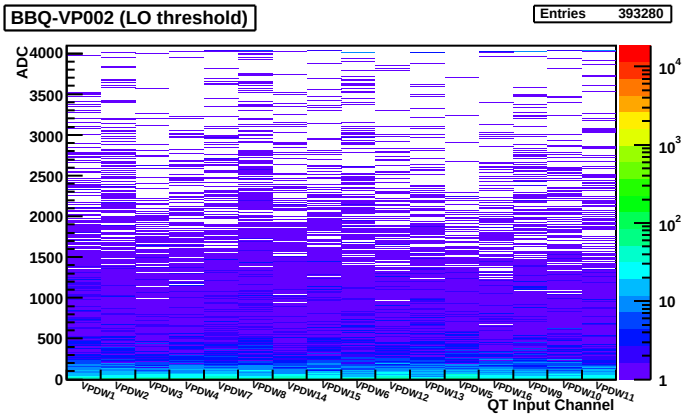

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 393280
TAC
QT Input Channel

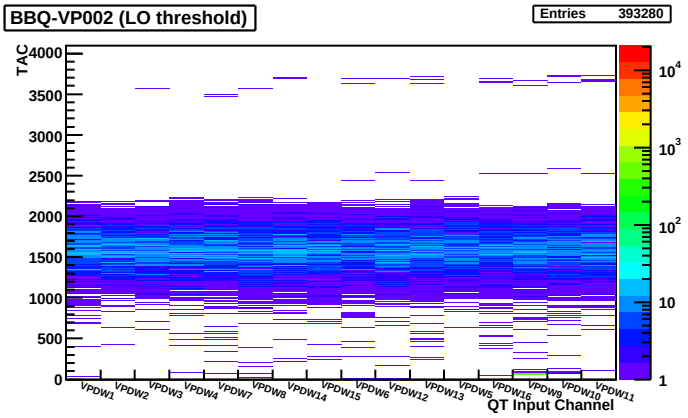

BBQ-ZD001
Entries 393280
ADC
QT Input Channel

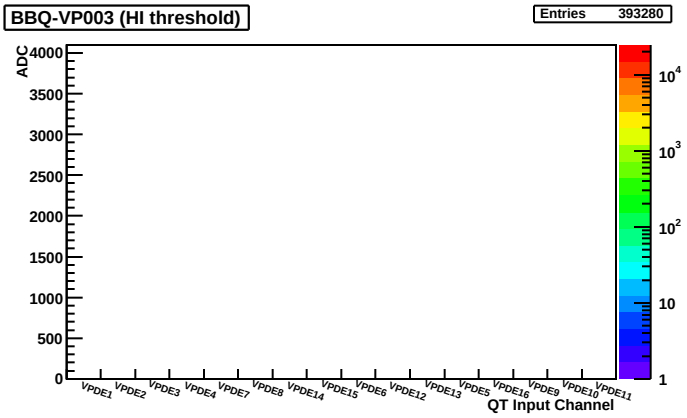

BBQ-ZD001
Entries 393280
TAC
QT Input Channel

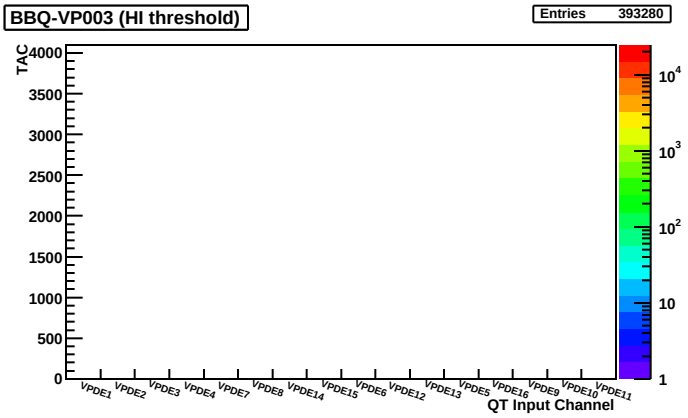

BBQ-VP001 (LO threshold)
Entries 393280
ADC
QT Input Channel

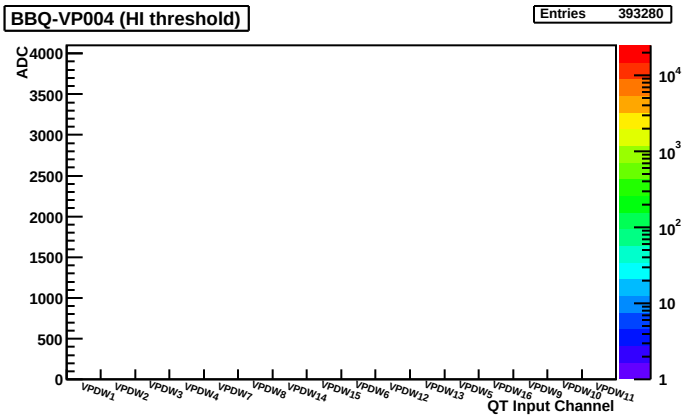

BBQ-VP001 (LO threshold)
Entries 393280
TAC
QT Input Channel

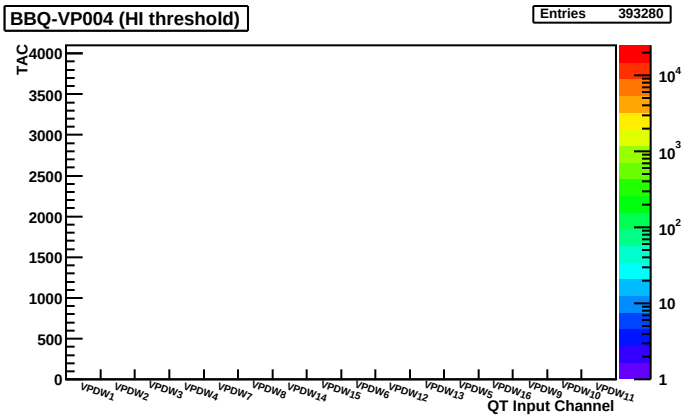

BBQ-VP002 (LO threshold)
Entries 393280
ADC
QT Input Channel

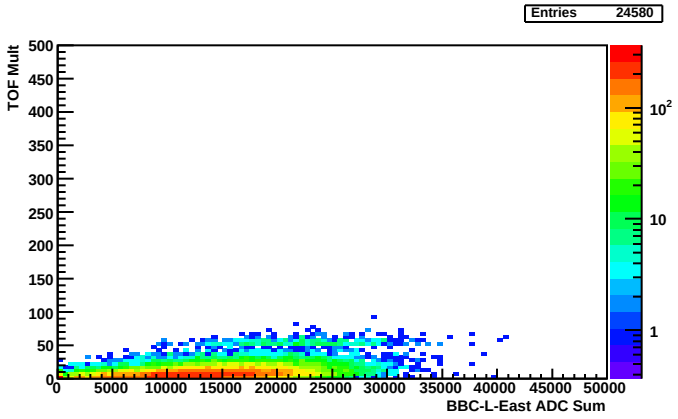

BBQ-VP002 (LO threshold)
Entries 393280
TAC
QT Input Channel

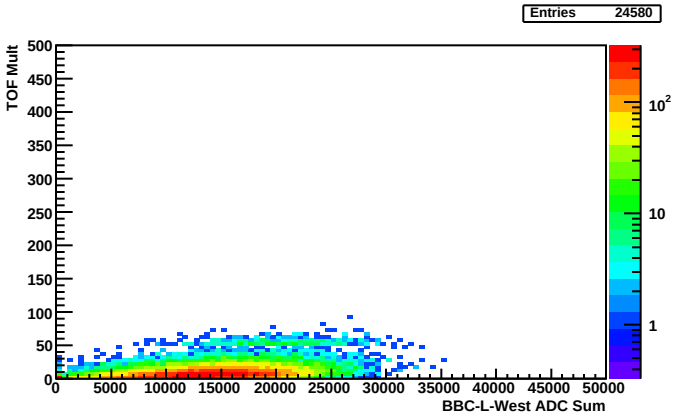

BBQ-VP003 (HI threshold)
Entries 393280
ADC
QT Input Channel

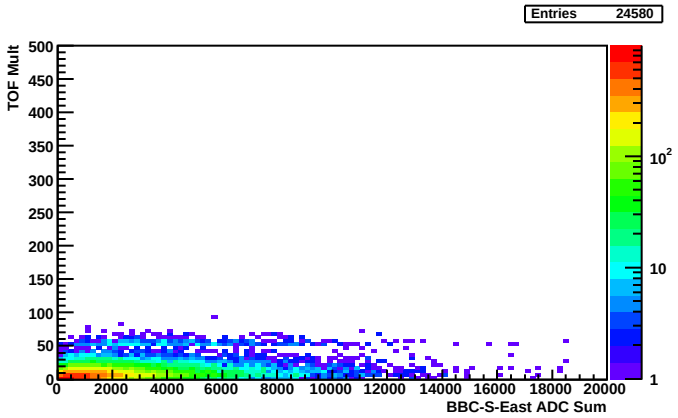

BBQ-VP003 (HI threshold)
Entries 393280
TAC
QT Input Channel

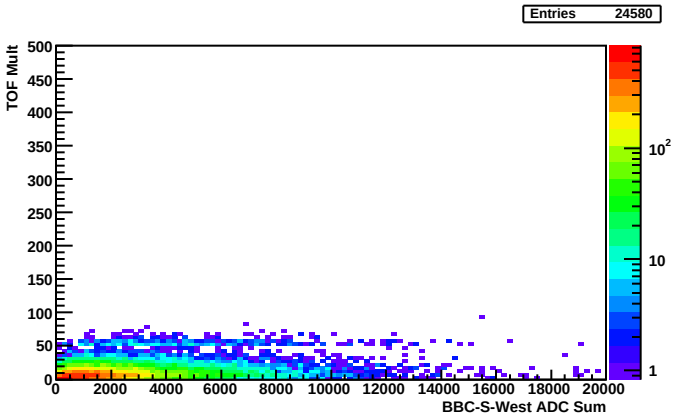

BBQ-VP004 (HI threshold)
Entries 393280
ADC
QT Input Channel

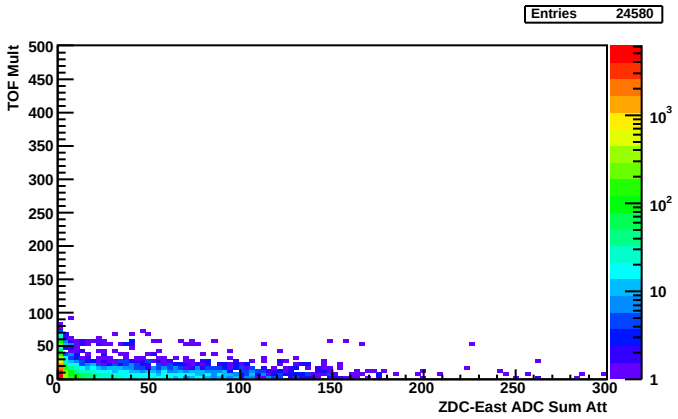

BBQ-VP004 (HI threshold)
Entries 393280
TAC
QT Input Channel

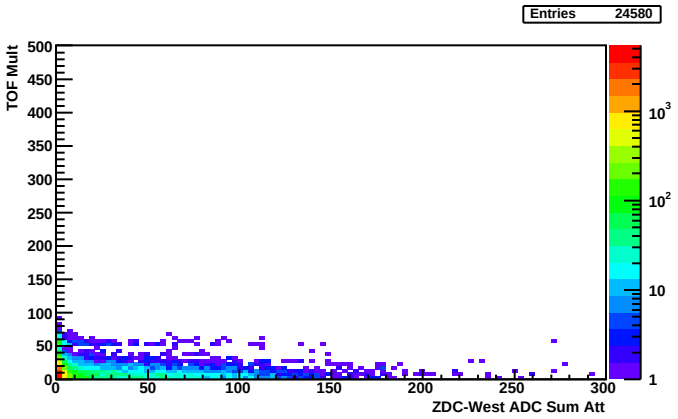

Entries 24580
TOF Mult
BBC-L-East ADC Sum

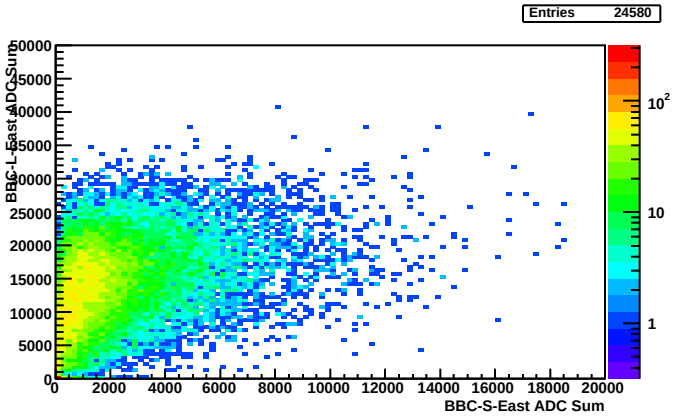

Entries 24580
TOF Mult
BBC-L-West ADC Sum

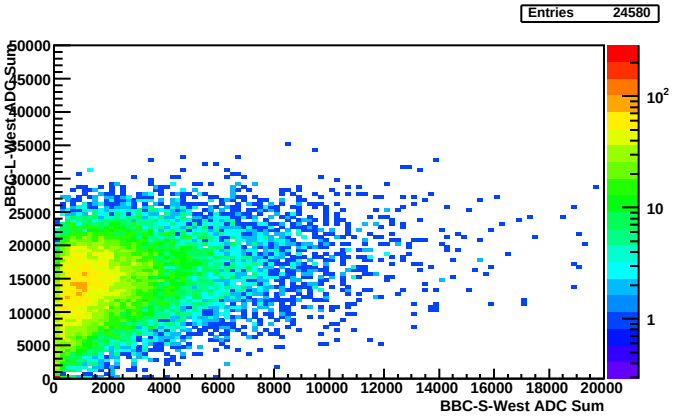

Entries 24580
TOF Mult
BBC-S-East ADC Sum

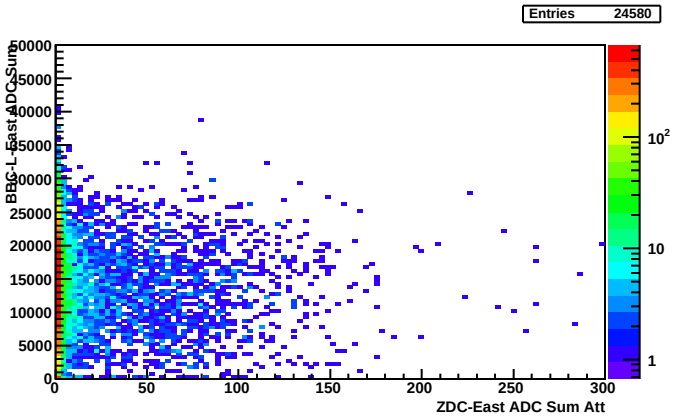

Entries 24580
TOF Mult
BBC-S-West ADC Sum

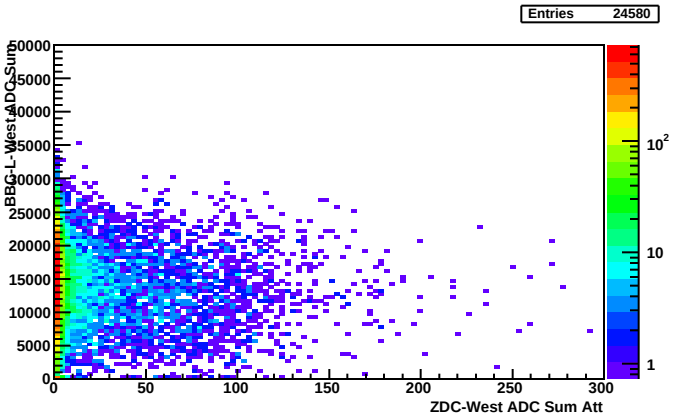

Entries 24580
TOF Mult
ZDC-East ADC Sum Att

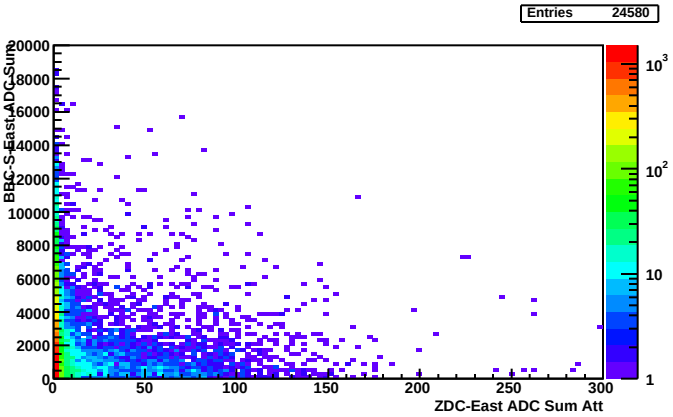

Entries 24580
TOF Mult
ZDC-West ADC Sum Att

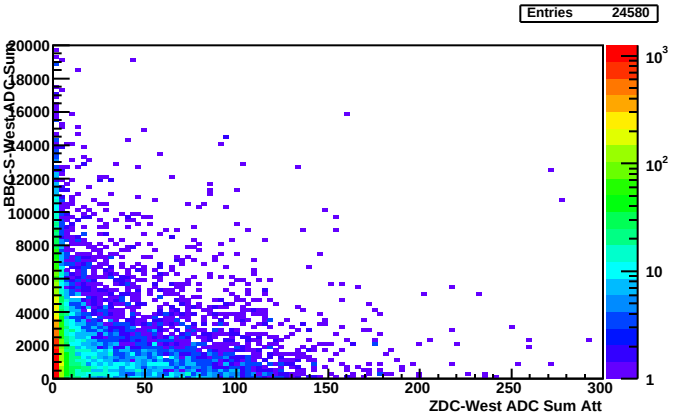

Entries 24580
BBC-L-East ADC Sum
BBC-S-East ADC Sum

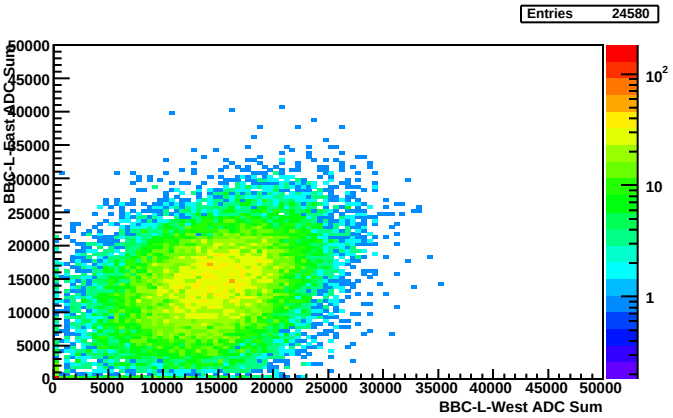

Entries 24580
BBC-L-West ADC Sum
BBC-S-West ADC Sum

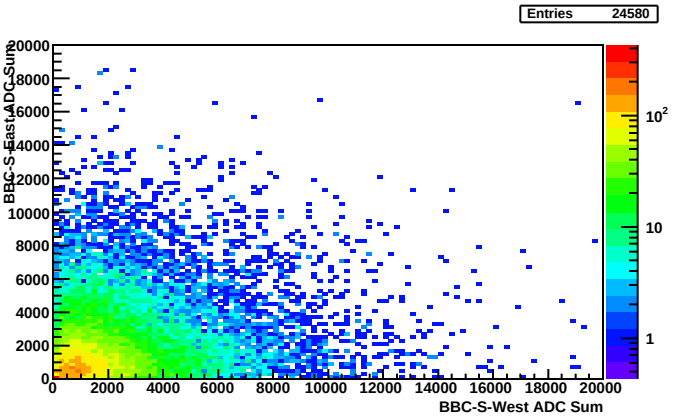

Entries 24580
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

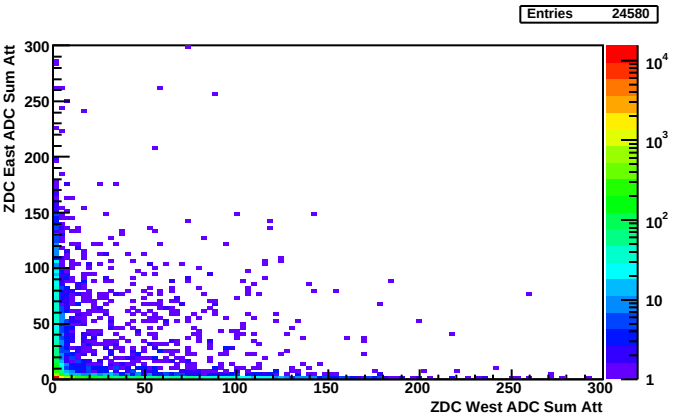

Entries 24580
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

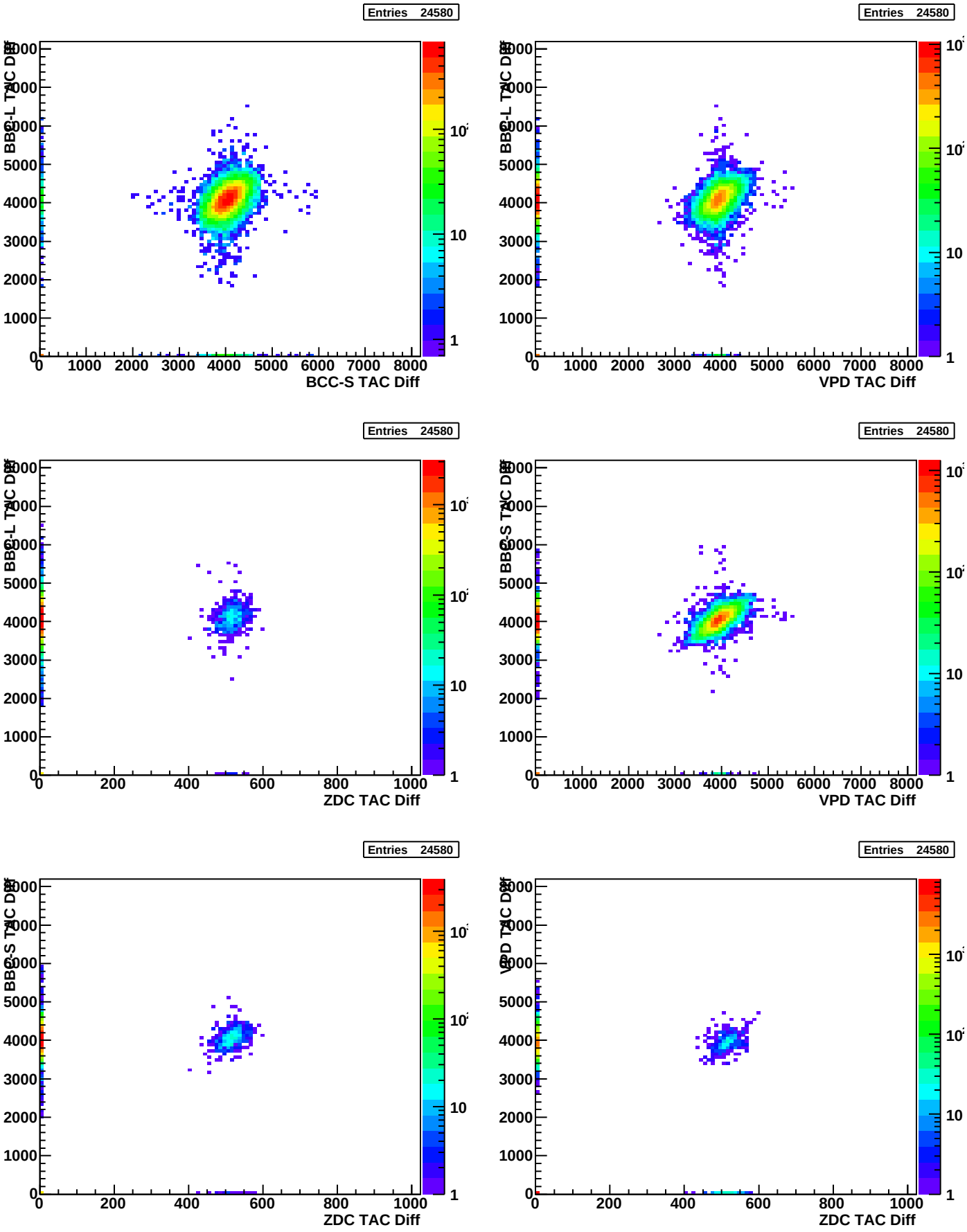

Entries 24580
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

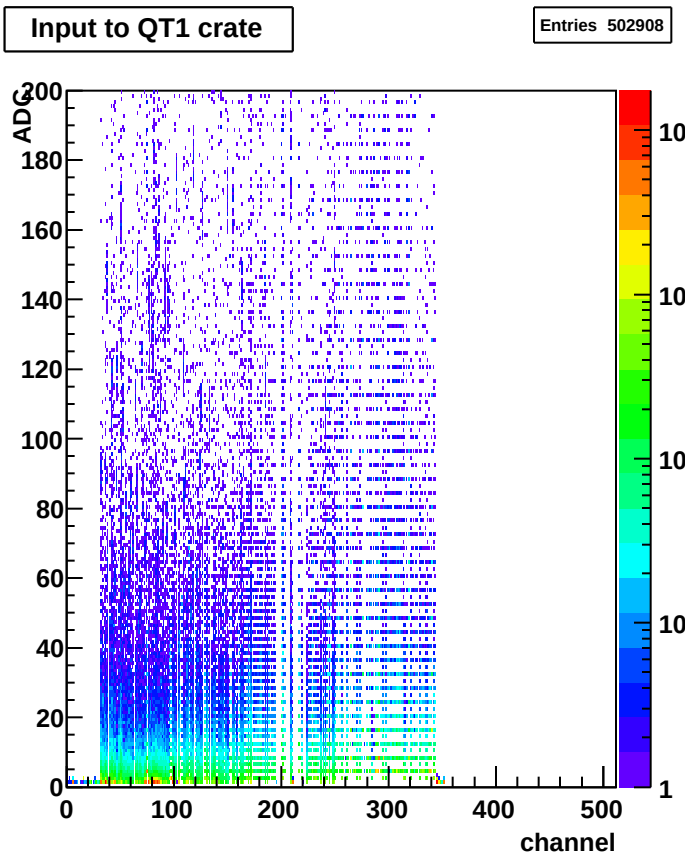

Entries 24580
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

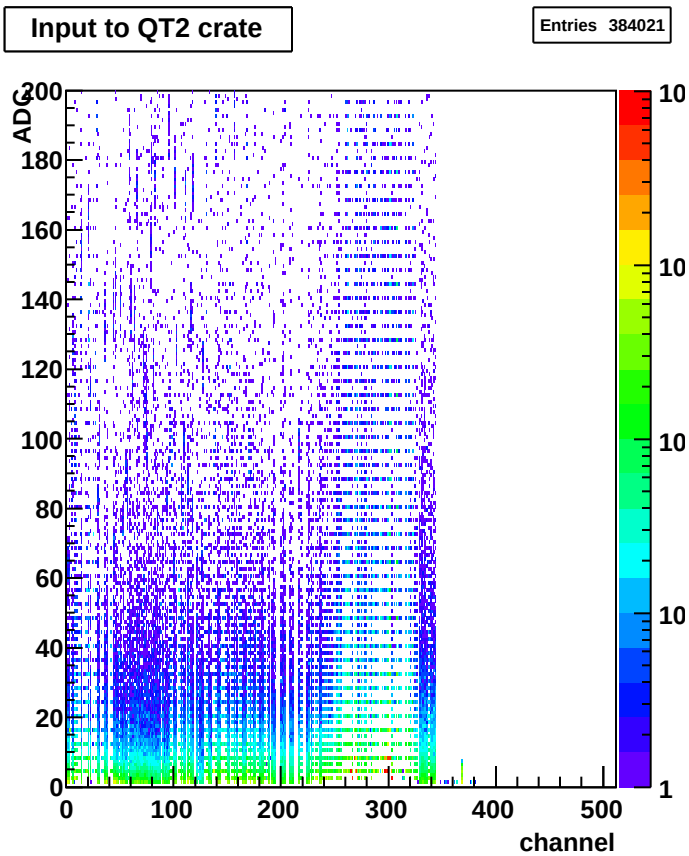

Entries 24580
BBC-L-East ADC Sum
BBC-L-West ADC Sum

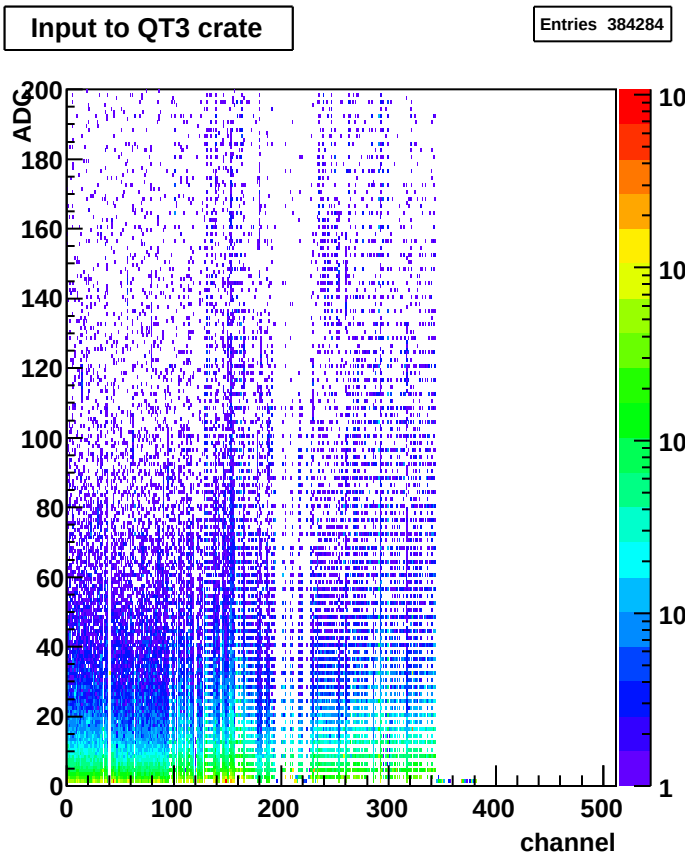

Entries 24580
BBC-S-East ADC Sum
BBC-S-West ADC Sum

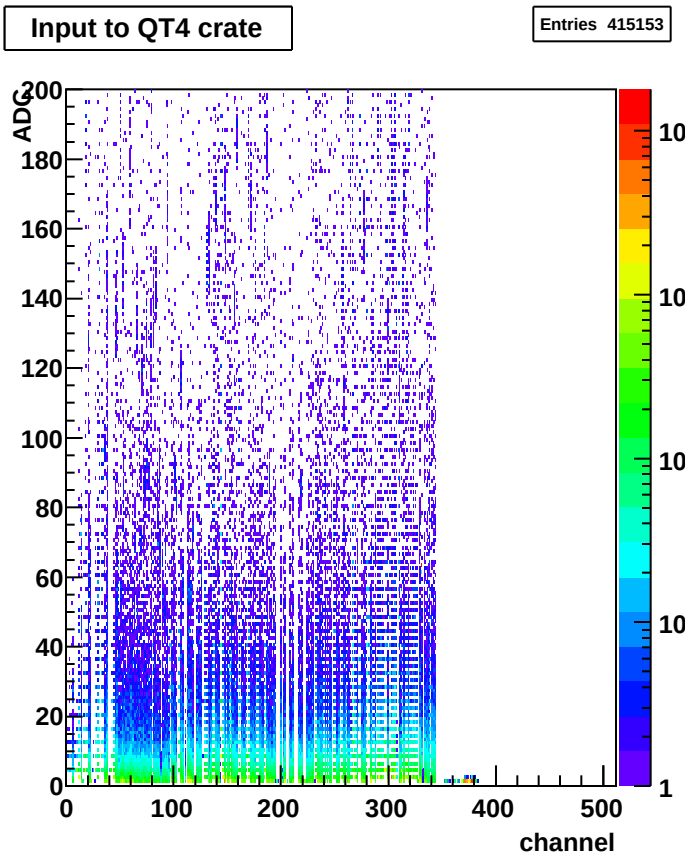

Entries 24580
ZDC East ADC Sum Att
ZDC West ADC Sum Att

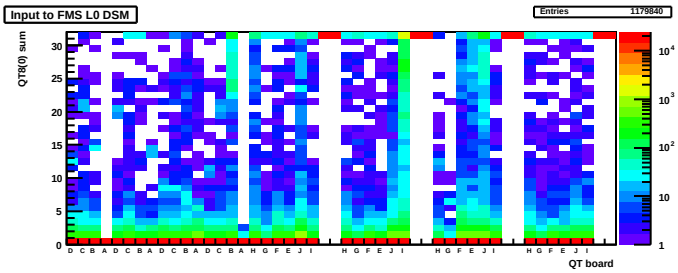

Entries 24580
BBC-L TAC Diff
BCC-S TAC Diff
Entries 24580
BBC-L TAC Diff
VPD TAC Diff
Entries 24580
BBC-L TAC Diff
ZDC TAC Diff
Entries 24580
BBC-S TAC Diff
VPD TAC Diff
Entries 24580
BBC-S TAC Diff
ZDC TAC Diff
Entries 24580
VPD TAC Diff
ZDC TAC Diff

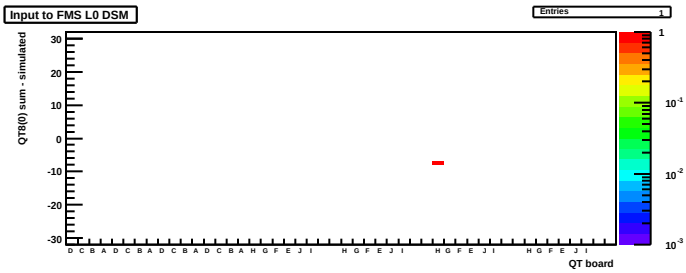

Input to QT1 crate
Entries 502908
ADC
channel

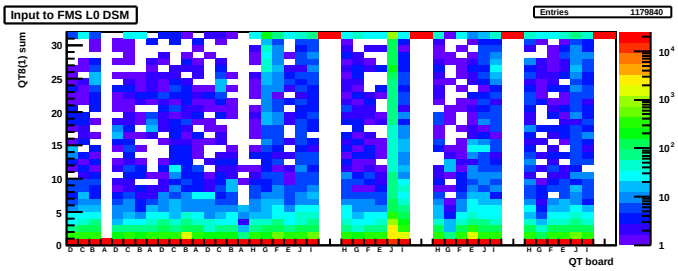

Input to QT2 crate
Entries 384021
ADC
channel

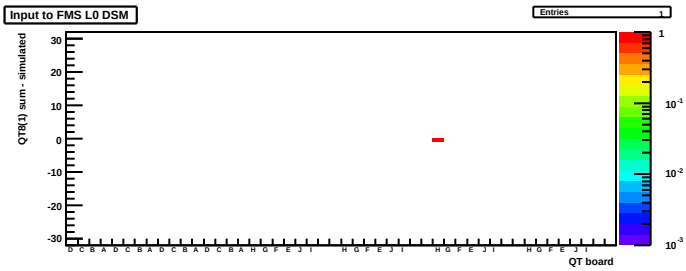

Input to QT3 crate
Entries 384284
ADC
channel

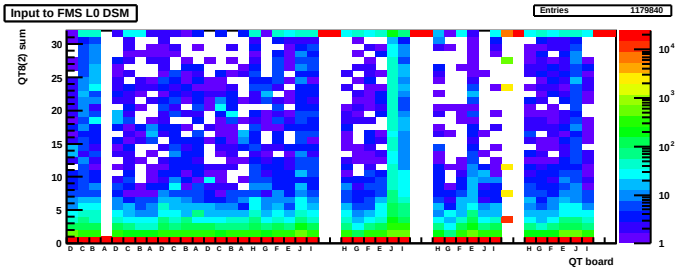

Input to QT4 crate
Entries 415153
ADC
channel

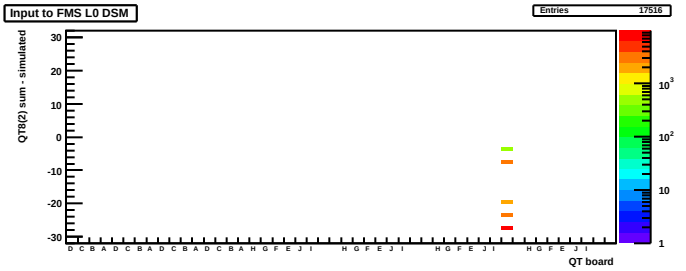

Input to FMS L0 DSM
Entries 1179840
QT8(0) sum
QT board

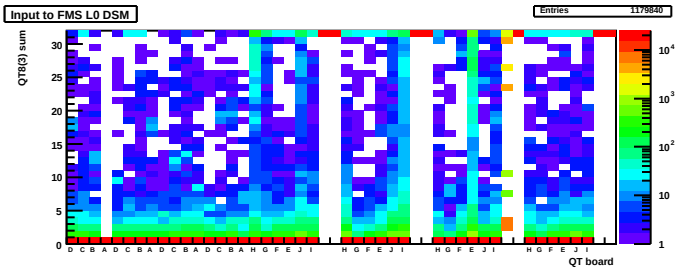

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

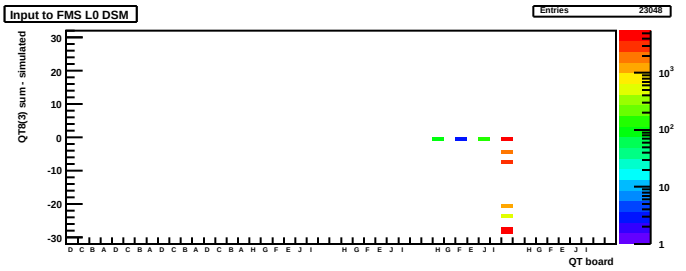

Input to FMS L0 DSM
Entries 1179840
QT8(1) sum
QT board

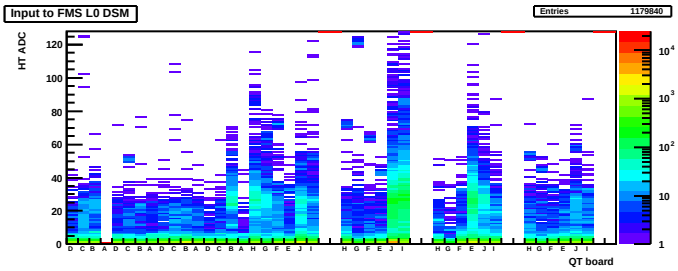

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

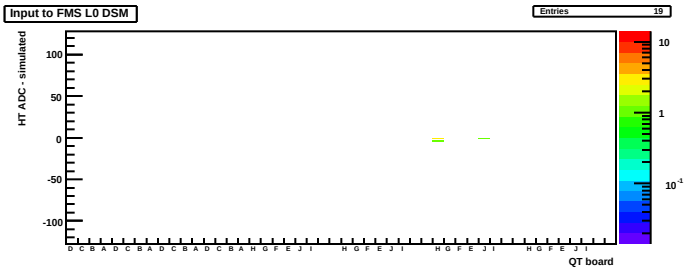

Input to FMS L0 DSM
Entries 1179840
QT8(2) sum
QT board

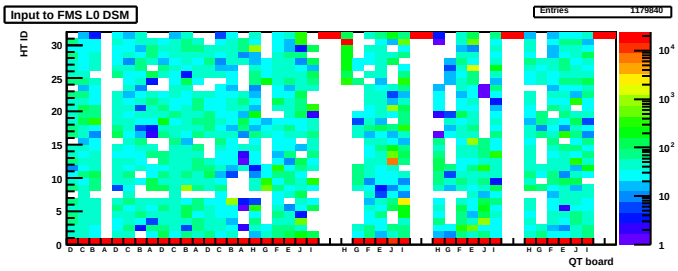

Input to FMS L0 DSM
Entries 17516
QT8(2) sum - simulated
QT board

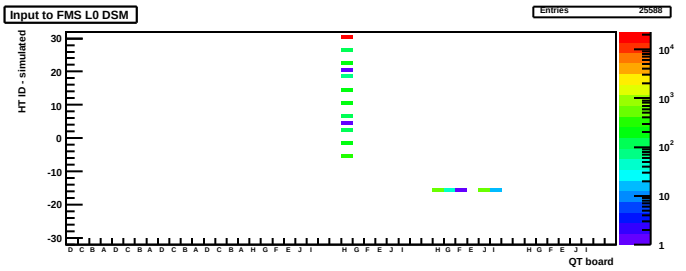

Input to FMS L0 DSM
Entries 1179840
QT8(3) sum
QT board

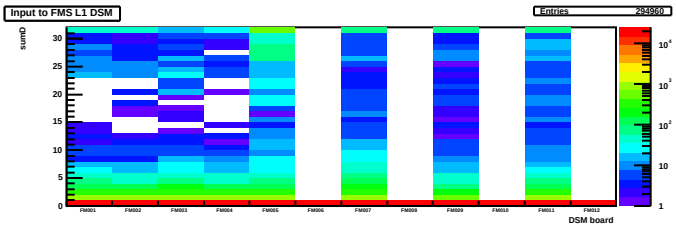

Input to FMS L0 DSM
Entries 23048
QT8(3) sum - simulated
QT board

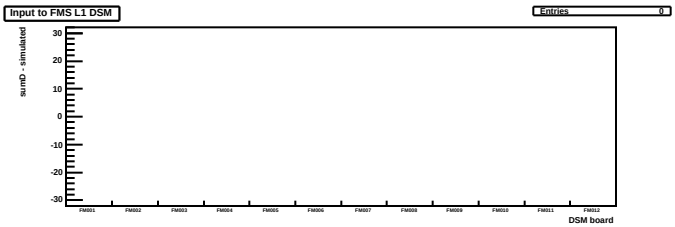

Input to FMS L0 DSM
Entries 1179840
HT ADC
QT board

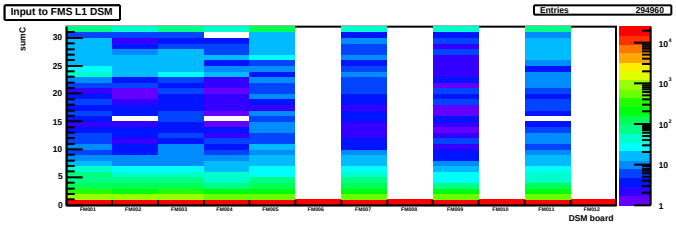

Input to FMS L0 DSM
Entries 19
HT ADC - simulated
QT board

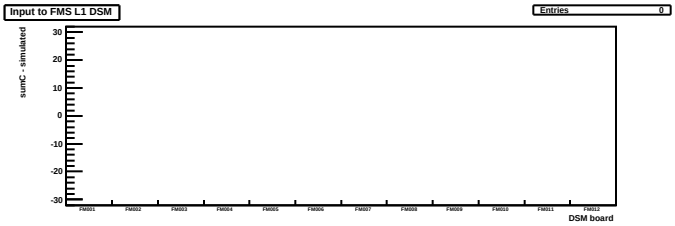

Input to FMS L0 DSM
Entries 1179840
HT ID
QT board

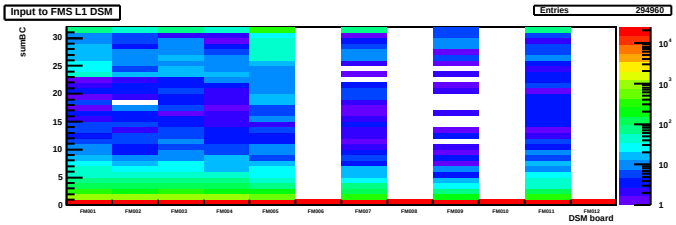

Input to FMS L0 DSM
Entries 25588
HT ID - simulated
QT board

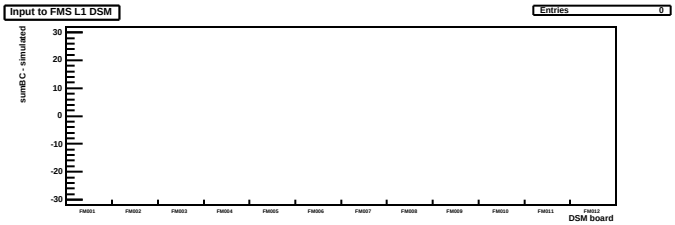

Input to FMS L1 DSM
Entries 294960
sumD
DSM board

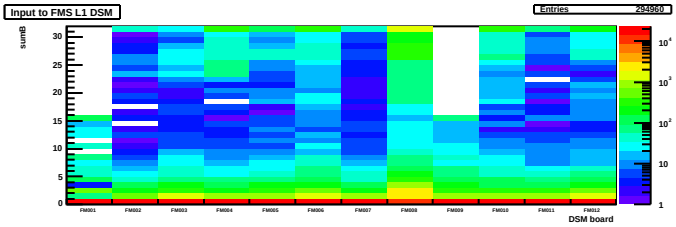

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

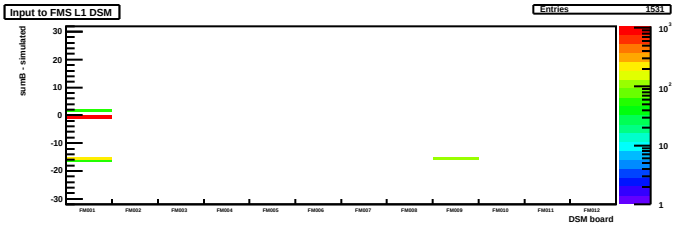

Input to FMS L1 DSM
Entries 294960
sumC
DSM board

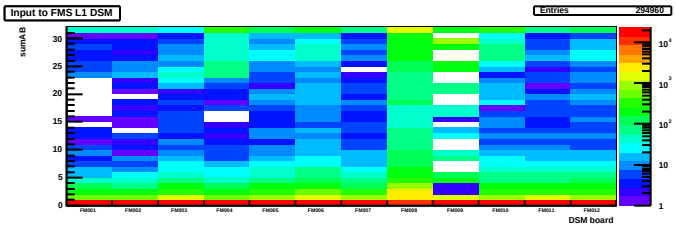

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

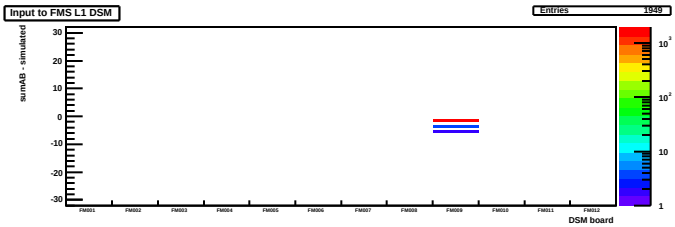

Input to FMS L1 DSM
Entries 294960
sumBC
DSM board

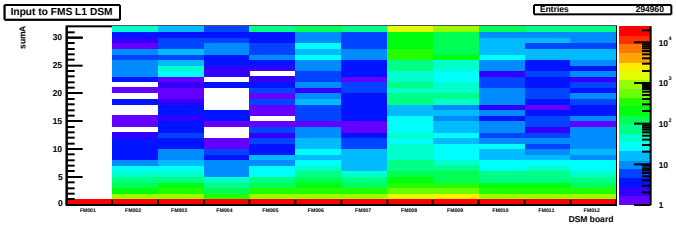

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

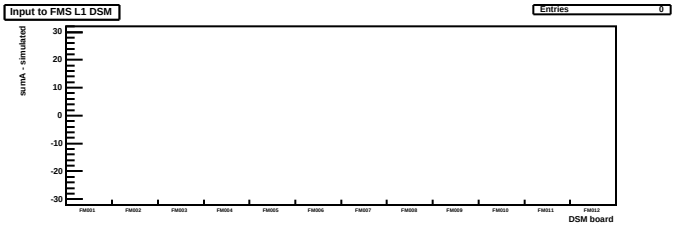

Input to FMS L1 DSM
Entries 294960
sumB
DSM board

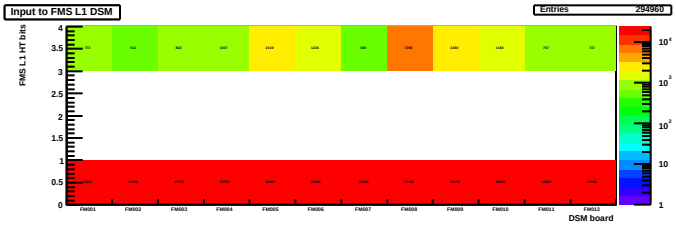

Input to FMS L1 DSM
Entries 1531
sumB - simulated
DSM board

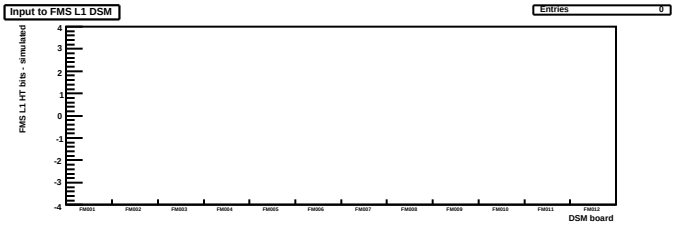

Input to FMS L1 DSM
Entries 294960
sumAB
DSM board

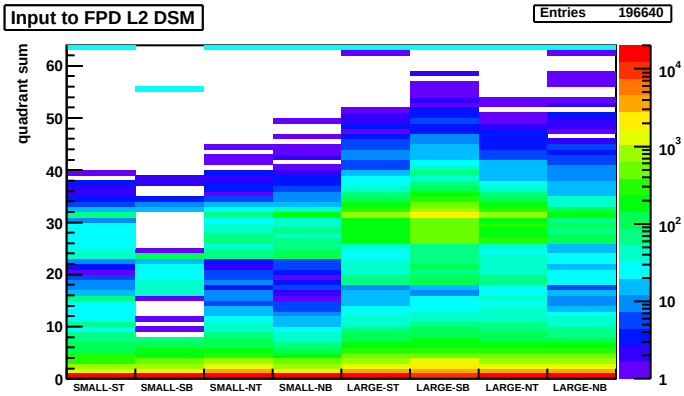

Input to FMS L1 DSM
Entries 1949
sumAB - simulated
DSM board

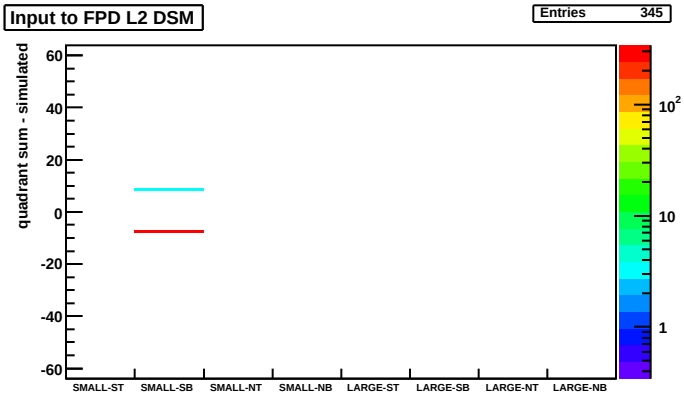

Input to FMS L1 DSM
Entries 294960
sumA
DSM board

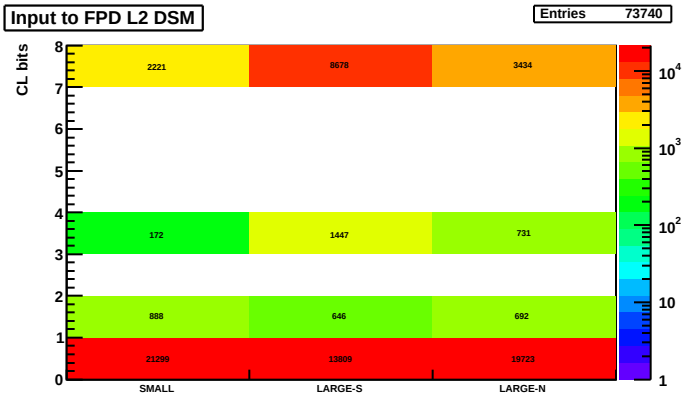

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

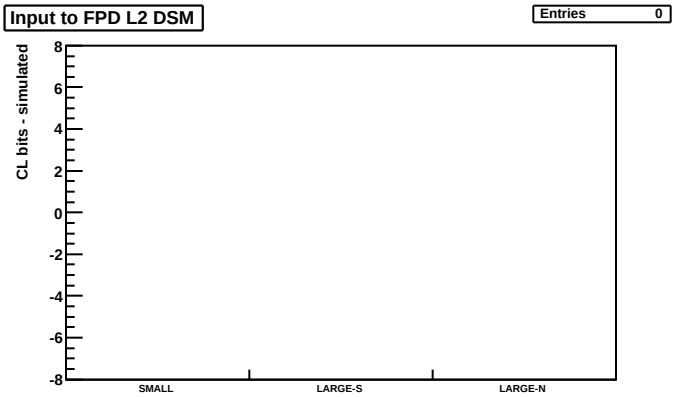

Input to FMS L1 DSM
Entries 294960
FMS L1 HT bits
DSM board

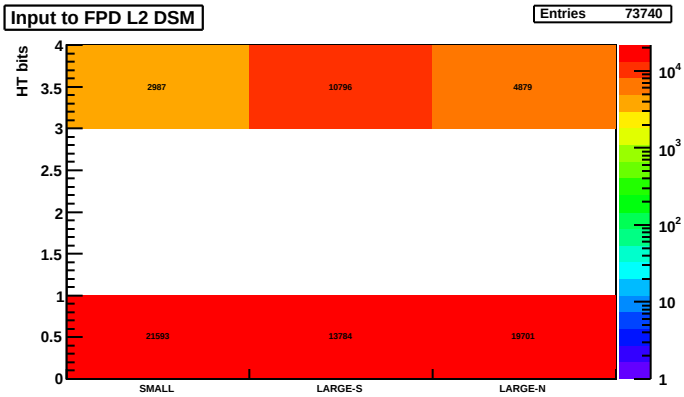

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

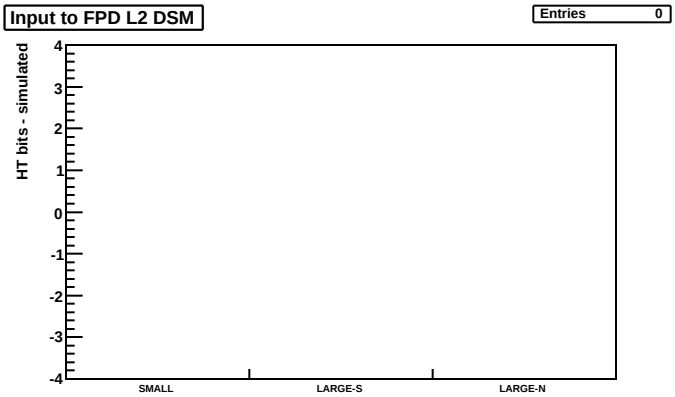

Input to FPD L2 DSM
Entries 196640
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

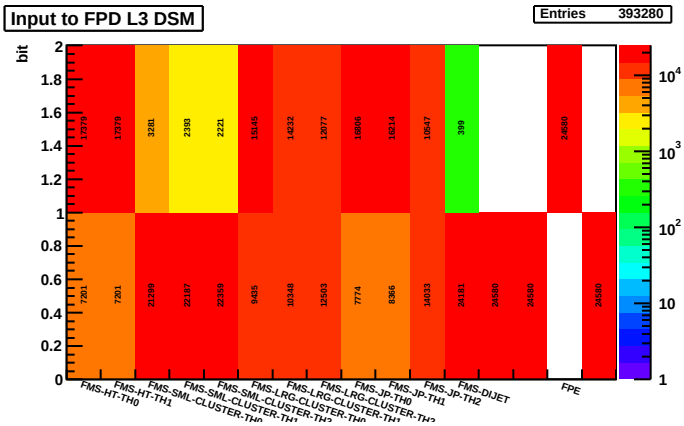

Input to FPD L2 DSM
Entries 345
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

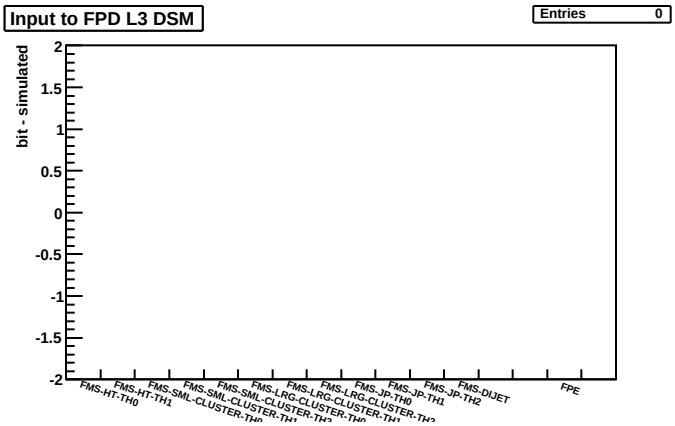

Input to FPD L2 DSM
Entries 73740
CL bits
2221
8678
3434
172
1447
731
888
646
692
21299
13809
19723
SMALL
LARGE-S
LARGE-N

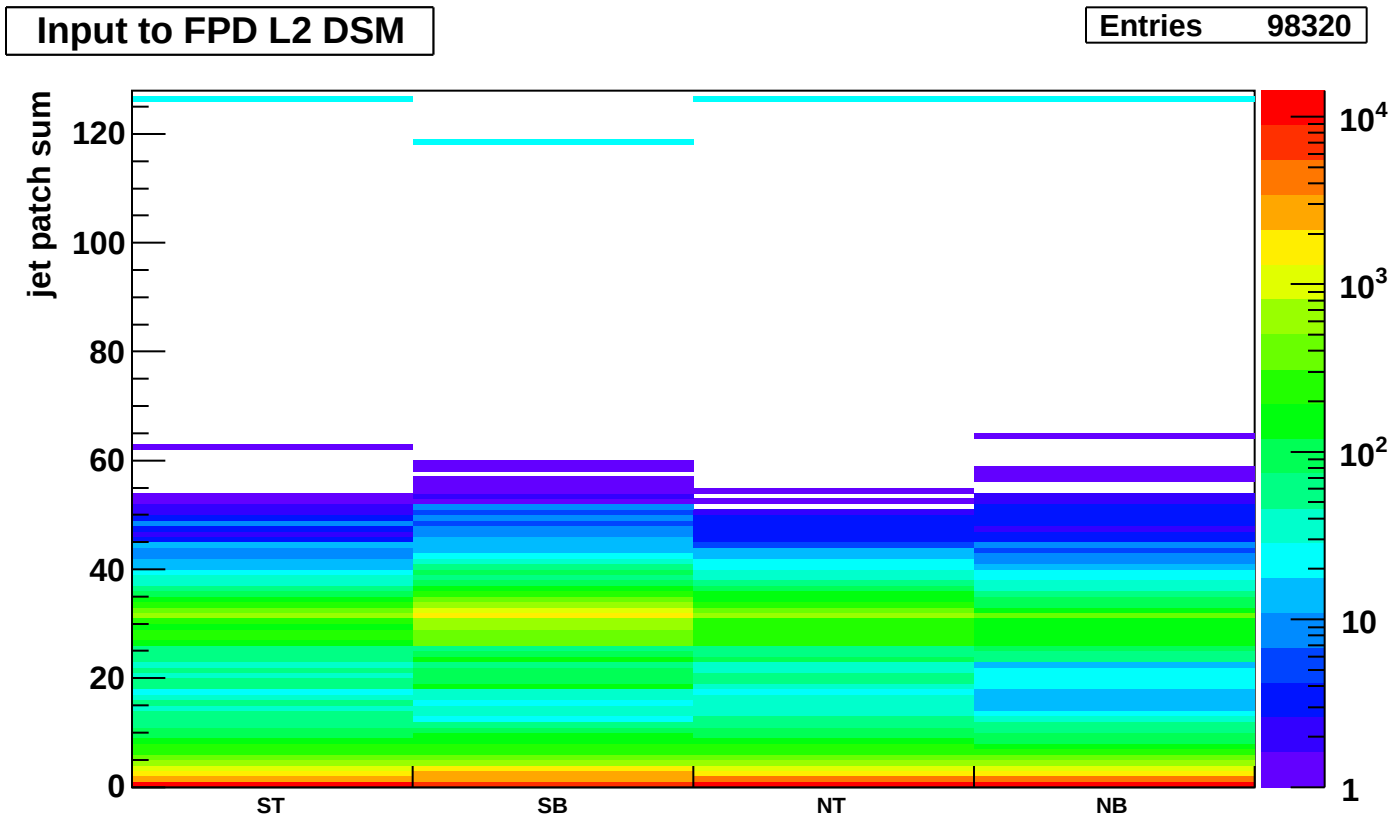

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

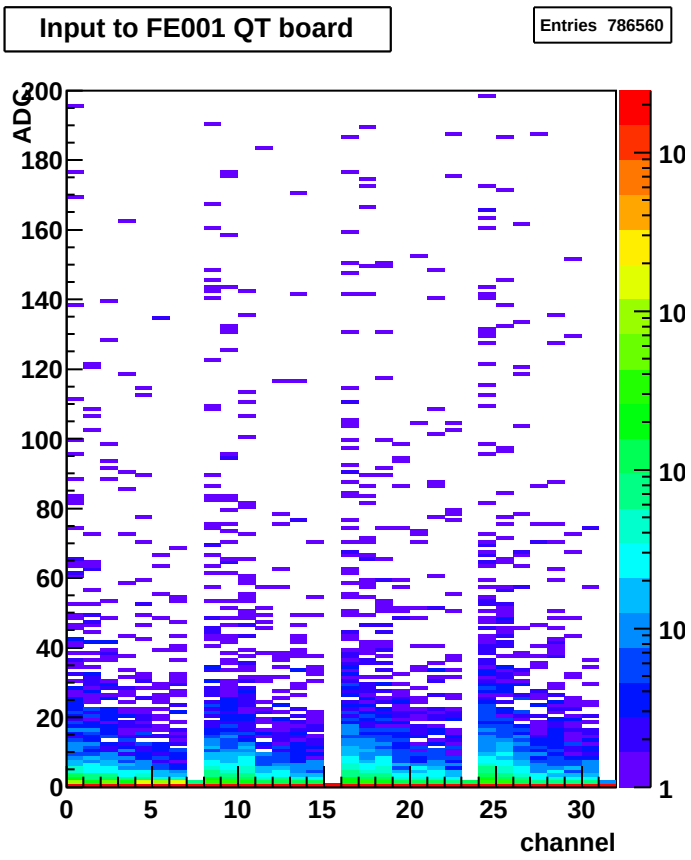

Input to FPD L2 DSM
Entries 73740
HT bits
2987
10796
4879
21593
13784
19701
SMALL
LARGE-S
LARGE-N

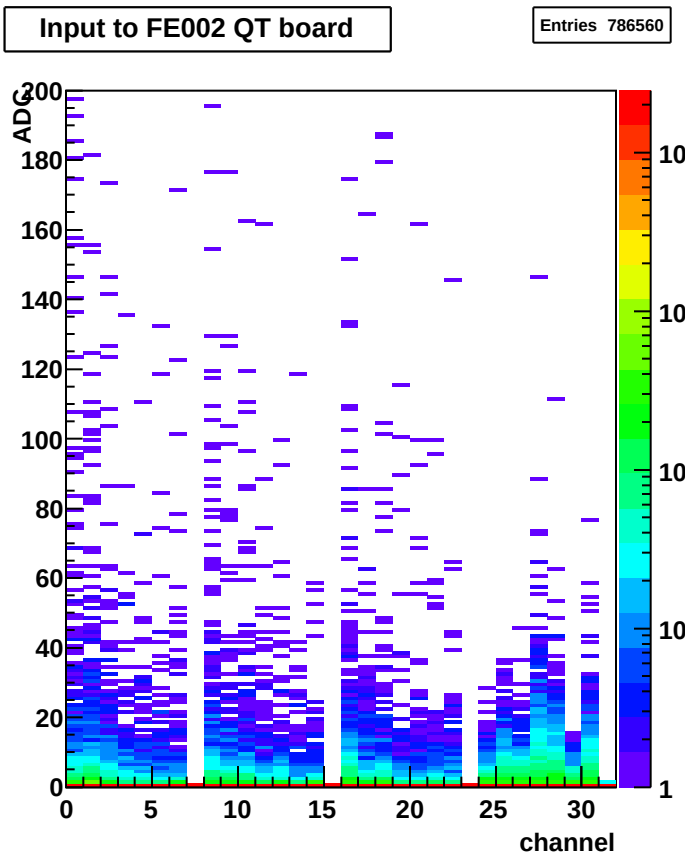

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

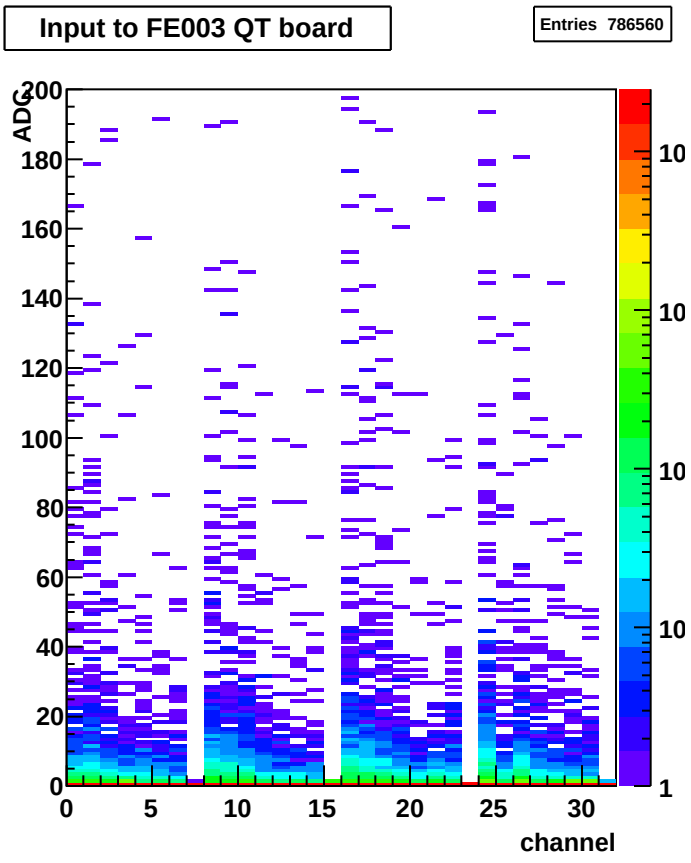

Input to FPD L3 DSM
Entries 393280
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

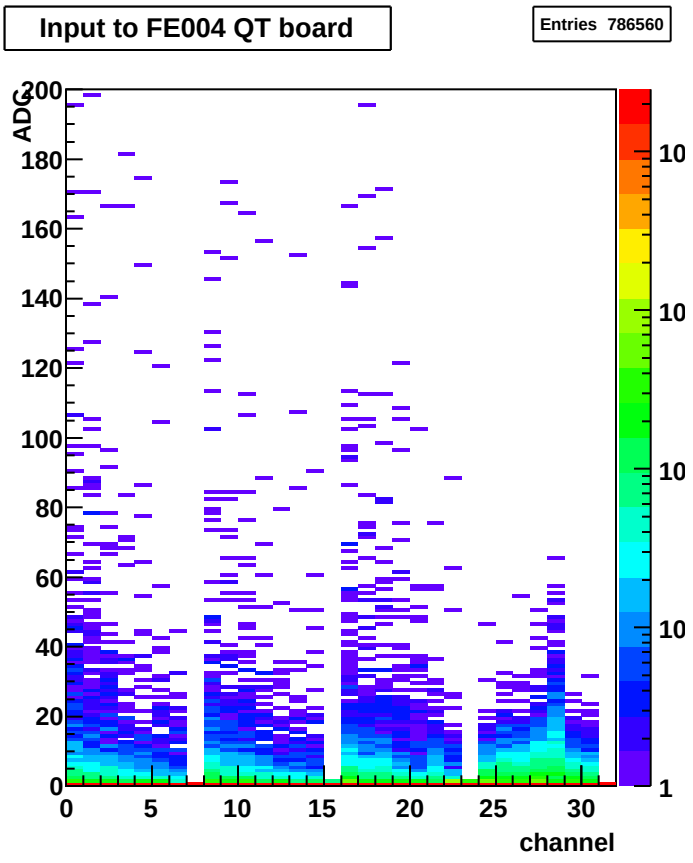

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

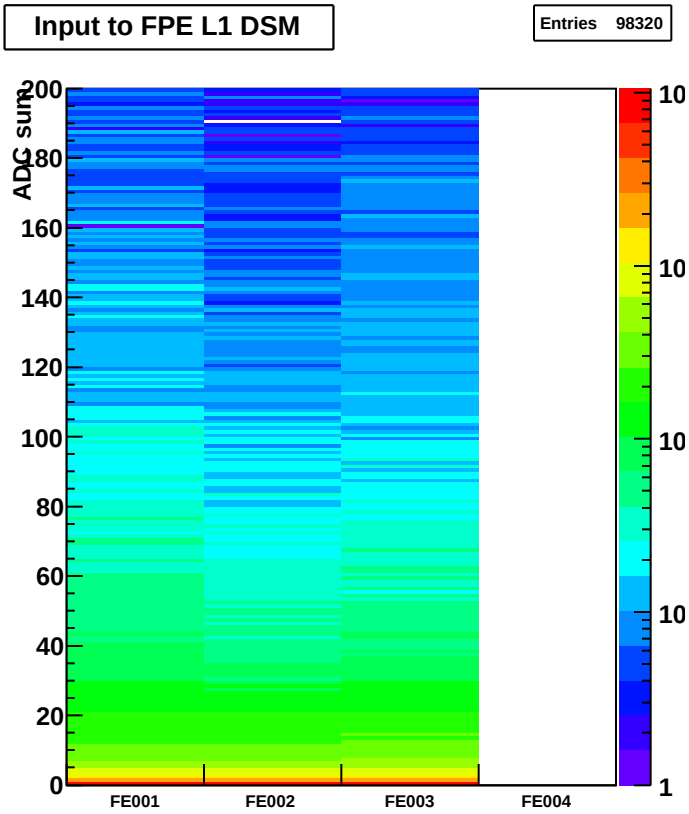

Input to FPD L2 DSM
Entries 98320
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

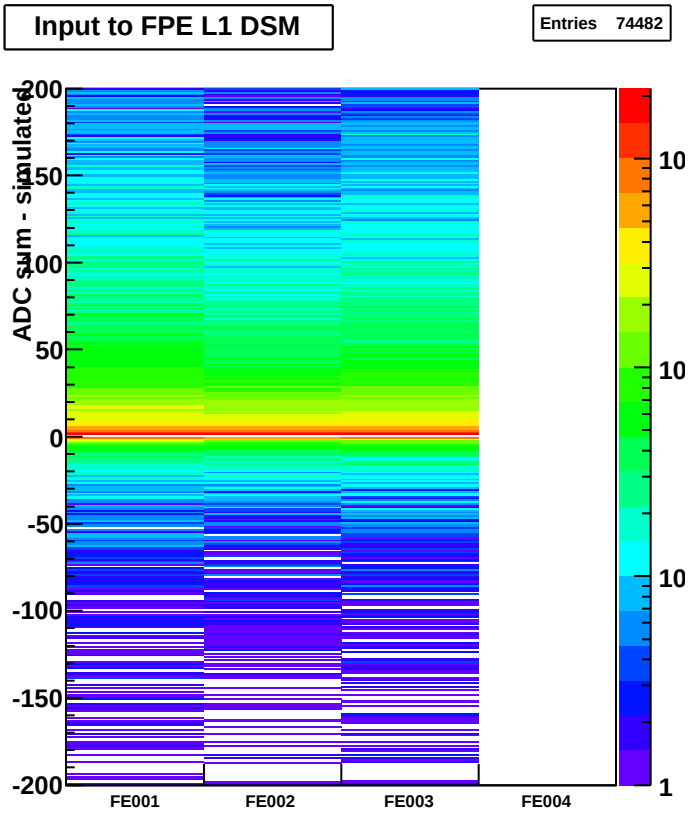

Input to FE001 QT board
Entries 786560
ADC
channel

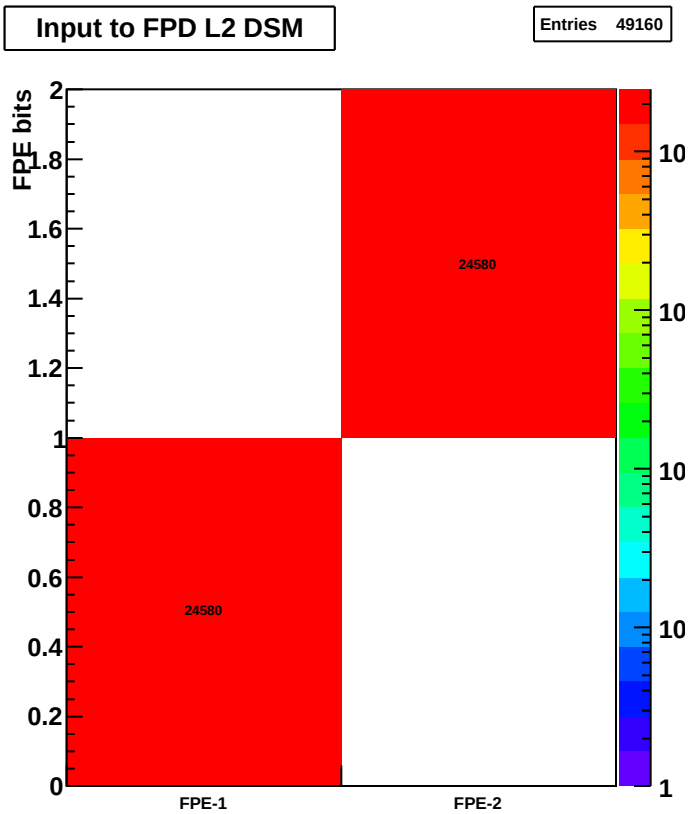

Input to FE002 QT board
Entries 786560
ADC
channel

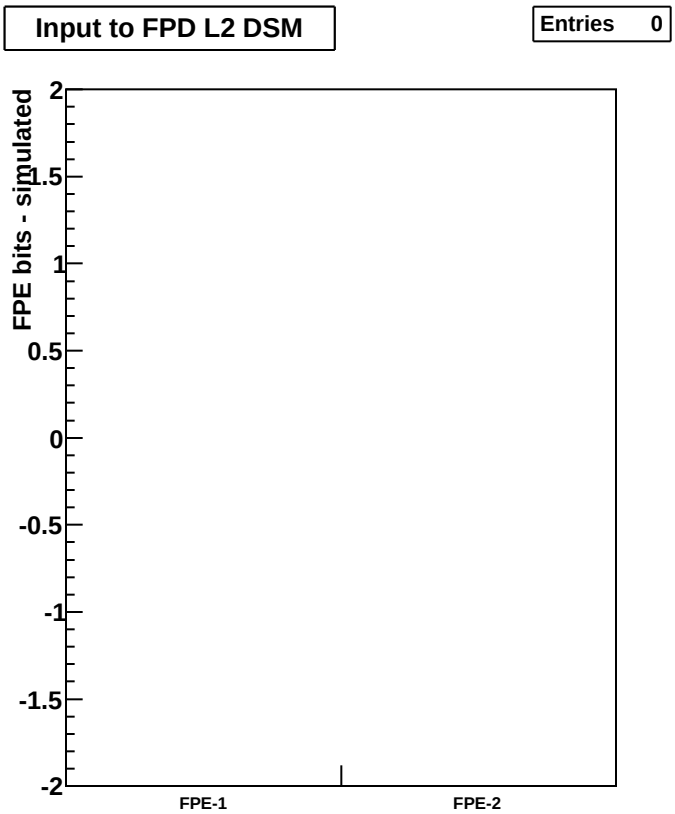

Input to FE003 QT board
Entries 786560
ADC
channel


Input to FE004 QT board
Entries 786560
ADC
channel


Input to FPE L1 DSM
Entries 98320
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 74482
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 49160
FPE bits
24580
24580
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2