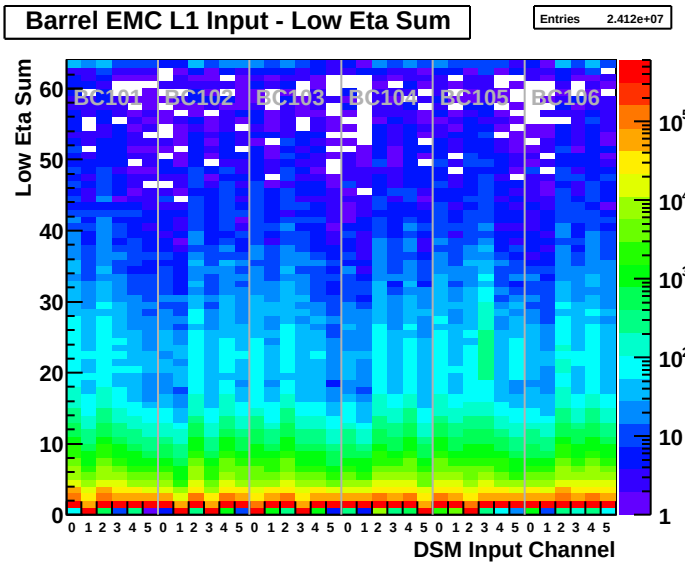
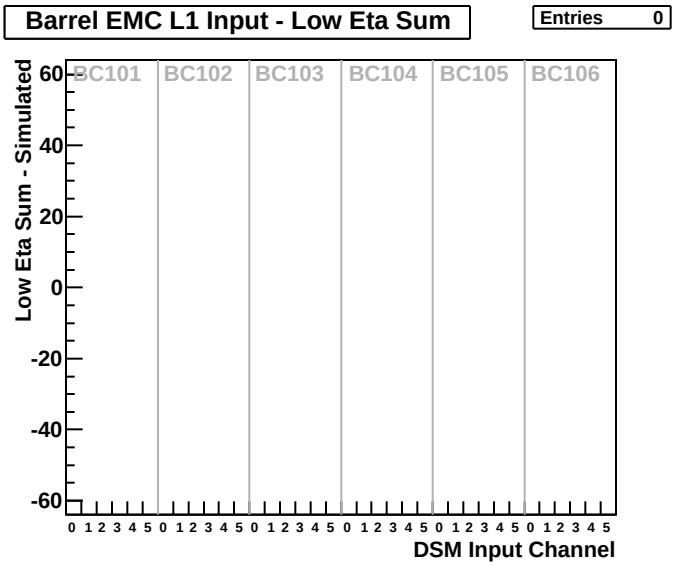
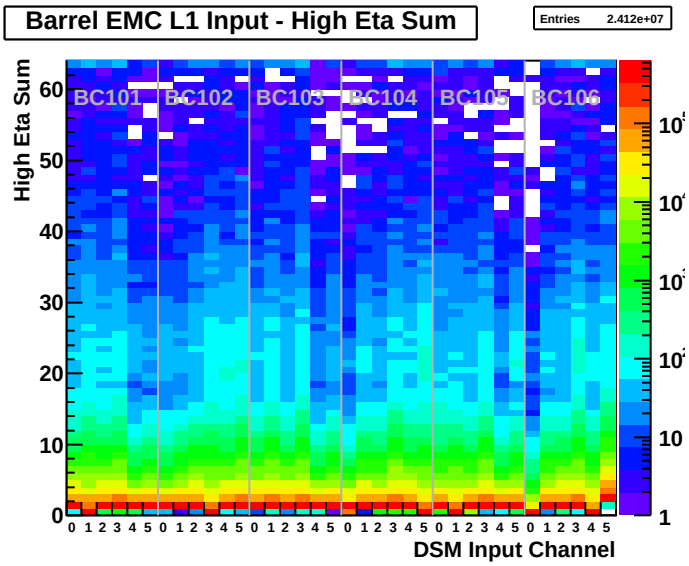
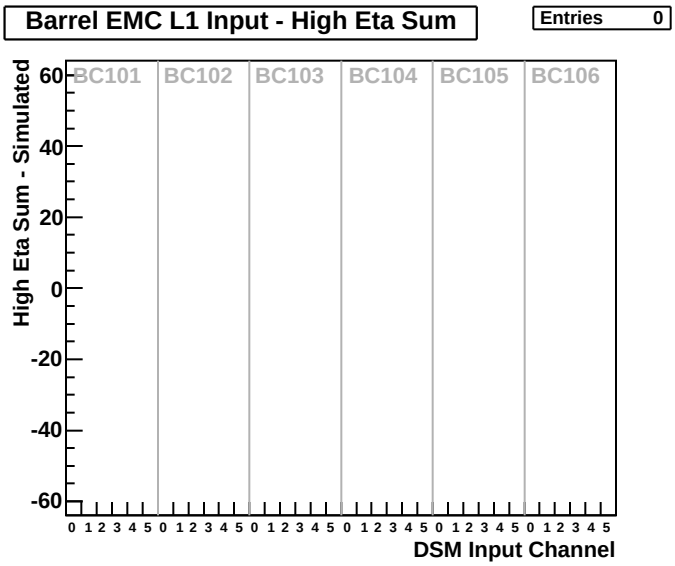
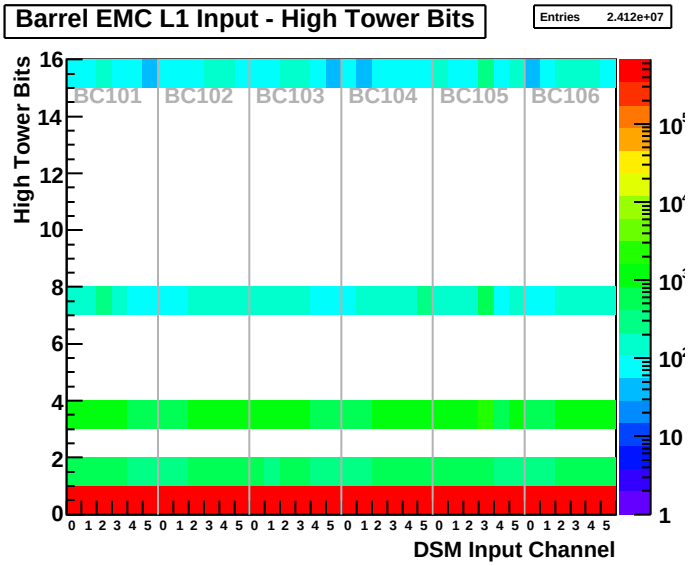
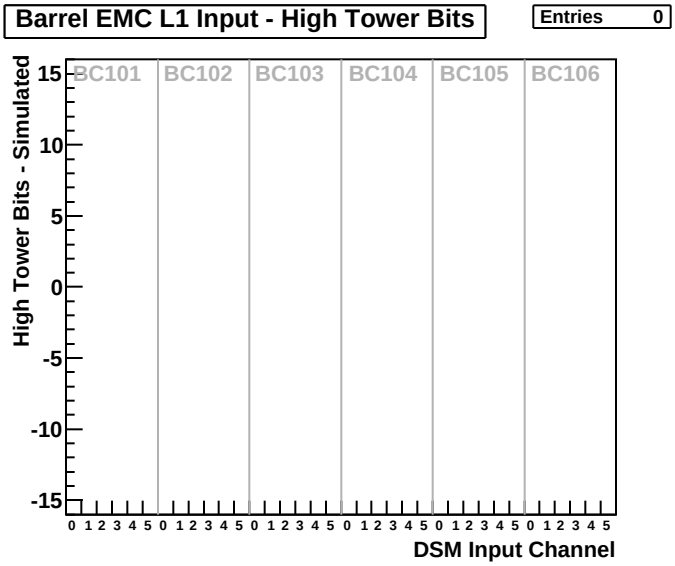

Barrel EMC L1 Input - Low Eta Sum
Entries 2.412e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

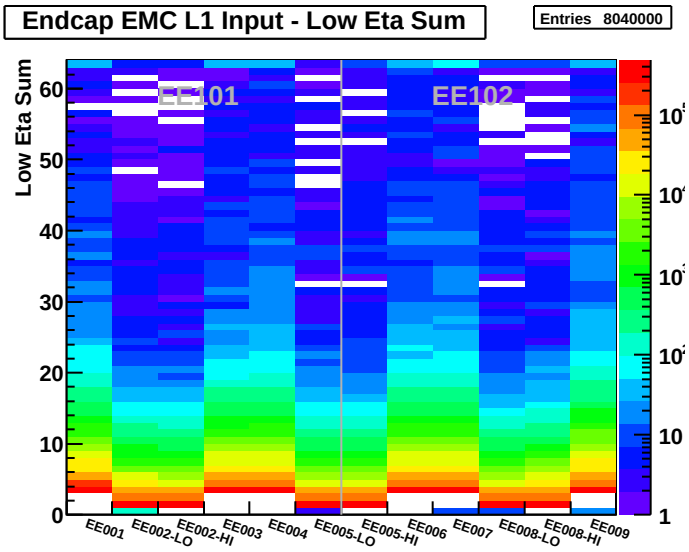

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

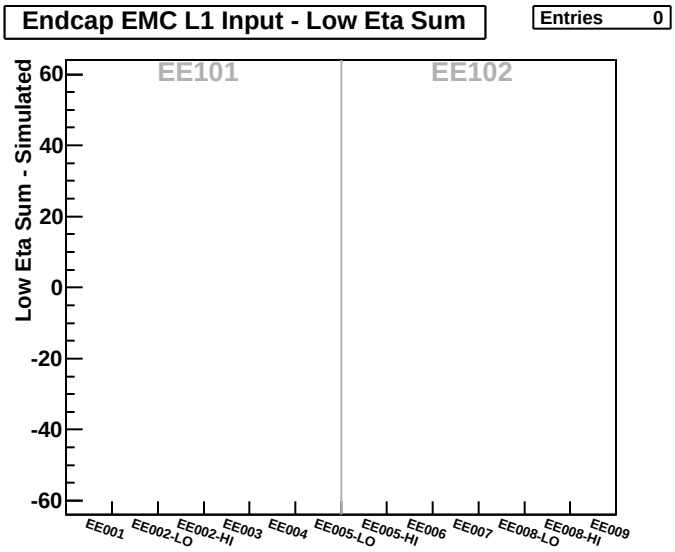

Barrel EMC L1 Input - High Eta Sum
Entries 2.412e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

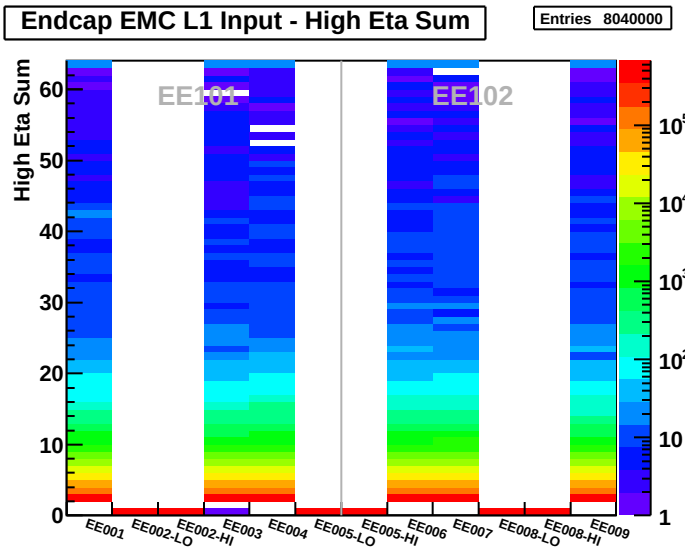

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

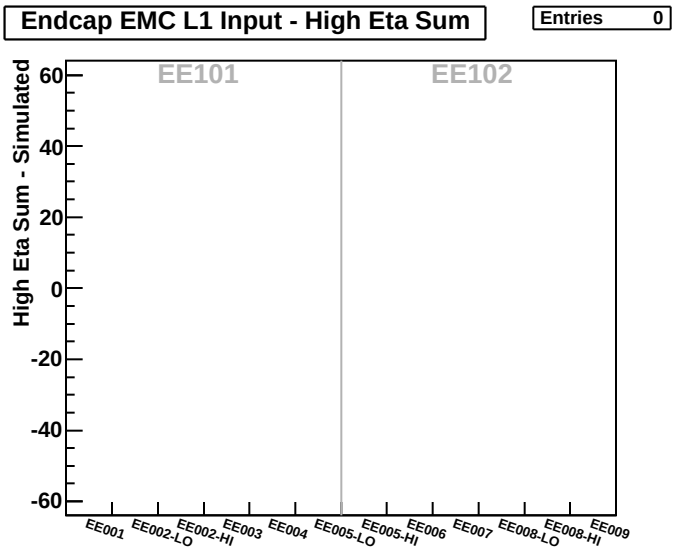

Barrel EMC L1 Input - High Tower Bits
Entries 2.412e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

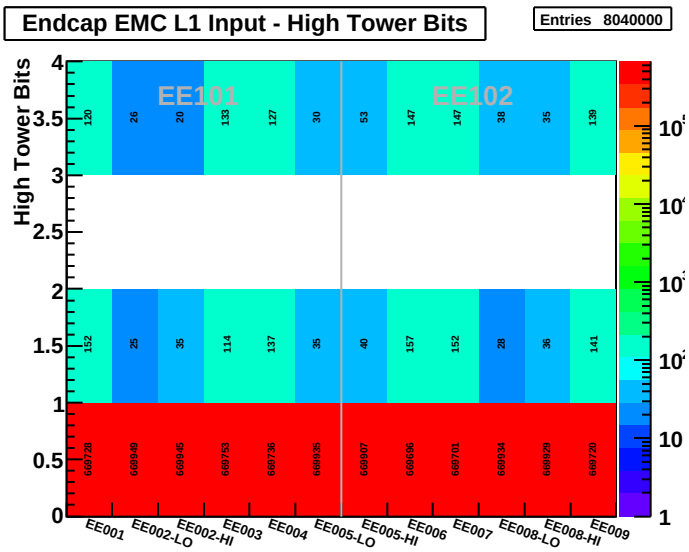

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

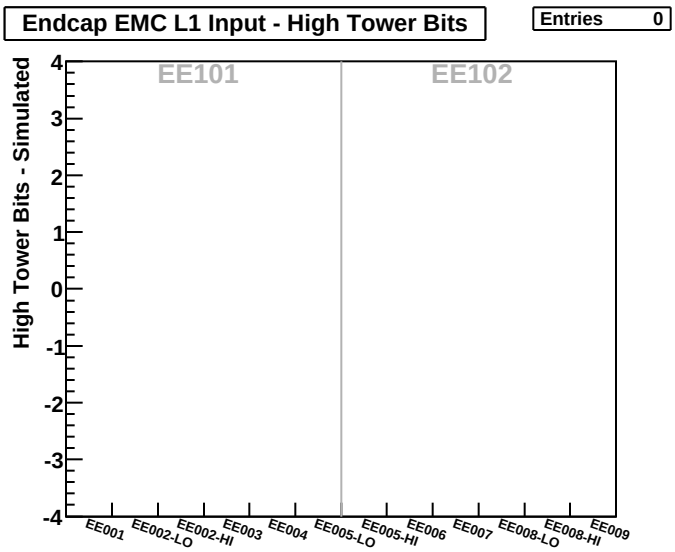

Endcap EMC L1 Input - Low Eta Sum
Entries 8040000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

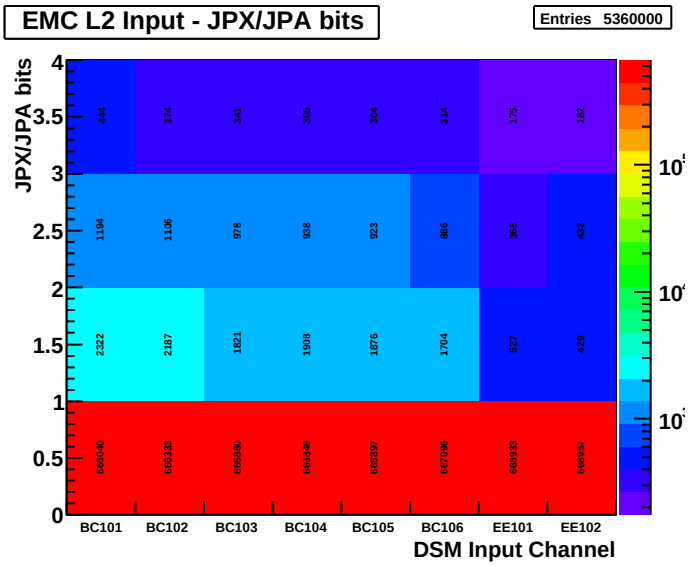

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

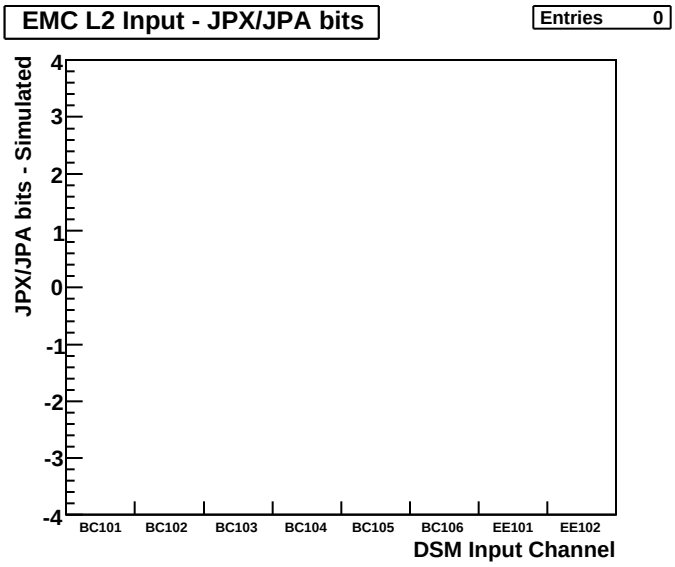

Endcap EMC L1 Input - High Eta Sum
Entries 8040000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

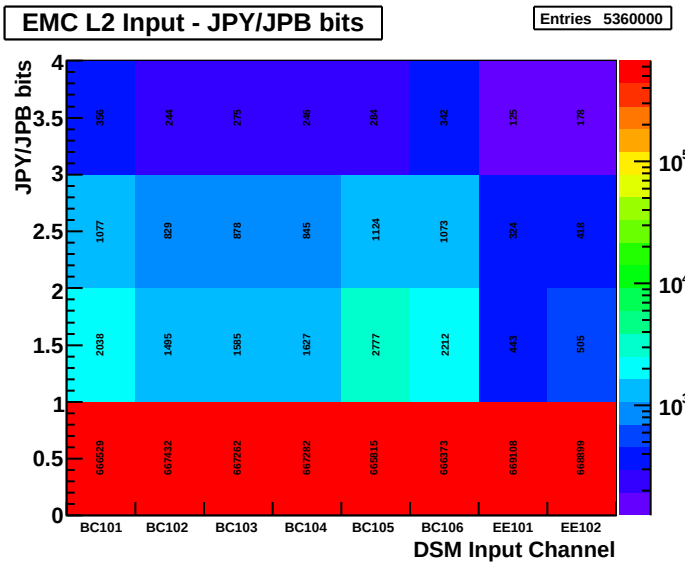

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

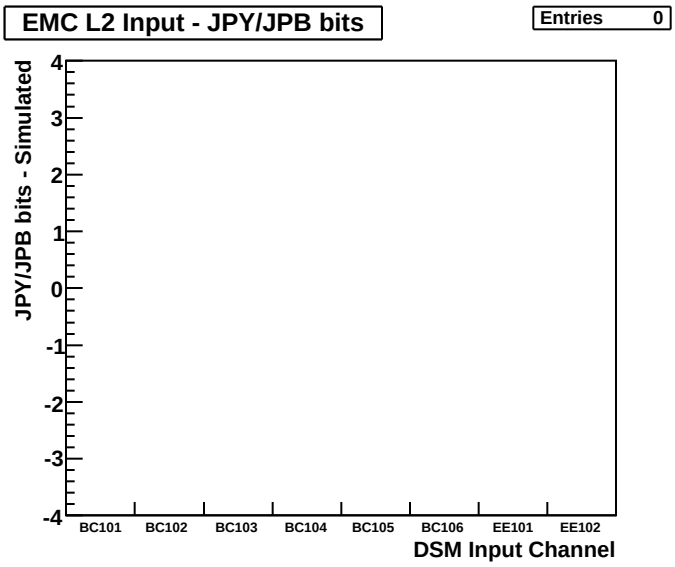

Endcap EMC L1 Input - High Tower Bits
Entries 8040000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

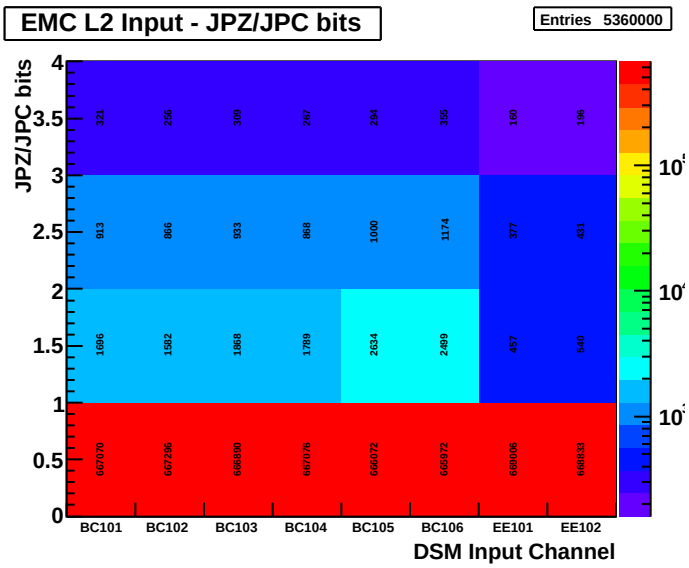

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

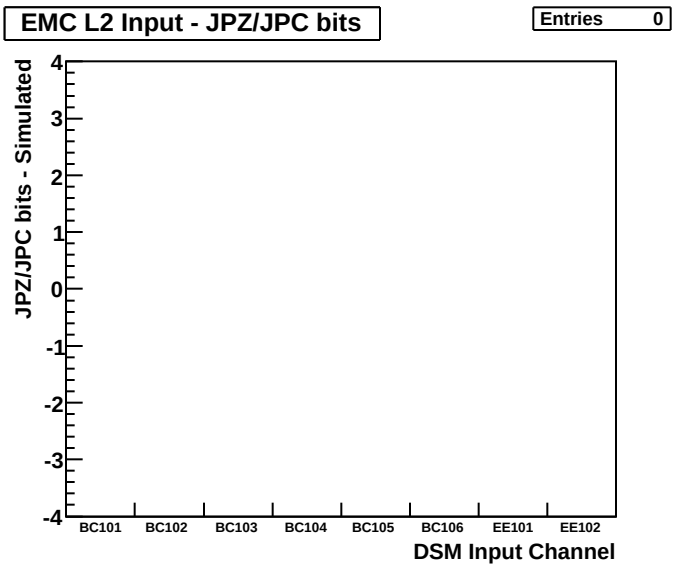

EMC L2 Input - JPX/JPA bits
Entries 5360000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
444 374 341 305 304 314 175 182
1194 1106 978 938 923 886 365 432
2322 2187 1821 1908 1876 1704 527 429
666040 666333 666860 666849 666897 667096 668933 668957

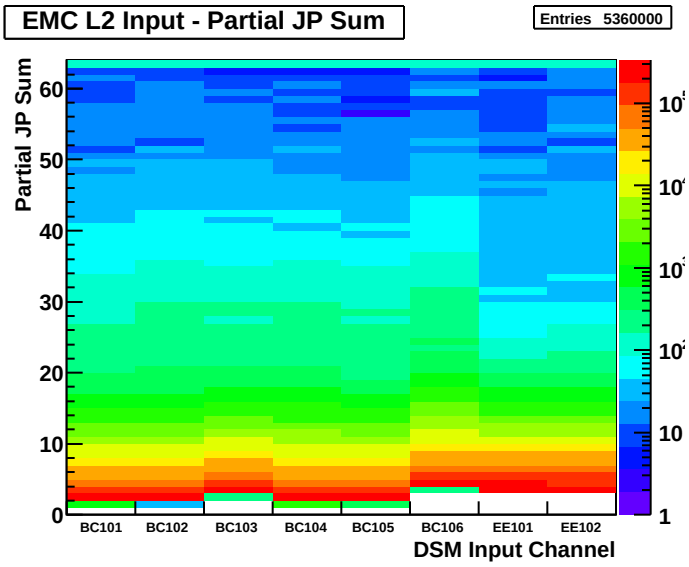

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

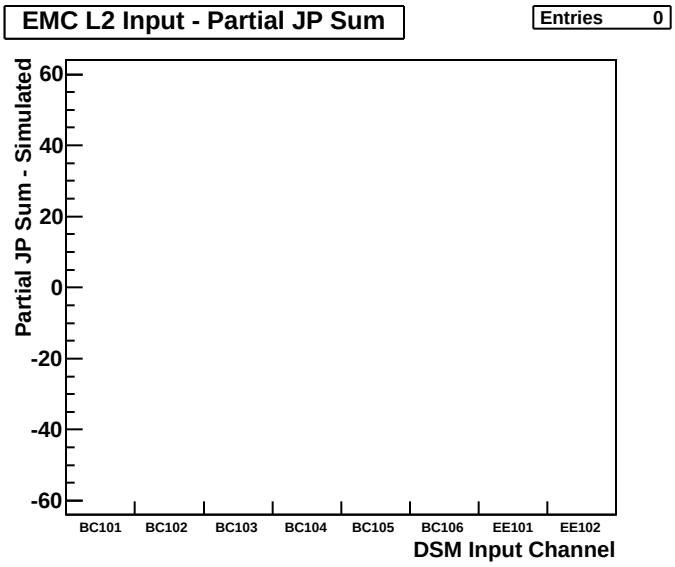

EMC L2 Input - JPY/JPB bits
Entries 5360000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
356 244 275 246 284 342 125 178
1077 829 878 845 1124 1073 324 418
2038 1495 1595 1627 2777 2212 443 505
666529 667432 667262 667282 668315 668373 669108 668899

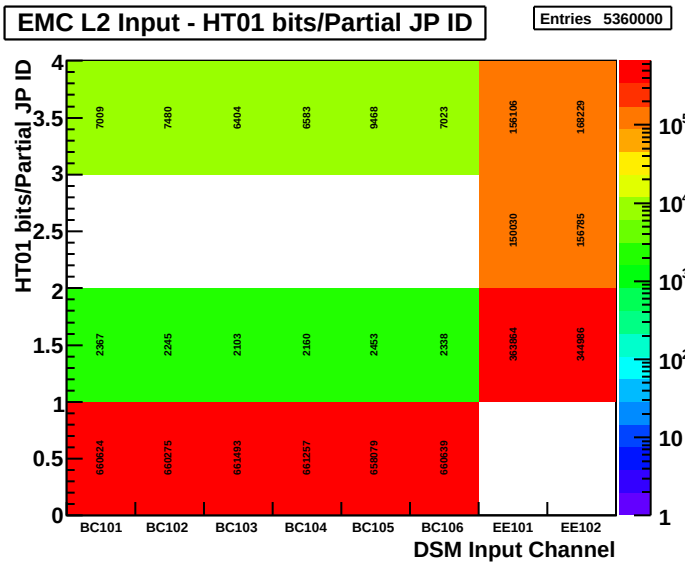

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

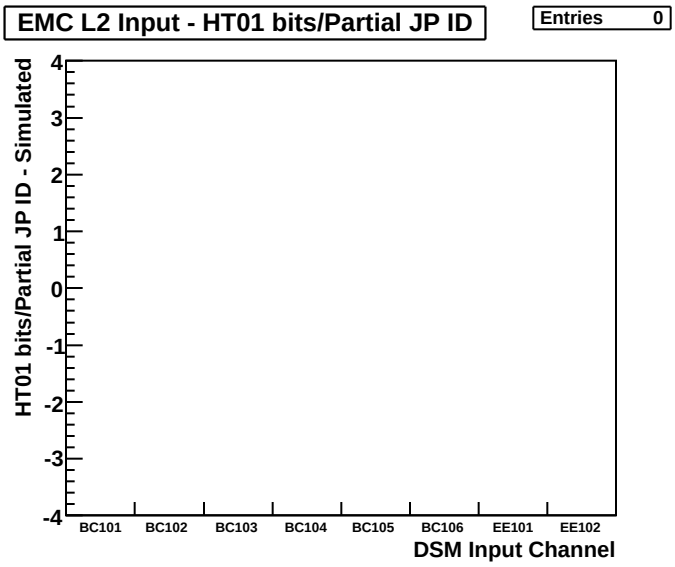

EMC L2 Input - JPZ/JPC bits
Entries 5360000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
321 256 309 267 294 355 160 196
913 866 933 868 1000 1174 377 431
1686 1582 1868 1789 2634 2499 457 540
667070 667296 666890 667076 666072 665972 669006 668833

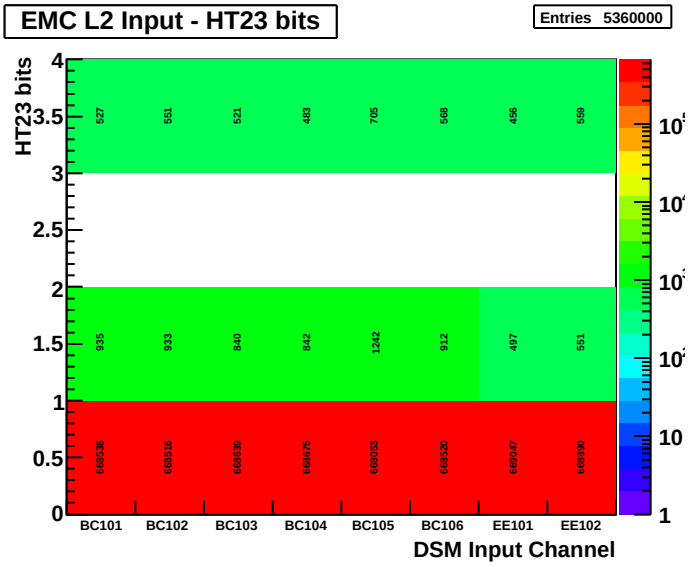

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

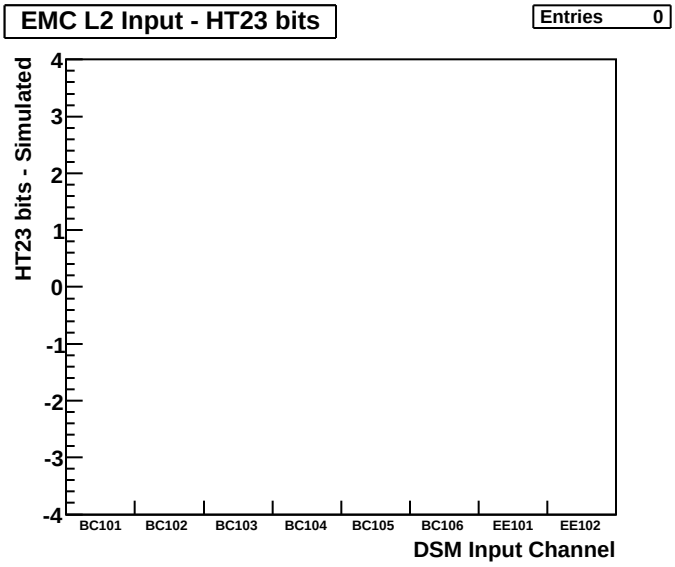

EMC L2 Input - Partial JP Sum
Entries 5360000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

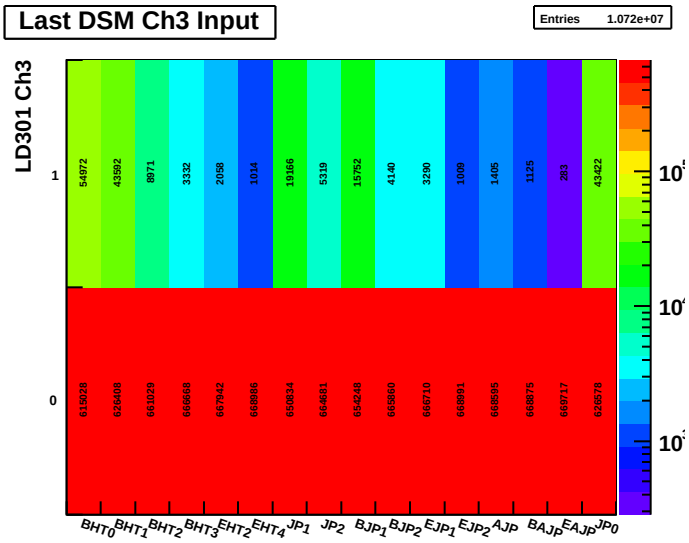

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

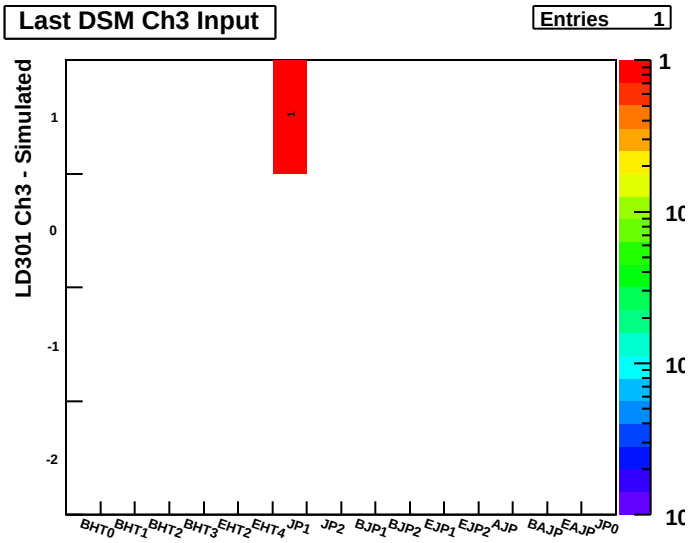

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5360000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

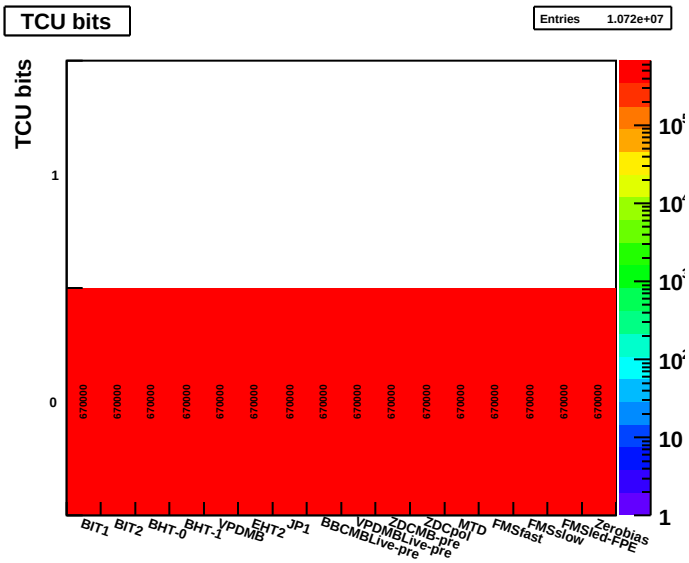

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

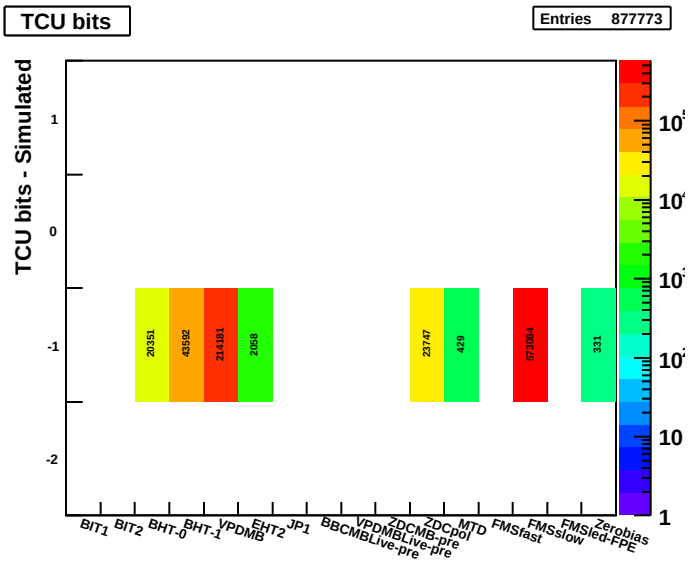

EMC L2 Input - HT23 bits
Entries 5360000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

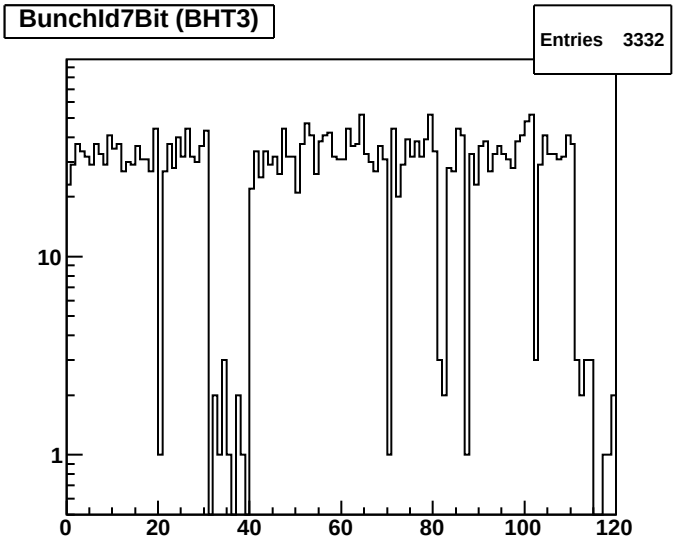

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

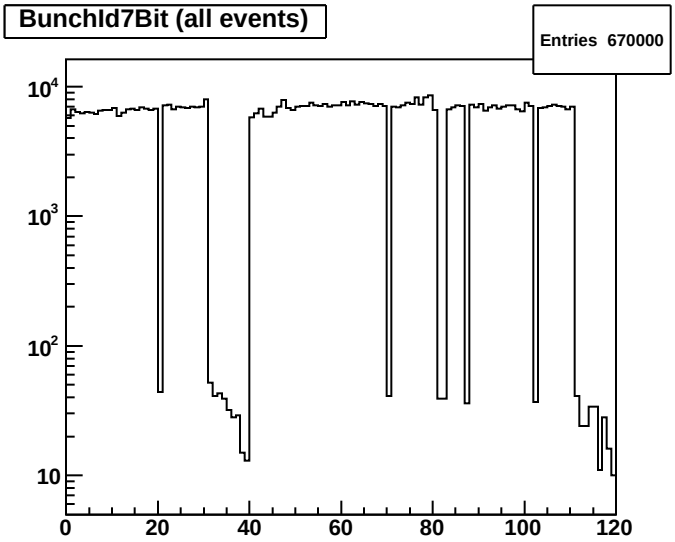

Last DSM Ch3 Input
Entries 1.072e+07
LD301 Ch3
54972 43592 8971 3332 2058 1014 19166 5319 15752 4140 3290 1009 1405 1125 283 43422
615028 626408 661029 666668 667942 668986 650834 664681 654248 665860 666710 668991 668595 668875 669717 626578
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

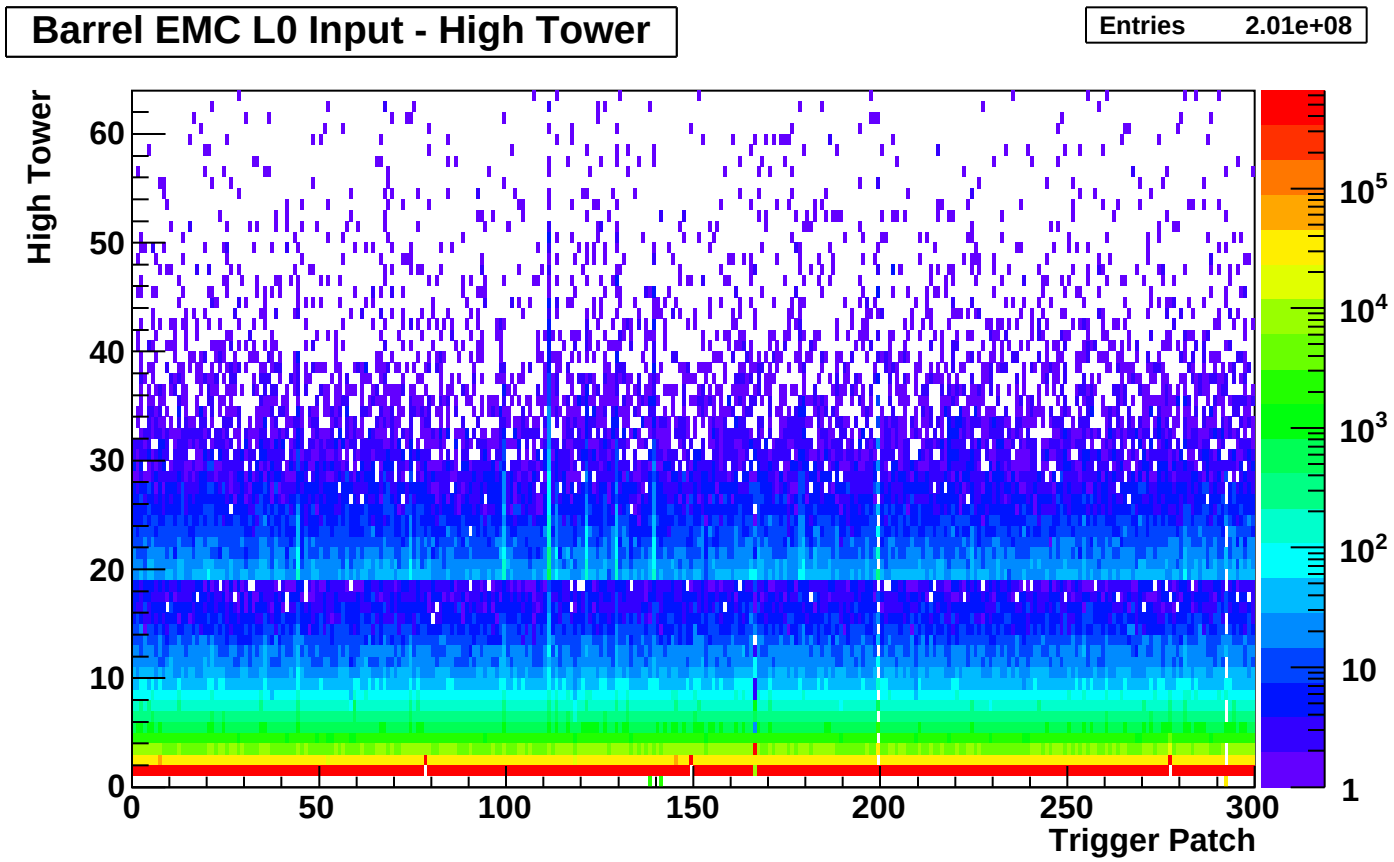

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

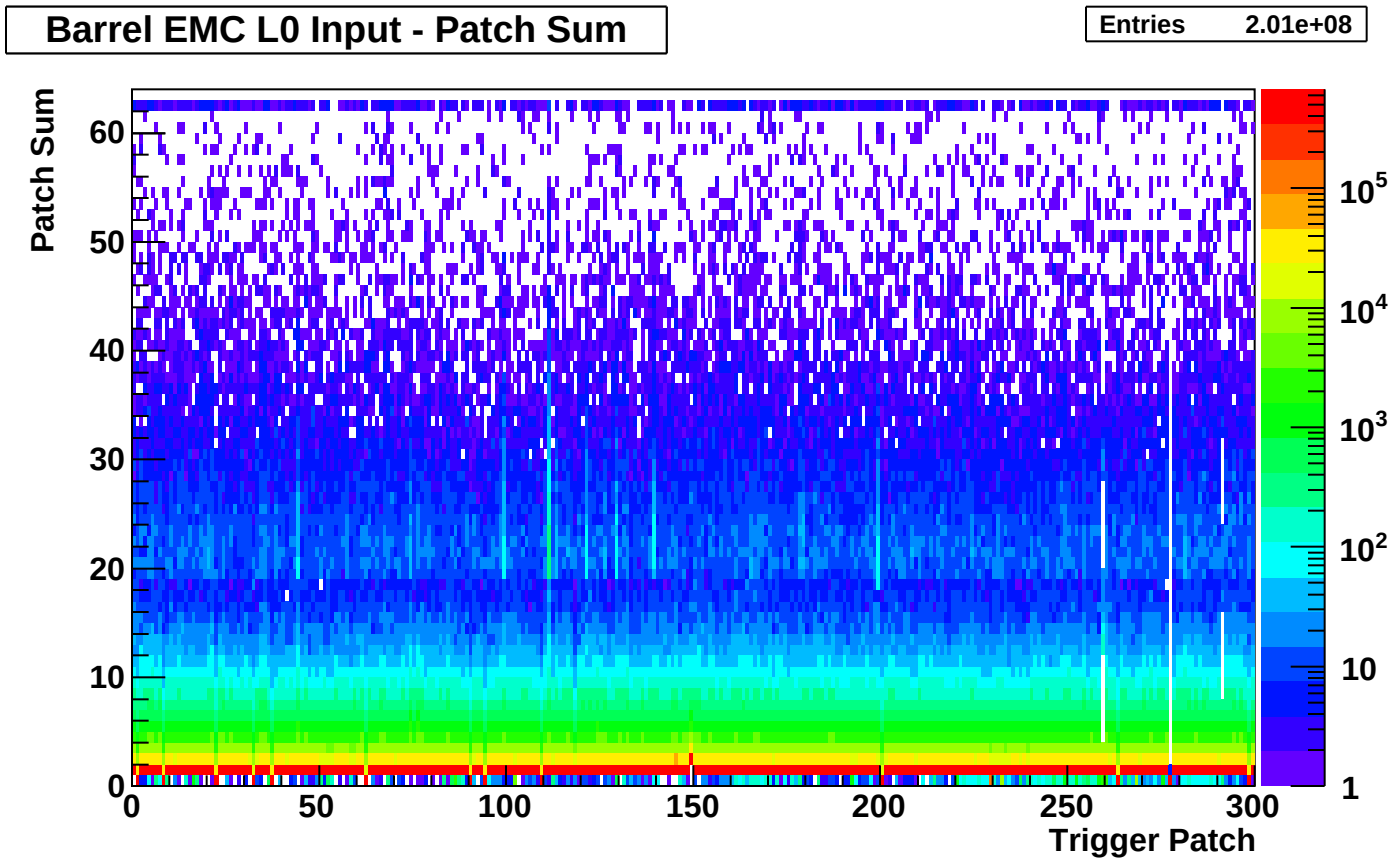

TCU bits
Entries 1.072e+07
TCU bits
670000 670000 670000 670000 670000 670000 670000 670000 670000 670000 670000 670000 670000 670000 670000
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

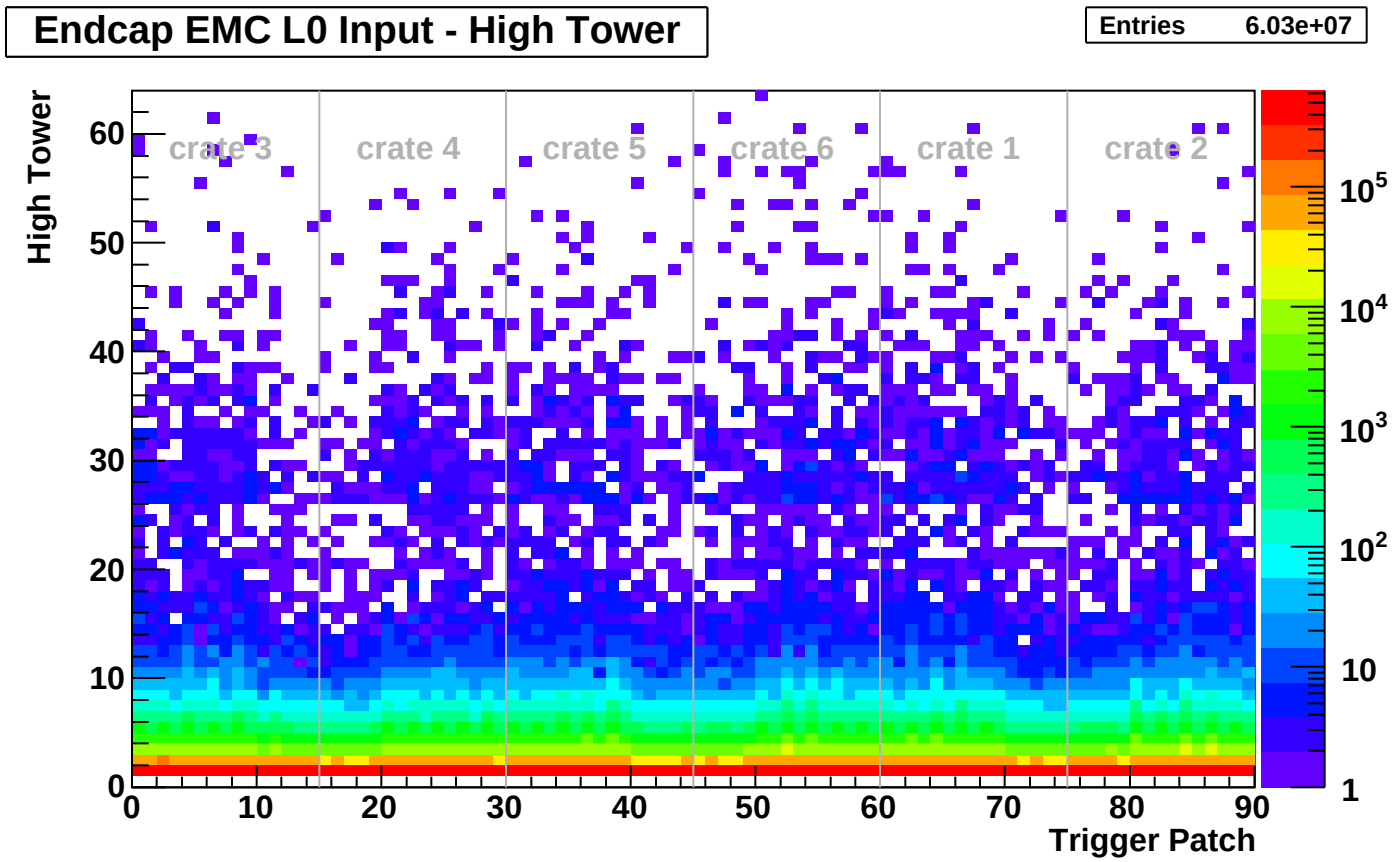

TCU bits
Entries 877773
TCU bits - Simulated
20351 43592 214181 2058 23747 429 573084 331
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

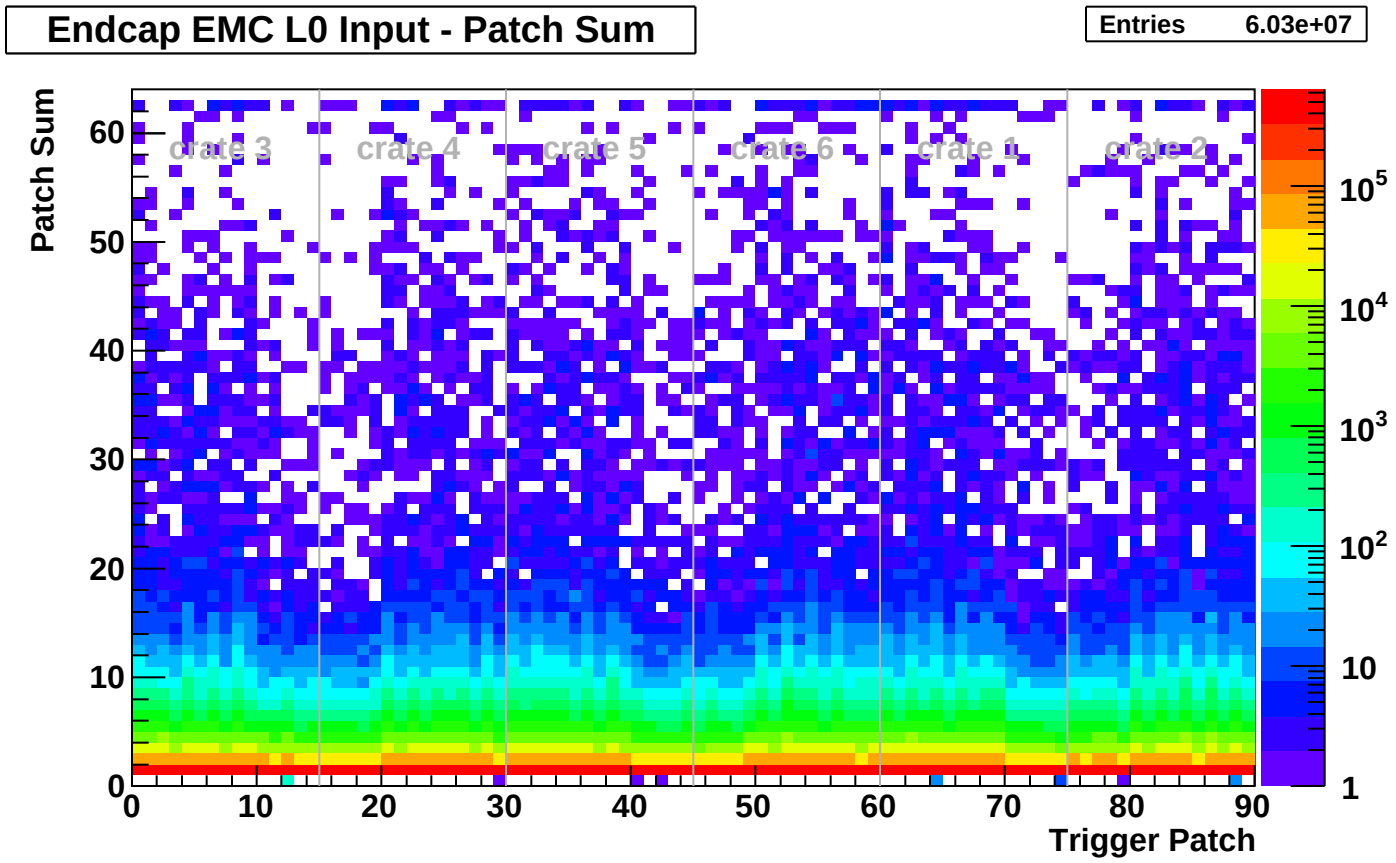

BunchId7Bit (BHT3)
Entries 3332

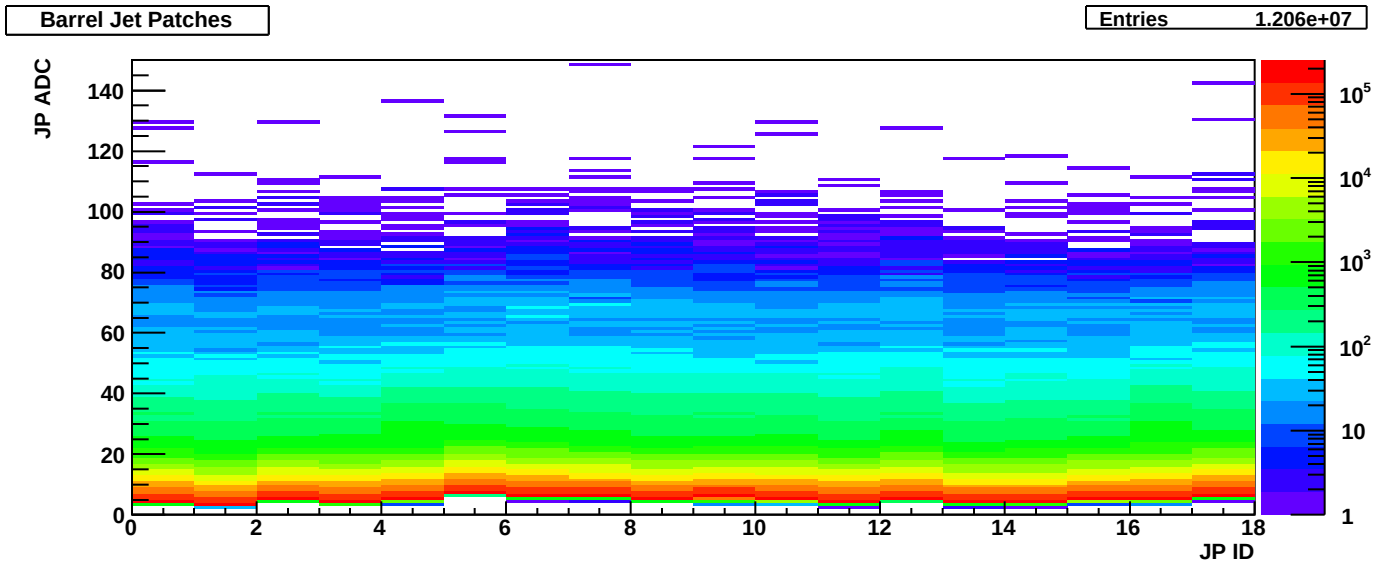

BunchId7Bit (all events)
Entries 670000

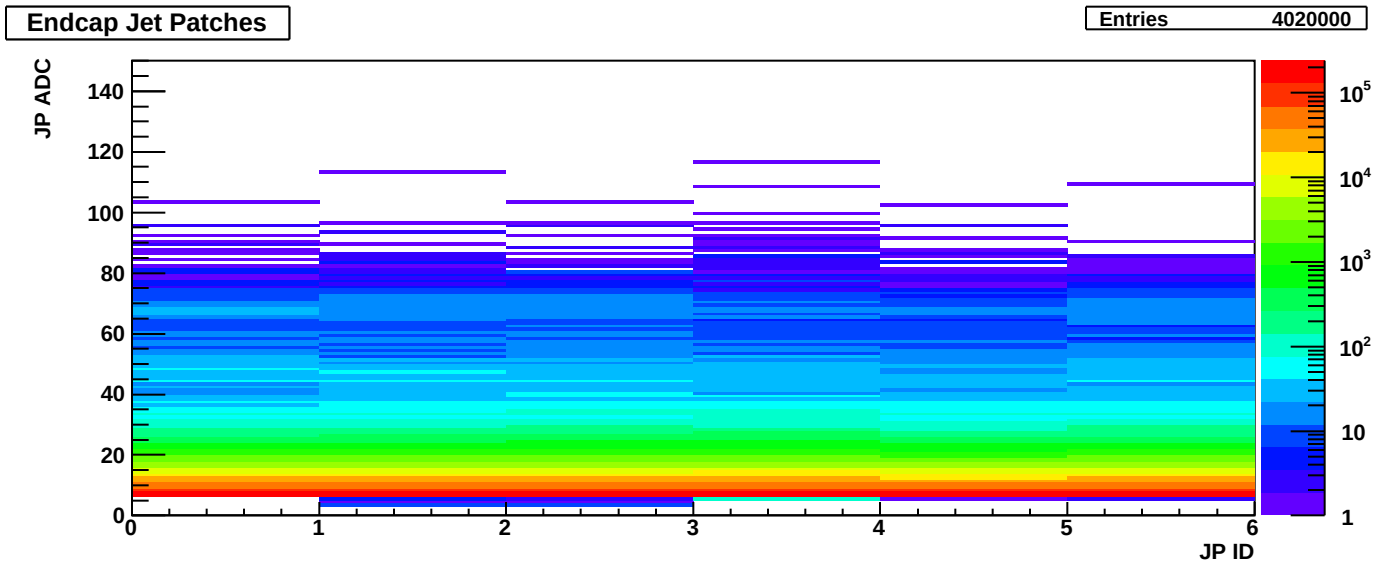

Barrel EMC L0 Input - High Tower
Entries 2.01e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

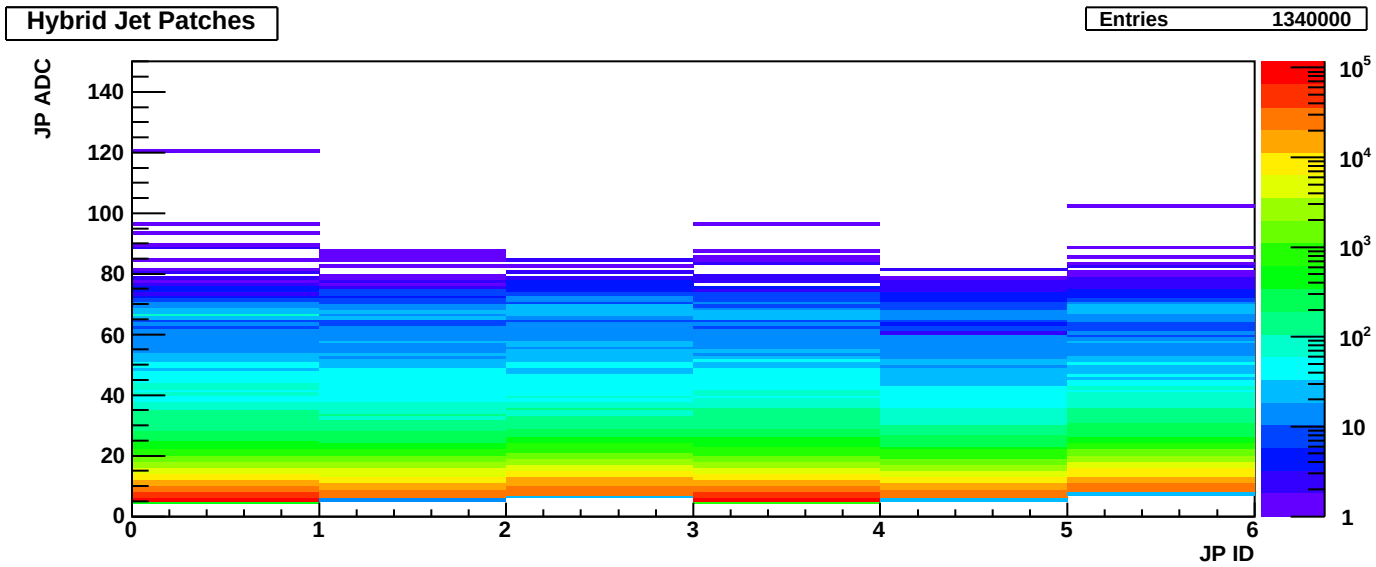

Barrel EMC L0 Input - Patch Sum
Entries 2.01e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

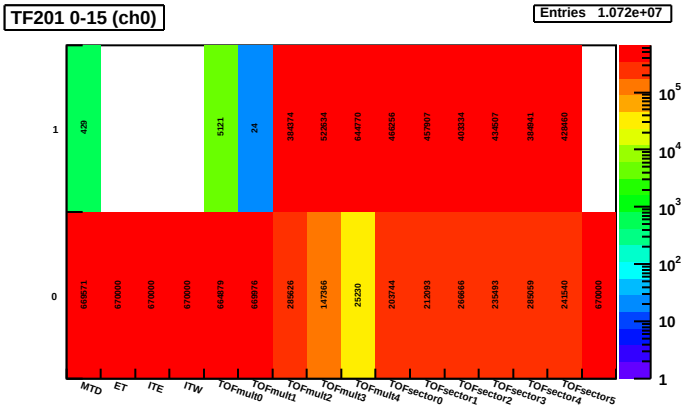

Endcap EMC L0 Input - High Tower
Entries 6.03e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

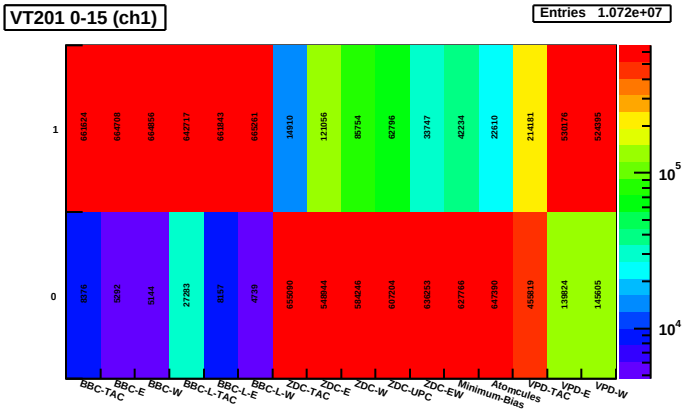

Endcap EMC L0 Input - Patch Sum
Entries 6.03e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

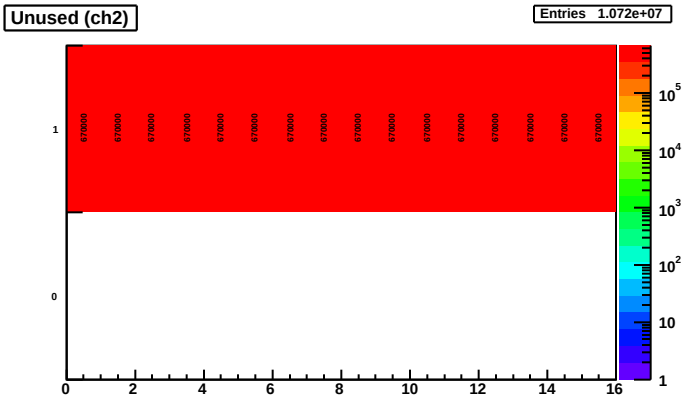

Barrel Jet Patches
Entries 1.206e+07
JP ADC
JP ID

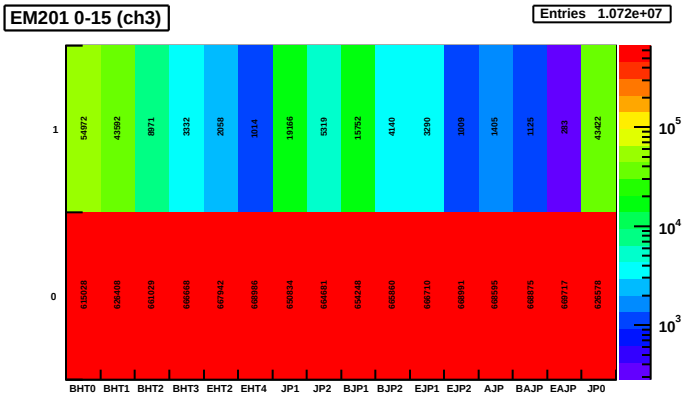

Endcap Jet Patches
Entries 4020000
JP ADC
JP ID

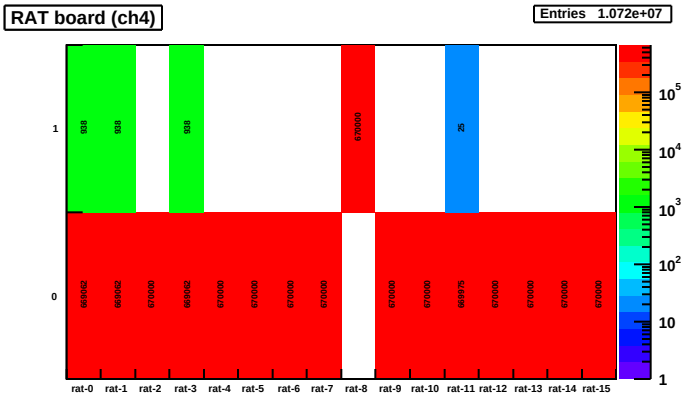

Hybrid Jet Patches
Entries 1340000
JP ADC
JP ID

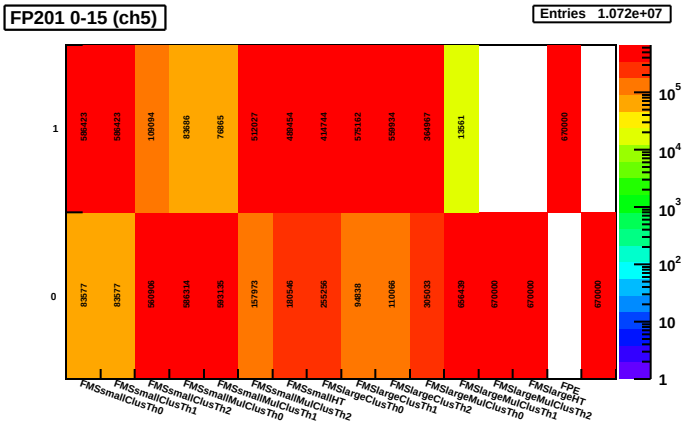

TF201 0-15 (ch0)
Entries 1.072e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

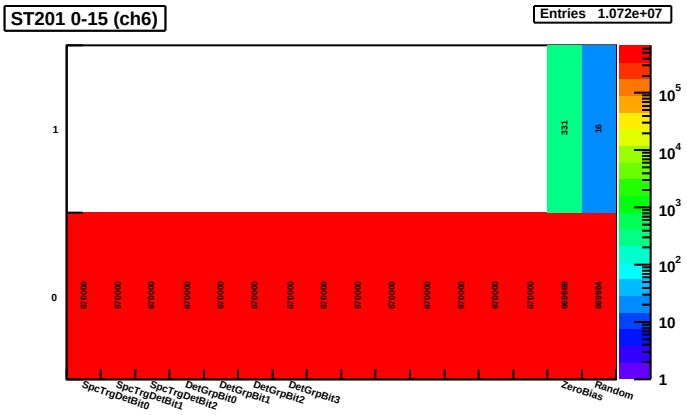

VT201 0-15 (ch1)
Entries 1.072e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

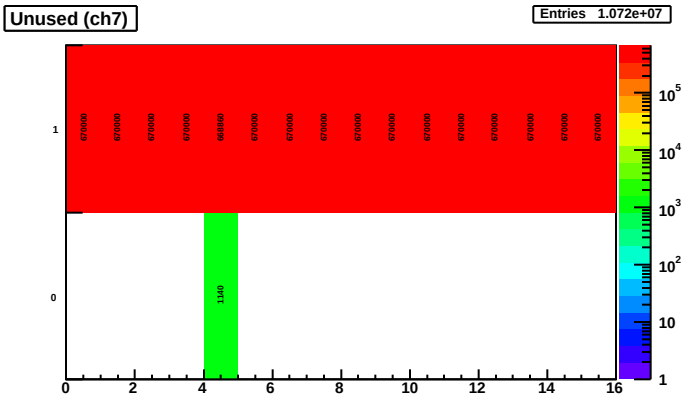

Unused (ch2)
Entries 1.072e+07

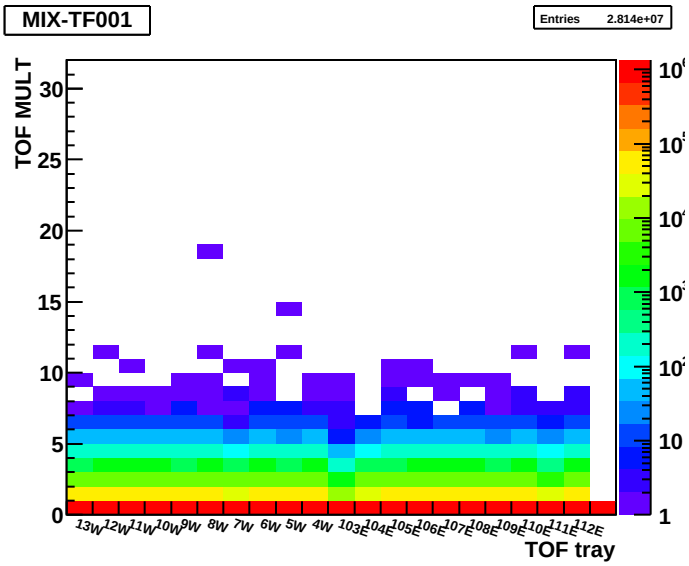

EM201 0-15 (ch3)
Entries 1.072e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

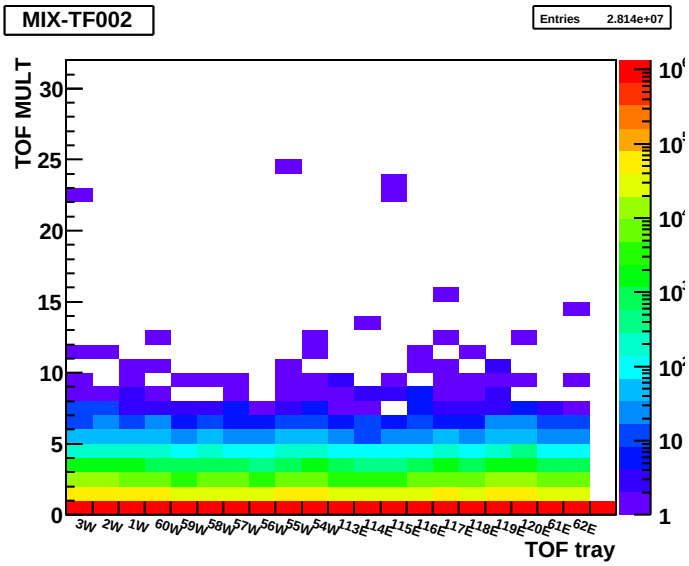

RAT board (ch4)
Entries 1.072e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

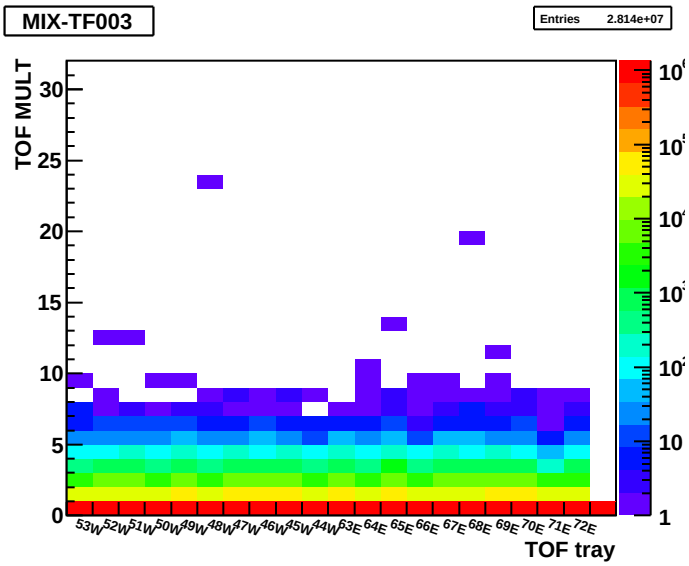

FP201 0-15 (ch5)
Entries 1.072e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

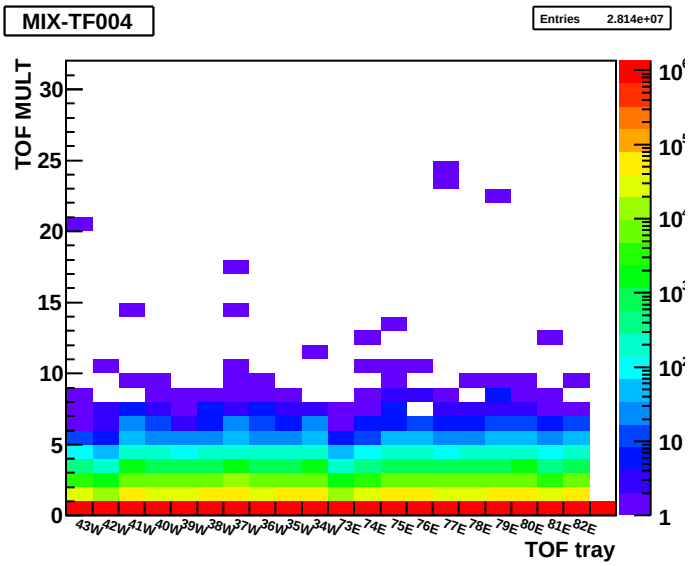

ST201 0-15 (ch6)
Entries 1.072e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

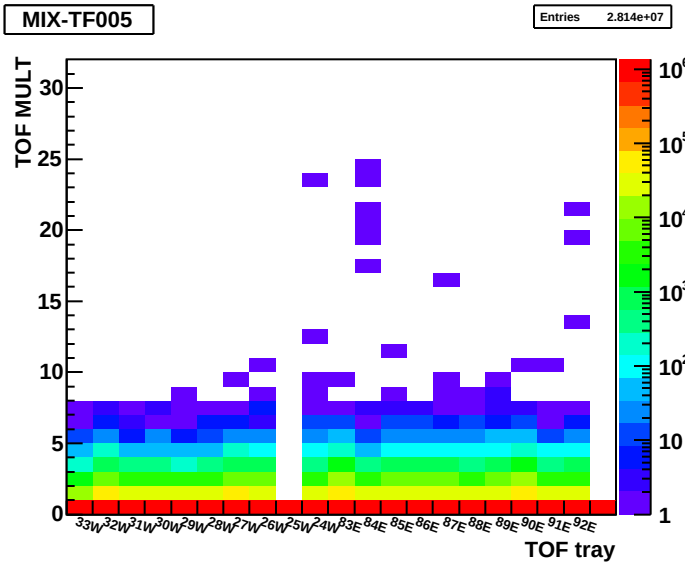

Unused (ch7)
Entries 1.072e+07

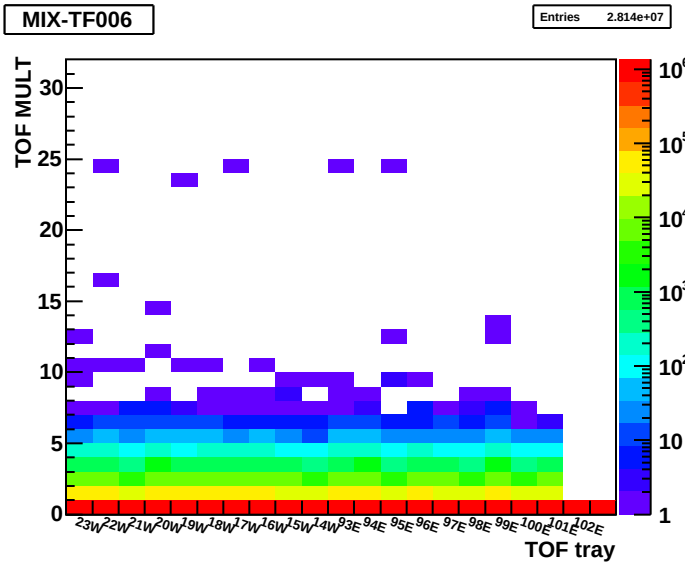

MIX-TF001
Entries 2.814e+07
TOF MULT
TOF tray

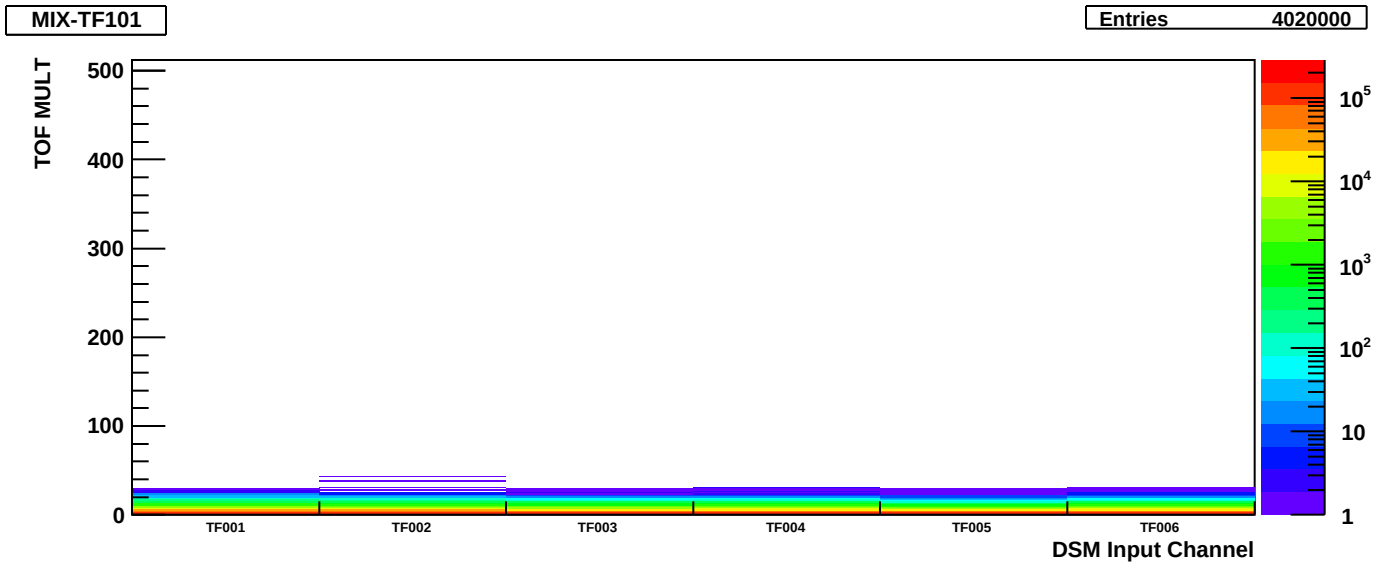

MIX-TF002
Entries 2.814e+07
TOF MULT
TOF tray

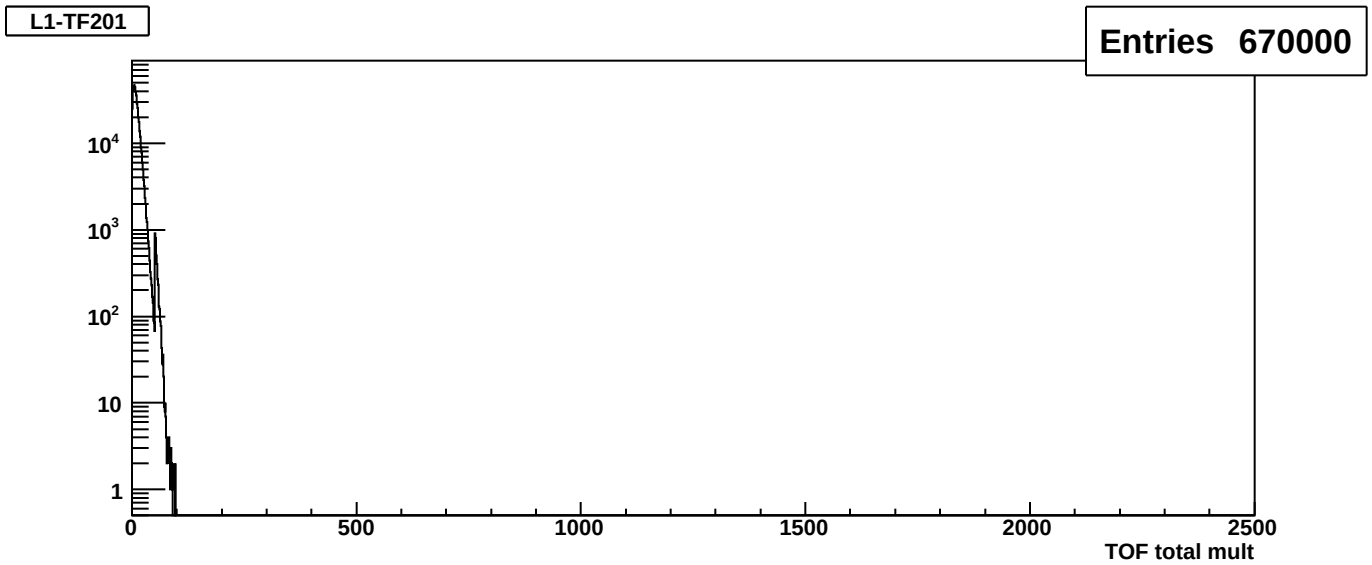

MIX-TF003
Entries 2.814e+07
TOF MULT
TOF tray

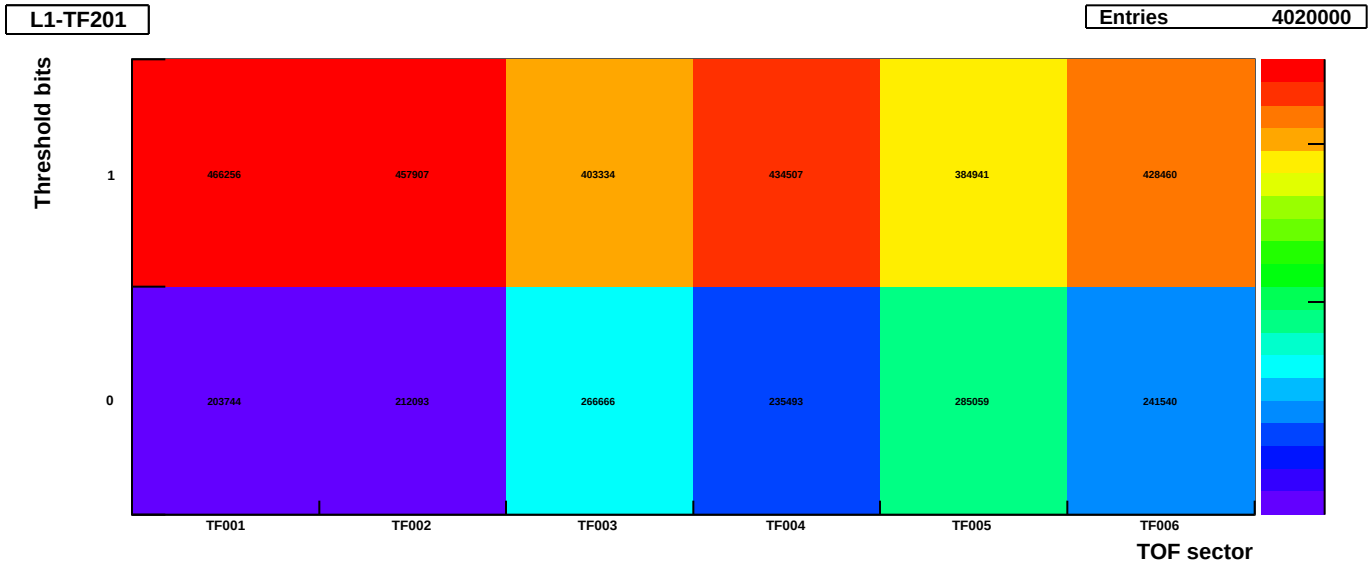

MIX-TF004
Entries 2.814e+07
TOF MULT
TOF tray

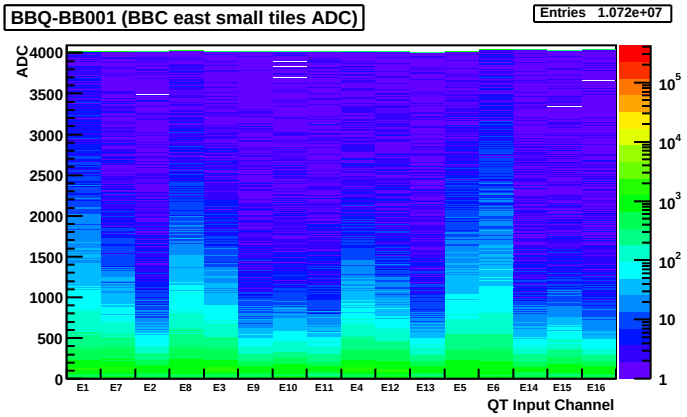

MIX-TF005
Entries 2.814e+07
TOF MULT
TOF tray

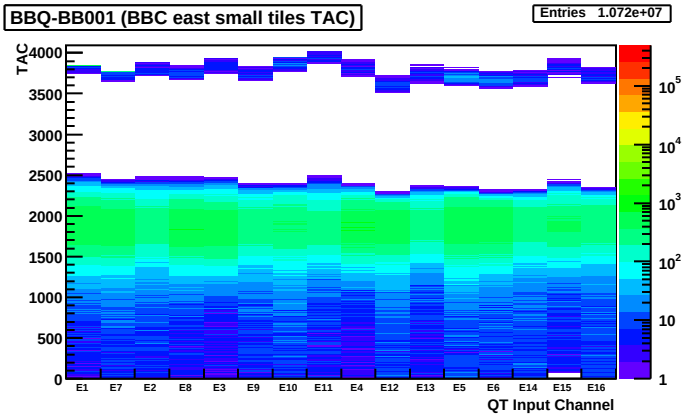

MIX-TF006
Entries 2.814e+07
TOF MULT
TOF tray

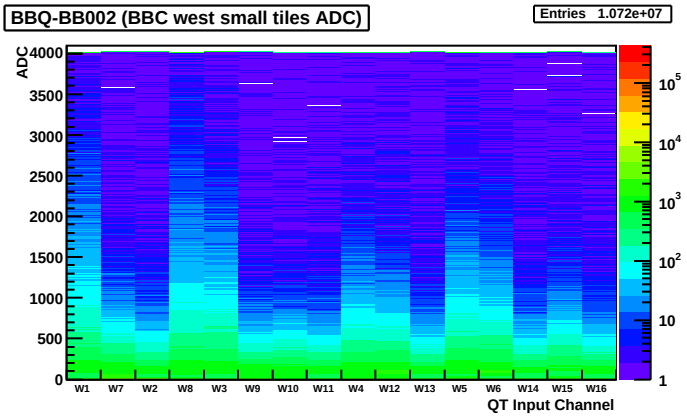

MIX-TF101
Entries 4020000
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

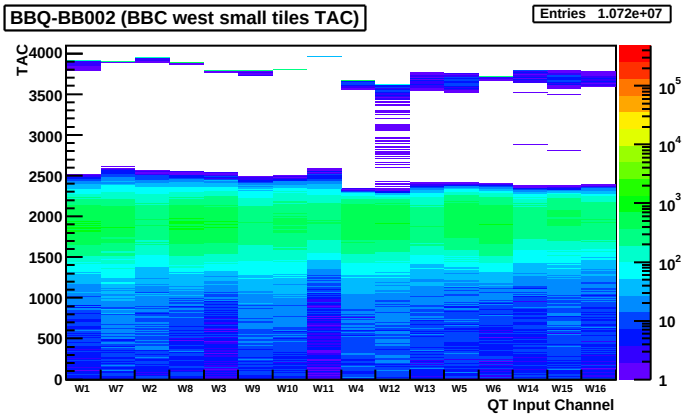

L1-TF201
Entries 670000
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

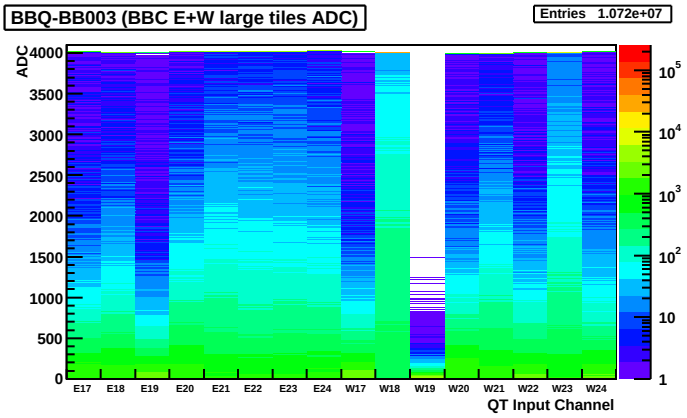

L1-TF201
Entries 4020000
Threshold bits
1
0
466256
457907
403334
434507
384941
428460
203744
212093
266666
235493
285059
241540
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

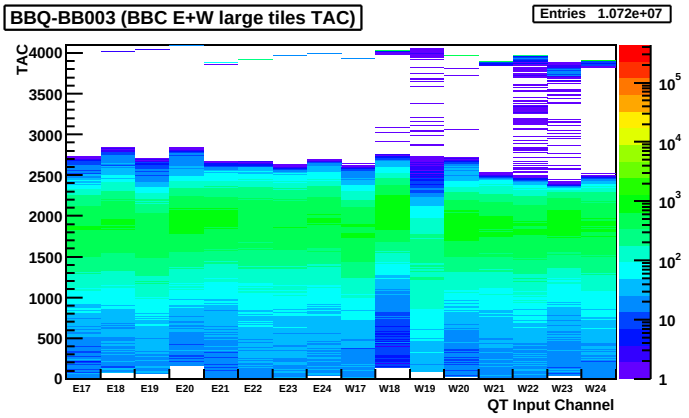

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.072e+07
ADC
QT Input Channel

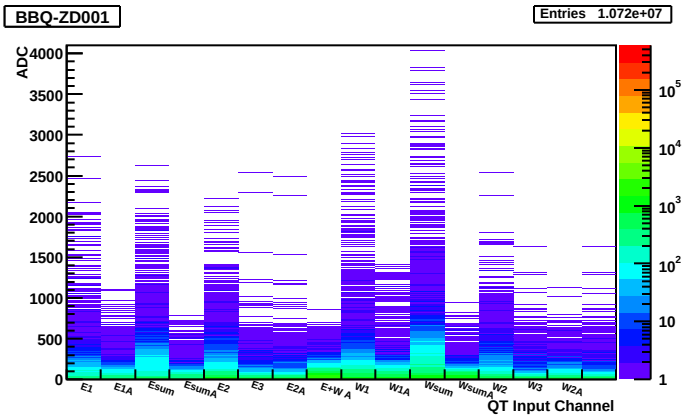

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.072e+07
TAC
QT Input Channel

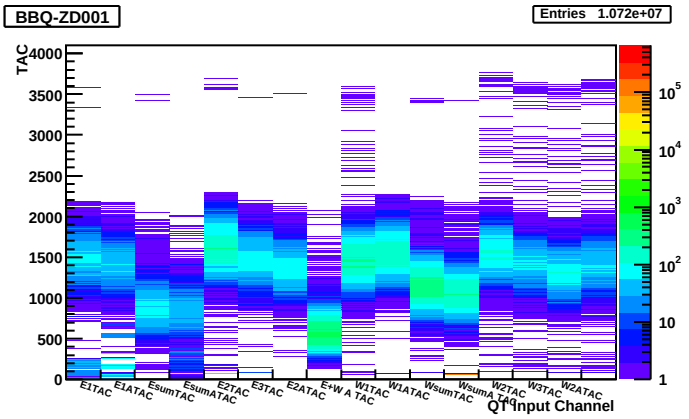

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.072e+07
ADC
QT Input Channel

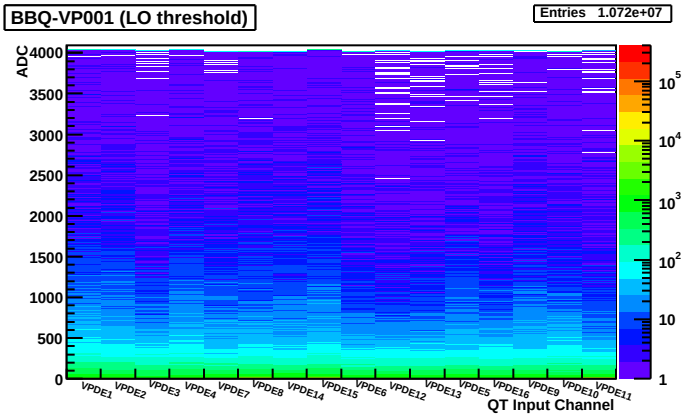

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.072e+07
TAC
QT Input Channel

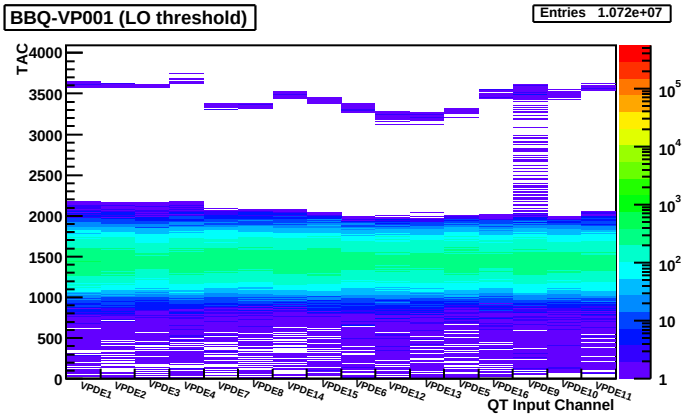

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.072e+07
ADC
QT Input Channel

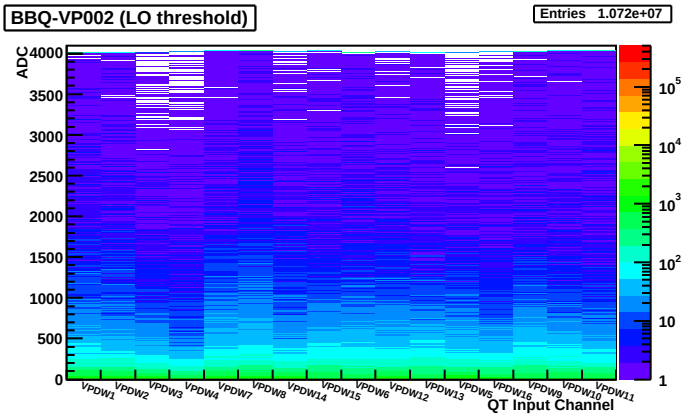

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.072e+07
TAC
QT Input Channel

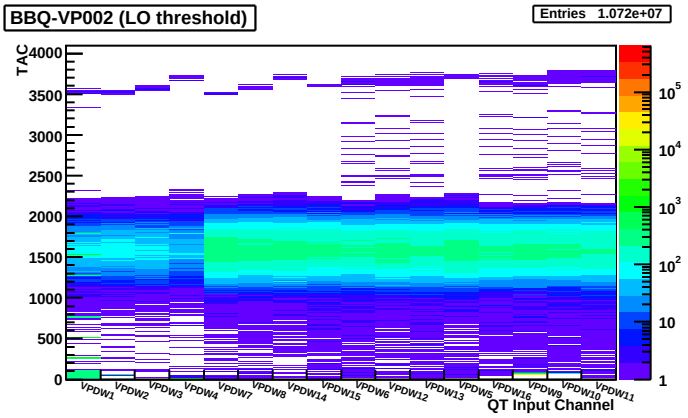

BBQ-ZD001
Entries 1.072e+07
ADC
QT Input Channel

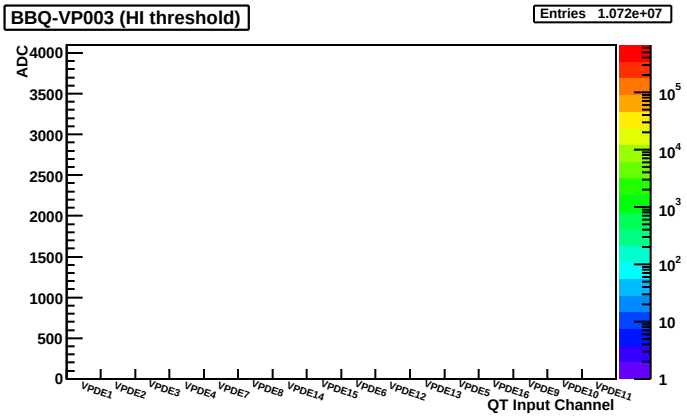

BBQ-ZD001
Entries 1.072e+07
TAC
QT Input Channel

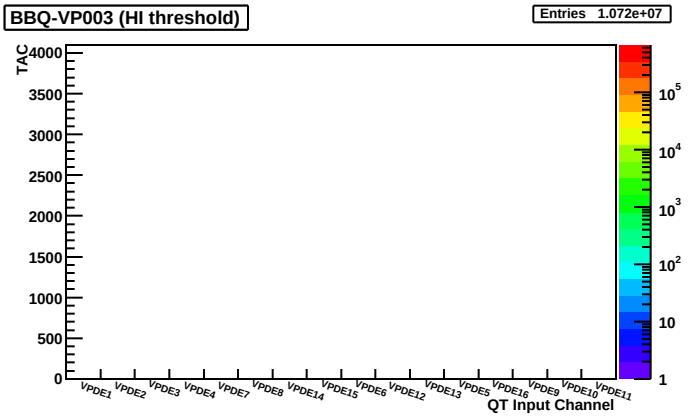

BBQ-VP001 (LO threshold)
Entries 1.072e+07
ADC
QT Input Channel

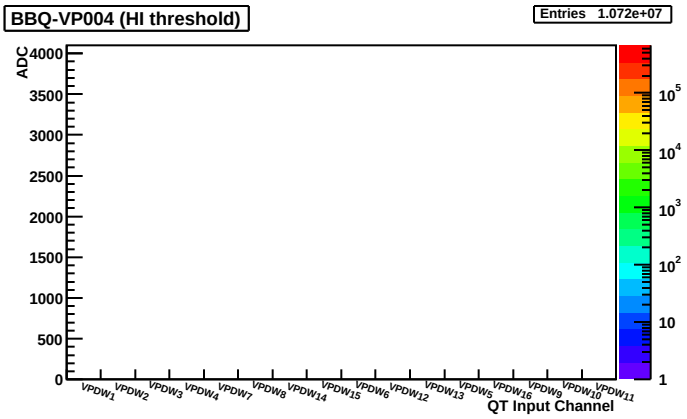

BBQ-VP001 (LO threshold)
Entries 1.072e+07
TAC
QT Input Channel

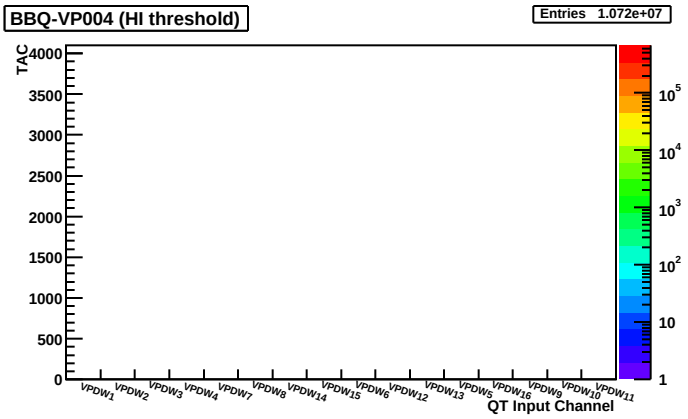

BBQ-VP002 (LO threshold)
Entries 1.072e+07
ADC
QT Input Channel

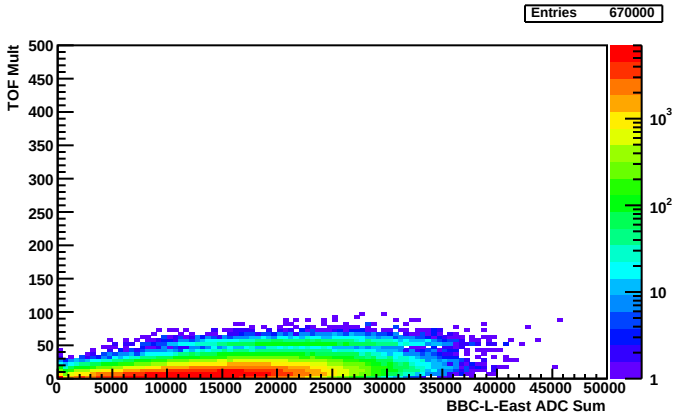

BBQ-VP002 (LO threshold)
Entries 1.072e+07
TAC
QT Input Channel

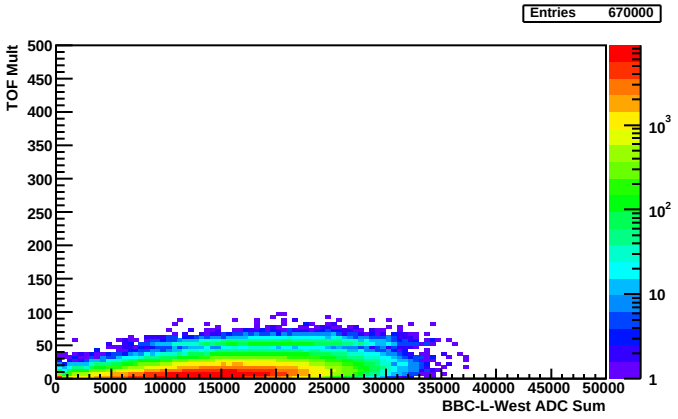

BBQ-VP003 (HI threshold)
Entries 1.072e+07
ADC
QT Input Channel

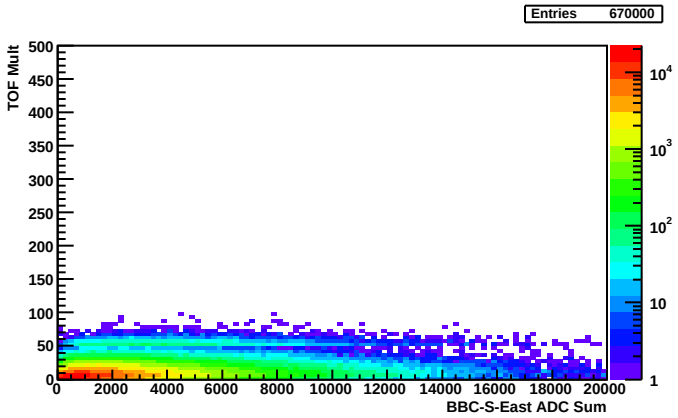

BBQ-VP003 (HI threshold)
Entries 1.072e+07
TAC
QT Input Channel

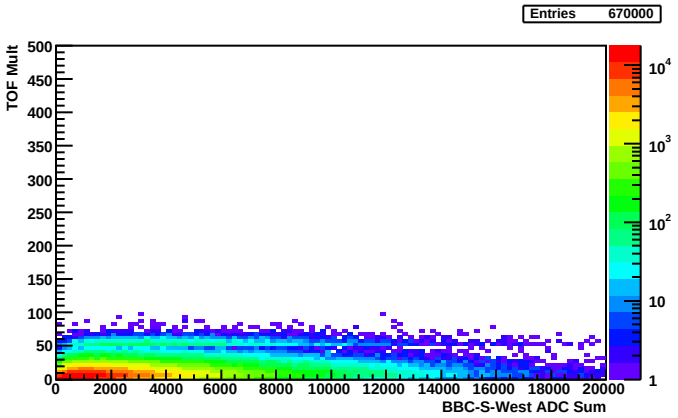

BBQ-VP004 (HI threshold)
Entries 1.072e+07
ADC
QT Input Channel

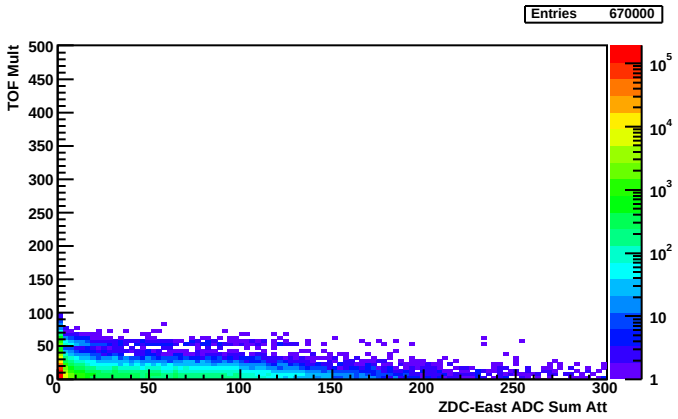

BBQ-VP004 (HI threshold)
Entries 1.072e+07
TAC
QT Input Channel

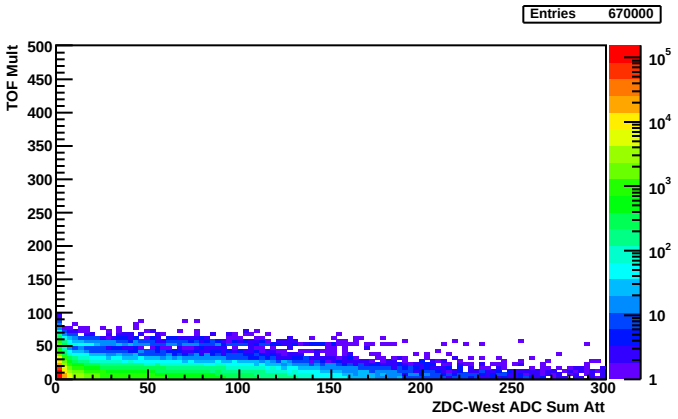

Entries 670000
TOF Mult
BBC-L-East ADC Sum

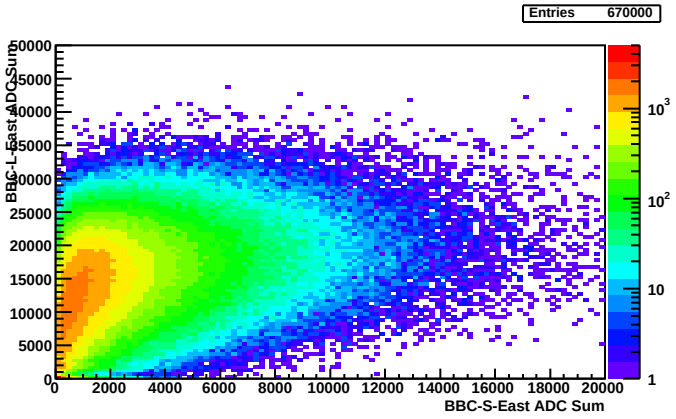

Entries 670000
TOF Mult
BBC-L-West ADC Sum

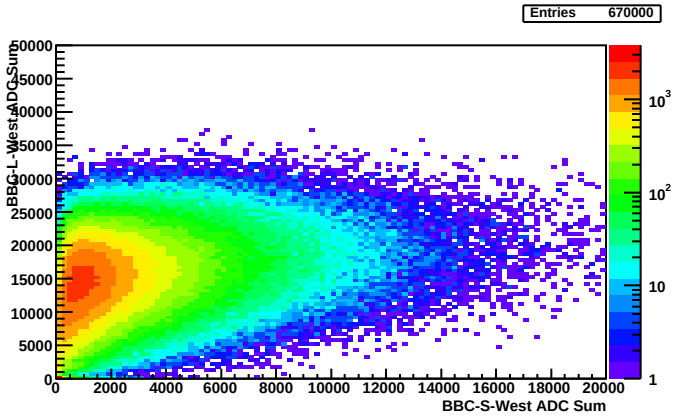

Entries 670000
TOF Mult
BBC-S-East ADC Sum

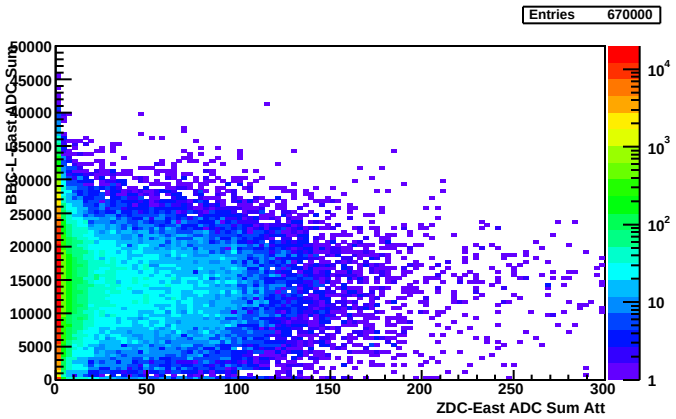

Entries 670000
TOF Mult
BBC-S-West ADC Sum

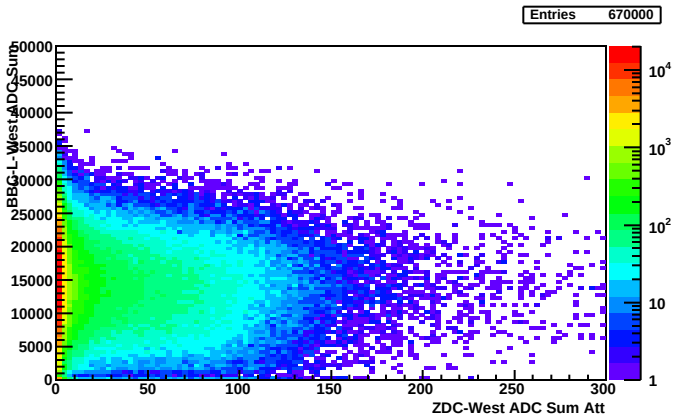

Entries 670000
TOF Mult
ZDC-East ADC Sum Att

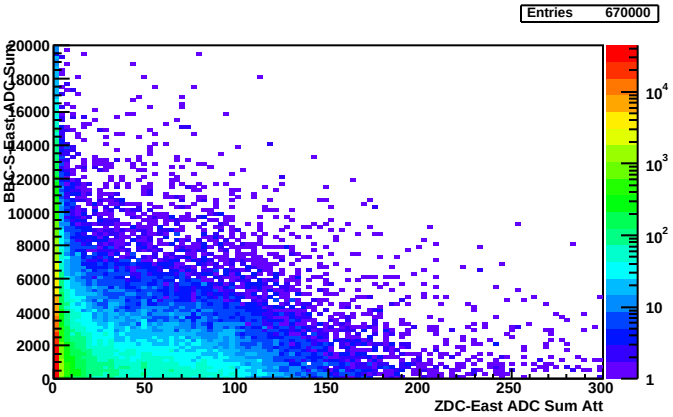

Entries 670000
TOF Mult
ZDC-West ADC Sum Att

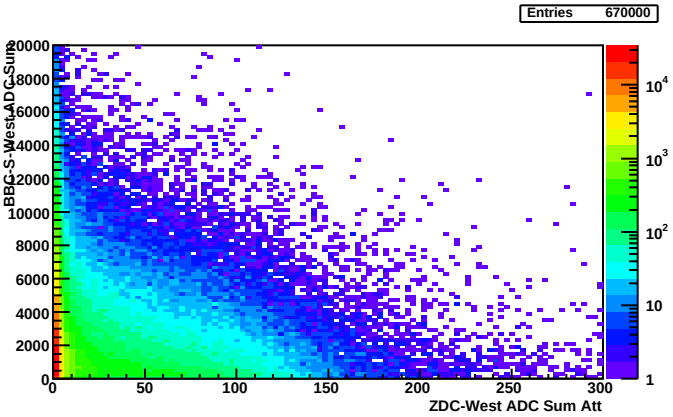

Entries 670000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

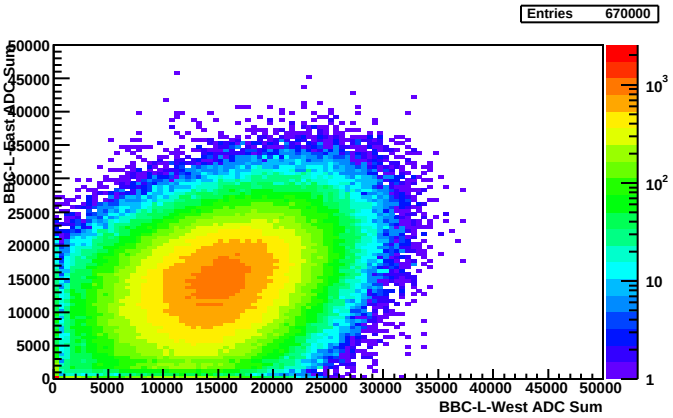

Entries 670000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

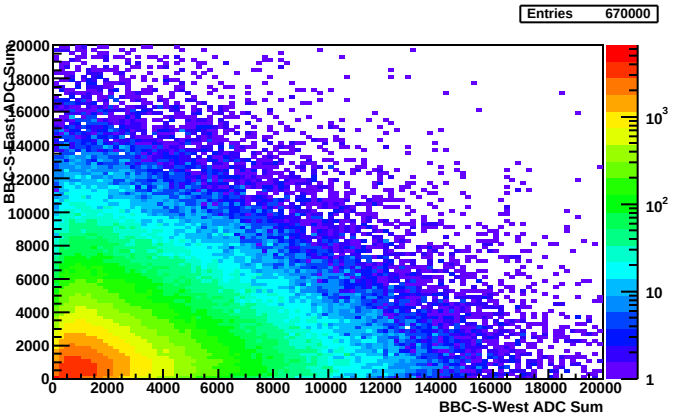

Entries 670000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

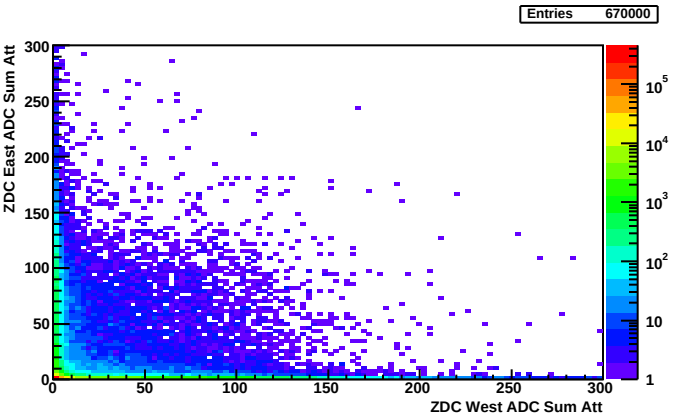

Entries 670000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

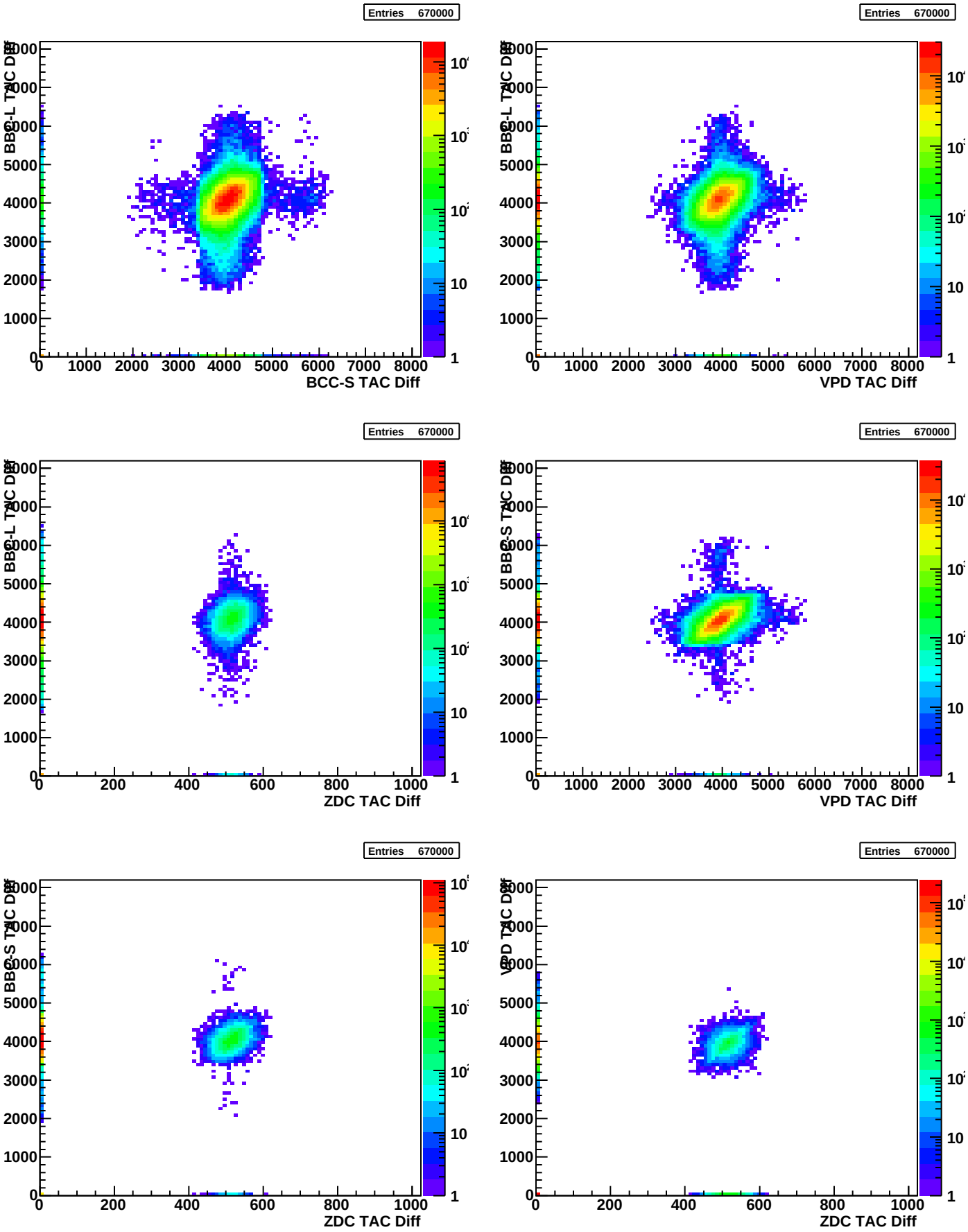

Entries 670000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

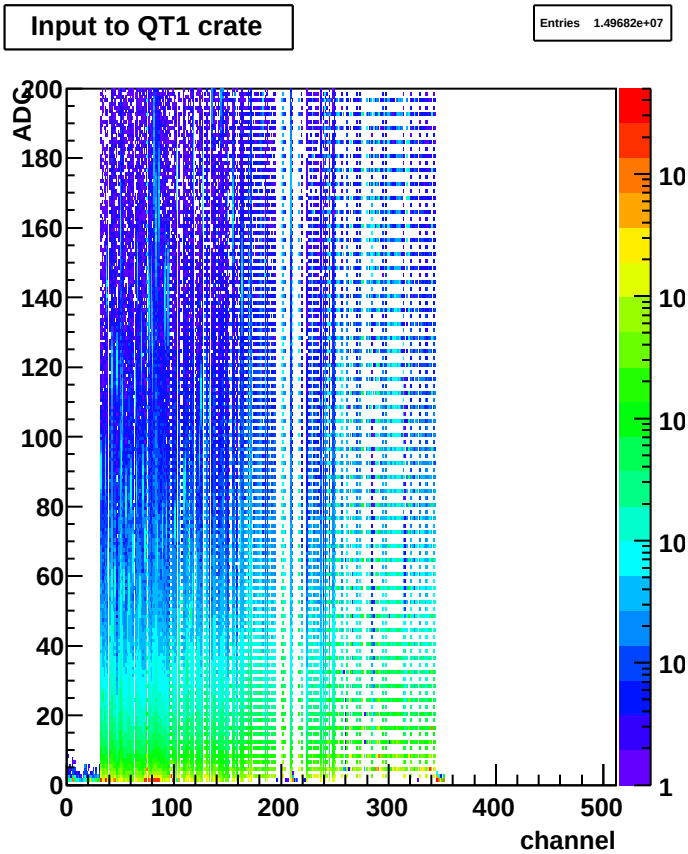

Entries 670000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

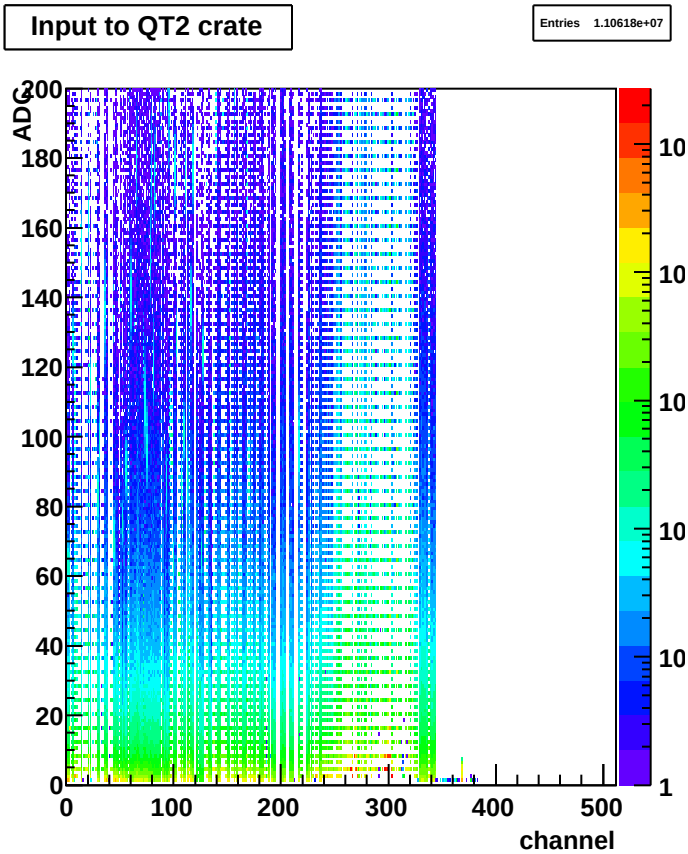

Entries 670000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

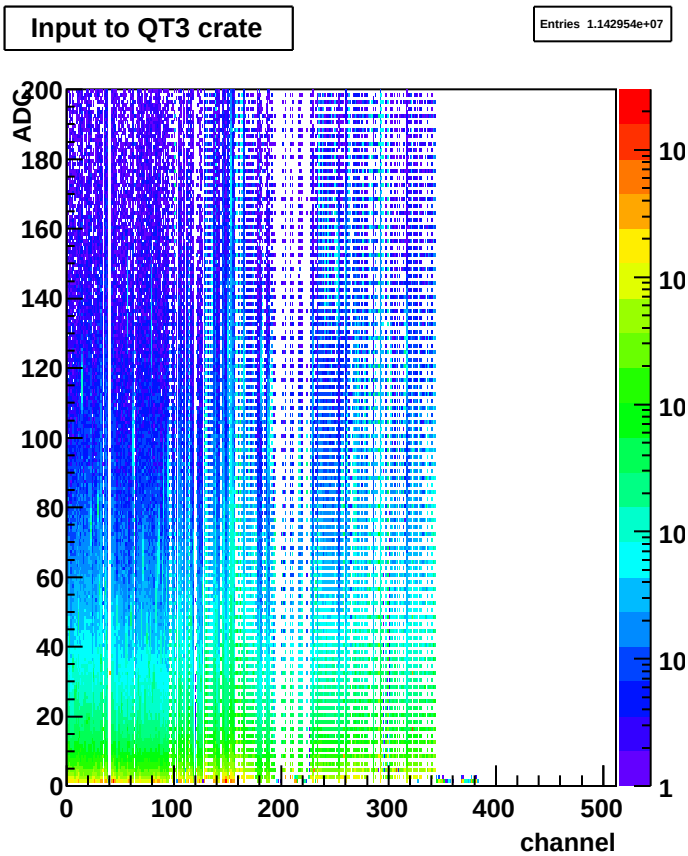

Entries 670000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

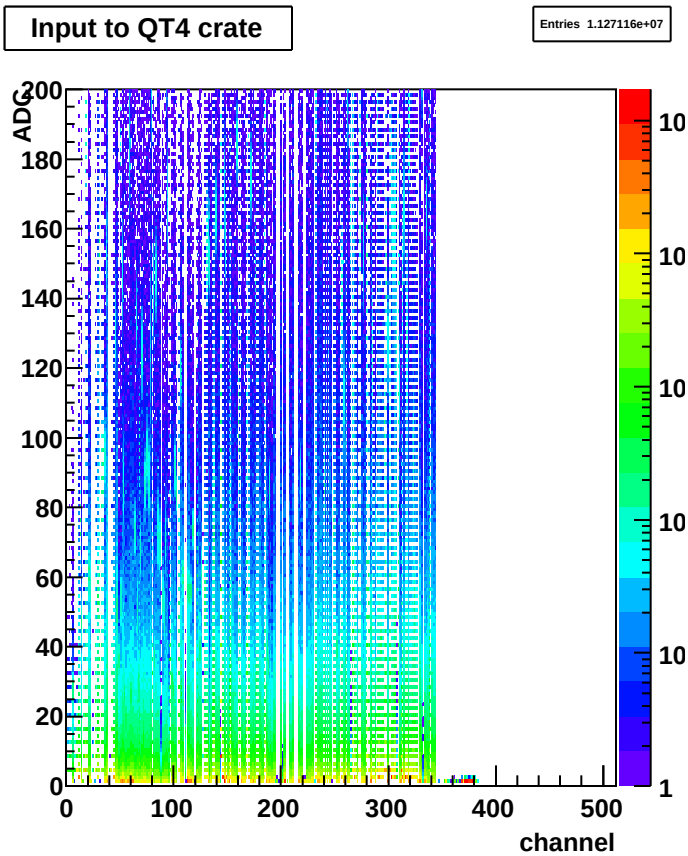

Entries 670000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

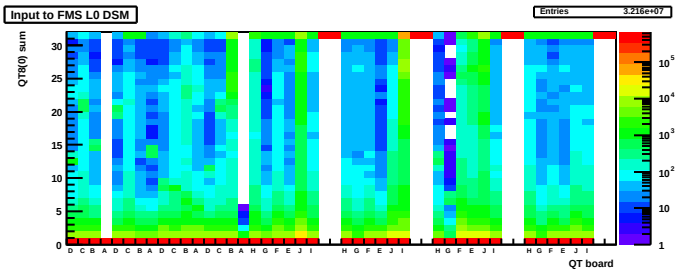

Entries 670000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 670000
BBC-L TAC Diff
VPD TAC Diff
Entries 670000
BBC-L TAC Diff
ZDC TAC Diff
Entries 670000
BBC-S TAC Diff
VPD TAC Diff
Entries 670000
BBC-S TAC Diff
ZDC TAC Diff
Entries 670000
VPD TAC Diff
ZDC TAC Diff

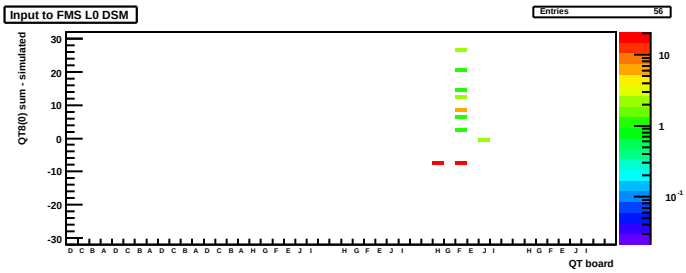

Input to QT1 crate
Entries 1.49682e+07
ADC
channel

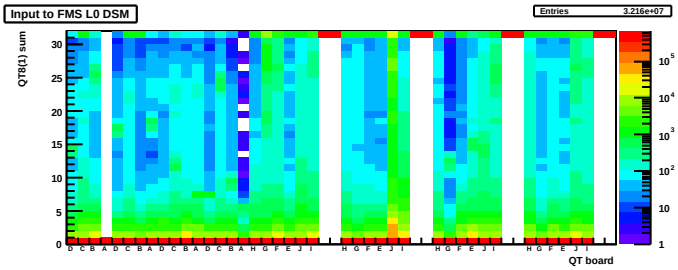

Input to QT2 crate
Entries 1.10618e+07
ADC
channel

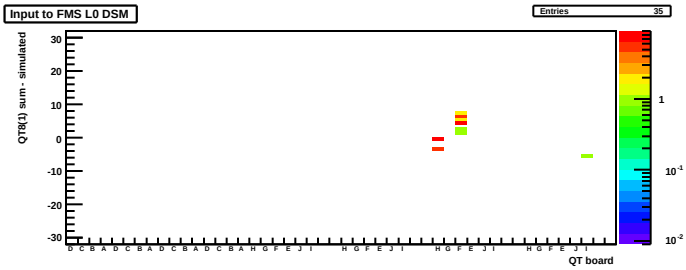

Input to QT3 crate
Entries 1.142954e+07
ADC
channel

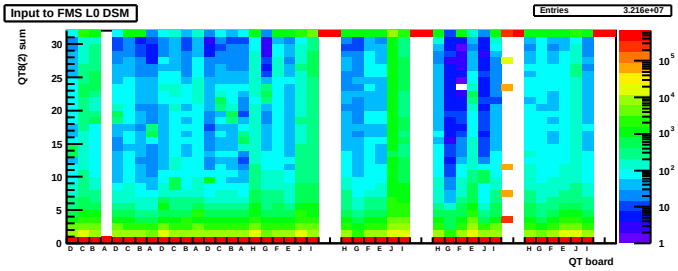

Input to QT4 crate
Entries 1.127116e+07
ADC
channel

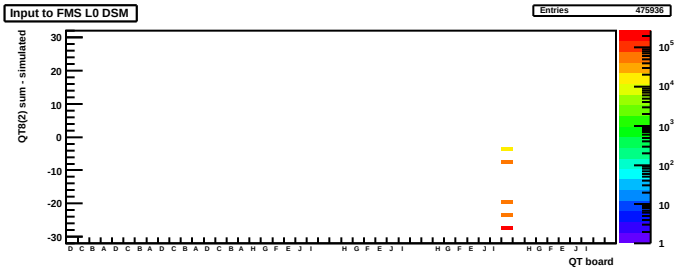

Input to FMS L0 DSM
Entries 3.216e+07
QT8(0) sum
QT board

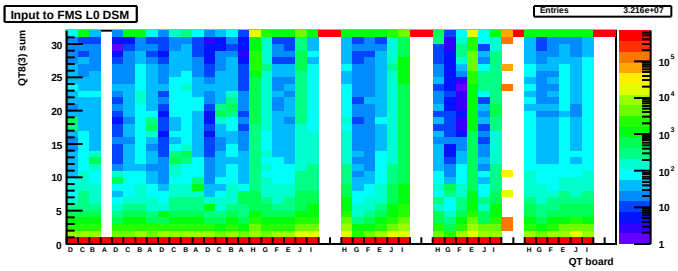

Input to FMS L0 DSM
Entries 56
QT8(0) sum - simulated
QT board

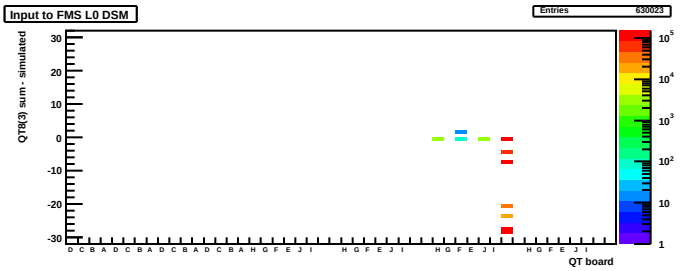

Input to FMS L0 DSM
Entries 3.216e+07
QT8(1) sum
QT board

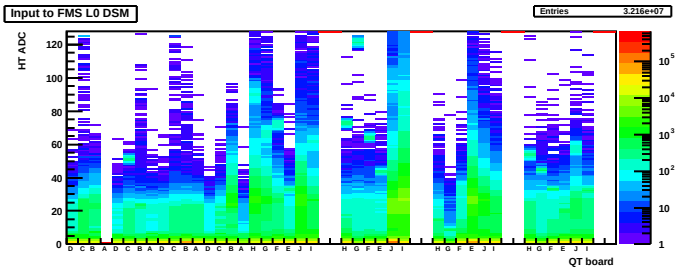

Input to FMS L0 DSM
Entries 35
QT8(1) sum - simulated
QT board

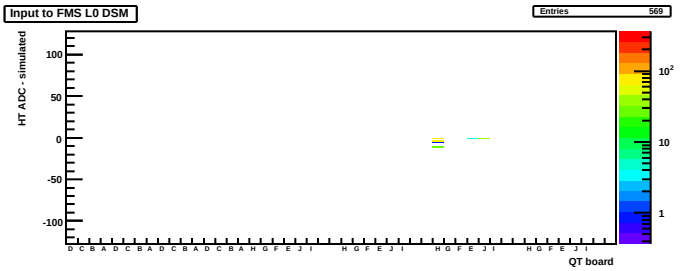

Input to FMS L0 DSM
Entries 3.216e+07
QT8(2) sum
QT board

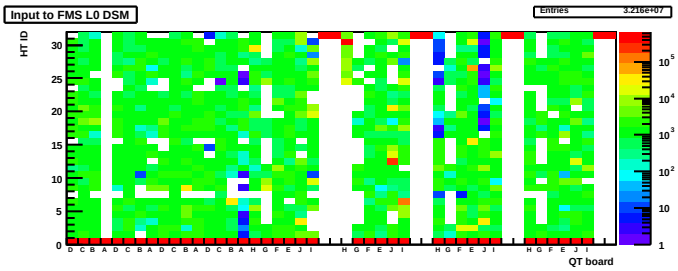

Input to FMS L0 DSM
Entries 475936
QT8(2) sum - simulated
QT board

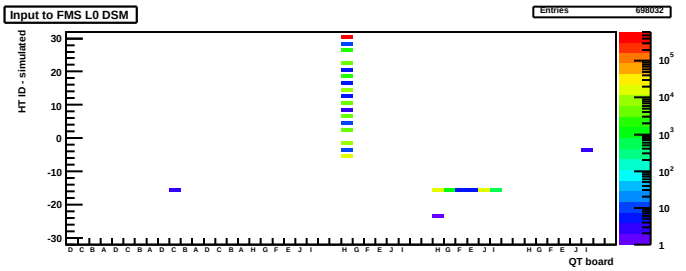

Input to FMS L0 DSM
Entries 3.216e+07
QT8(3) sum
QT board

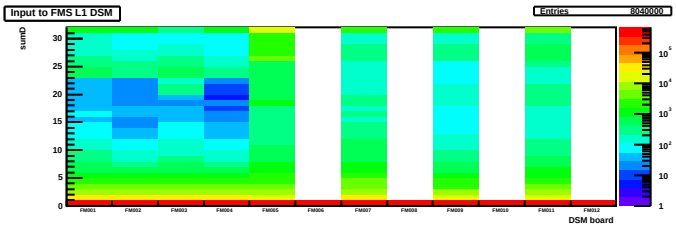

Input to FMS L0 DSM
Entries 630023
QT8(3) sum - simulated
QT board

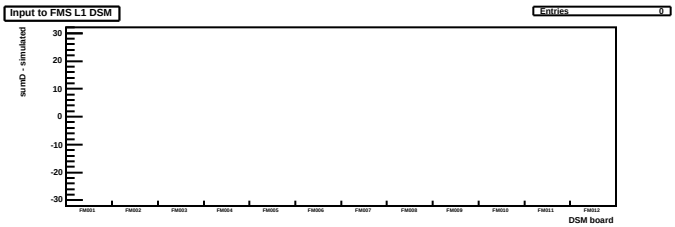

Input to FMS L0 DSM
Entries 3.216e+07
HT ADC
QT board

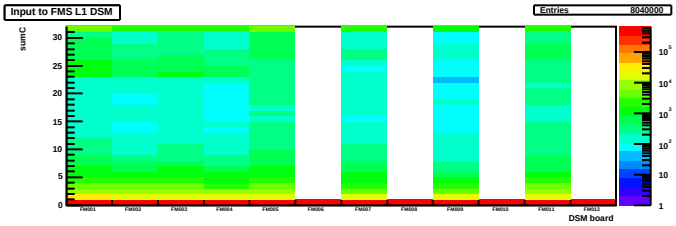

Input to FMS L0 DSM
Entries 569
HT ADC - simulated
QT board

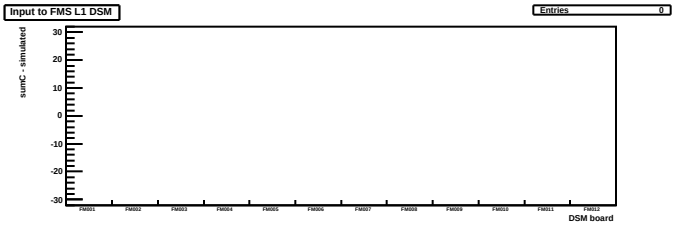

Input to FMS L0 DSM
Entries 3.216e+07
HT ID
QT board

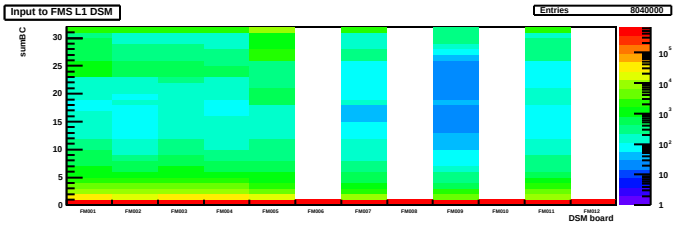

Input to FMS L0 DSM
Entries 698032
HT ID - simulated
QT board

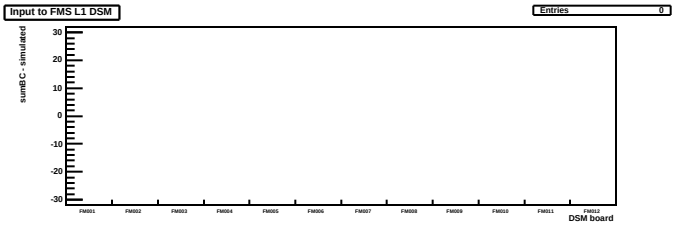

Input to FMS L1 DSM
Entries 8040000
sumD
DSM board

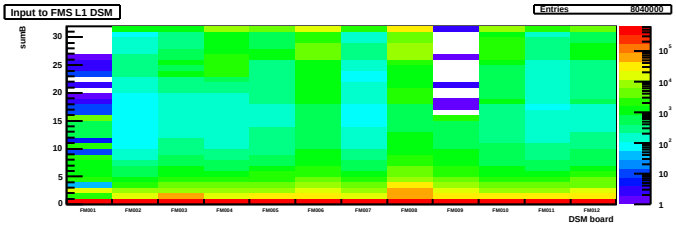

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

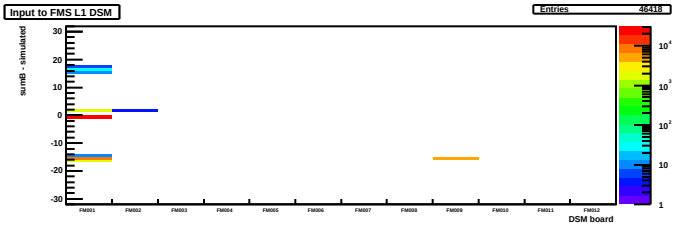

Input to FMS L1 DSM
Entries 8040000
sumC
DSM board

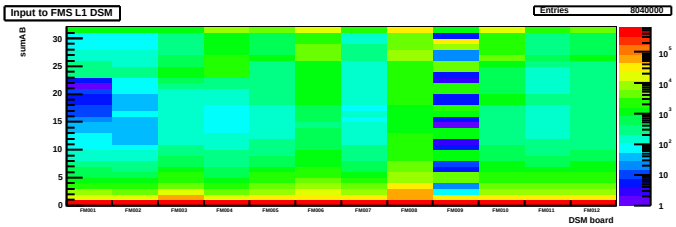

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

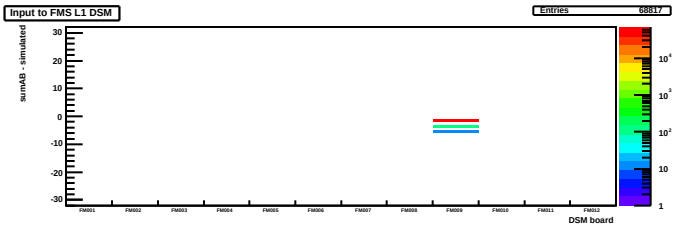

Input to FMS L1 DSM
Entries 8040000
sumBC
DSM board

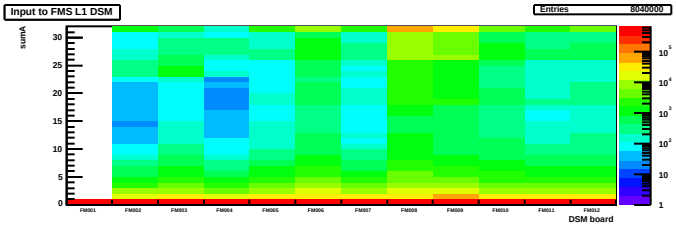

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

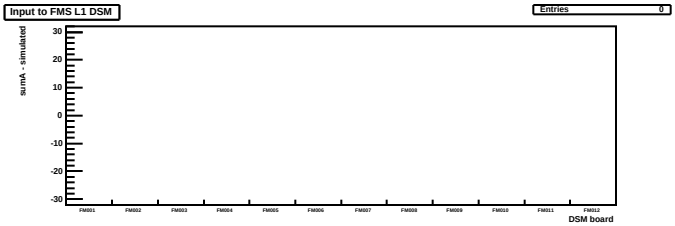

Input to FMS L1 DSM
Entries 8040000
sumB
DSM board

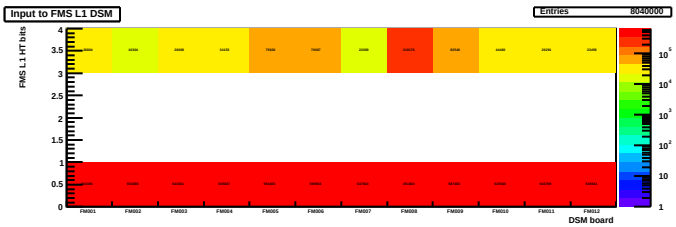

Input to FMS L1 DSM
Entries 46418
sumB - simulated
DSM board

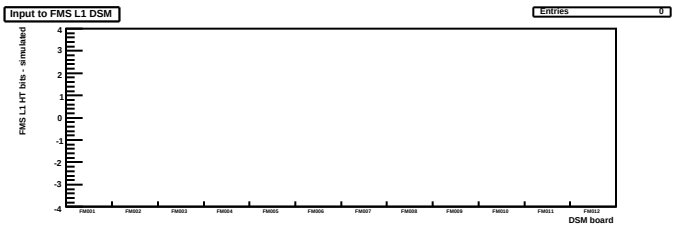

Input to FMS L1 DSM
Entries 8040000
sumAB
DSM board

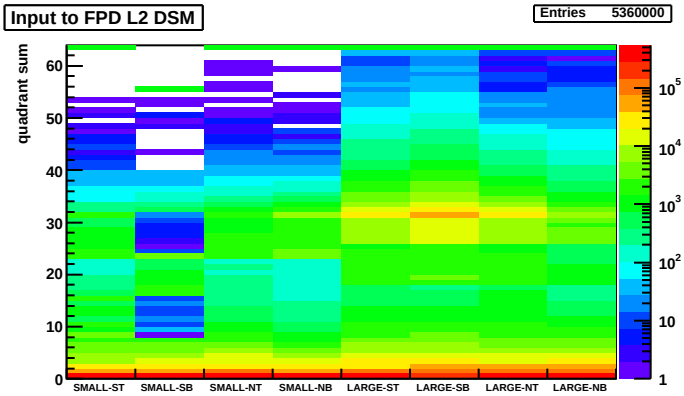

Input to FMS L1 DSM
Entries 68817
sumAB - simulated
DSM board

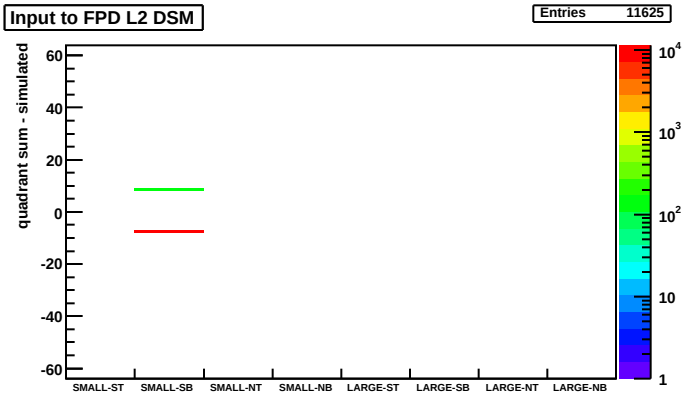

Input to FMS L1 DSM
Entries 8040000
sumA
DSM board

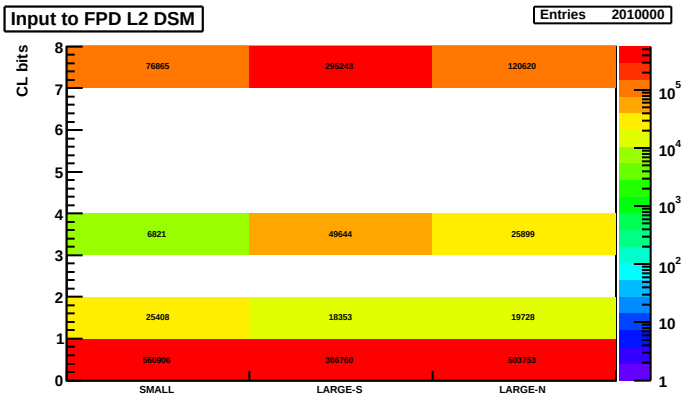

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

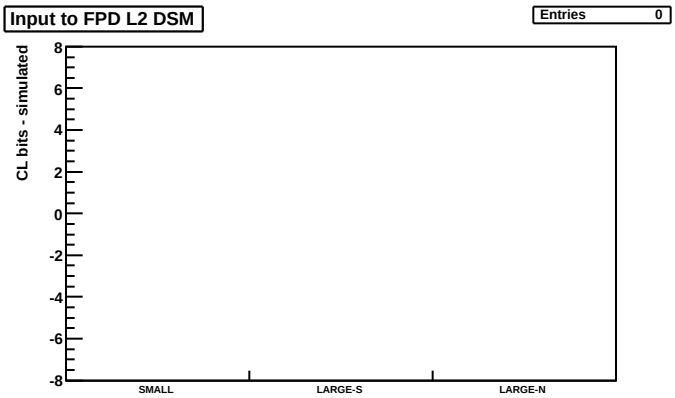

Input to FMS L1 DSM
Entries 8040000
FMS L1 HT bits
DSM board

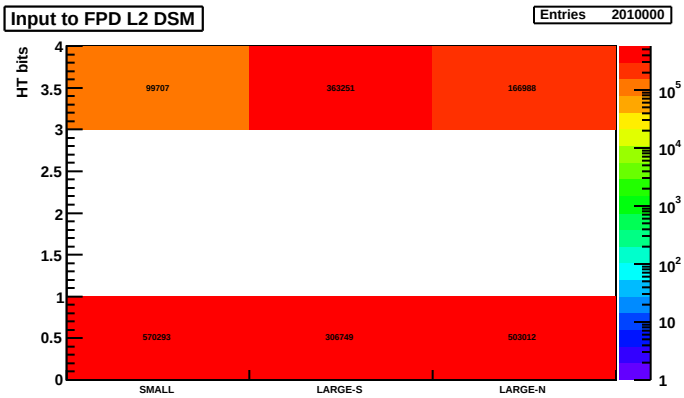

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

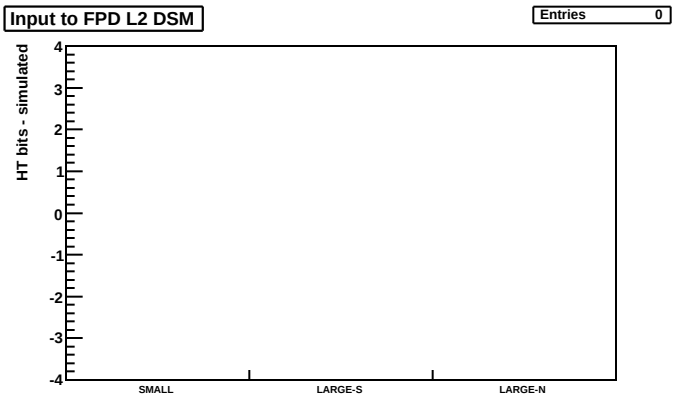

Input to FPD L2 DSM
Entries 5360000
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

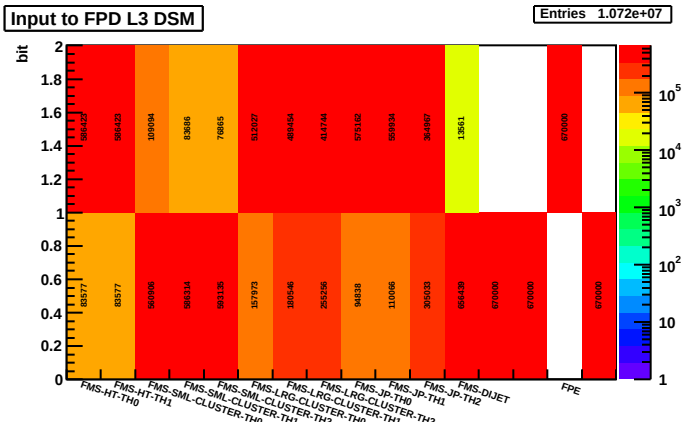

Input to FPD L2 DSM
Entries 11625
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

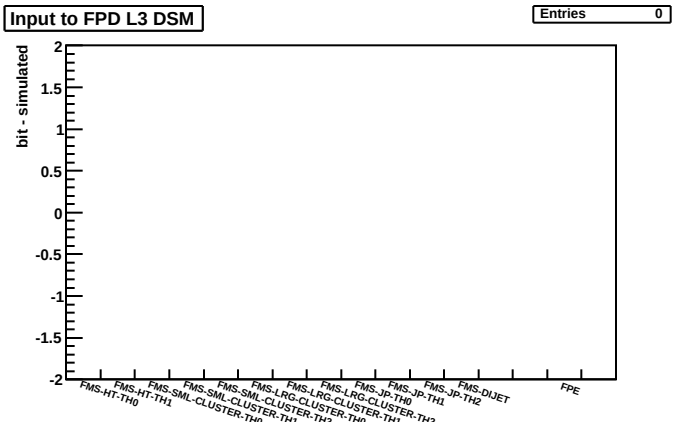

Input to FPD L2 DSM
Entries 2010000
CL bits
76865
295243
120620
6821
49644
25899
25408
18353
19728
560906
306760
503753
SMALL
LARGE-S
LARGE-N

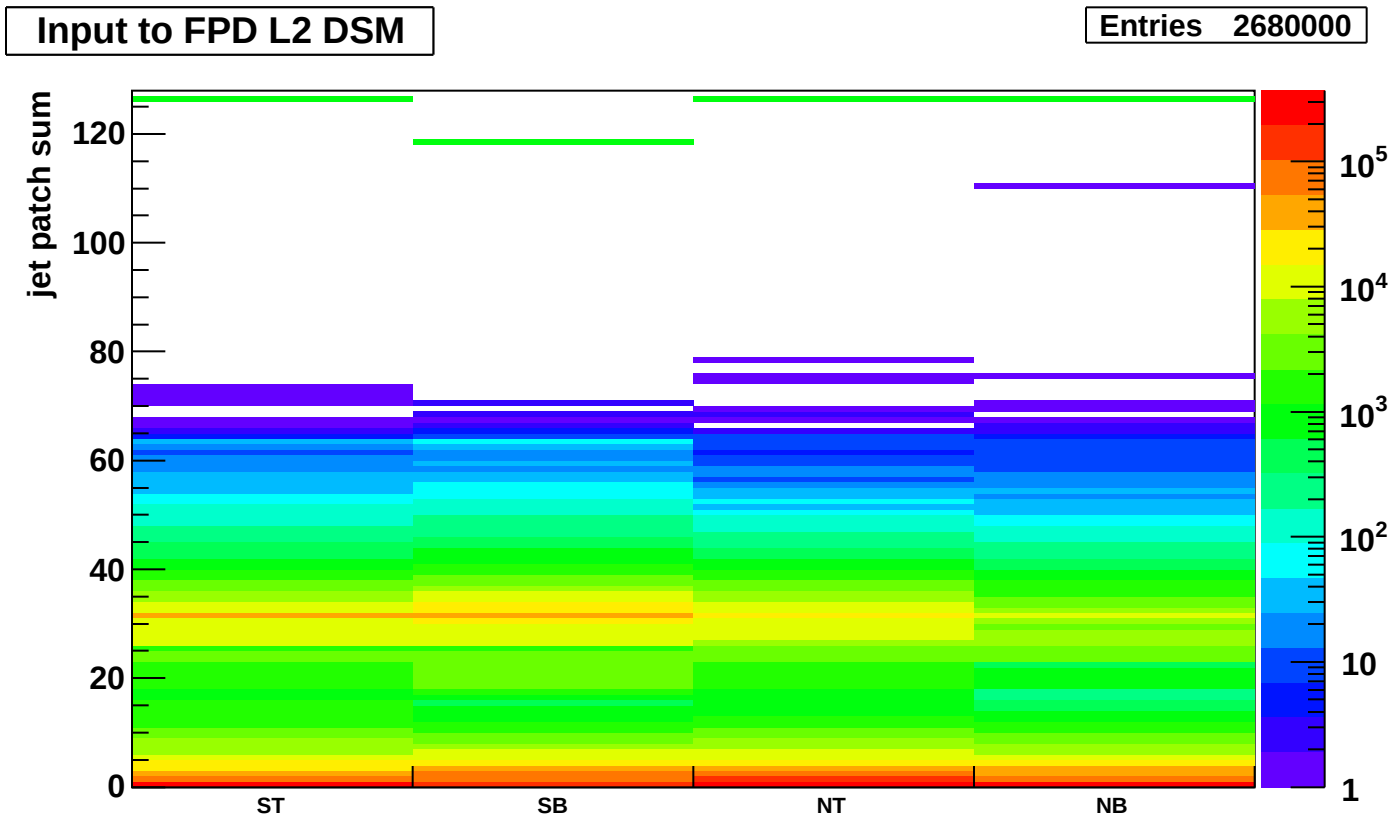

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

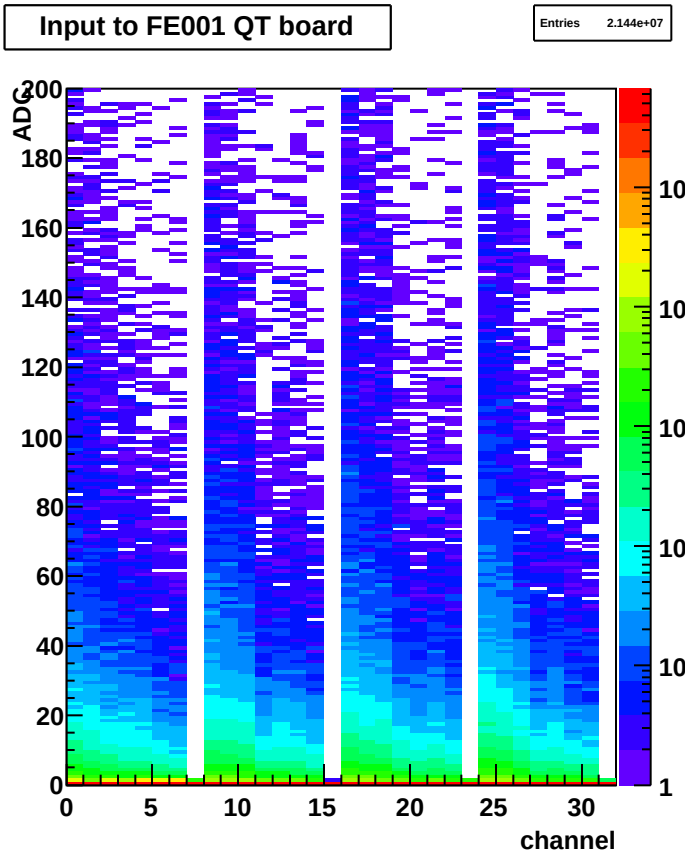

Input to FPD L2 DSM
Entries 2010000
HT bits
99707
363251
166988
570293
306749
503012
SMALL
LARGE-S
LARGE-N

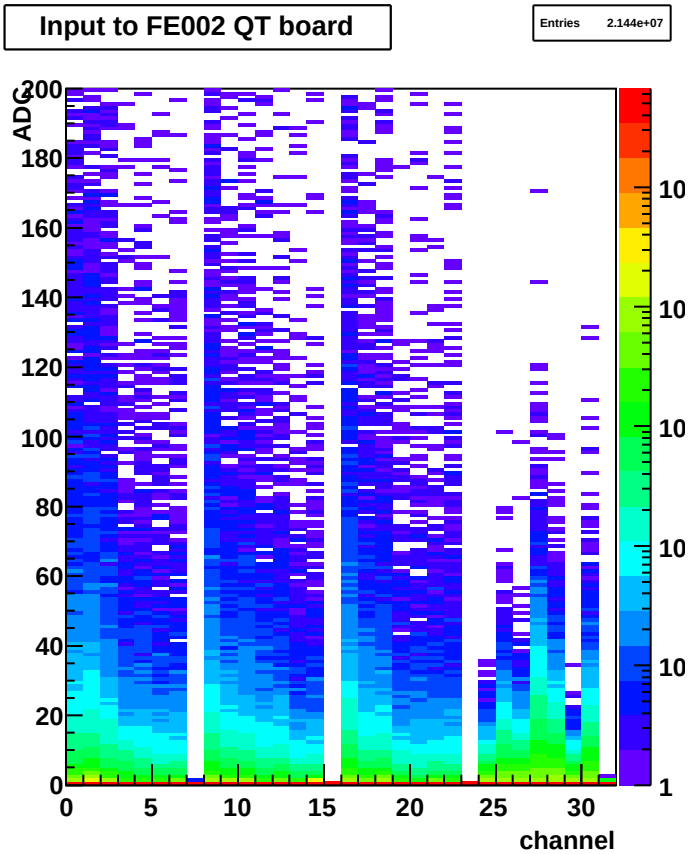

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

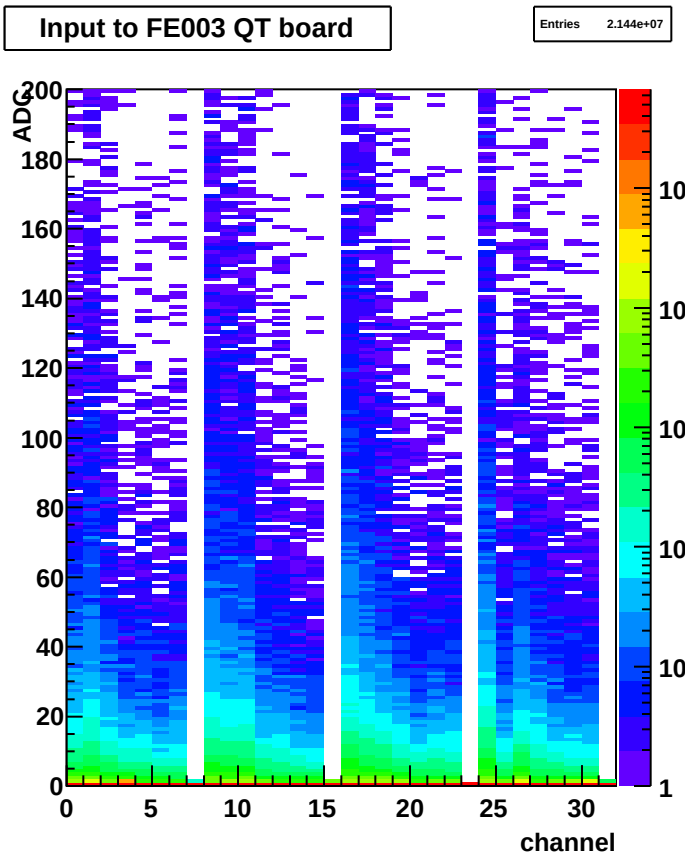

Input to FPD L3 DSM
Entries 1.072e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

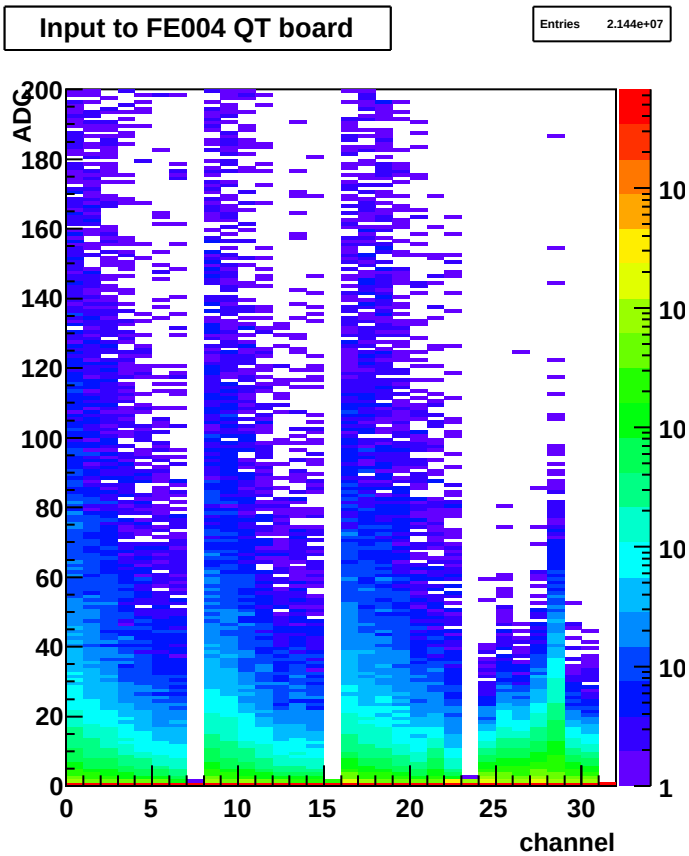

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

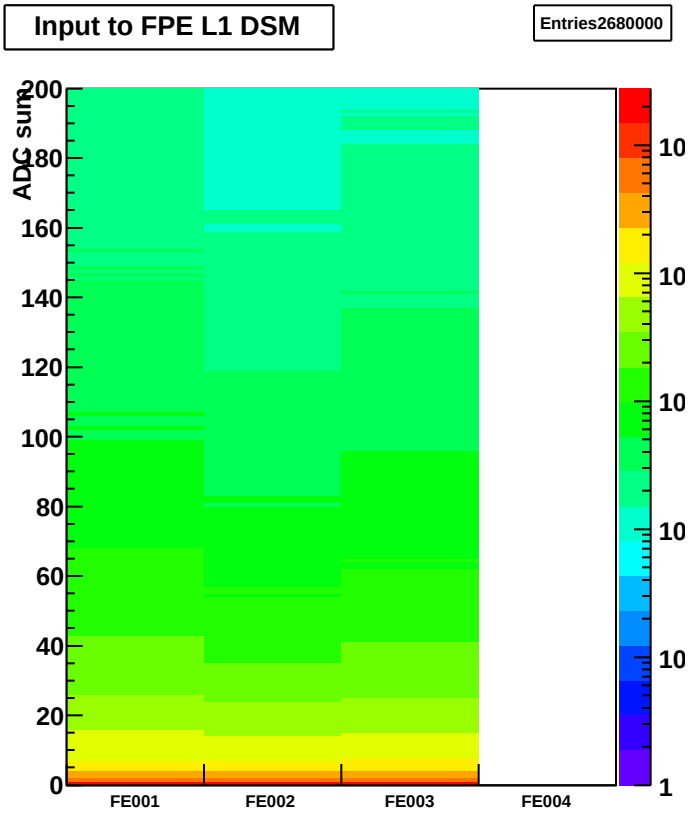

Input to FPD L2 DSM
Entries 2680000
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

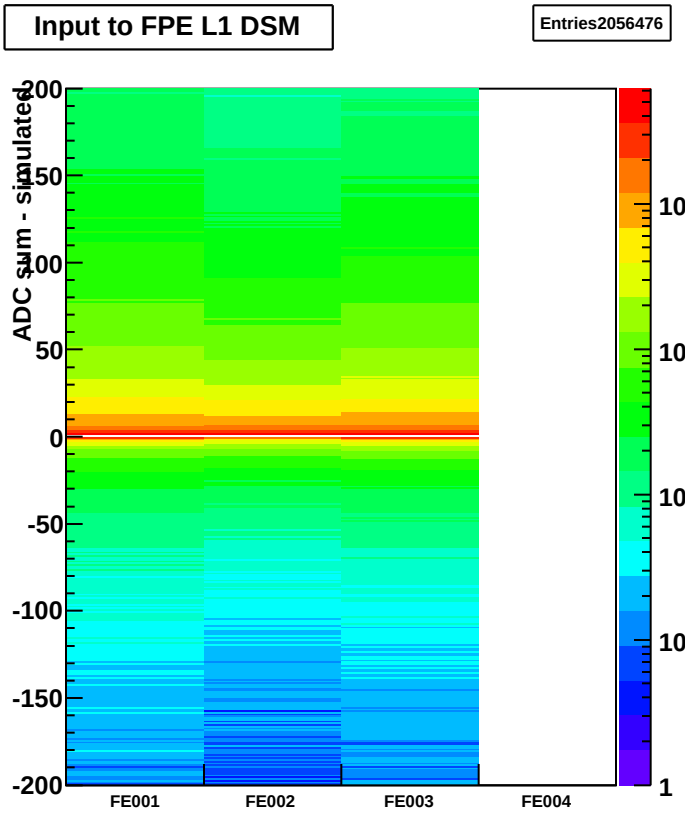

Input to FE001 QT board
Entries 2.144e+07
ADC
channel

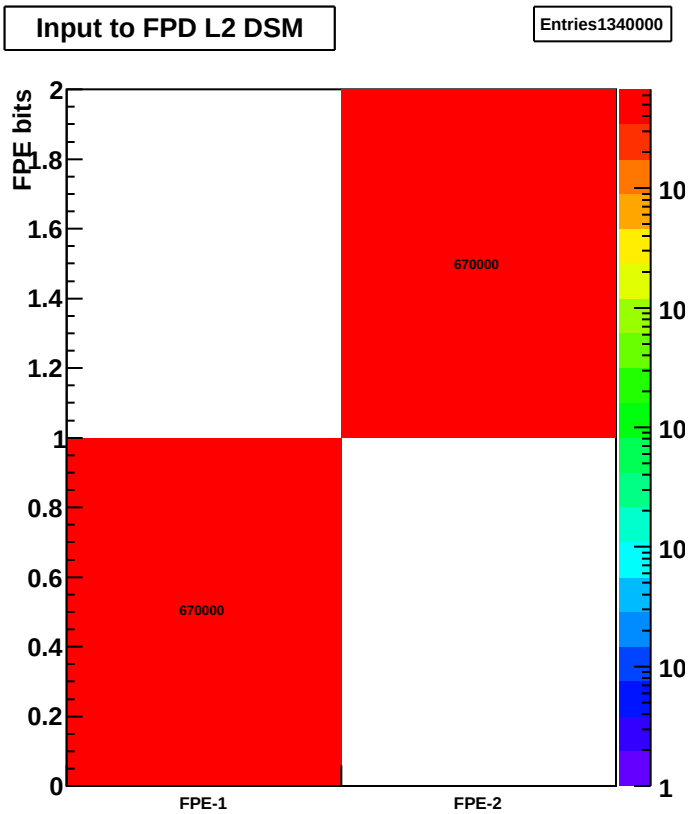

Input to FE002 QT board
Entries 2.144e+07
ADC
channel

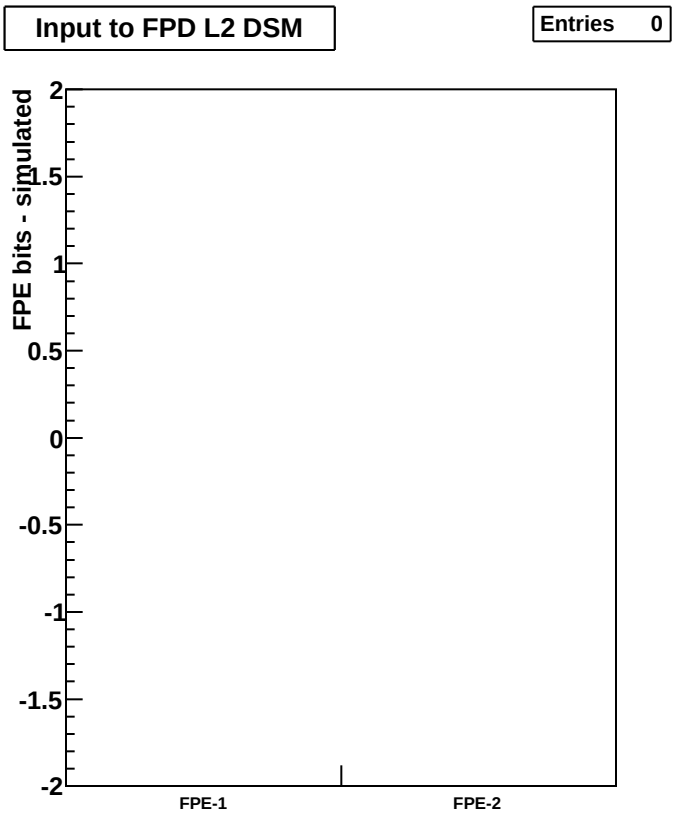

Input to FE003 QT board
Entries 2.144e+07
ADC
channel


Input to FE004 QT board
Entries 2.144e+07
ADC
channel


Input to FPE L1 DSM
Entries2680000
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries2056476
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1340000
FPE bits
670000
670000
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2