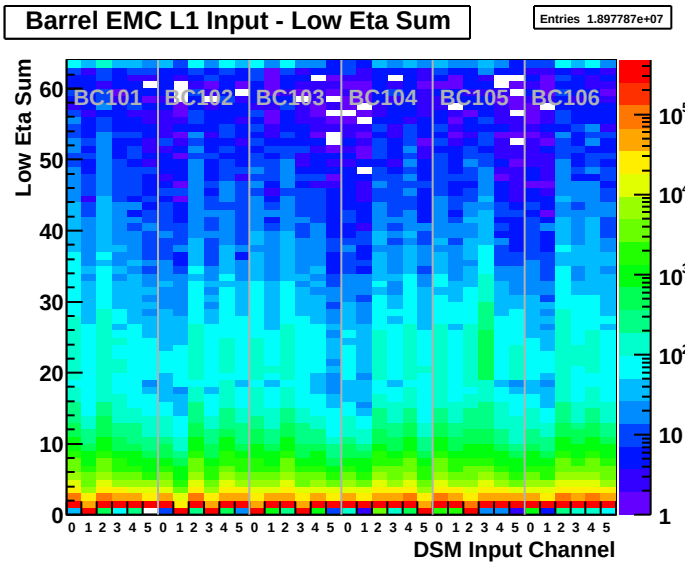
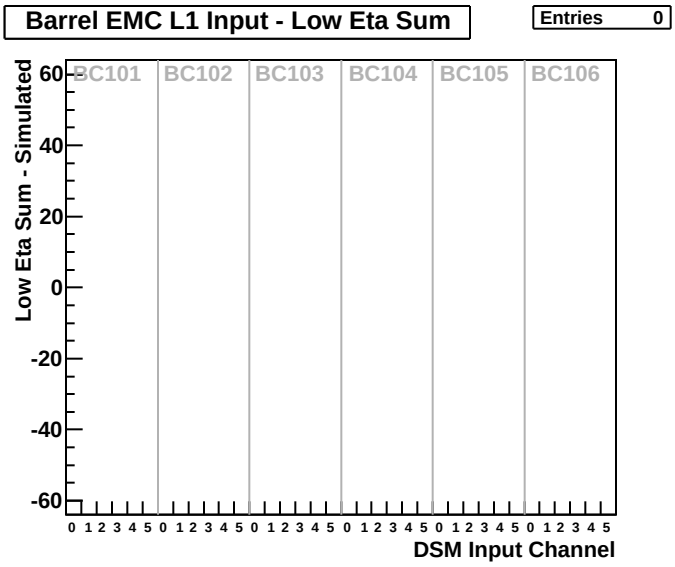
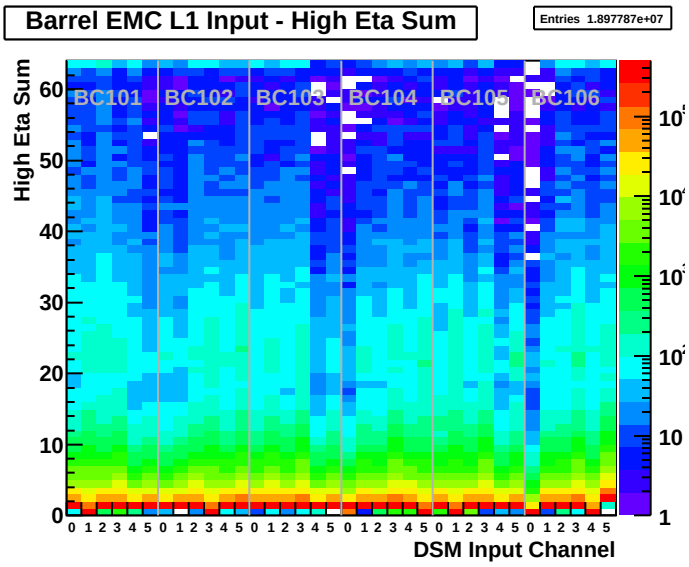
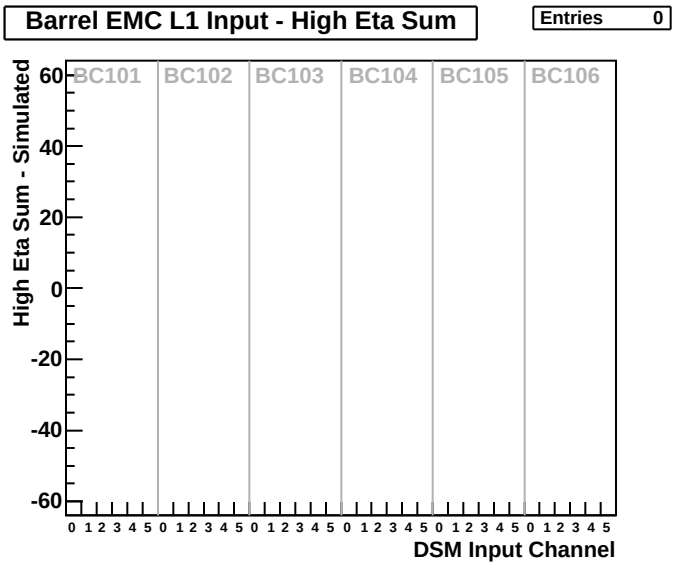
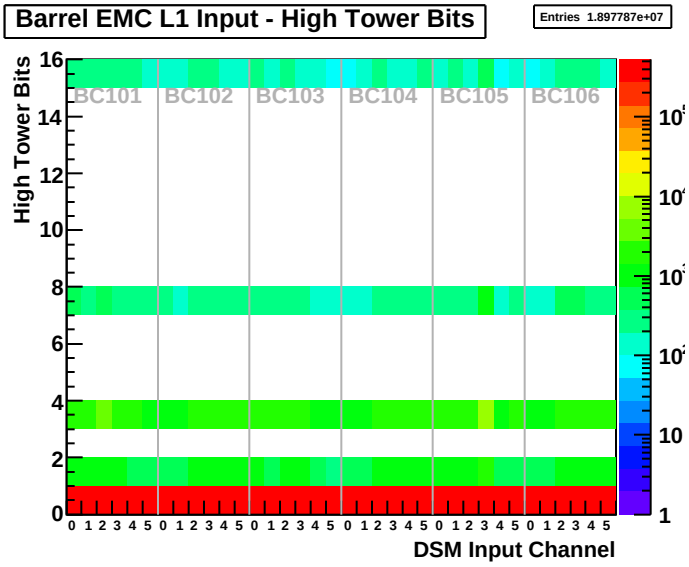
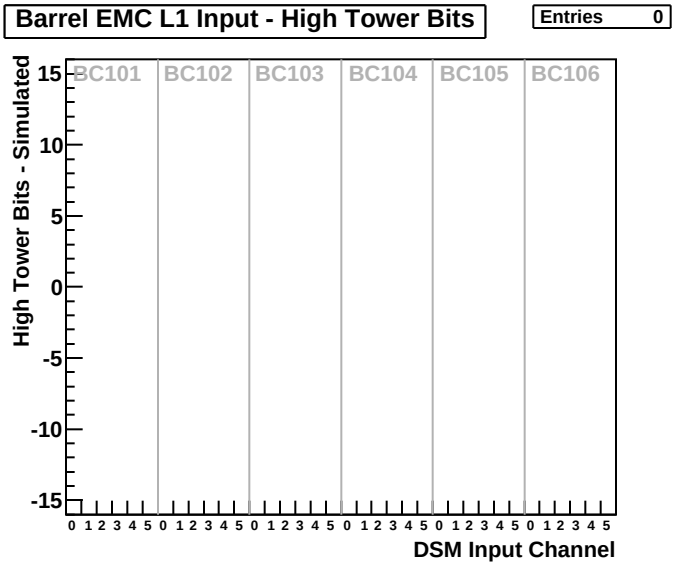

Barrel EMC L1 Input - Low Eta Sum
Entries 1.897787e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

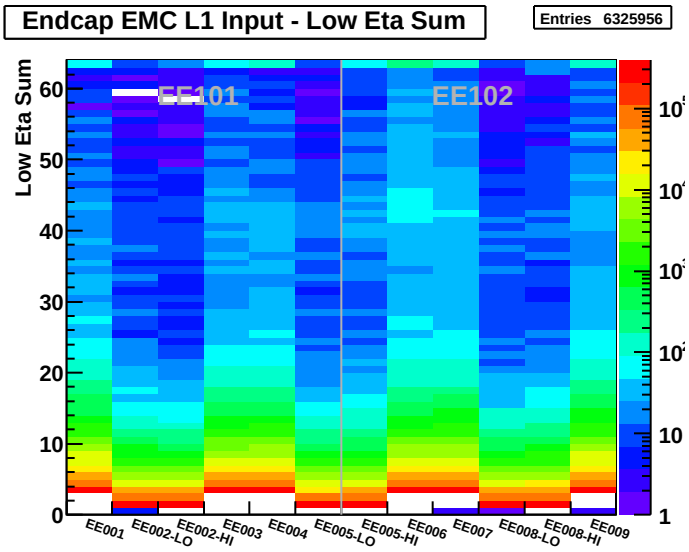

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

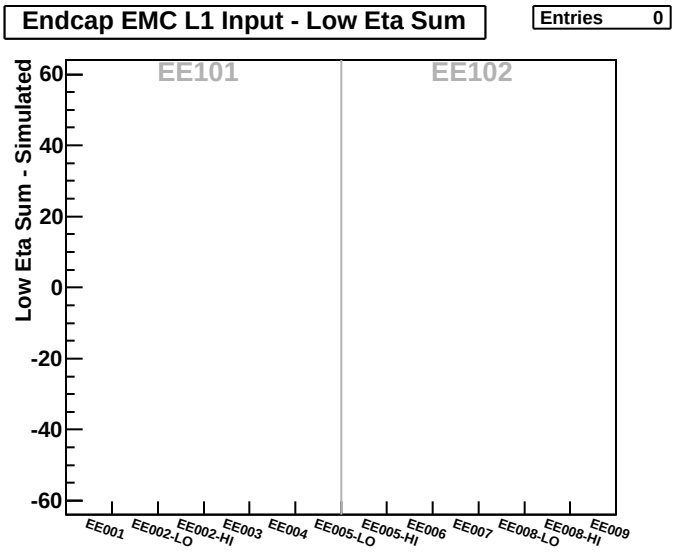

Barrel EMC L1 Input - High Eta Sum
Entries 1.897787e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

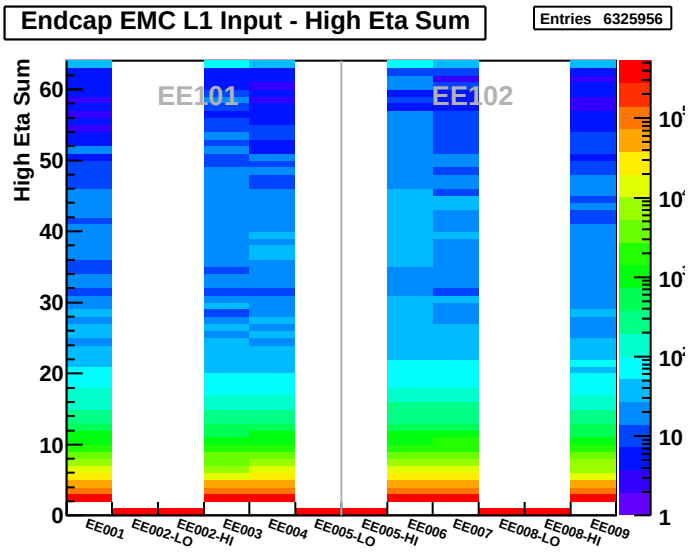

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

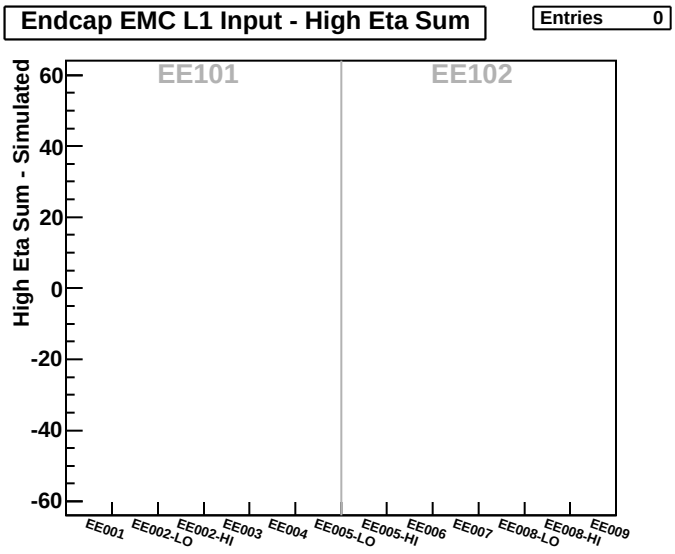

Barrel EMC L1 Input - High Tower Bits
Entries 1.897787e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

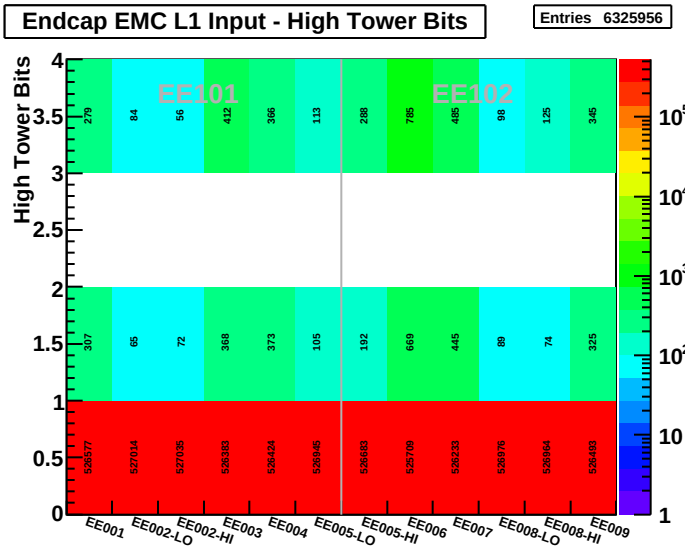

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

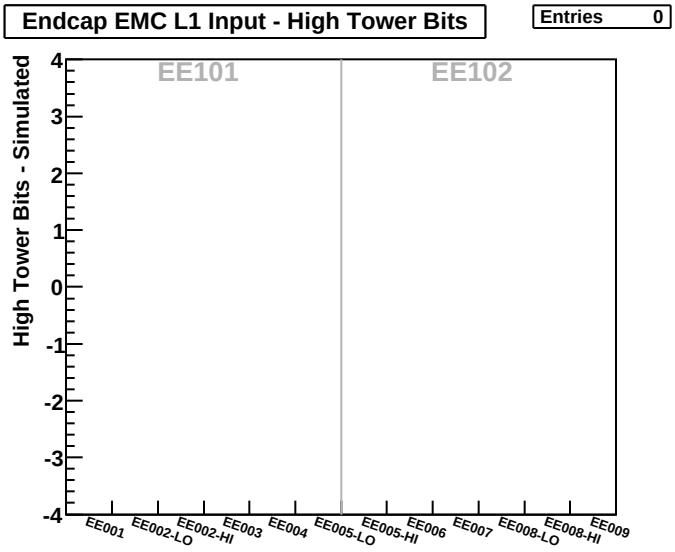

Endcap EMC L1 Input - Low Eta Sum
Entries 6325956
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

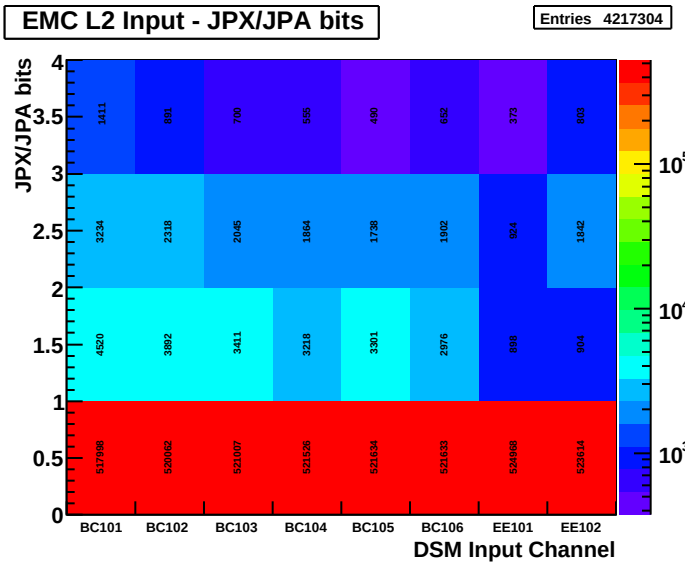

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

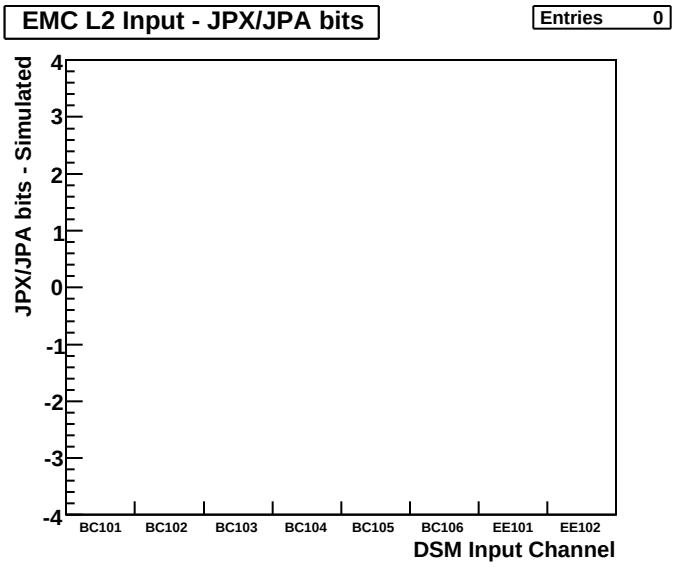

Endcap EMC L1 Input - High Eta Sum
Entries 6325956
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

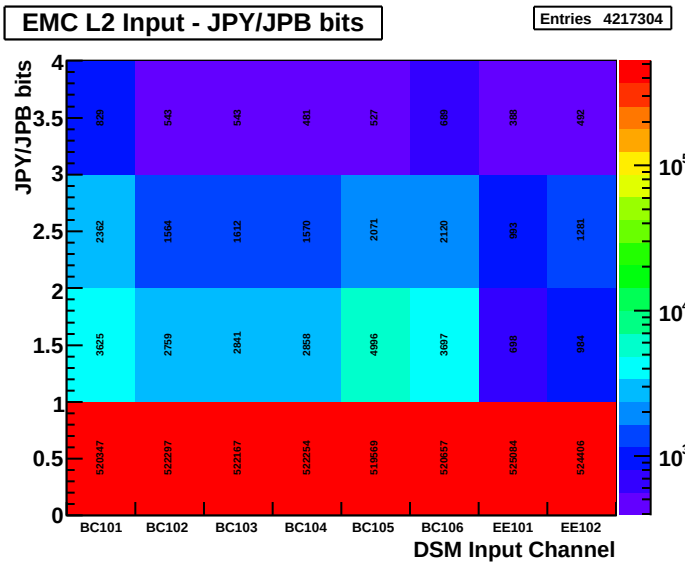

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

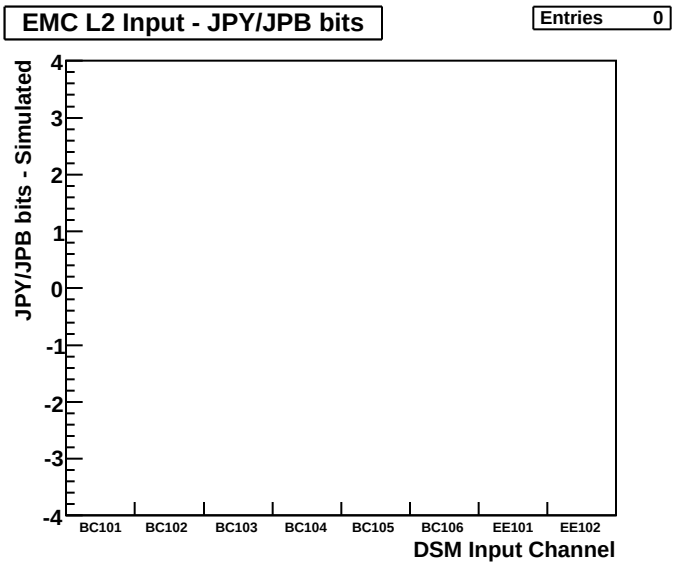

Endcap EMC L1 Input - High Tower Bits
Entries 6325956
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

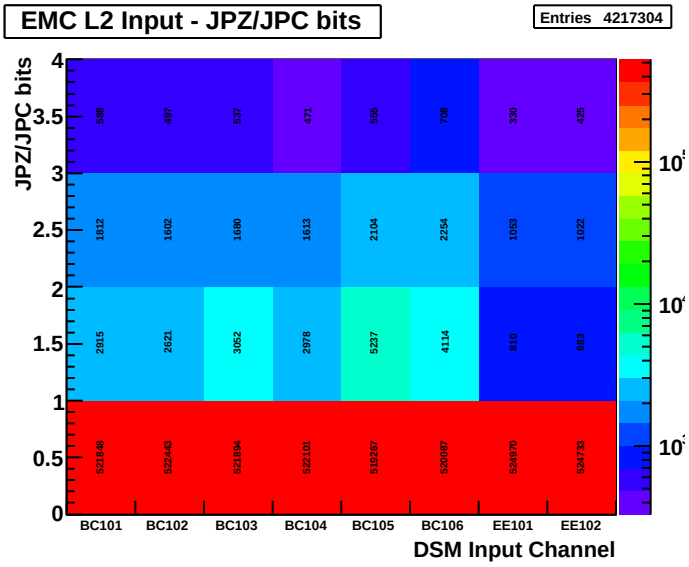

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

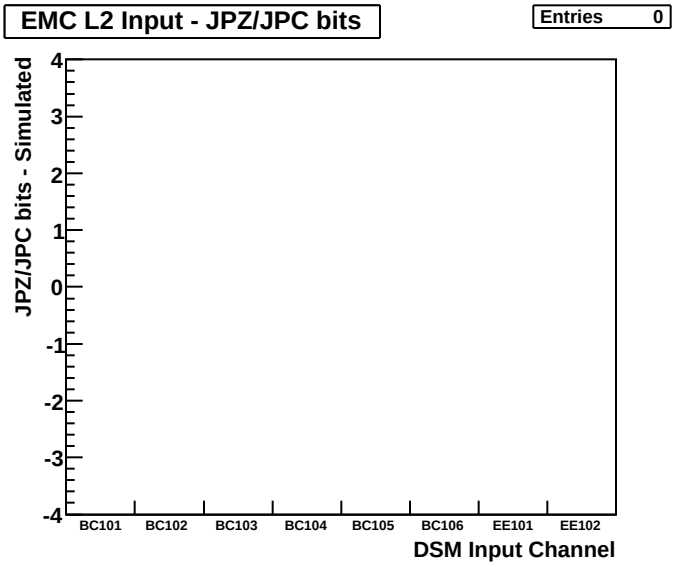

EMC L2 Input - JPX/JPA bits
Entries 4217304
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1411 891 700 555 490 652 373 803
3234 2318 2045 1864 1738 1902 924 1842
4520 3892 3411 3218 3301 2976 898 904
517998 520062 521007 521526 521634 521633 524968 522614

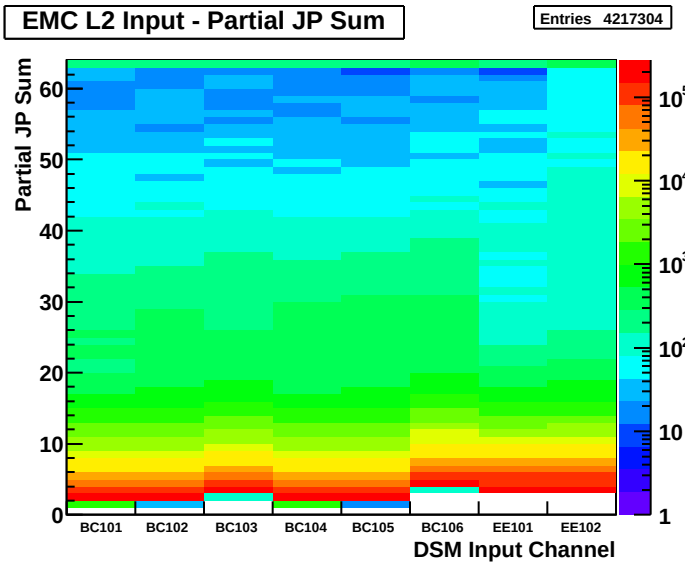

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

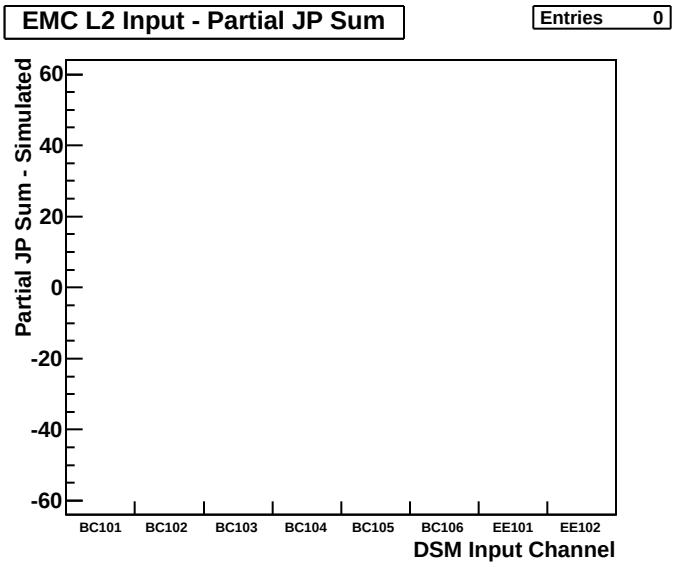

EMC L2 Input - JPY/JPB bits
Entries 4217304
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
626 543 543 431 527 689 388 492
2362 1564 1612 1570 2071 2120 983 1281
3625 2759 2841 2858 4996 3697 698 934
520347 522297 522167 522254 519569 520657 525084 524406

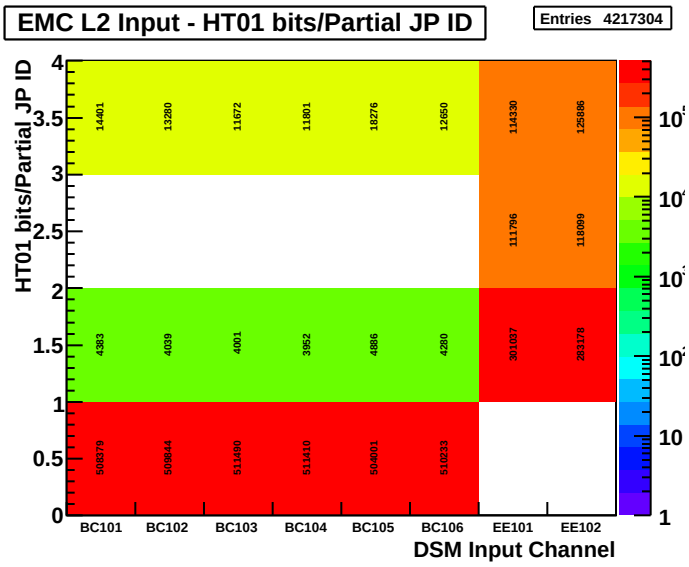

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

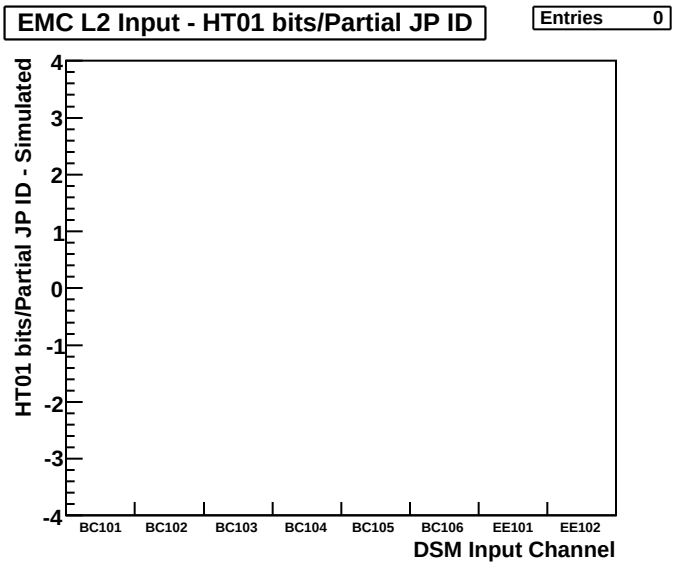

EMC L2 Input - JPZ/JPC bits
Entries 4217304
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
588 497 537 471 555 708 330 425
1812 1602 1680 1613 2104 2254 1053 1022
2915 2621 3062 2978 5237 4114 810 933
521948 522443 521894 522101 519267 520087 524970 524733

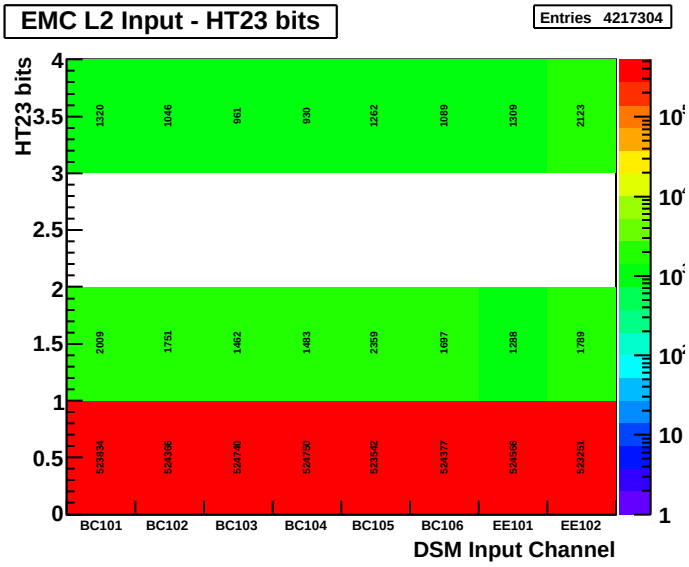

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

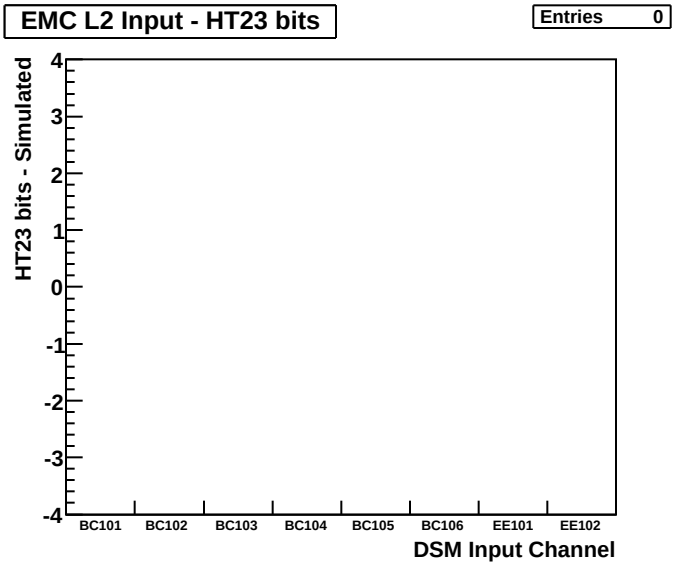

EMC L2 Input - Partial JP Sum
Entries 4217304
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

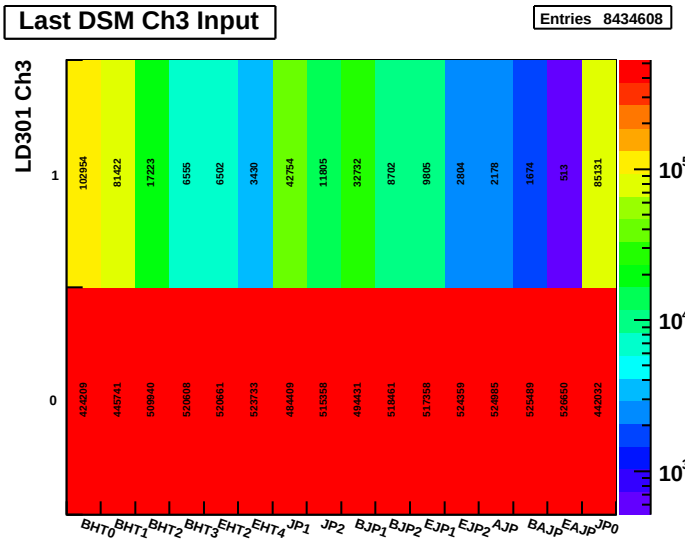

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

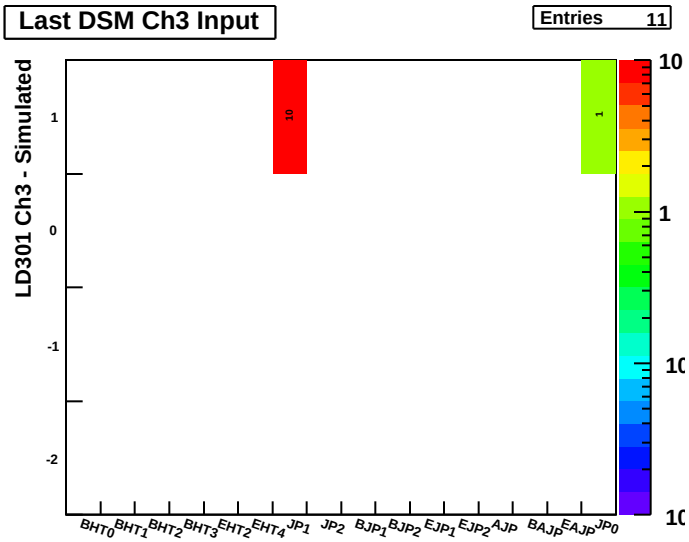

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4217304
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

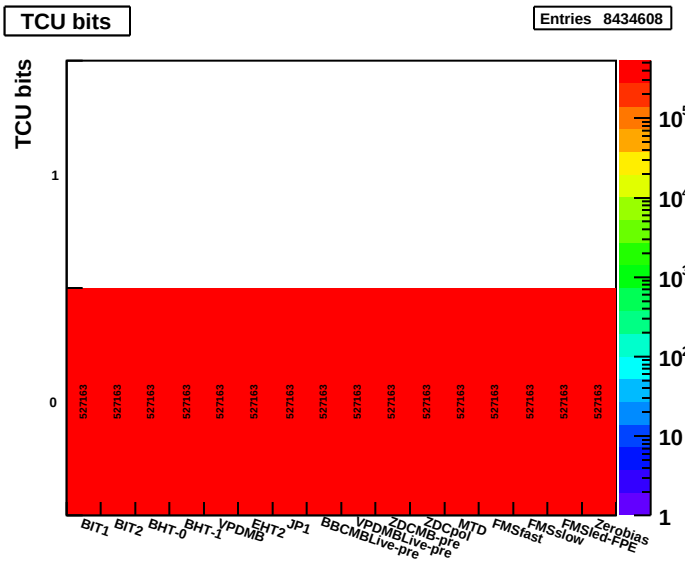

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

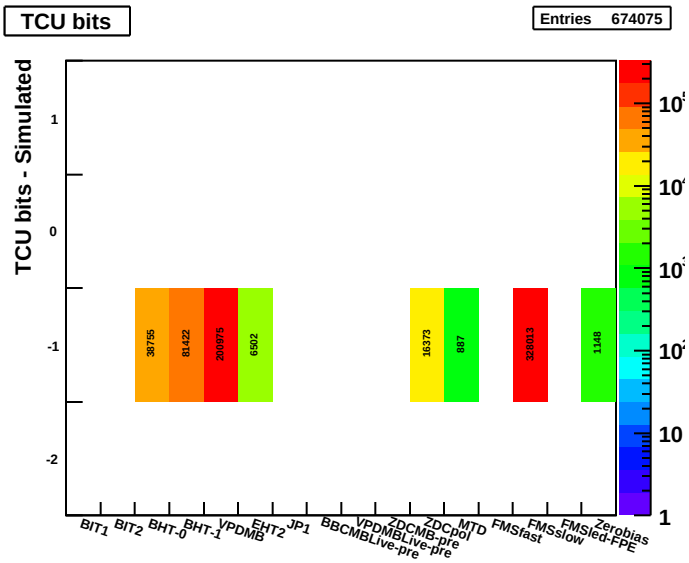

EMC L2 Input - HT23 bits
Entries 4217304
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

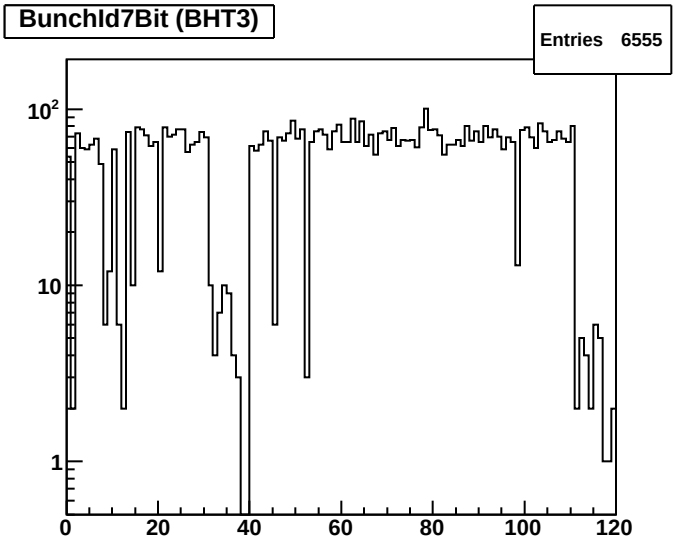

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

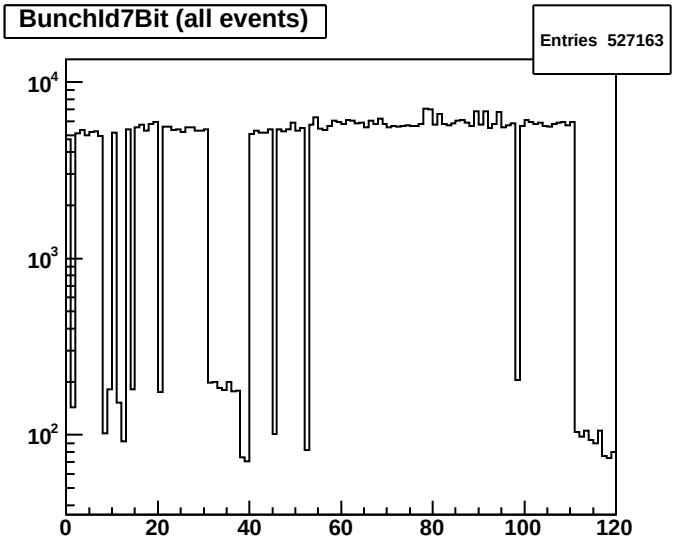

Last DSM Ch3 Input
Entries 8434608
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

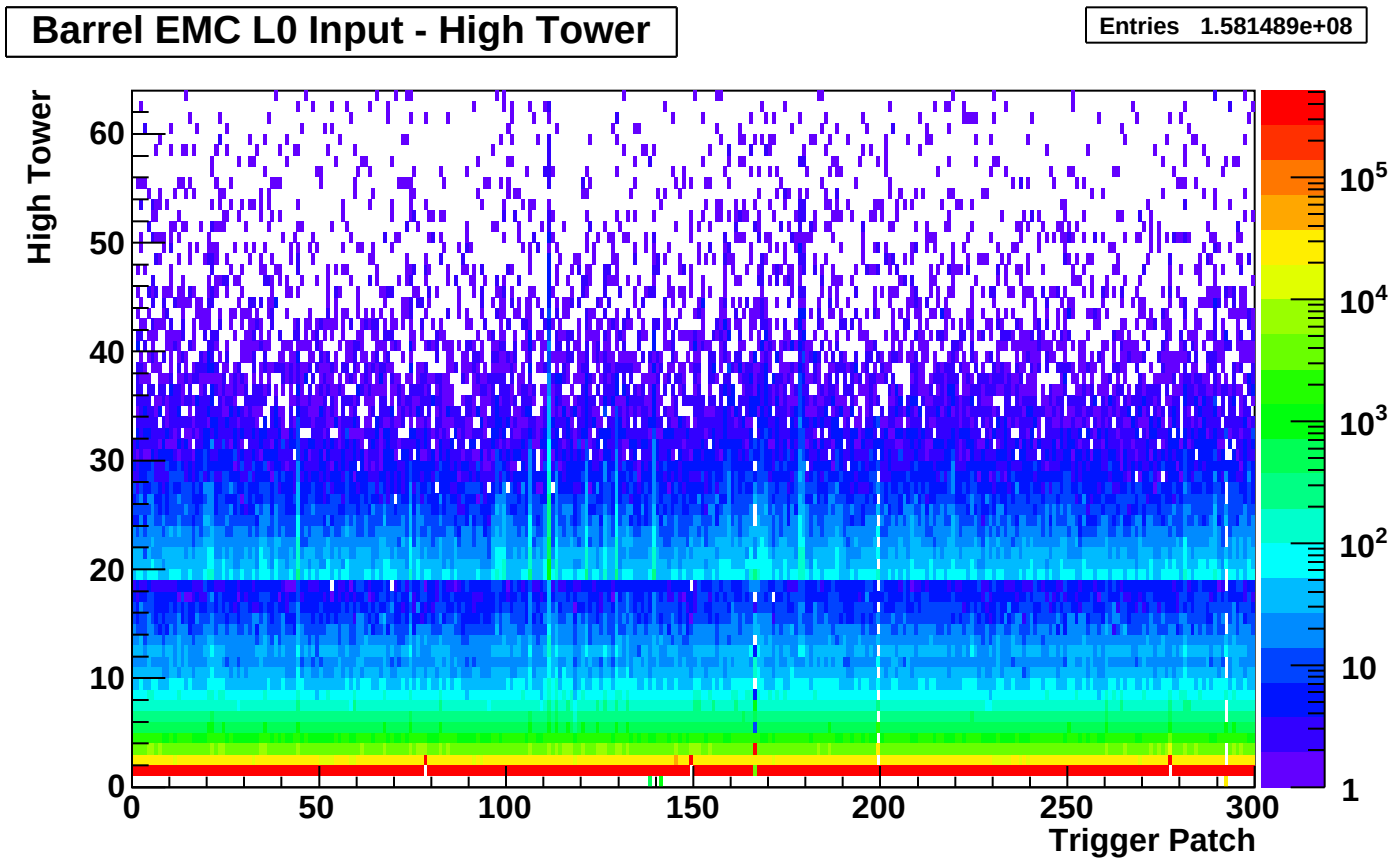

Last DSM Ch3 Input
Entries 11
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

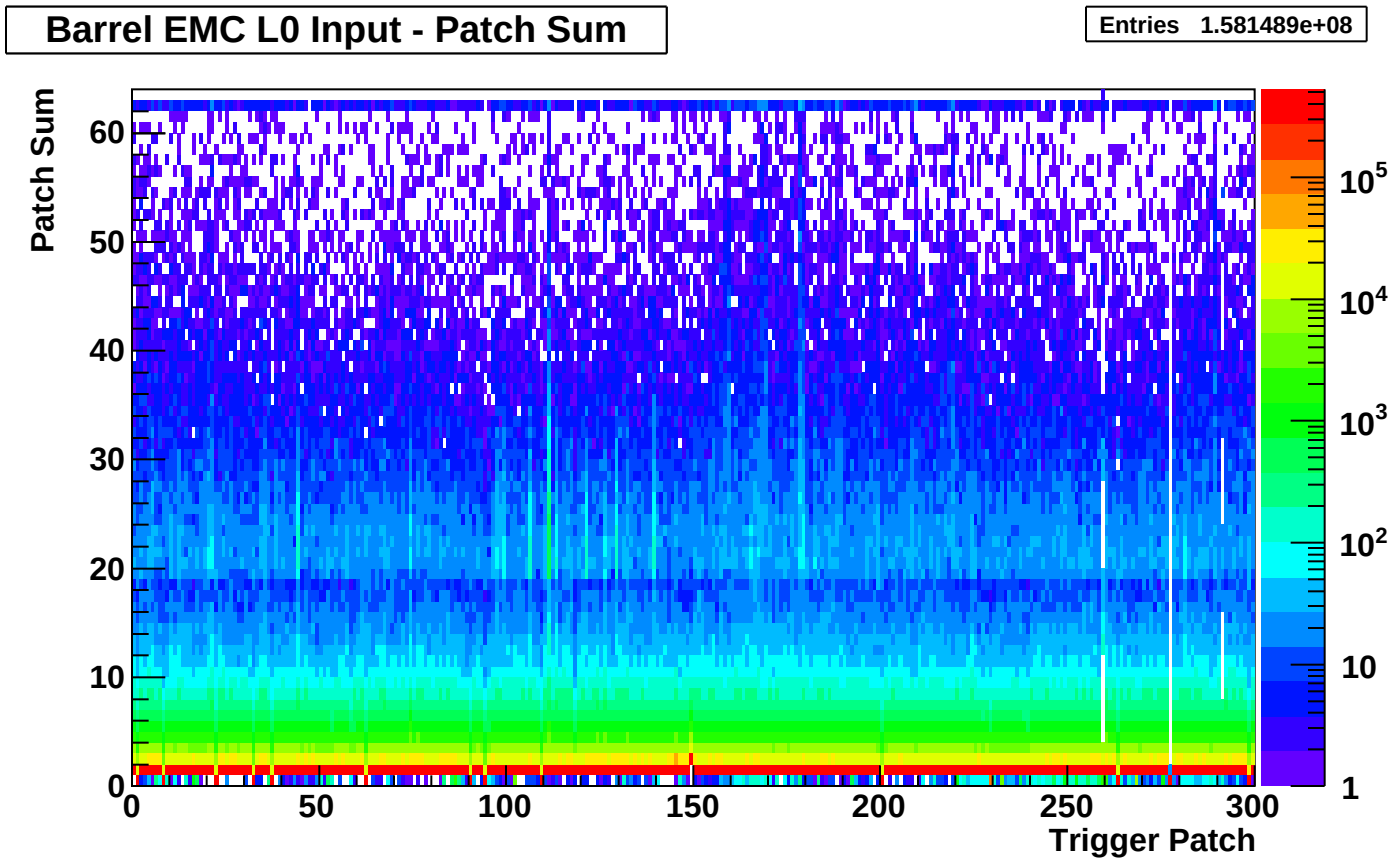

TCU bits
Entries 8434608
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

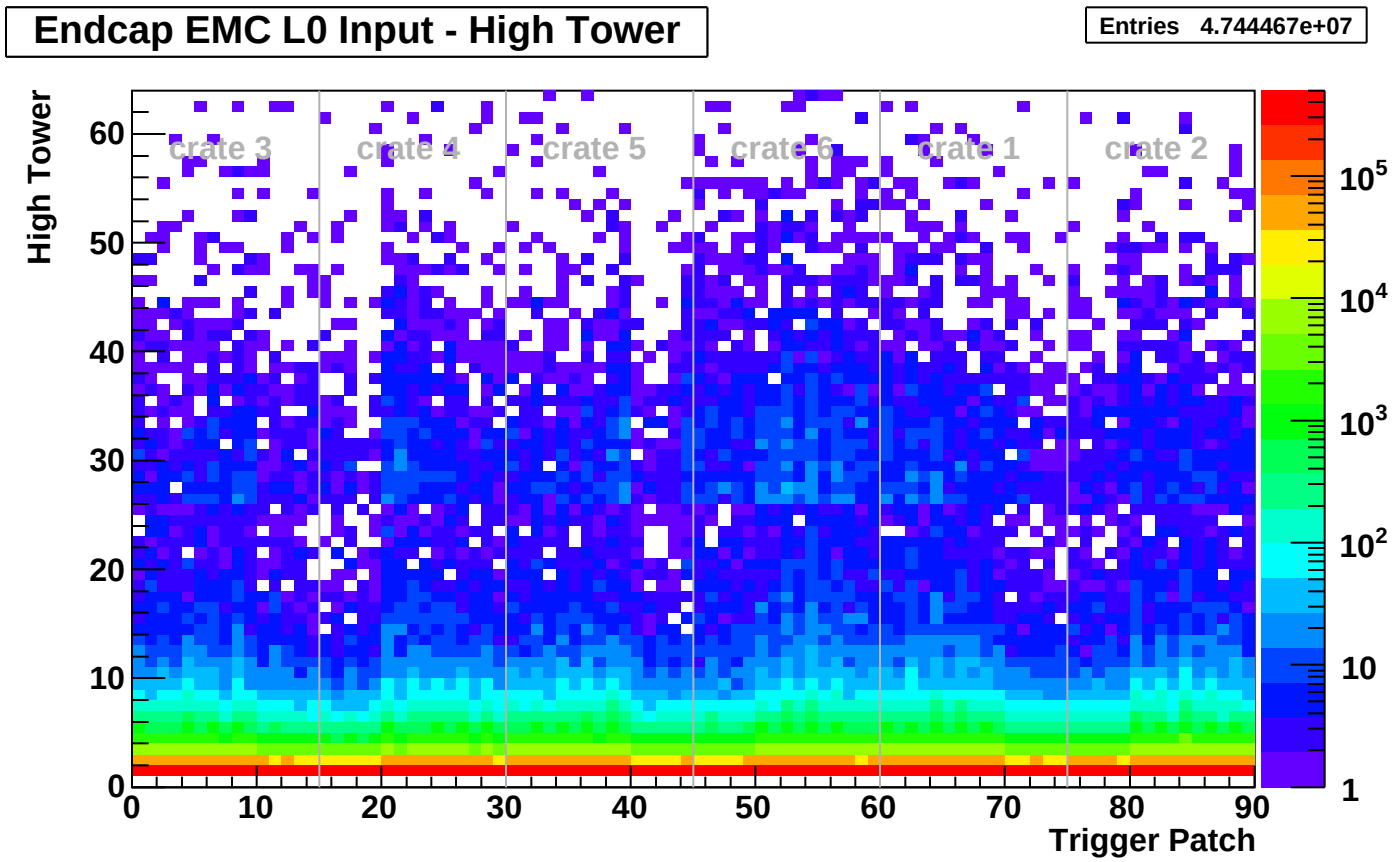

TCU bits
Entries 674075
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

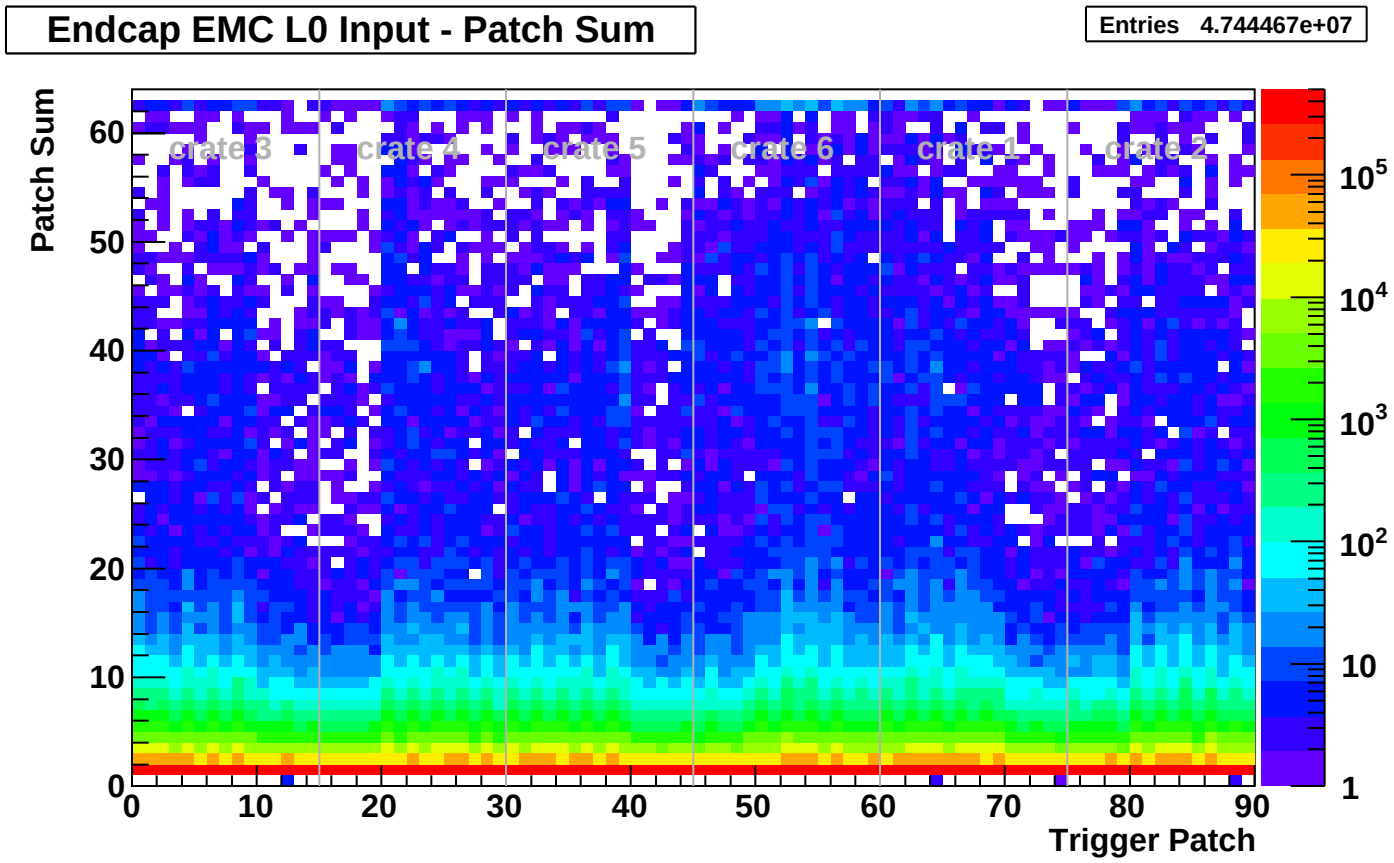

BunchId7Bit (BHT3)
Entries 6555

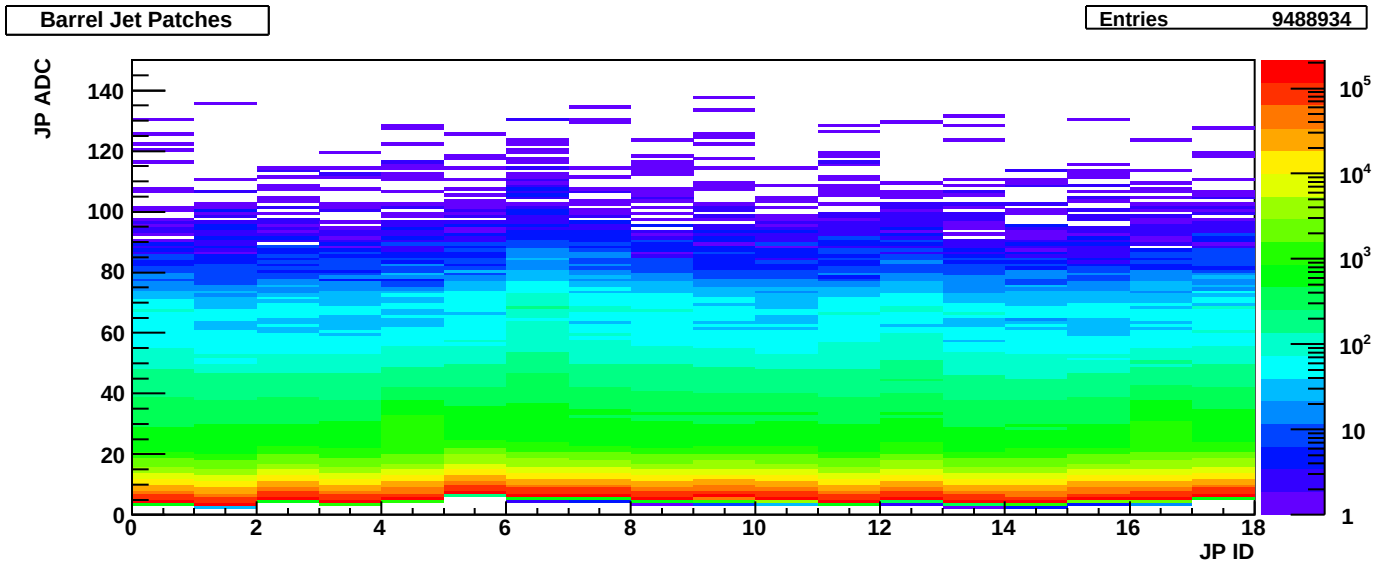

BunchId7Bit (all events)
Entries 527163

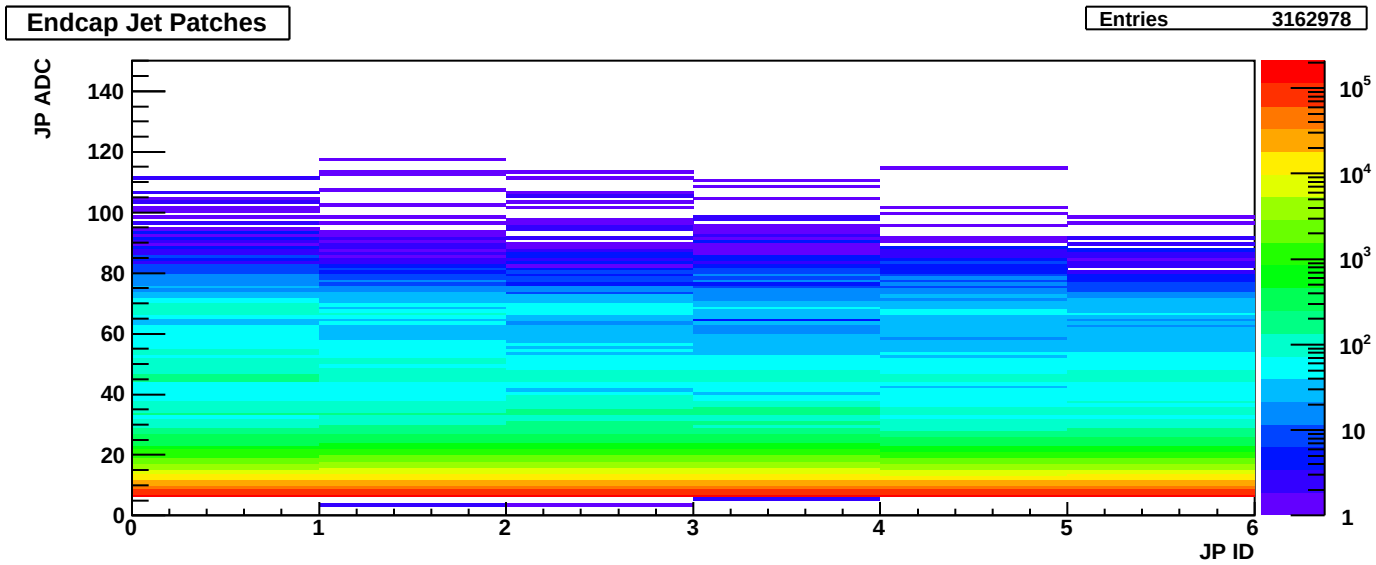

Barrel EMC L0 Input - High Tower
Entries 1.581489e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

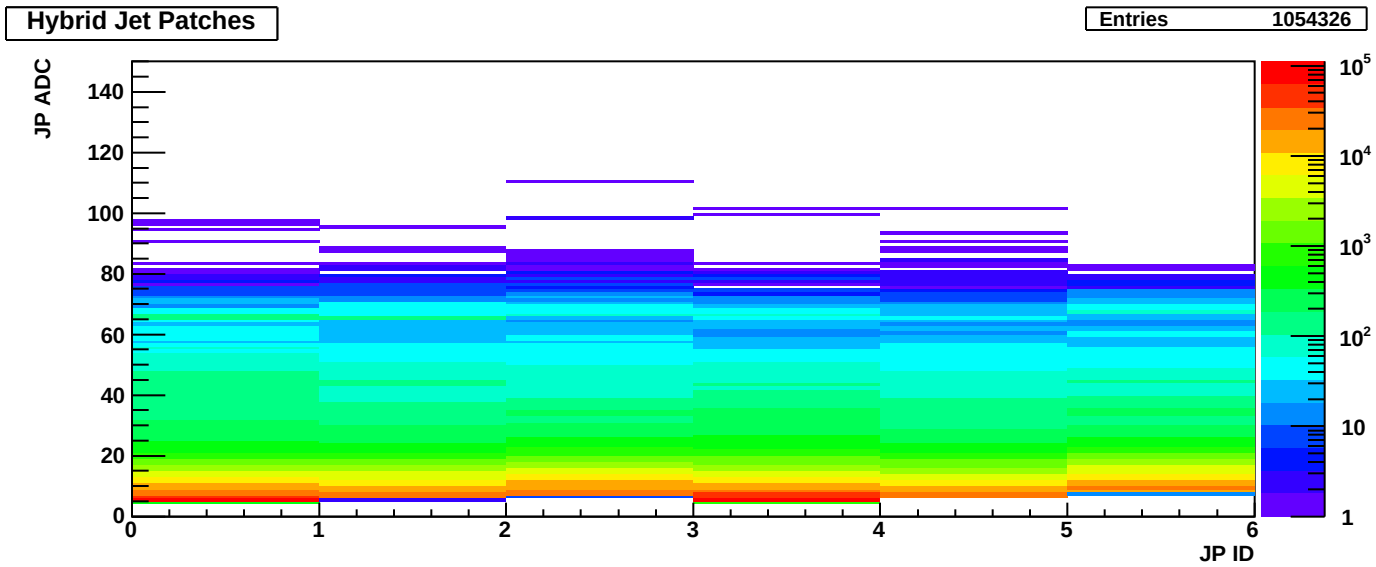

Barrel EMC L0 Input - Patch Sum
Entries 1.581489e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

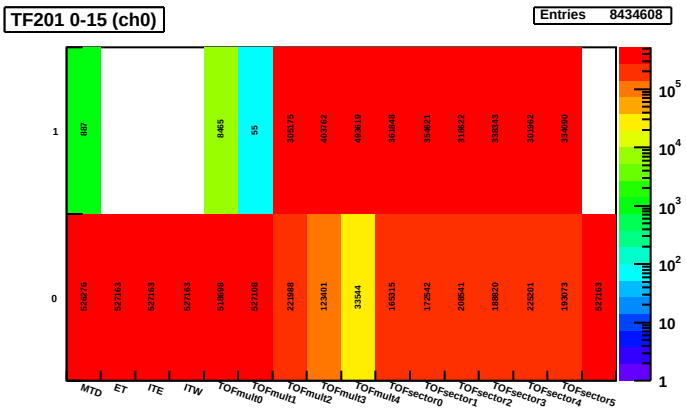

Endcap EMC L0 Input - High Tower
Entries 4.744467e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

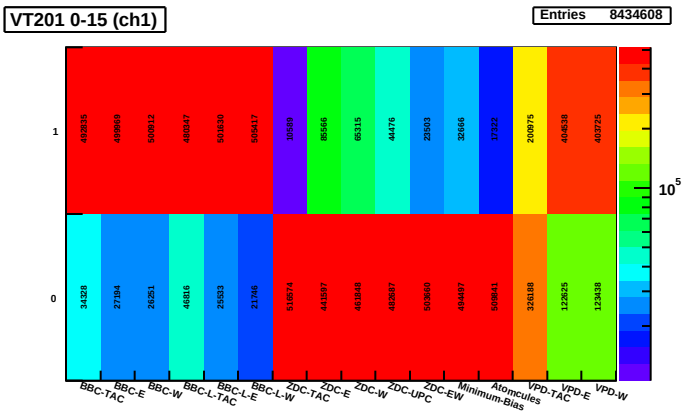

Endcap EMC L0 Input - Patch Sum
Entries 4.744467e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

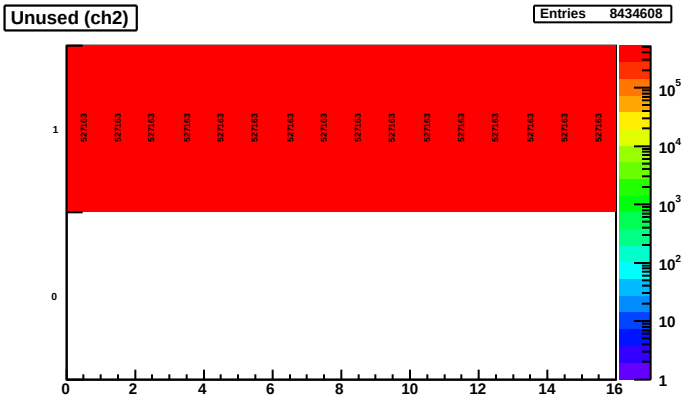

Barrel Jet Patches
Entries 9488934
JP ADC
JP ID

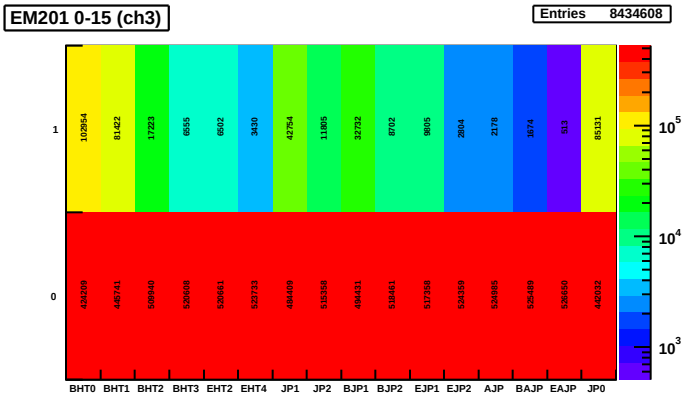

Endcap Jet Patches
Entries 3162978
JP ADC
JP ID

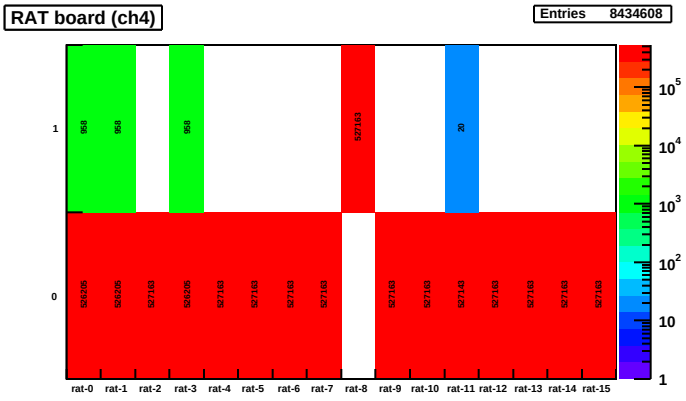

Hybrid Jet Patches
Entries 1054326
JP ADC
JP ID

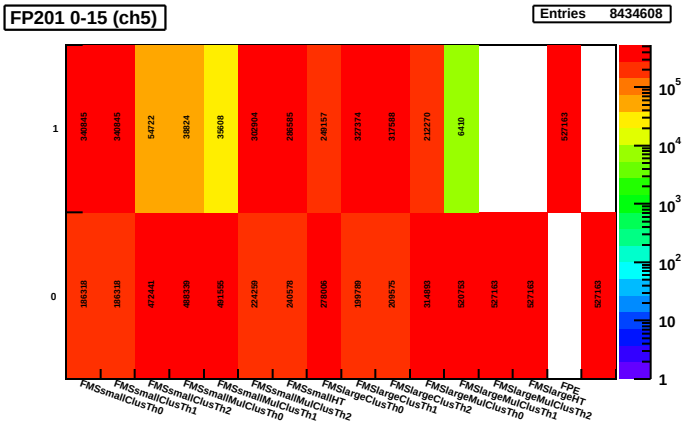

TF201 0-15 (ch0)
Entries 8434608
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

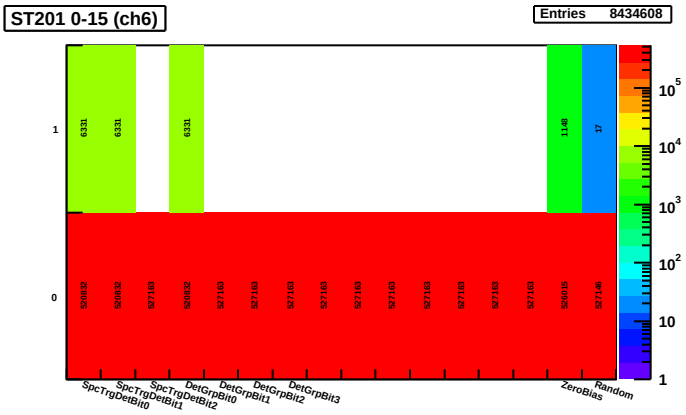

VT201 0-15 (ch1)
Entries 8434608
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

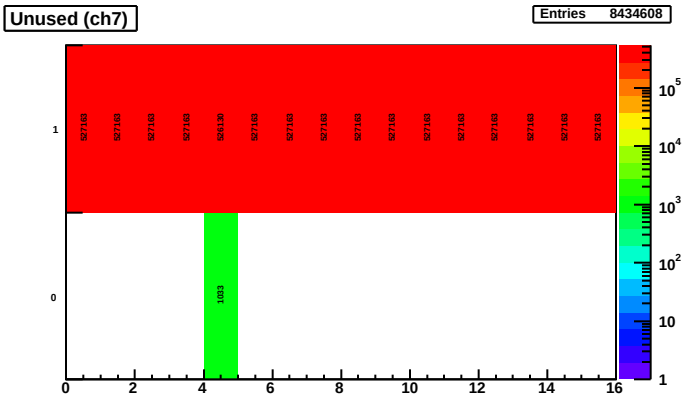

Unused (ch2)
Entries 8434608

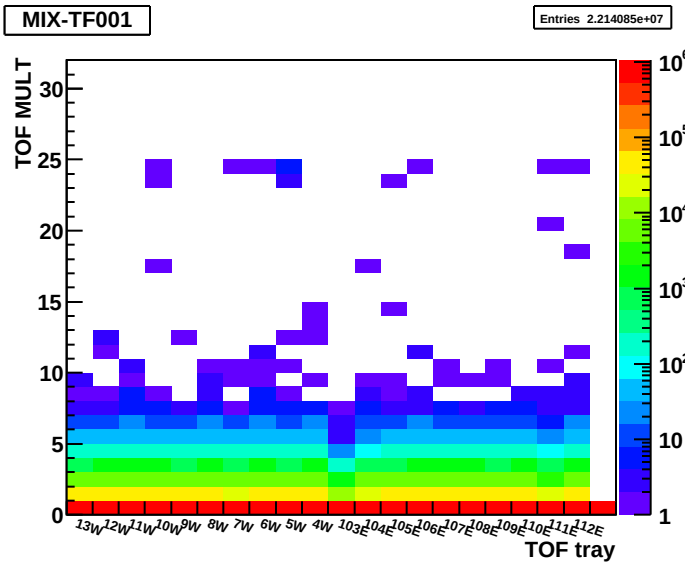

EM201 0-15 (ch3)
Entries 8434608
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

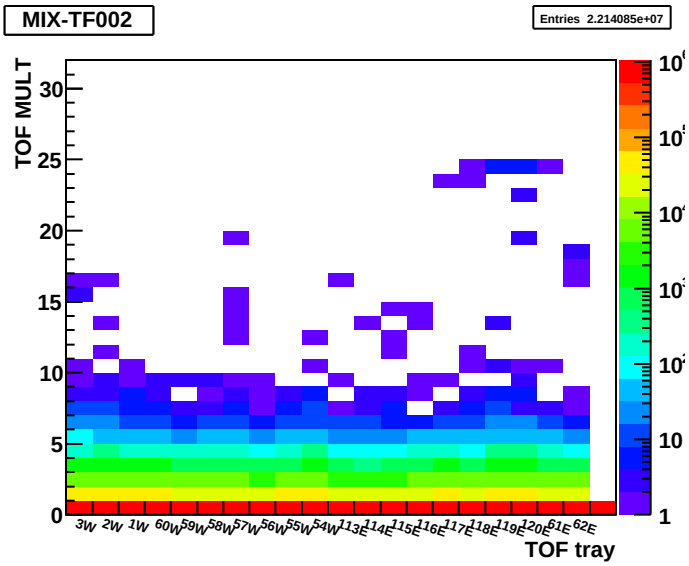

RAT board (ch4)
Entries 8434608
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

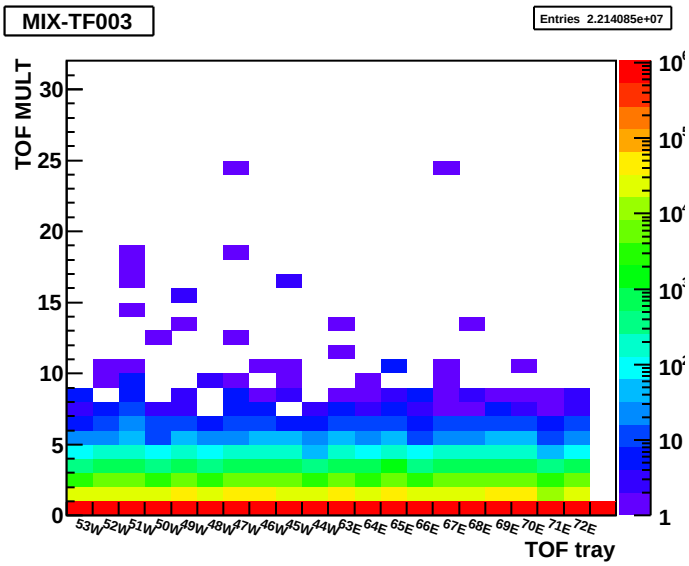

FP201 0-15 (ch5)
Entries 8434608
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

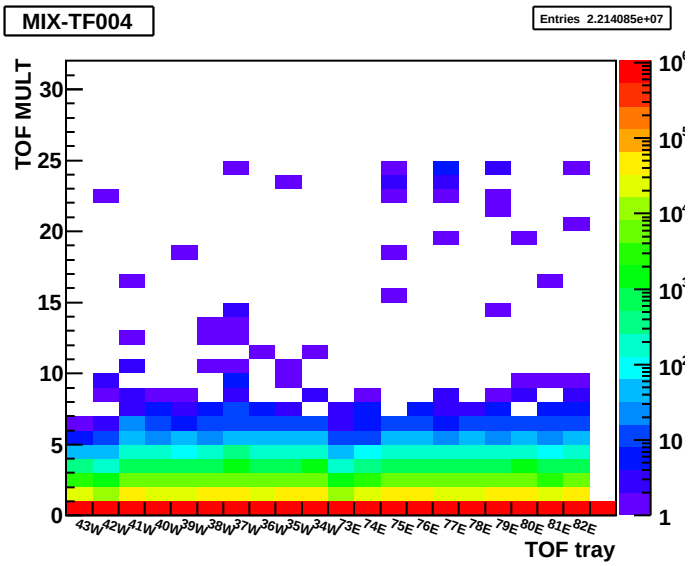

ST201 0-15 (ch6)
Entries 8434608
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

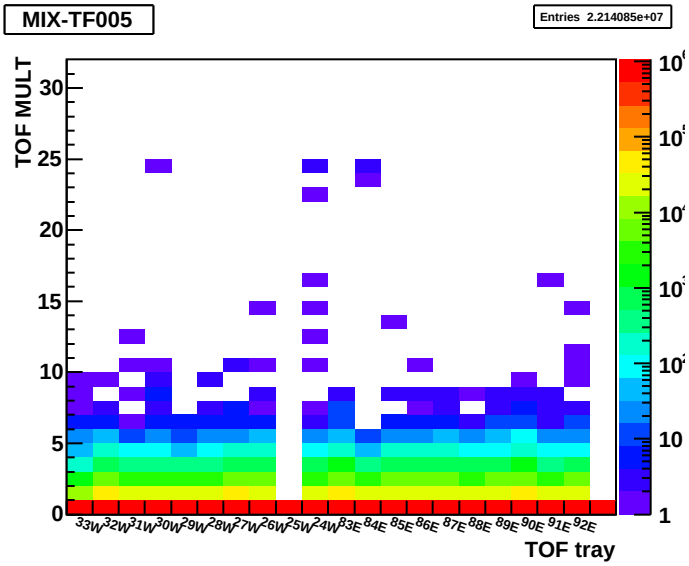

Unused (ch7)
Entries 8434608

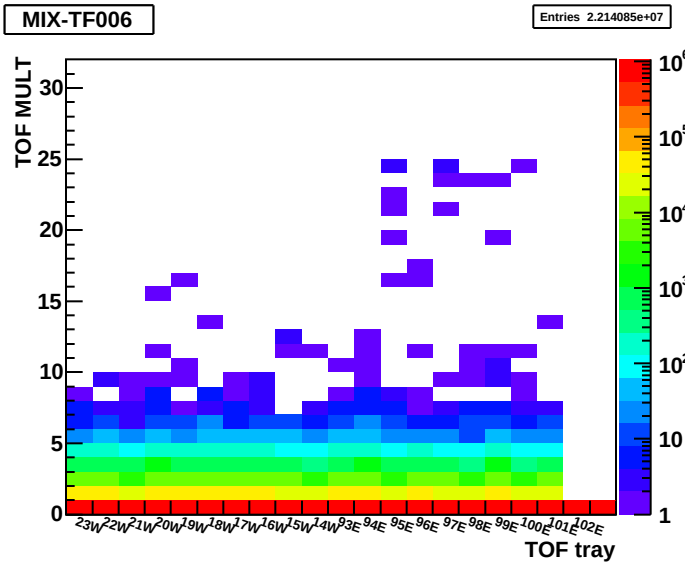

MIX-TF001
Entries 2.214085e+07
TOF MULT
TOF tray

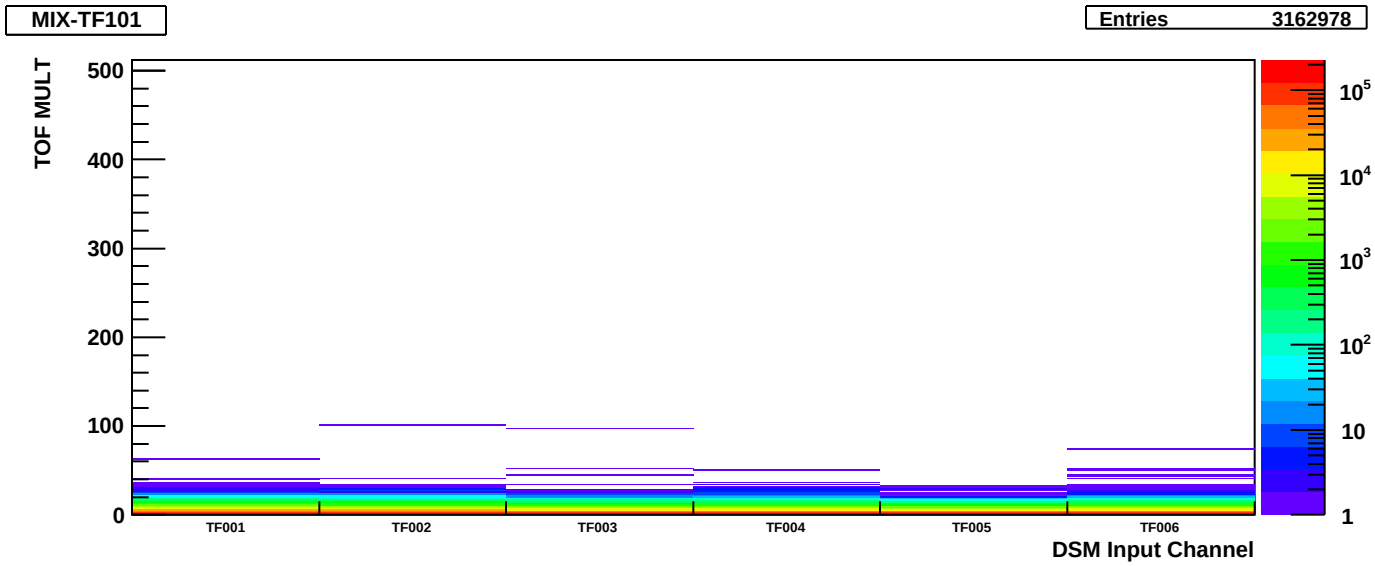

MIX-TF002
Entries 2.214085e+07
TOF MULT
TOF tray

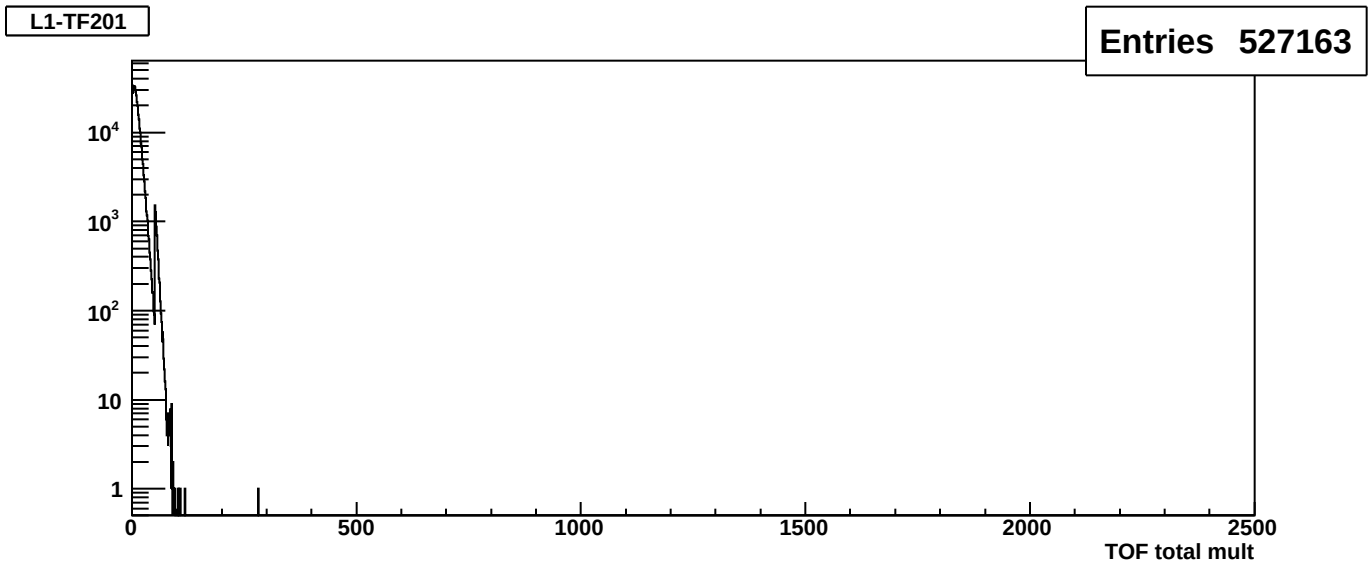

MIX-TF003
Entries 2.214085e+07
TOF MULT
TOF tray

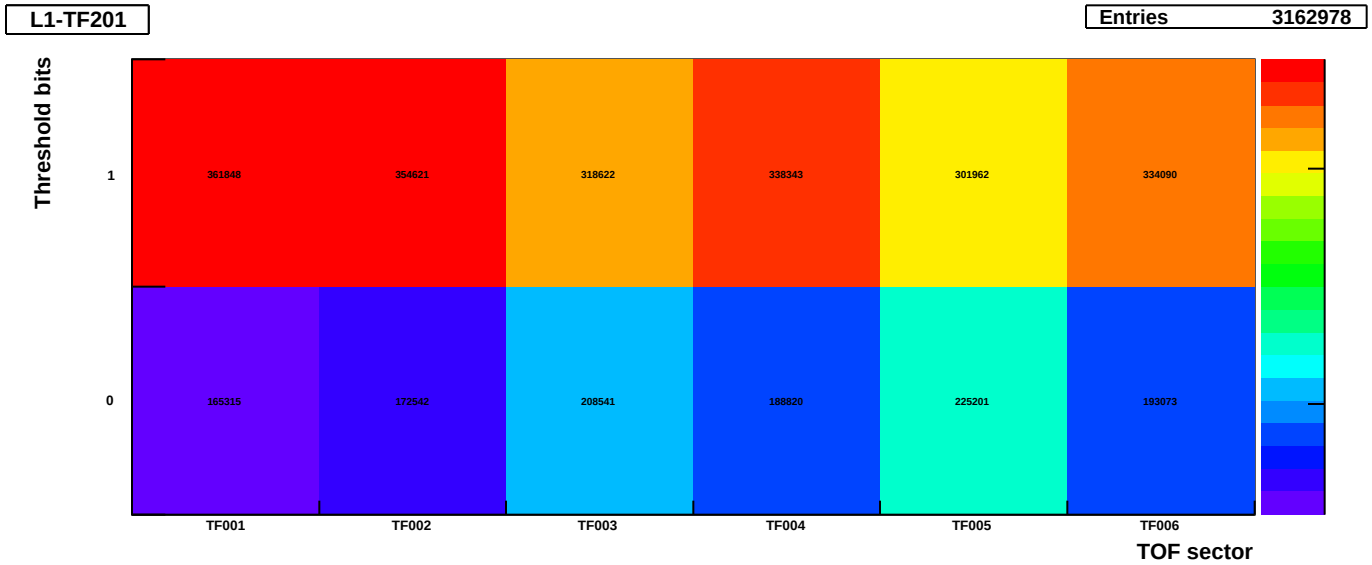

MIX-TF004
Entries 2.214085e+07
TOF MULT
TOF tray

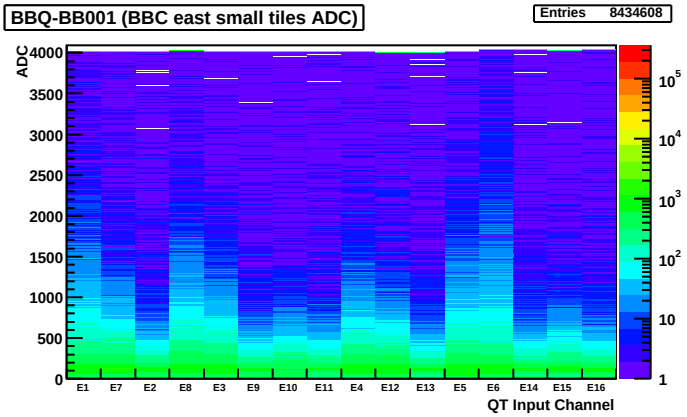

MIX-TF005
Entries 2.214085e+07
TOF MULT
TOF tray

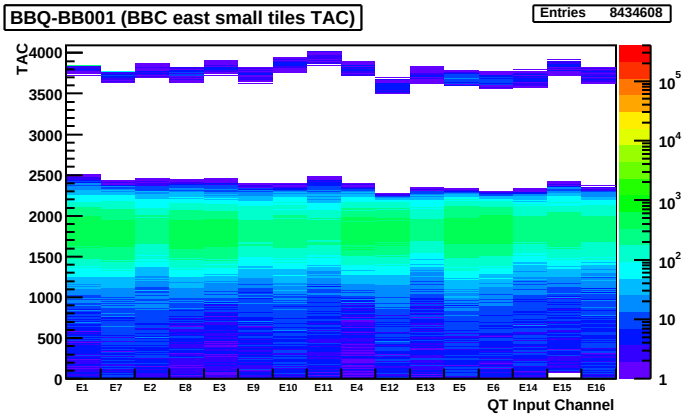

MIX-TF006
Entries 2.214085e+07
TOF MULT
TOF tray

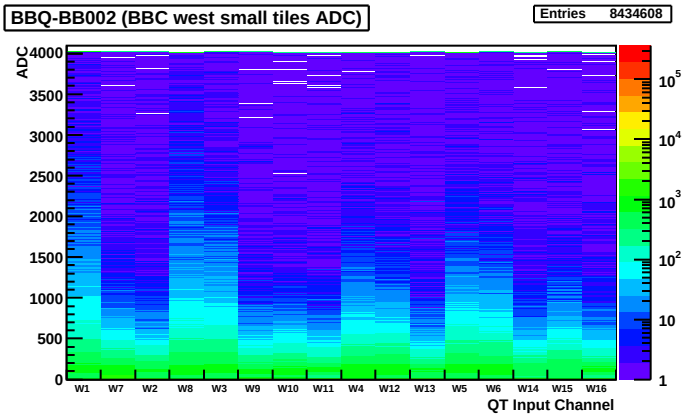

MIX-TF101
Entries 3162978
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

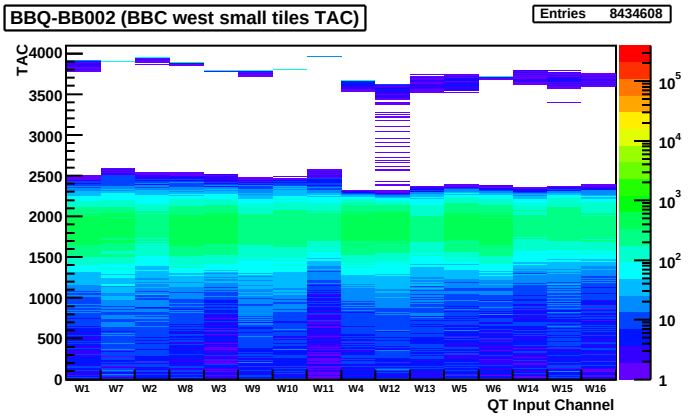

L1-TF201
Entries 527163
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

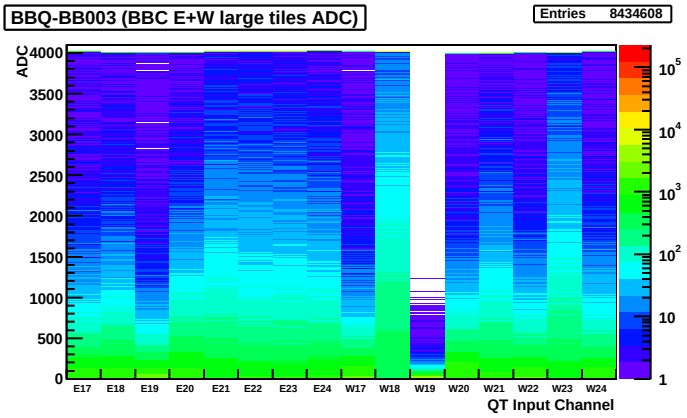

L1-TF201
Entries 3162978
Threshold bits
1
0
361848
354621
318622
338343
301962
334090
165315
172542
208541
188820
225201
193073
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

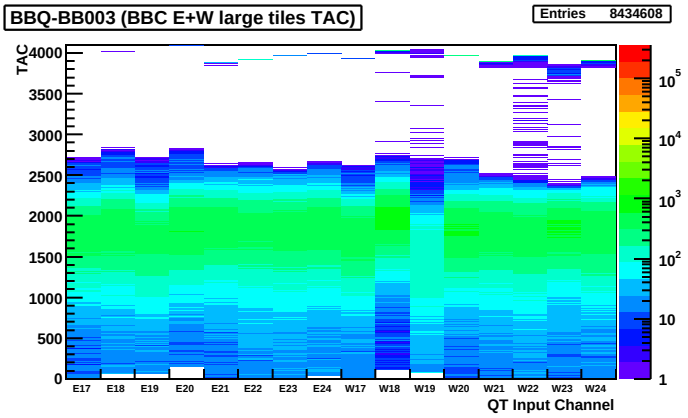

BBQ-BB001 (BBC east small tiles ADC)
Entries 8434608
ADC
QT Input Channel

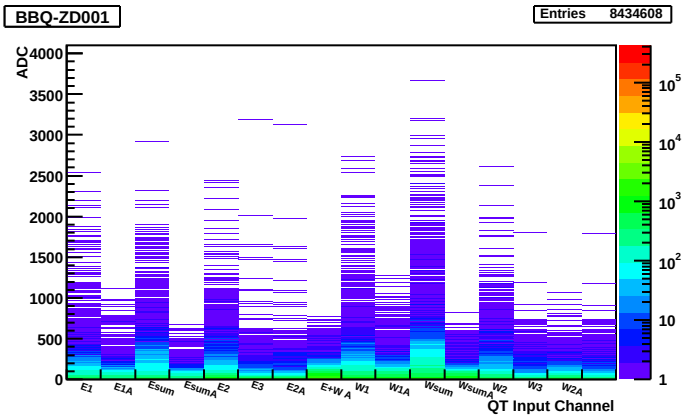

BBQ-BB001 (BBC east small tiles TAC)
Entries 8434608
TAC
QT Input Channel

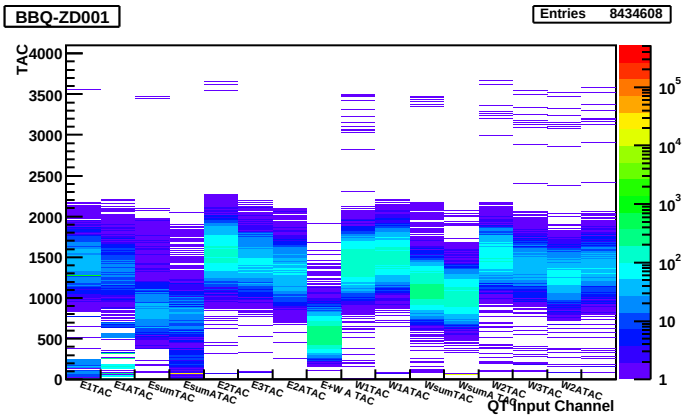

BBQ-BB002 (BBC west small tiles ADC)
Entries 8434608
ADC
QT Input Channel

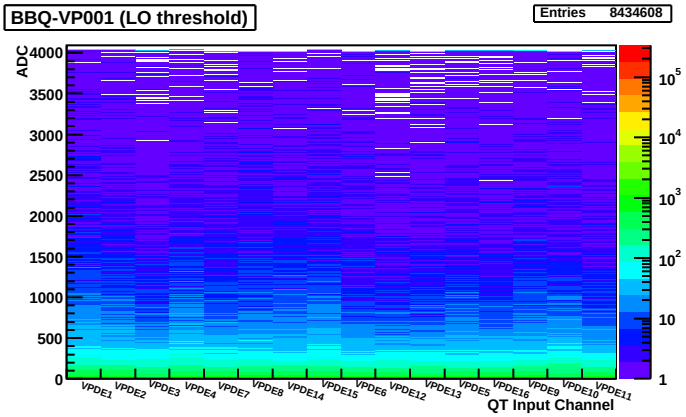

BBQ-BB002 (BBC west small tiles TAC)
Entries 8434608
TAC
QT Input Channel

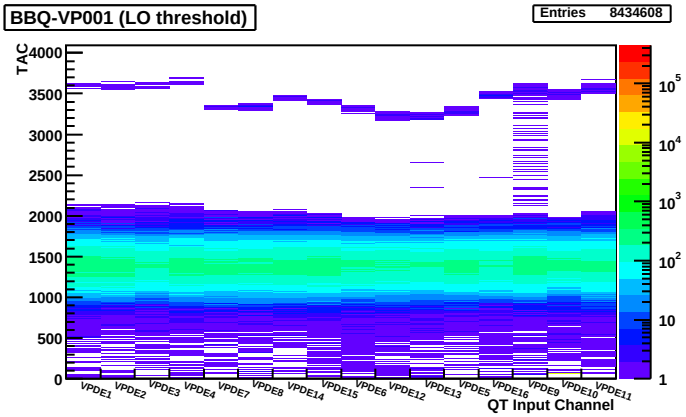

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8434608
ADC
QT Input Channel

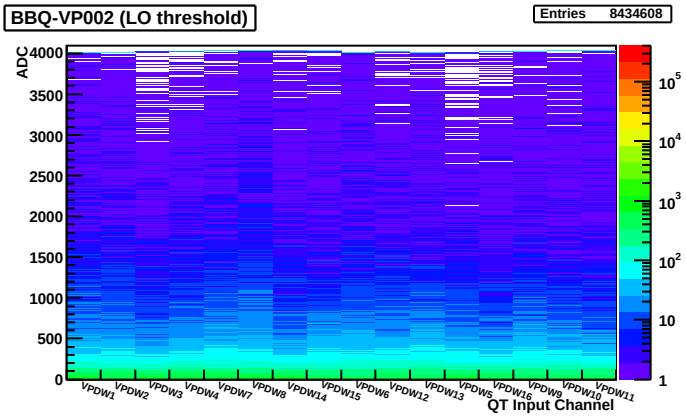

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8434608
TAC
QT Input Channel

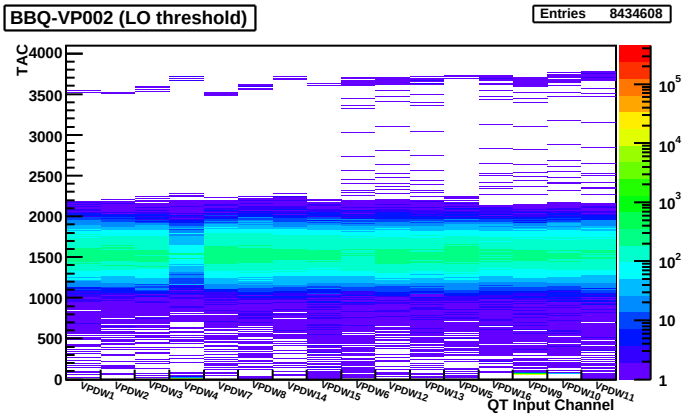

BBQ-ZD001
Entries 8434608
ADC
QT Input Channel

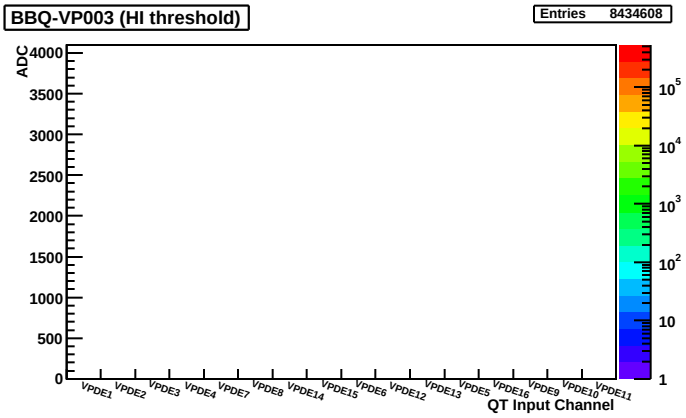

BBQ-ZD001
Entries 8434608
TAC
QT Input Channel

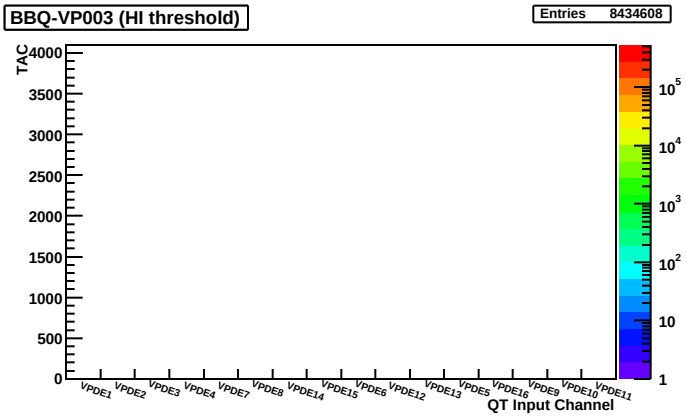

BBQ-VP001 (LO threshold)
Entries 8434608
ADC
QT Input Channel

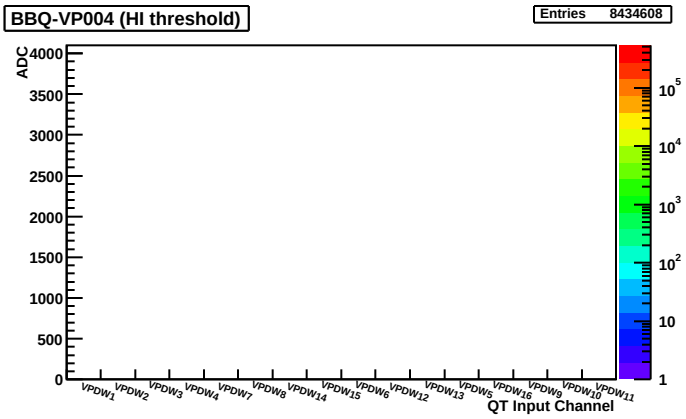

BBQ-VP001 (LO threshold)
Entries 8434608
TAC
QT Input Channel

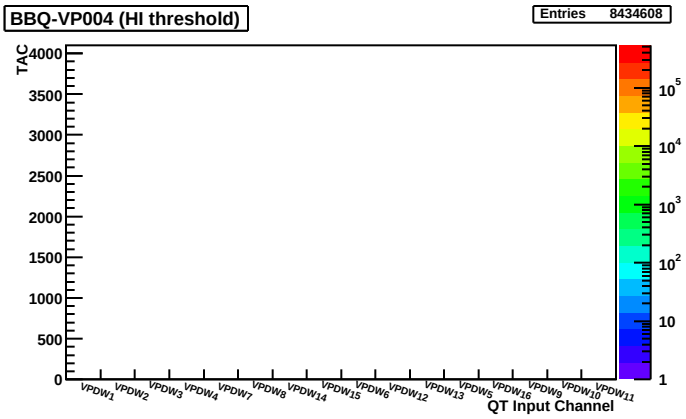

BBQ-VP002 (LO threshold)
Entries 8434608
ADC
QT Input Channel

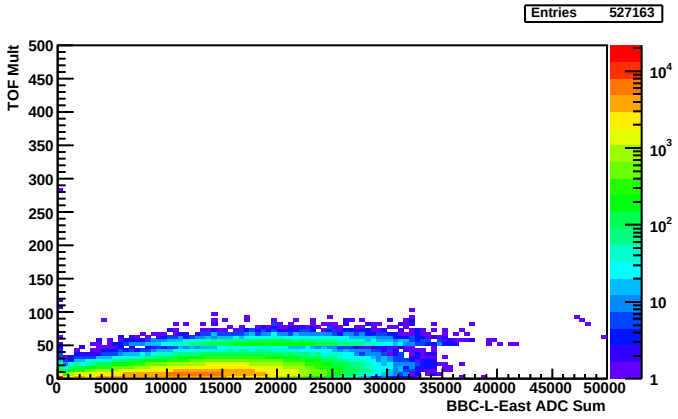

BBQ-VP002 (LO threshold)
Entries 8434608
TAC
QT Input Channel

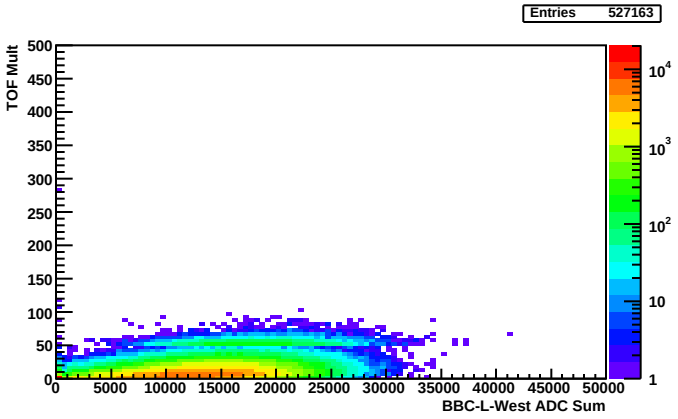

BBQ-VP003 (HI threshold)
Entries 8434608
ADC
QT Input Channel

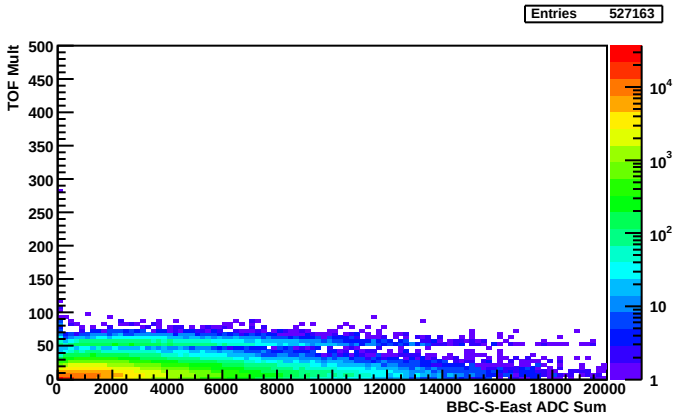

BBQ-VP003 (HI threshold)
Entries 8434608
TAC
QT Input Channel

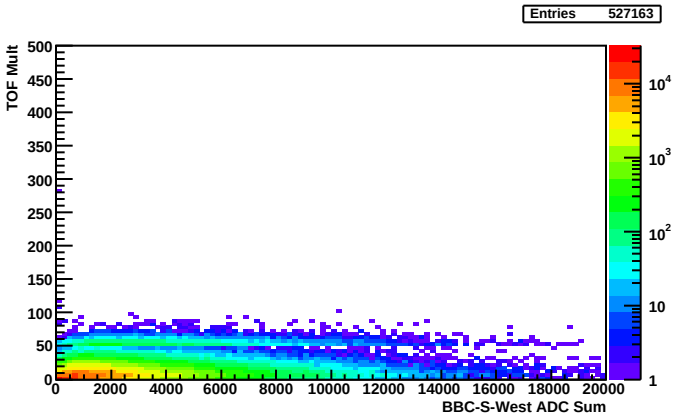

BBQ-VP004 (HI threshold)
Entries 8434608
ADC
QT Input Channel

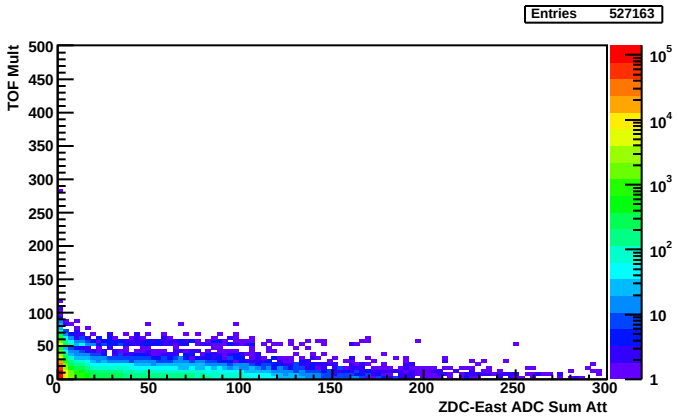

BBQ-VP004 (HI threshold)
Entries 8434608
TAC
QT Input Channel

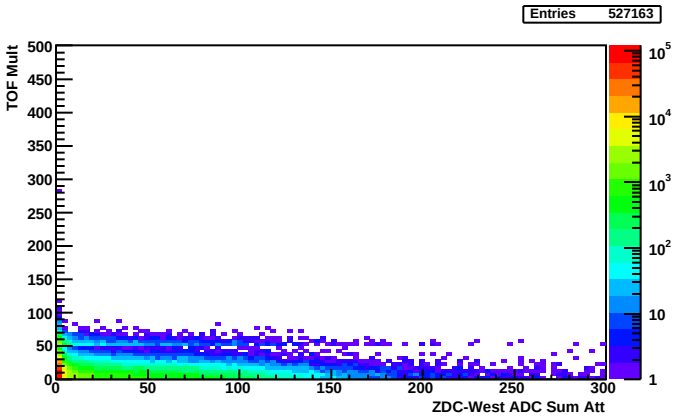

Entries 527163
TOF Mult
BBC-L-East ADC Sum

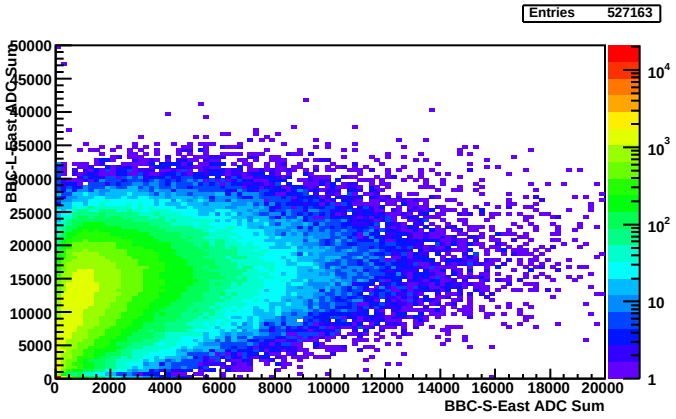

Entries 527163
TOF Mult
BBC-L-West ADC Sum

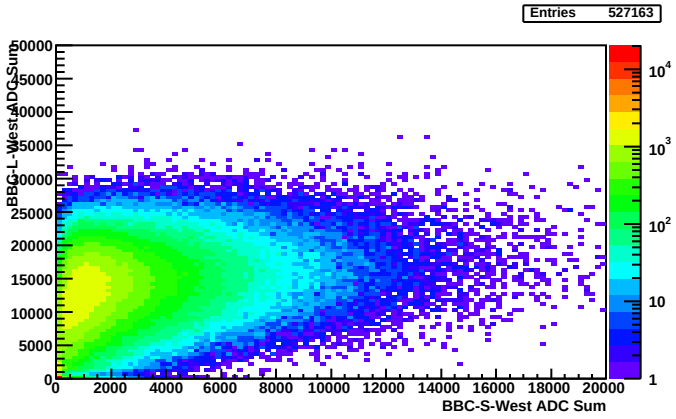

Entries 527163
TOF Mult
BBC-S-East ADC Sum

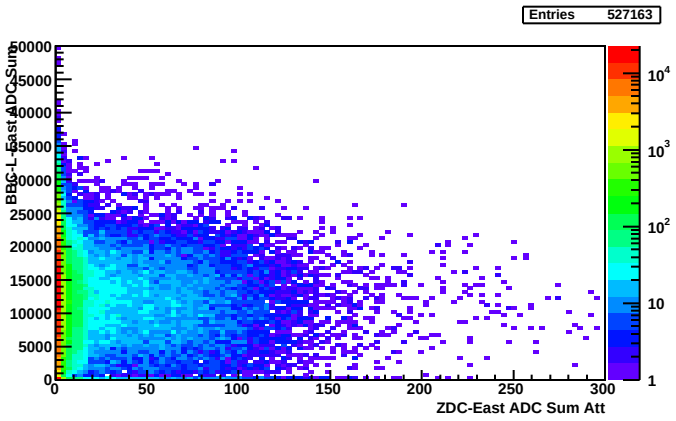

Entries 527163
TOF Mult
BBC-S-West ADC Sum

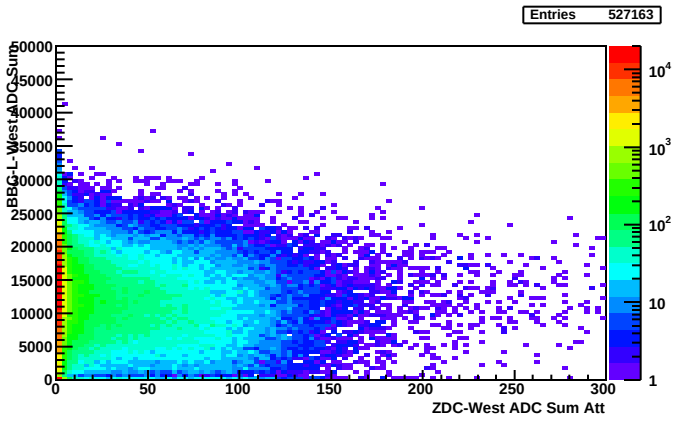

Entries 527163
TOF Mult
ZDC-East ADC Sum Att

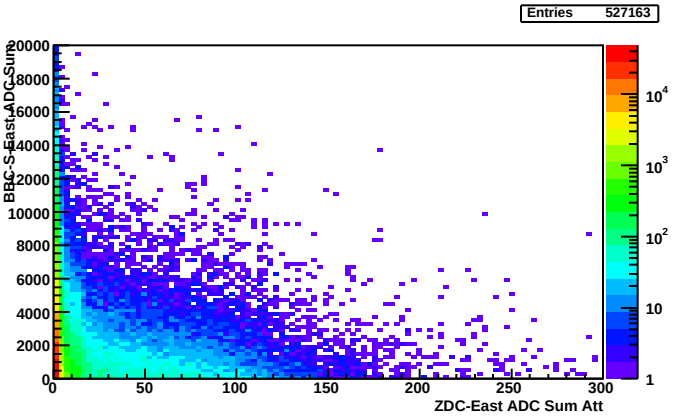

Entries 527163
TOF Mult
ZDC-West ADC Sum Att

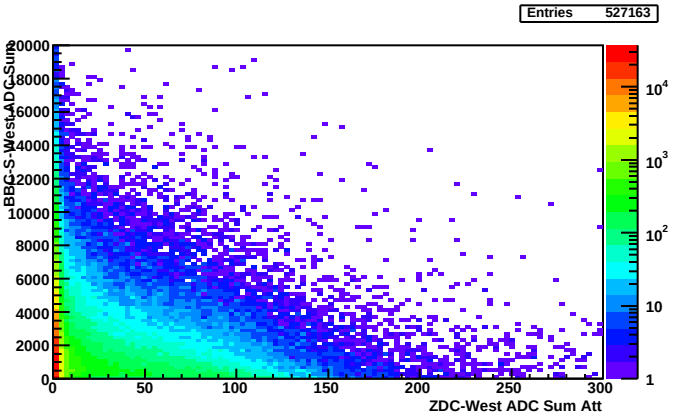

Entries 527163
BBC-L-East ADC Sum
BBC-S-East ADC Sum

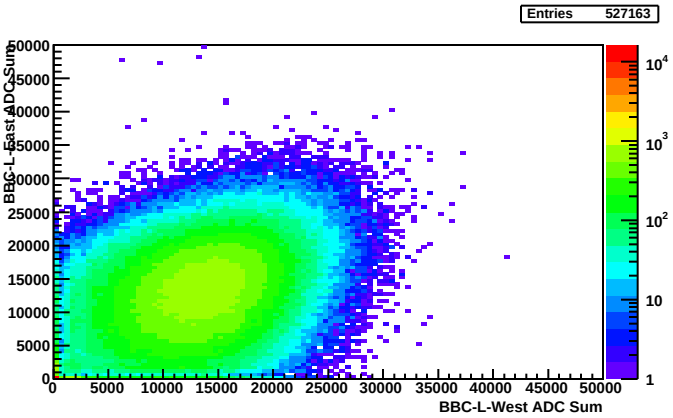

Entries 527163
BBC-L-West ADC Sum
BBC-S-West ADC Sum

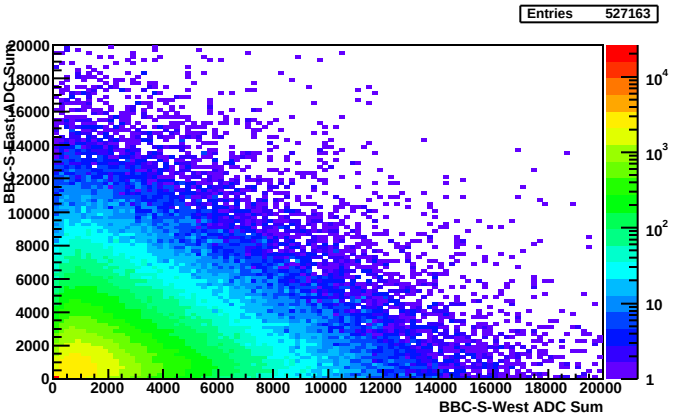

Entries 527163
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

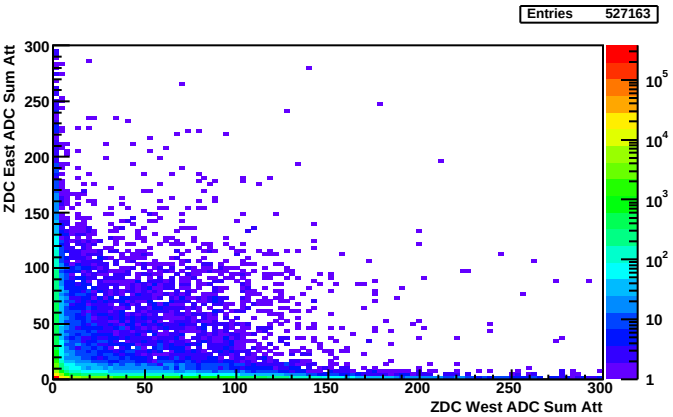

Entries 527163
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

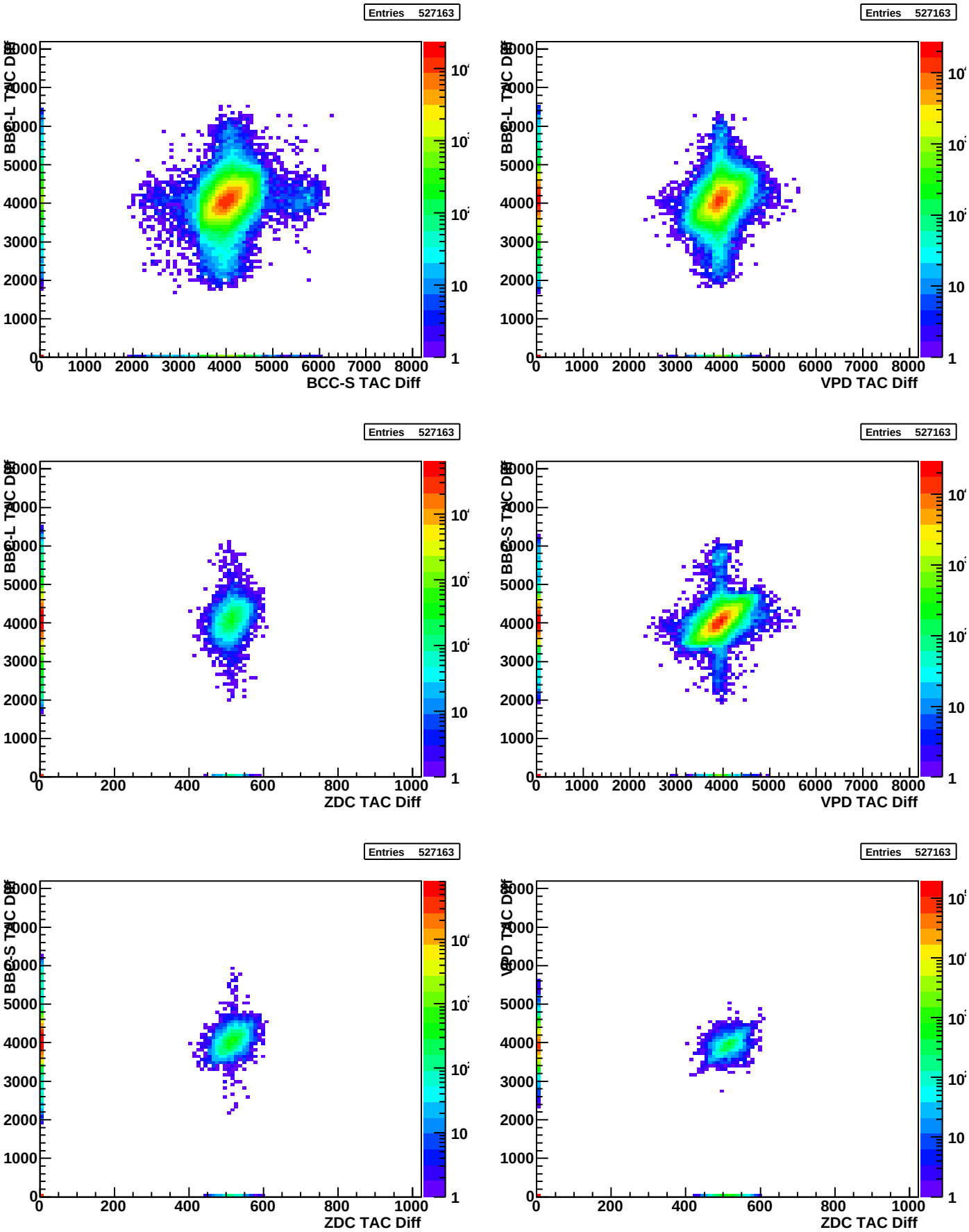

Entries 527163
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

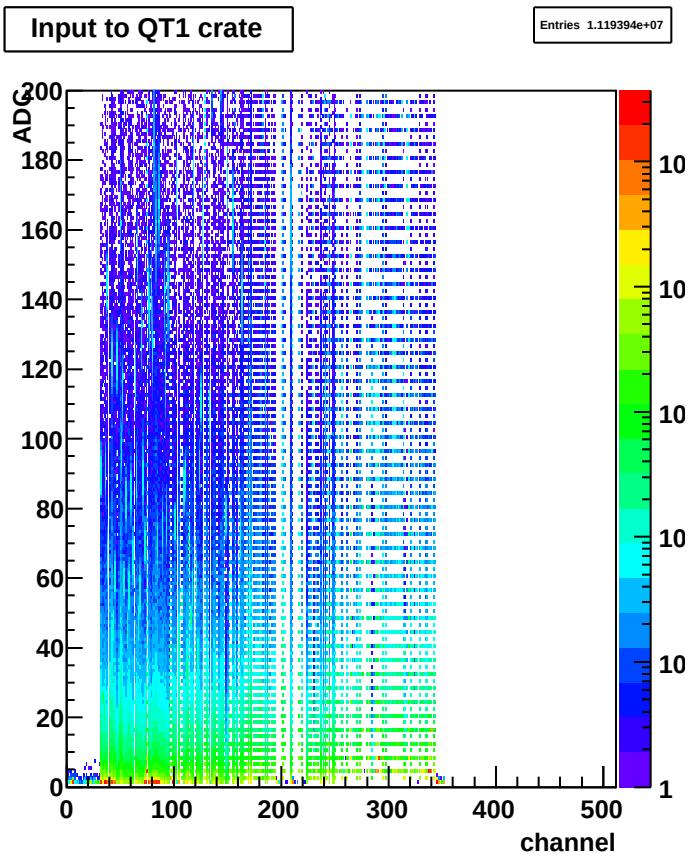

Entries 527163
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

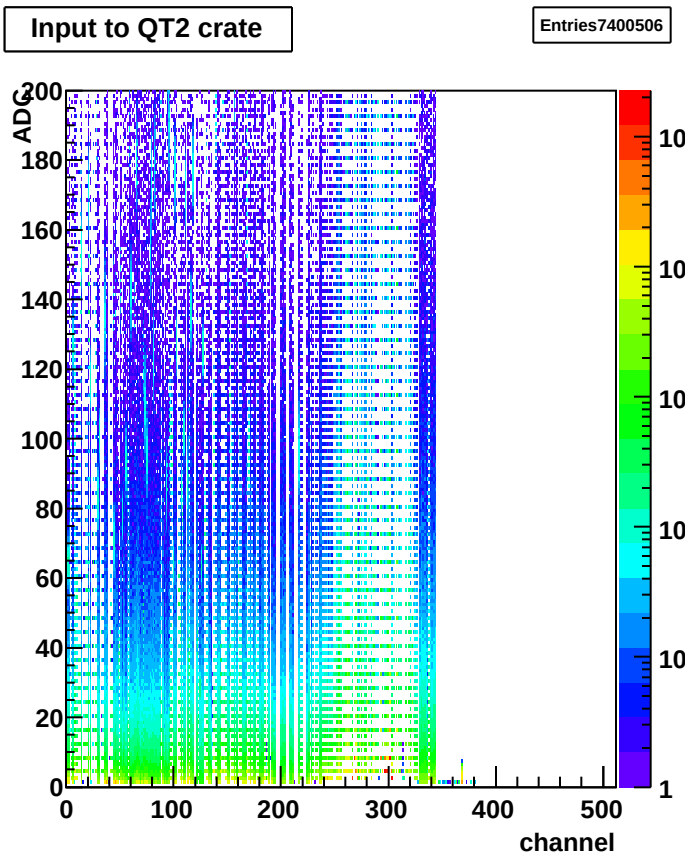

Entries 527163
BBC-L-East ADC Sum
BBC-L-West ADC Sum

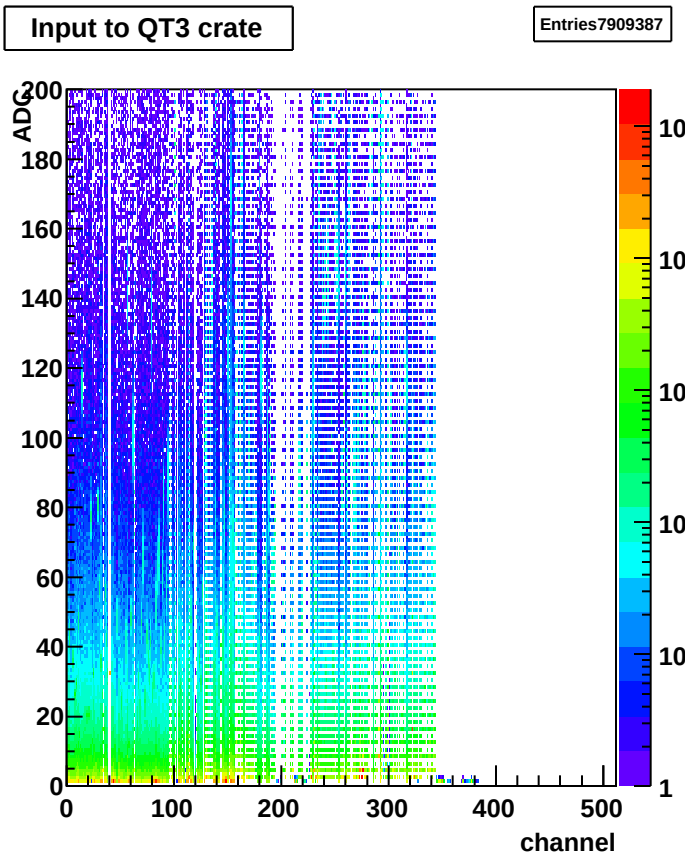

Entries 527163
BBC-S-East ADC Sum
BBC-S-West ADC Sum

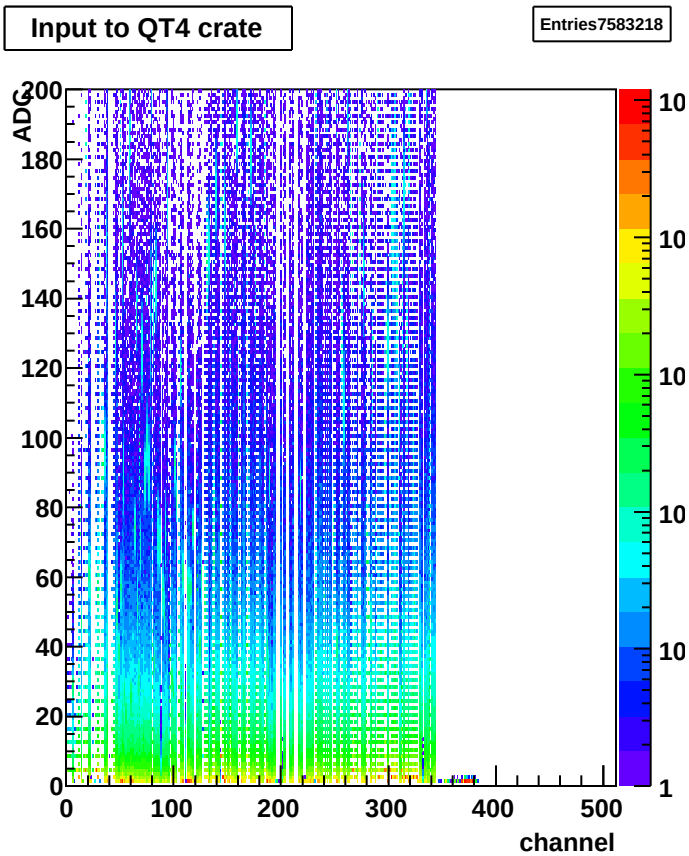

Entries 527163
ZDC East ADC Sum Att
ZDC West ADC Sum Att

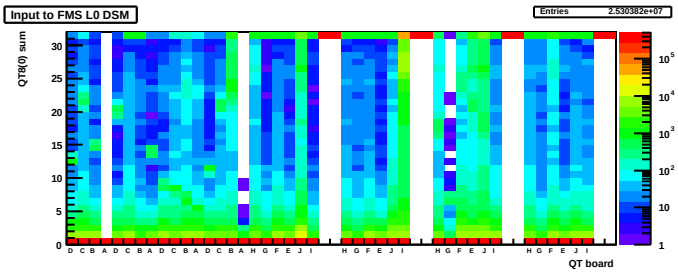

Entries 527163
BBC-L TAC Diff
BCC-S TAC Diff
Entries 527163
BBC-L TAC Diff
VPD TAC Diff
Entries 527163
BBC-L TAC Diff
ZDC TAC Diff
Entries 527163
BBC-S TAC Diff
VPD TAC Diff
Entries 527163
BBC-S TAC Diff
ZDC TAC Diff
Entries 527163
VPD TAC Diff
ZDC TAC Diff

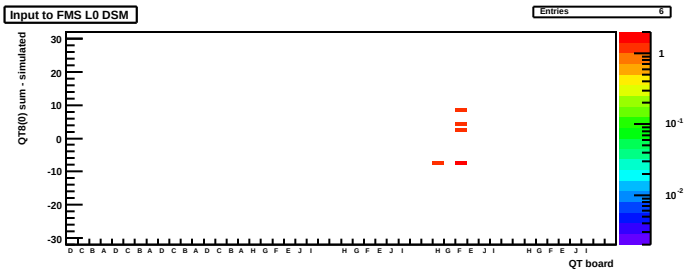

Input to QT1 crate
Entries 1.119394e+07
ADC
channel

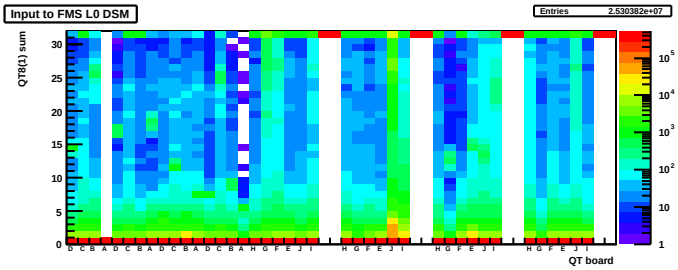

Input to QT2 crate
Entries7400506
ADC
channel

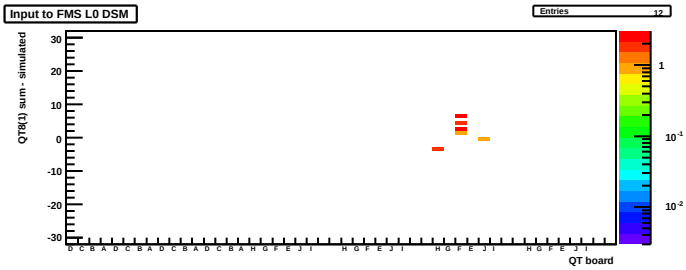

Input to QT3 crate
Entries7909387
ADC
channel

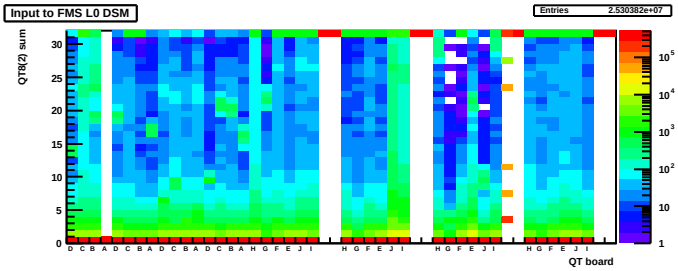

Input to QT4 crate
Entries7583218
ADC
channel

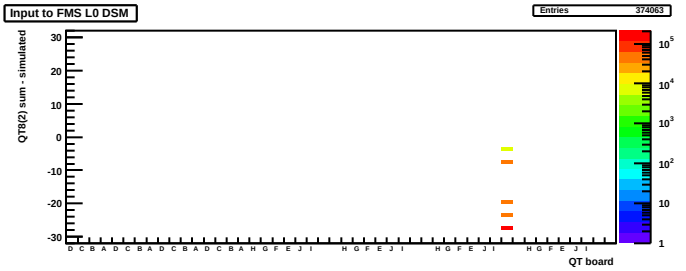

Input to FMS L0 DSM
Entries 2.530382e+07
QT8(0) sum
QT board

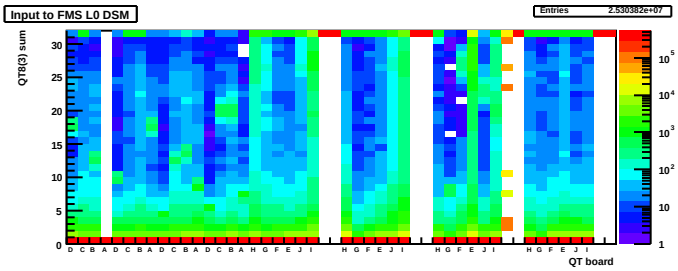

Input to FMS L0 DSM
Entries 6
QT8(0) sum - simulated
QT board

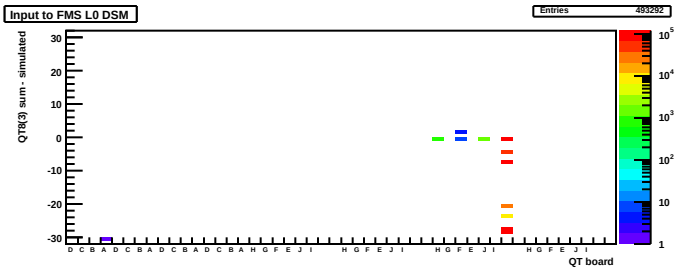

Input to FMS L0 DSM
Entries 2.530382e+07
QT8(1) sum
QT board

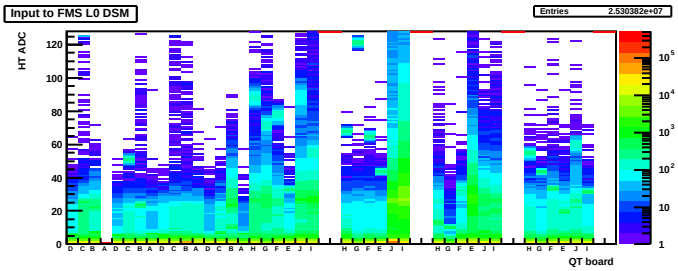

Input to FMS L0 DSM
Entries 12
QT8(1) sum - simulated
QT board

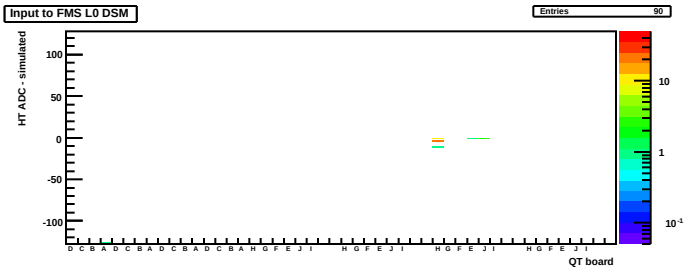

Input to FMS L0 DSM
Entries 2.530382e+07
QT8(2) sum
QT board

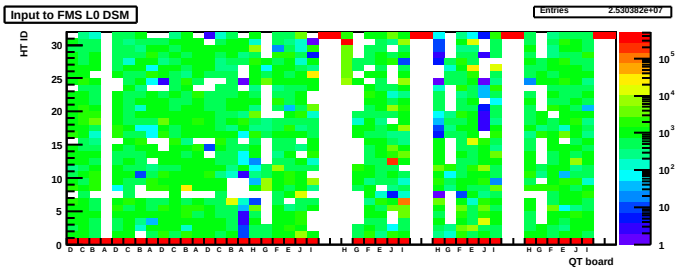

Input to FMS L0 DSM
Entries 374063
QT8(2) sum - simulated
QT board

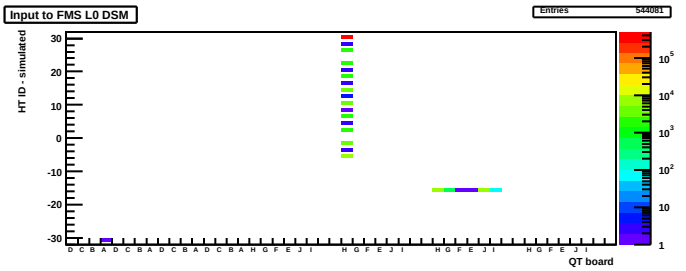

Input to FMS L0 DSM
Entries 2.530382e+07
QT8(3) sum
QT board

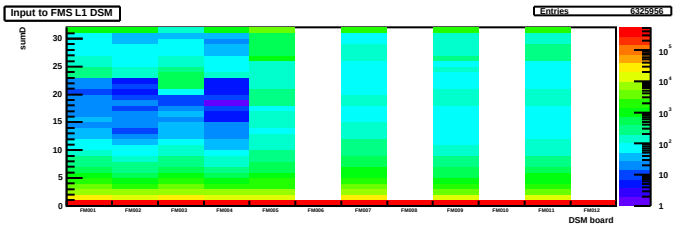

Input to FMS L0 DSM
Entries 493292
QT8(3) sum - simulated
QT board

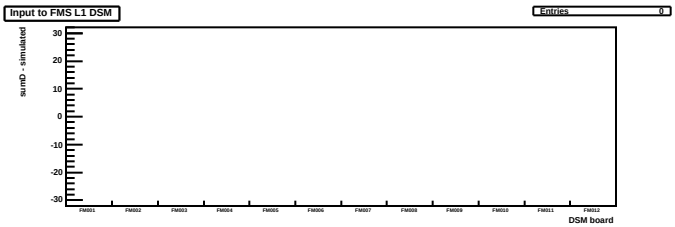

Input to FMS L0 DSM
Entries 2.530382e+07
HT ADC
QT board

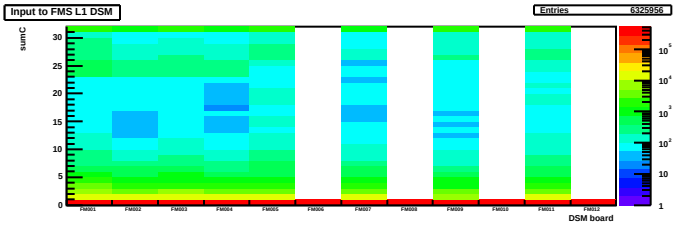

Input to FMS L0 DSM
Entries 90
HT ADC - simulated
QT board

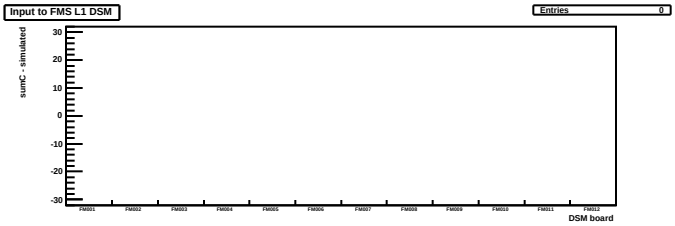

Input to FMS L0 DSM
Entries 2.530382e+07
HT ID
QT board

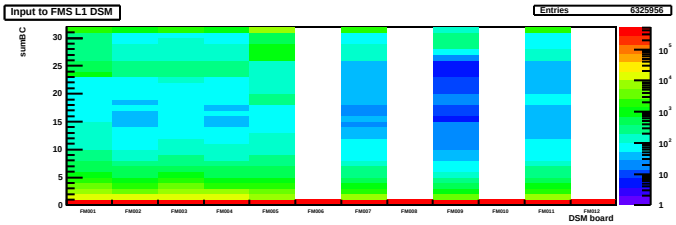

Input to FMS L0 DSM
Entries 544081
HT ID - simulated
QT board

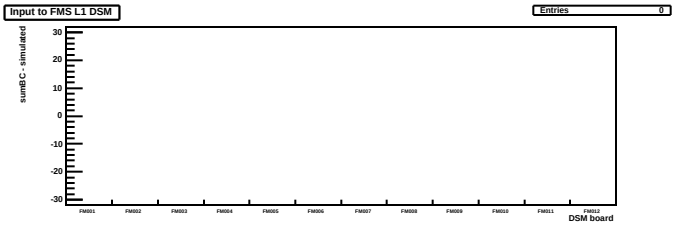

Input to FMS L1 DSM
Entries 6325956
sumD
DSM board

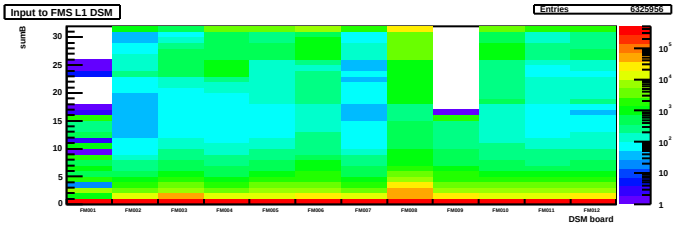

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

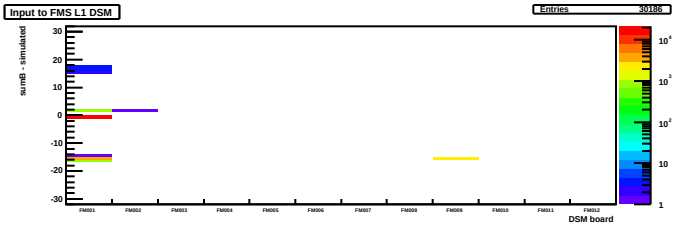

Input to FMS L1 DSM
Entries 6325956
sumC
DSM board

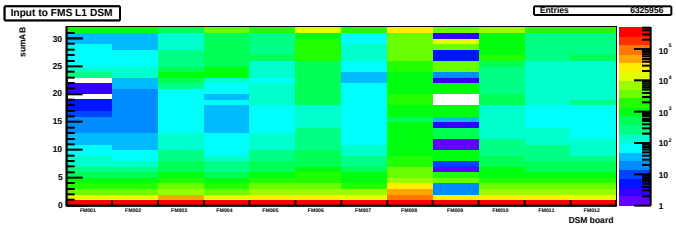

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

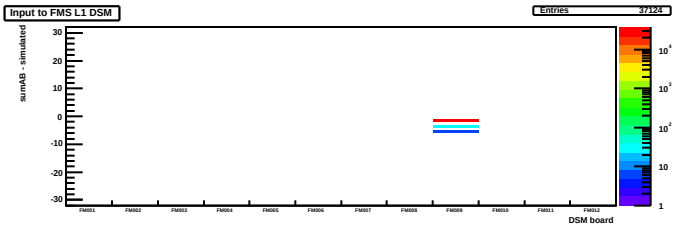

Input to FMS L1 DSM
Entries 6325956
sumBC
DSM board

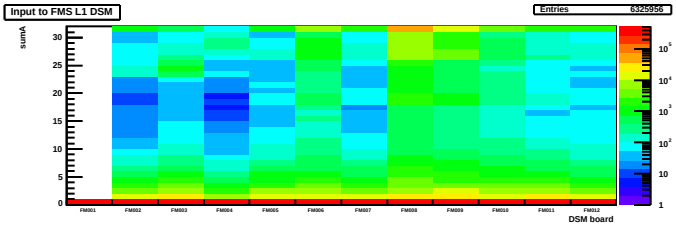

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

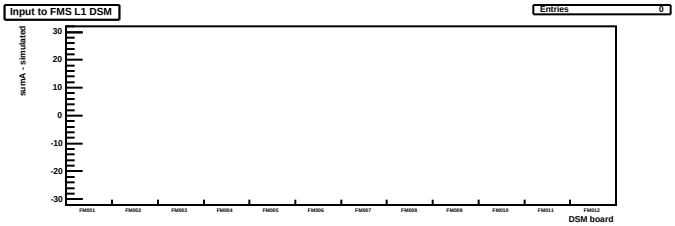

Input to FMS L1 DSM
Entries 6325956
sumB
DSM board

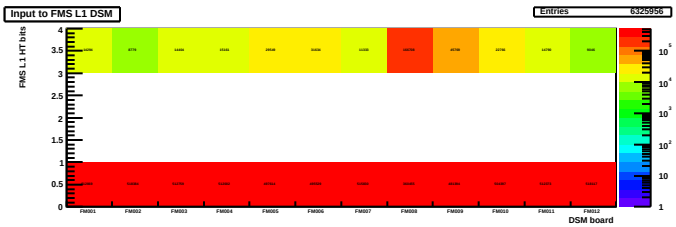

Input to FMS L1 DSM
Entries 30186
sumB - simulated
DSM board

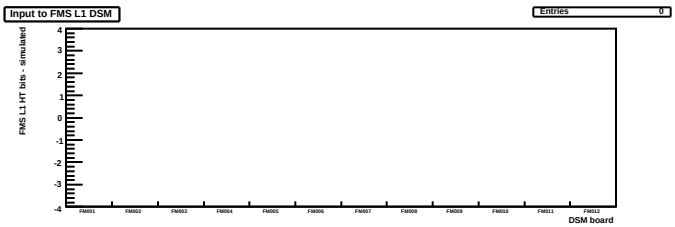

Input to FMS L1 DSM
Entries 6325956
sumAB
DSM board

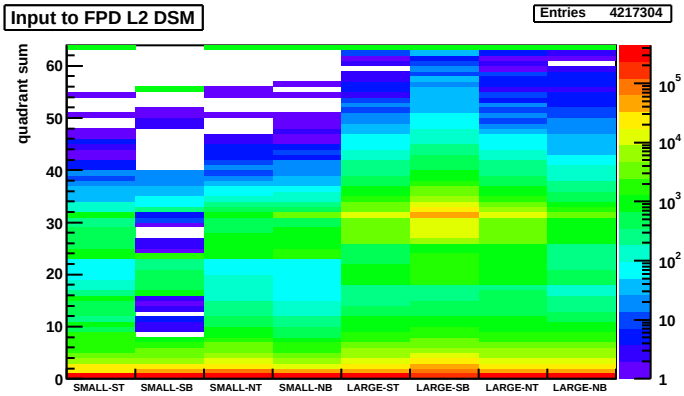

Input to FMS L1 DSM
Entries 37124
sumAB - simulated
DSM board

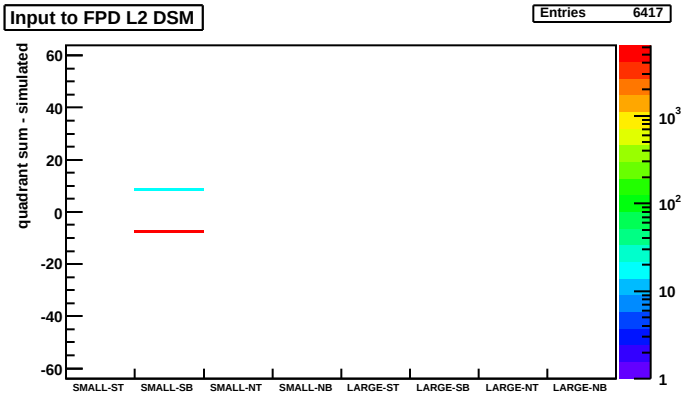

Input to FMS L1 DSM
Entries 6325956
sumA
DSM board

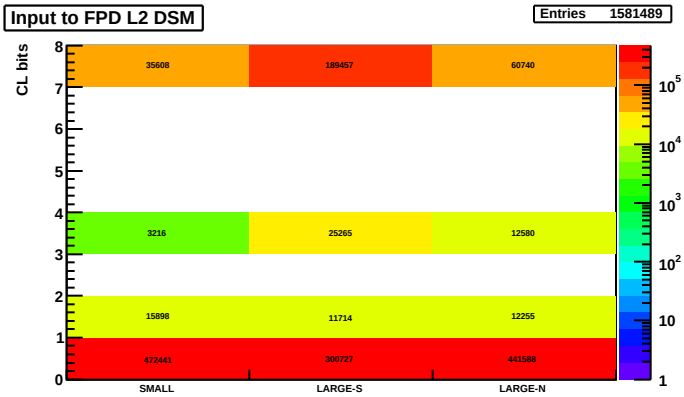

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

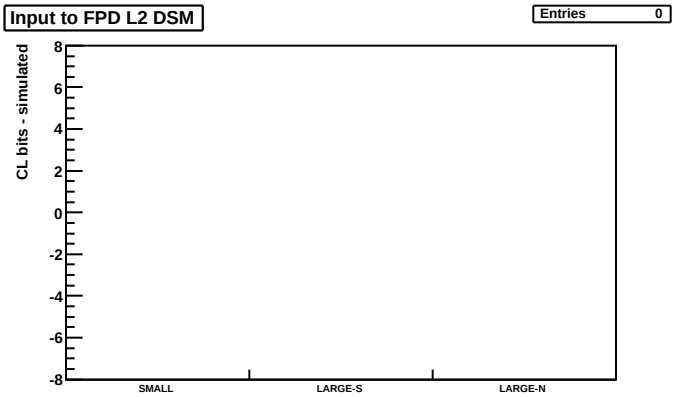

Input to FMS L1 DSM
Entries 6325956
FMS L1 HT bits
DSM board

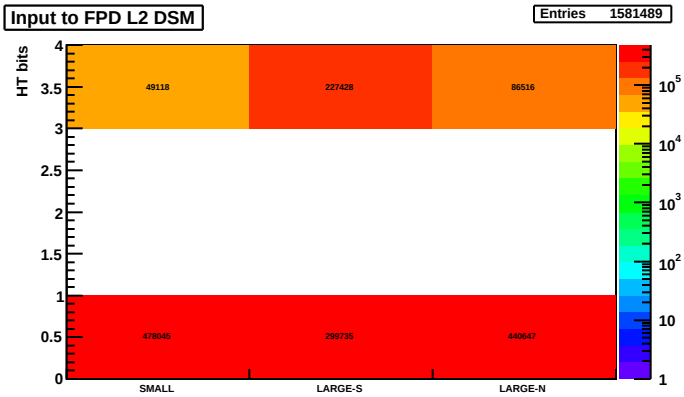

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

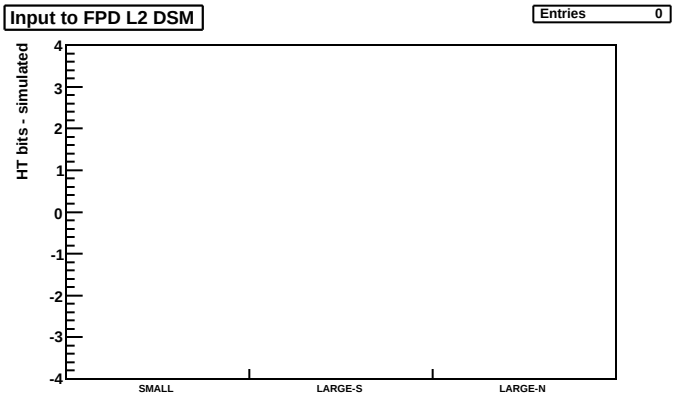

Input to FPD L2 DSM
Entries 4217304
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

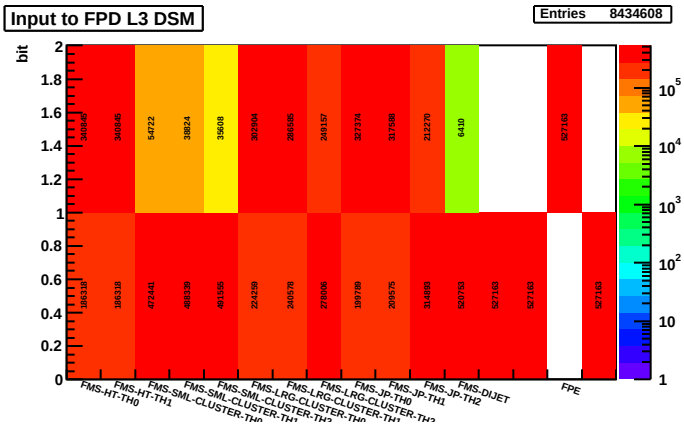

Input to FPD L2 DSM
Entries 6417
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

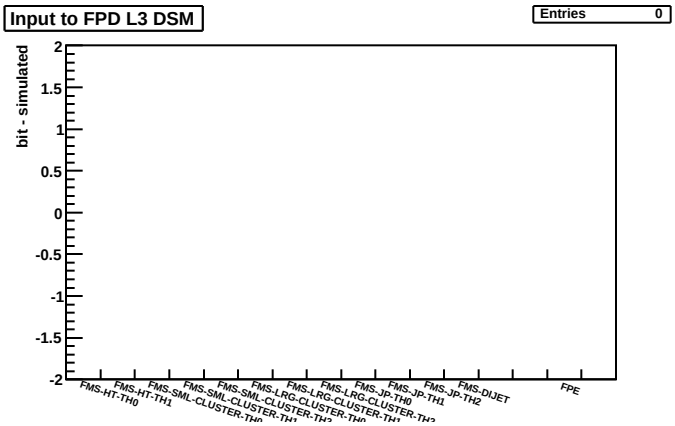

Input to FPD L2 DSM
Entries 1581489
CL bits
35608
189457
60740
3216
25265
12580
15898
11714
12255
472441
300727
441588
SMALL
LARGE-S
LARGE-N

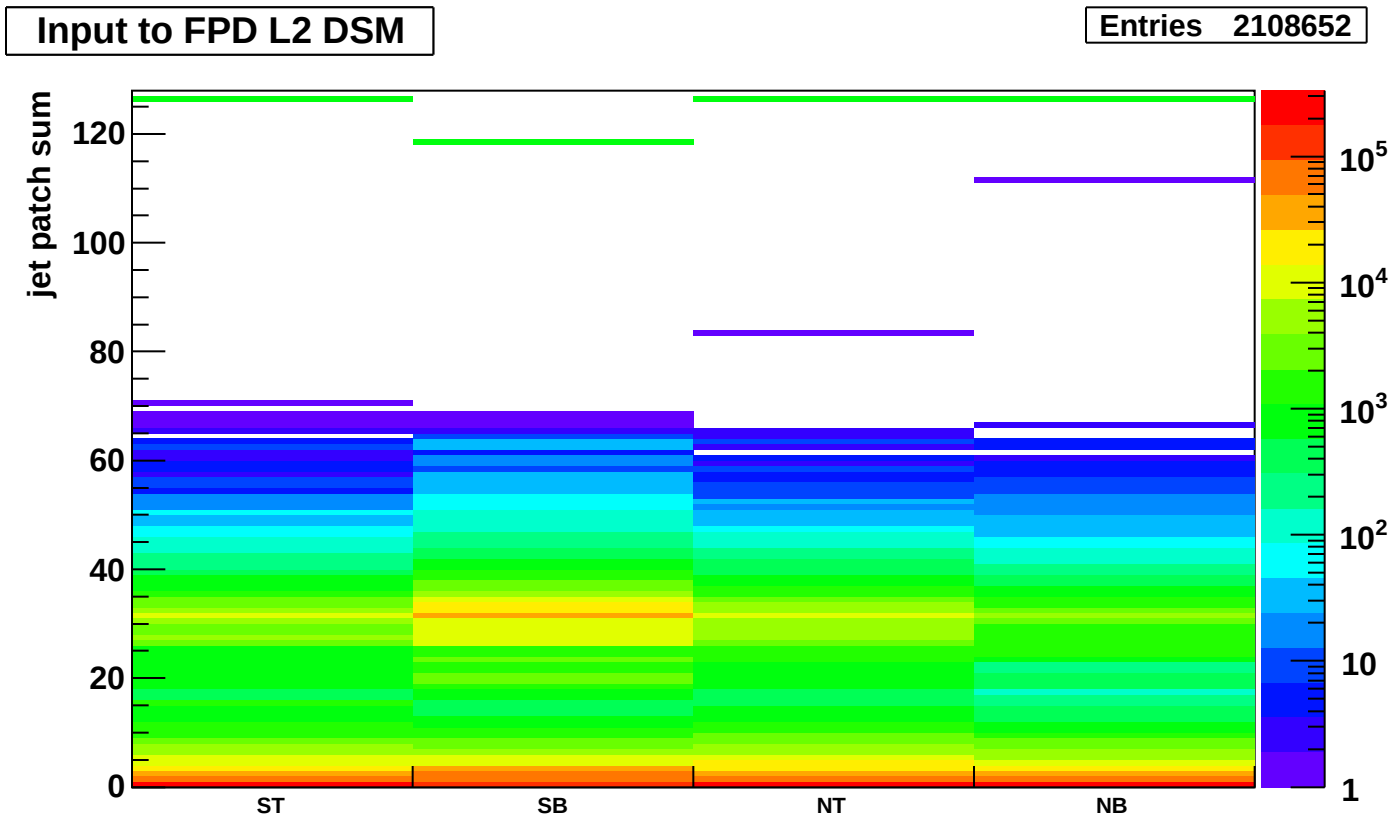

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

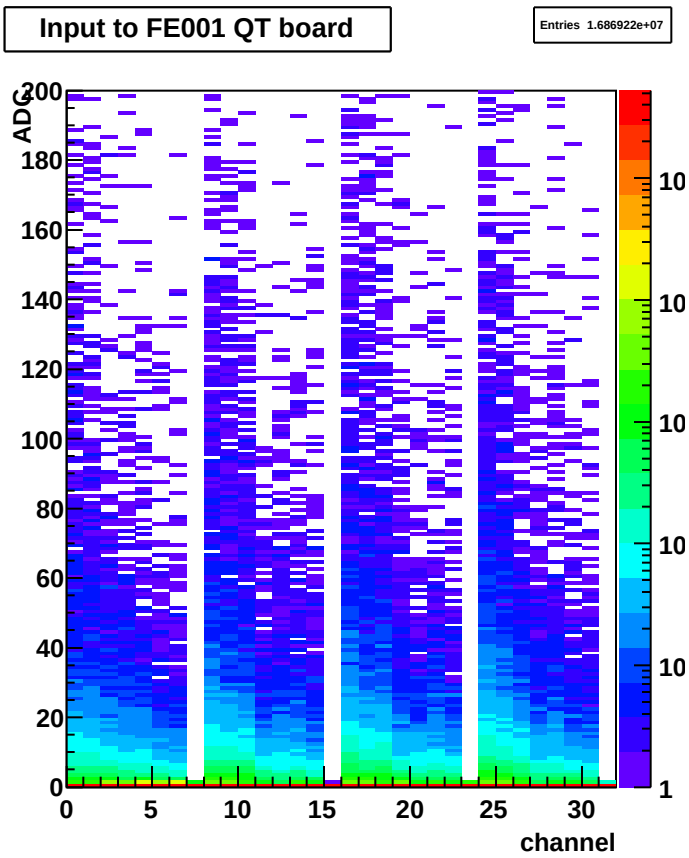

Input to FPD L2 DSM
Entries 1581489
HT bits
49118
227428
86516
478045
299735
440647
SMALL
LARGE-S
LARGE-N

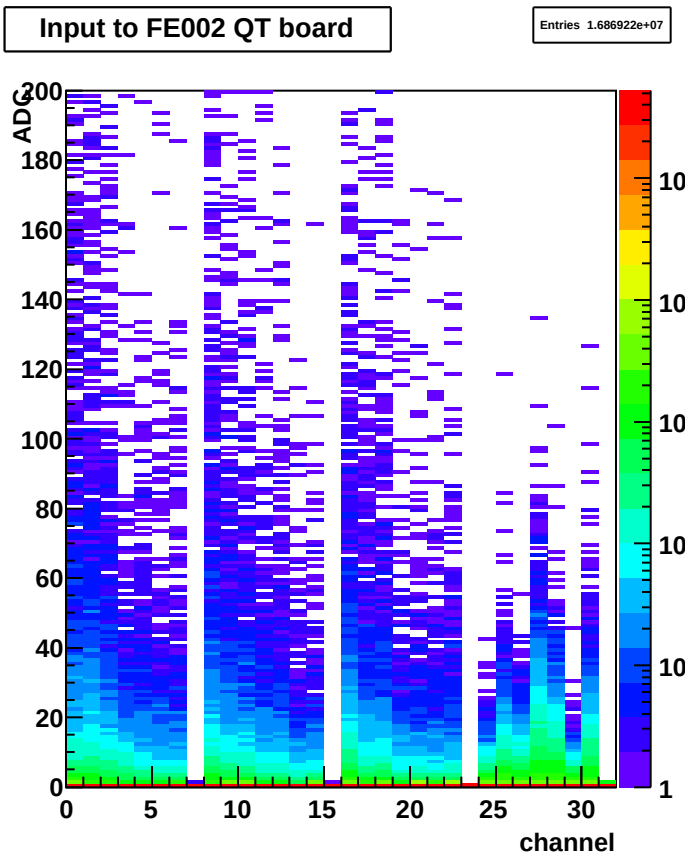

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

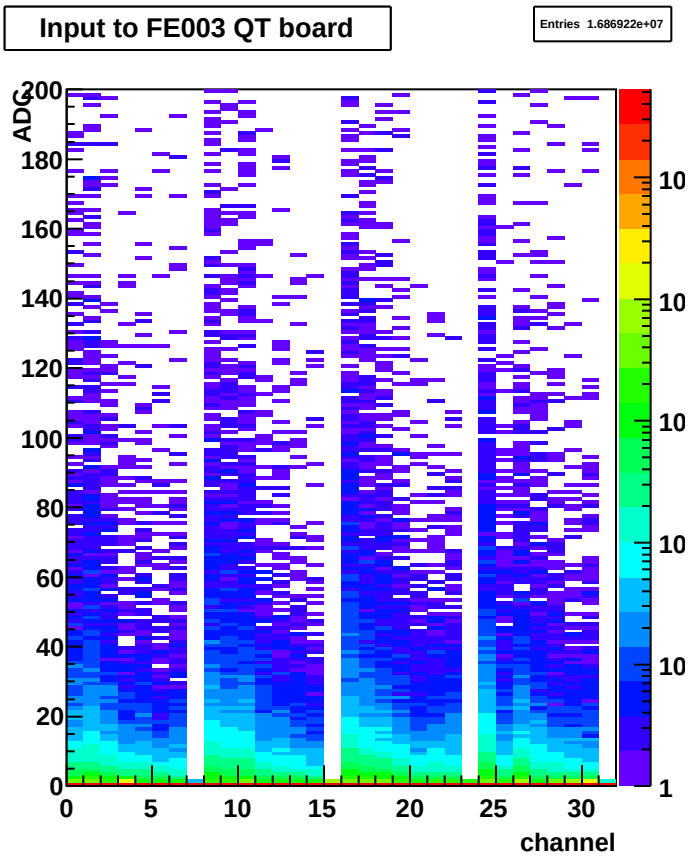

Input to FPD L3 DSM
Entries 8434608
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

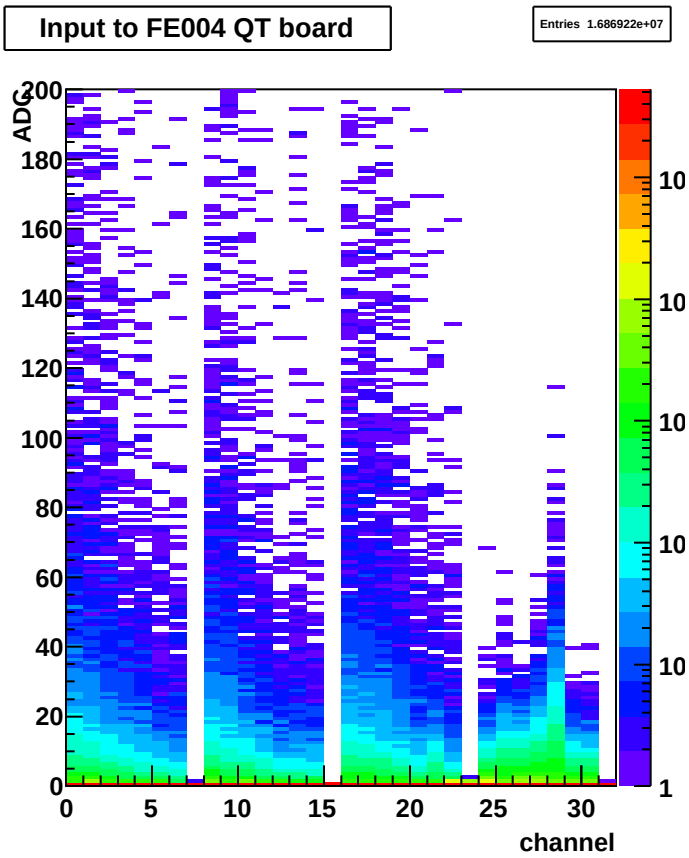

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

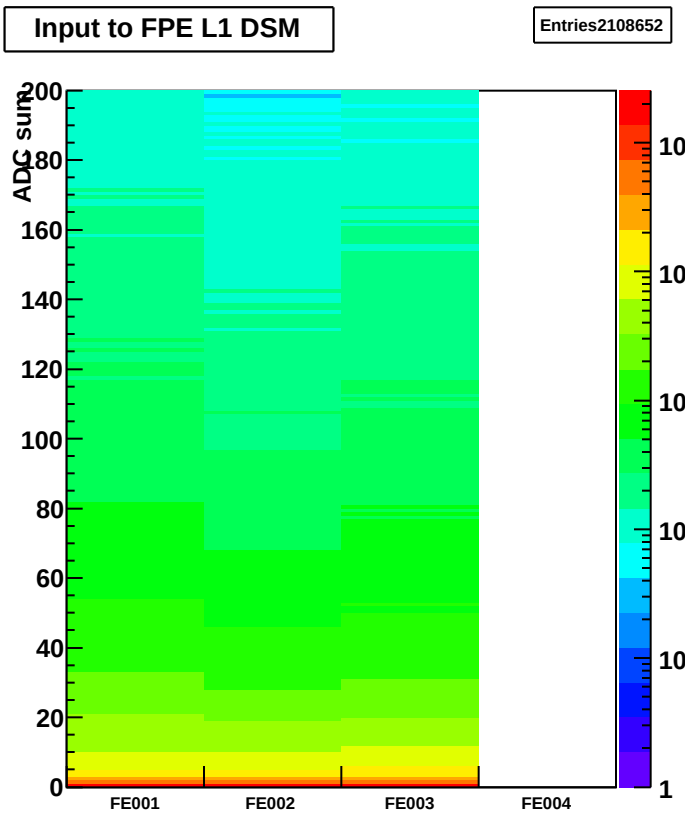

Input to FPD L2 DSM
Entries 2108652
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

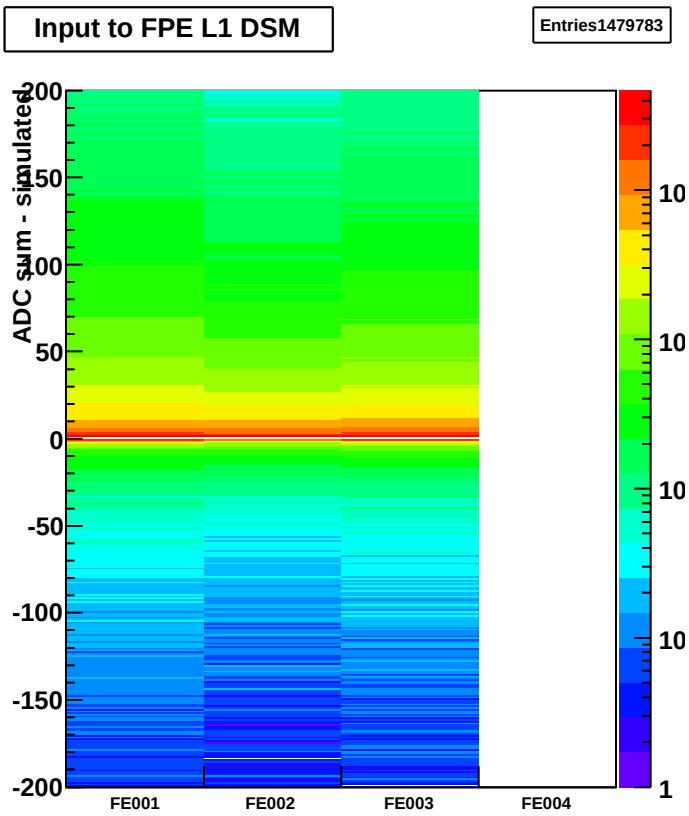

Input to FE001 QT board
Entries 1.686922e+07
ADC
channel

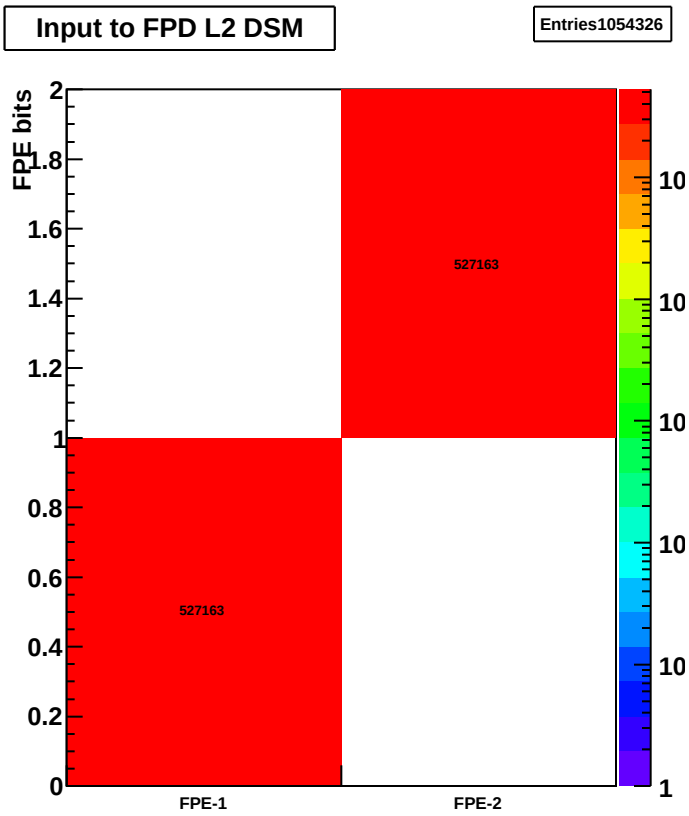

Input to FE002 QT board
Entries 1.686922e+07
ADC
channel

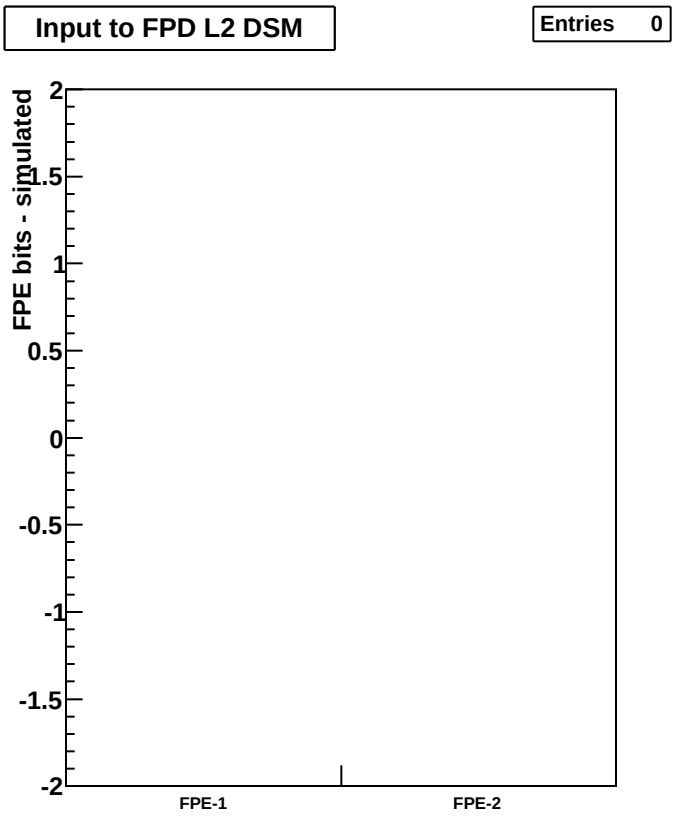

Input to FE003 QT board
Entries 1.686922e+07
ADC
channel


Input to FE004 QT board
Entries 1.686922e+07
ADC
channel


Input to FPE L1 DSM
Entries2108652
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1479783
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1054326
FPE bits
527163
527163
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2