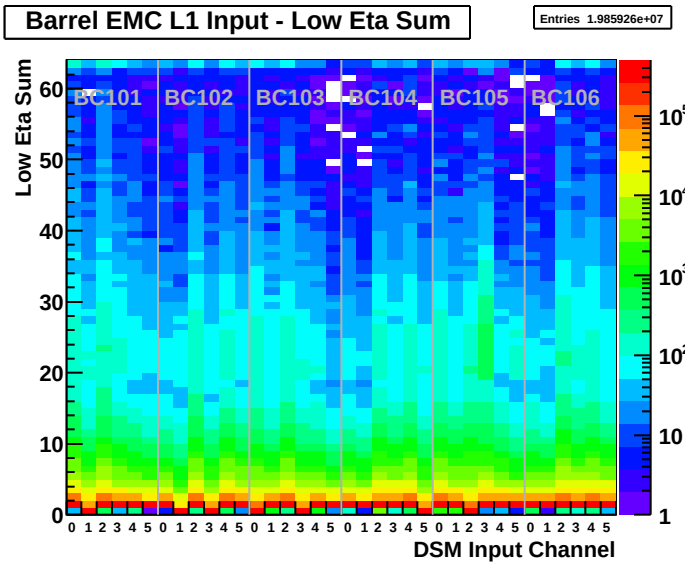
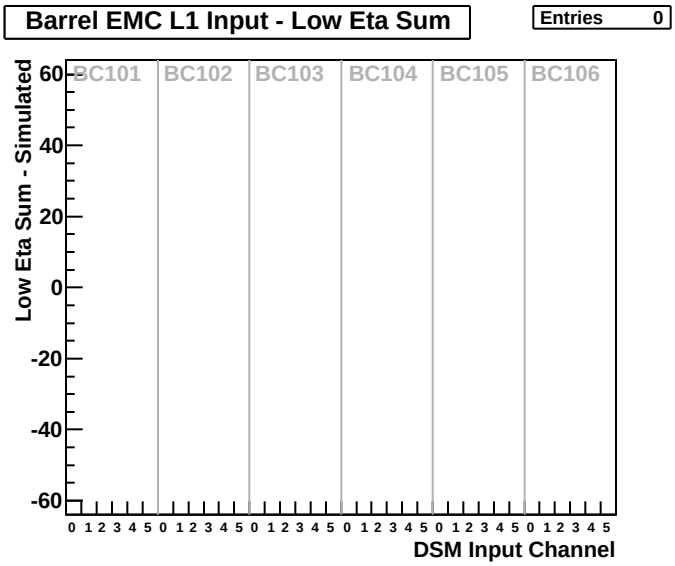
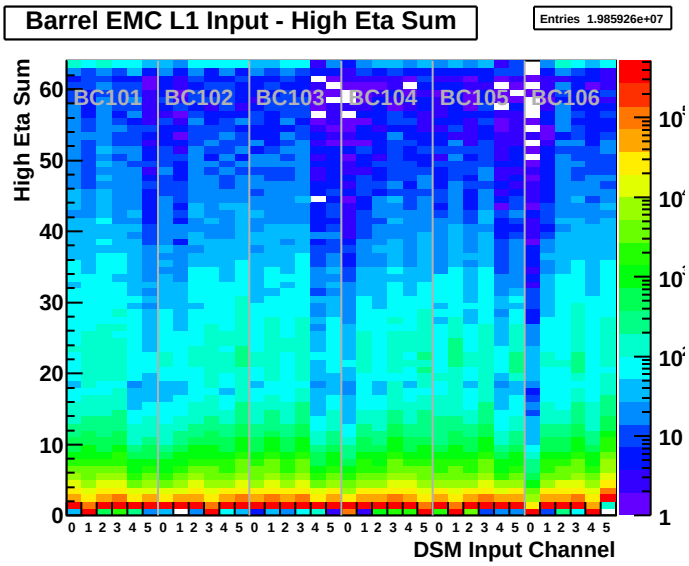
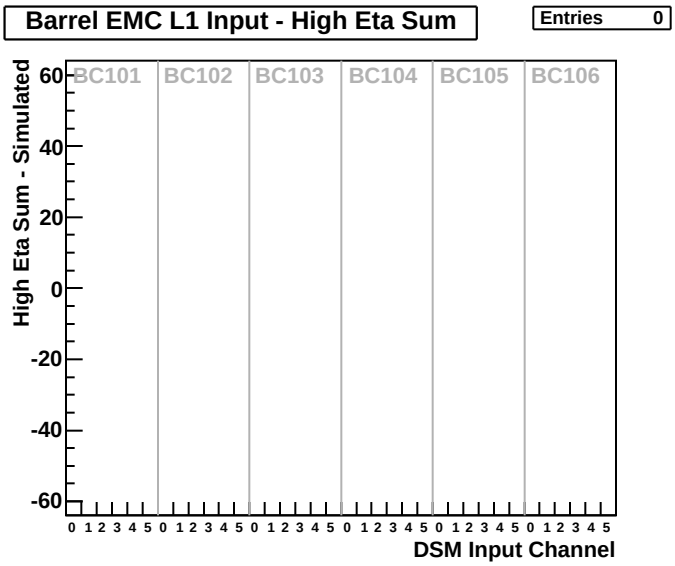
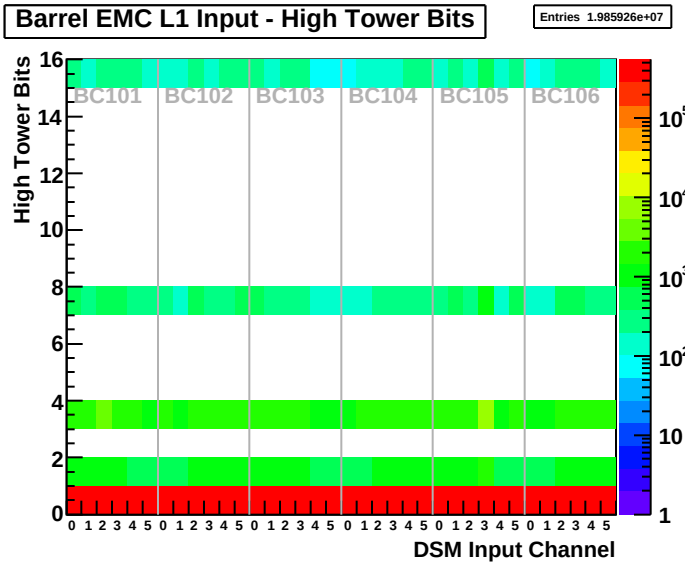
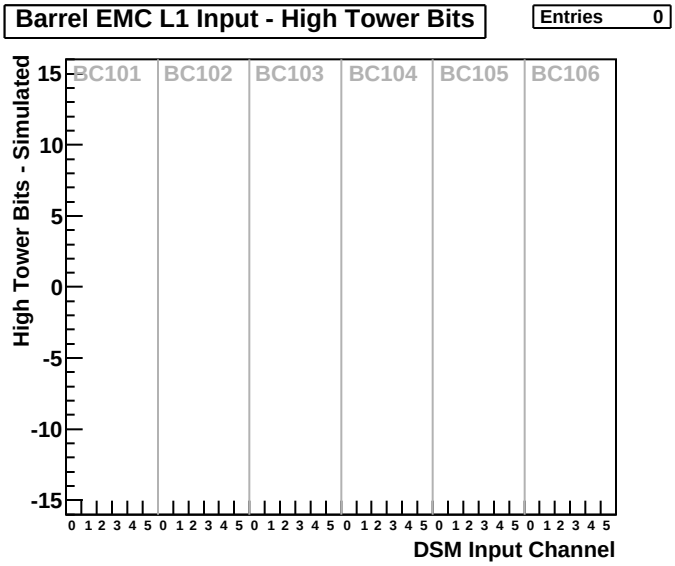

Barrel EMC L1 Input - Low Eta Sum
Entries 1.985926e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

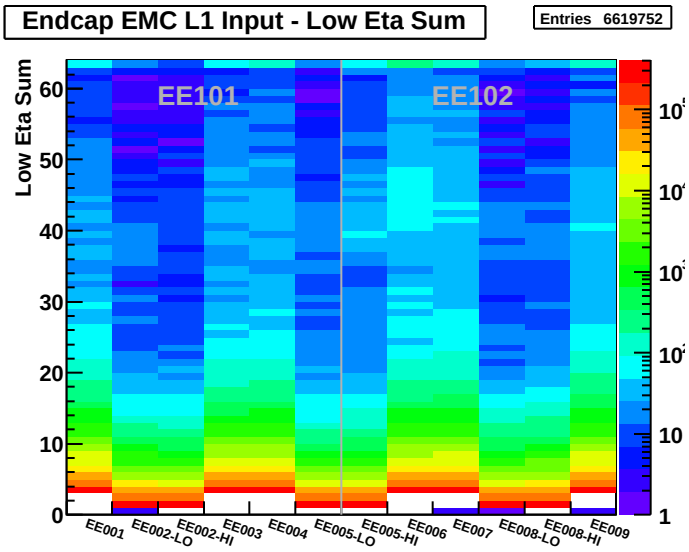

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

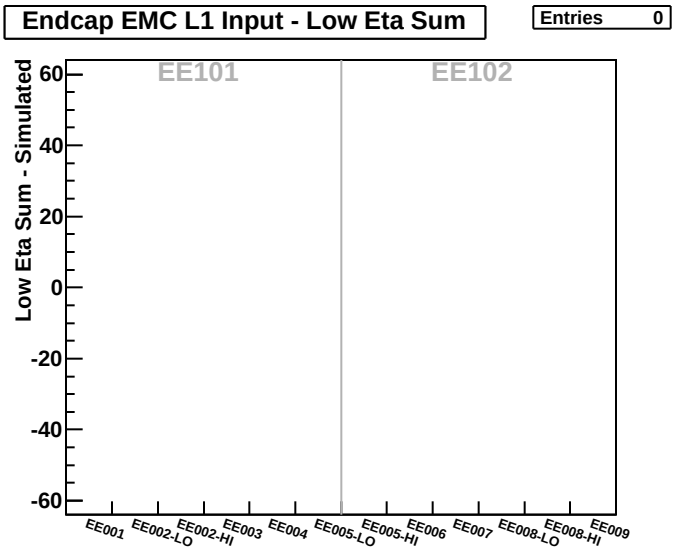

Barrel EMC L1 Input - High Eta Sum
Entries 1.985926e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

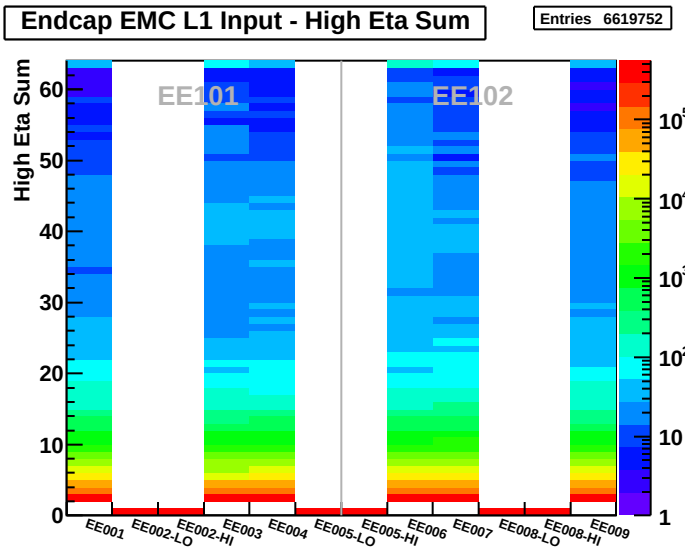

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

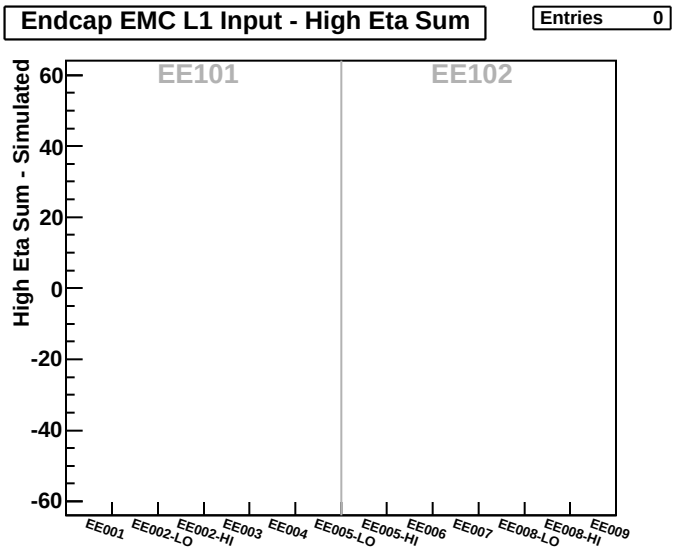

Barrel EMC L1 Input - High Tower Bits
Entries 1.985926e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

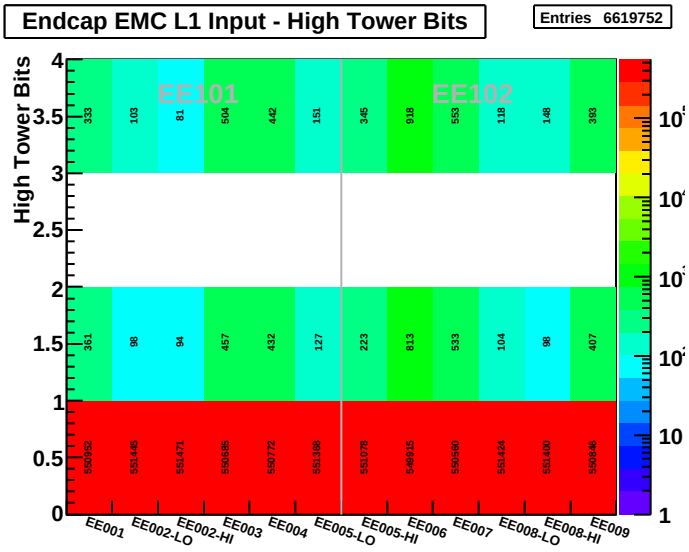

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

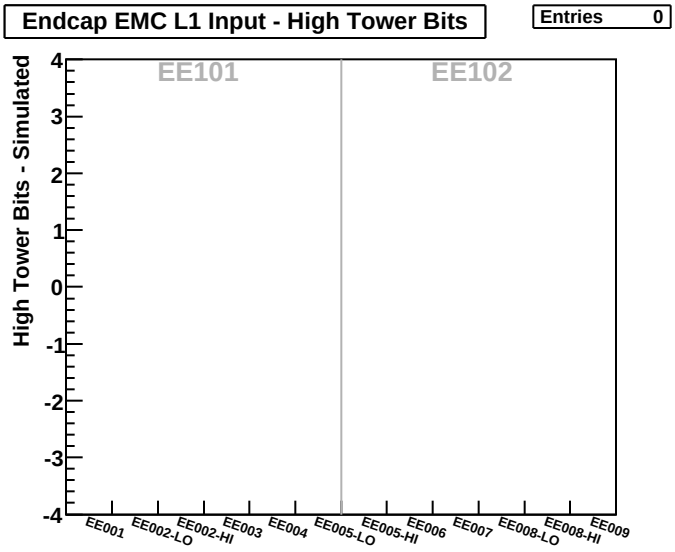

Endcap EMC L1 Input - Low Eta Sum
Entries 6619752
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

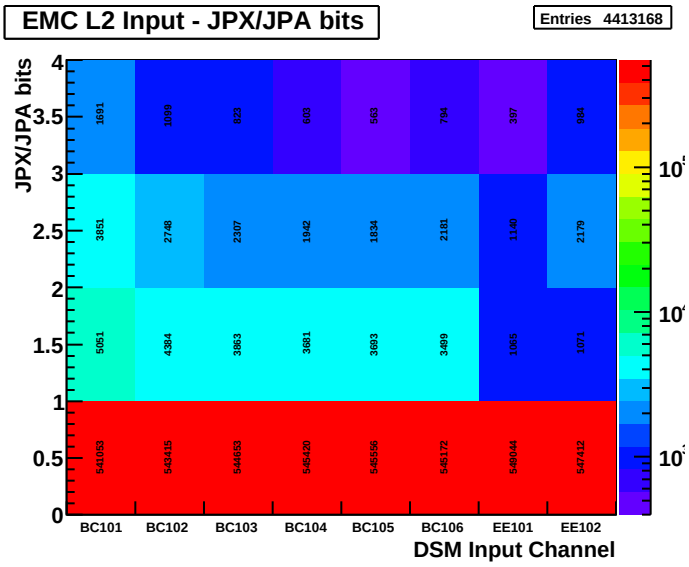

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

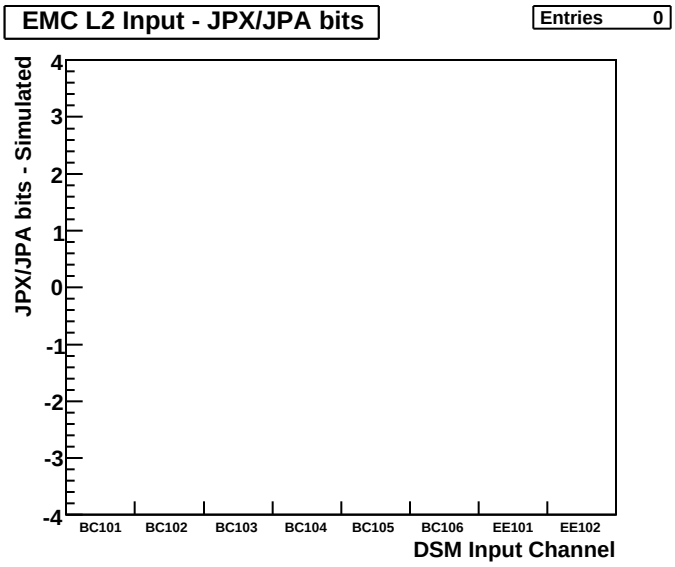

Endcap EMC L1 Input - High Eta Sum
Entries 6619752
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

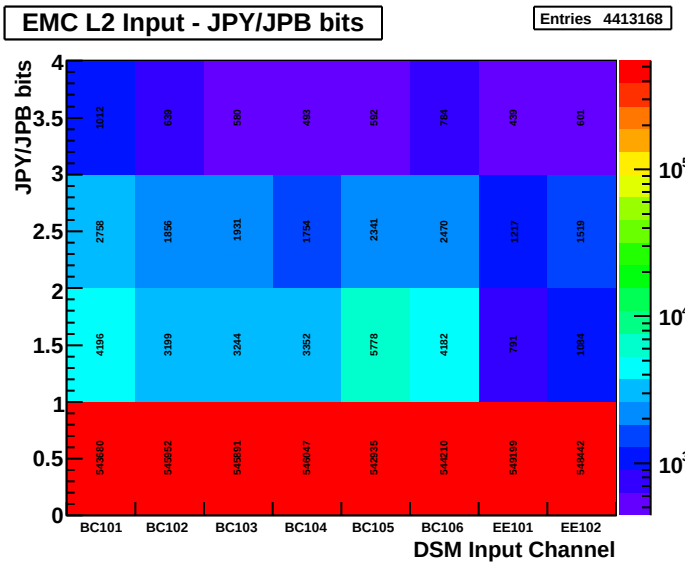

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

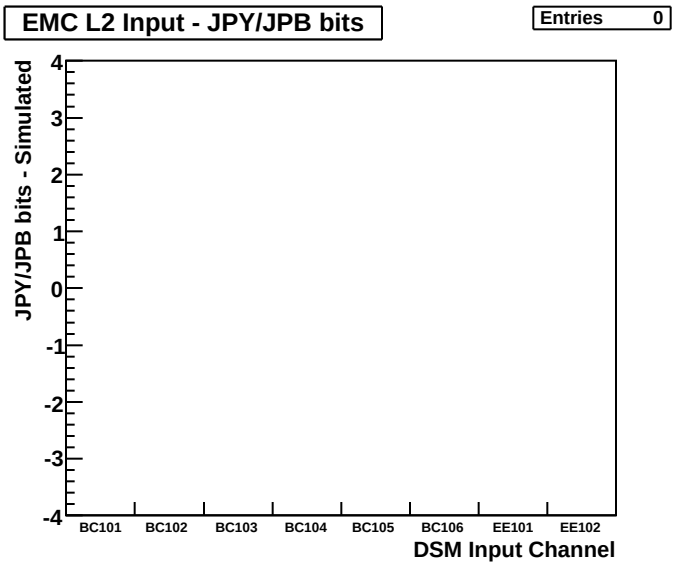

Endcap EMC L1 Input - High Tower Bits
Entries 6619752
High Tower Bits
EE101
EE102
333 103 81 504 442 151 345 918 553 118 148 393
361 98 94 457 432 127 223 813 533 104 98 407
550952 551445 551471 550685 550772 551388 551078 549915 550560 551424 551400 550945
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

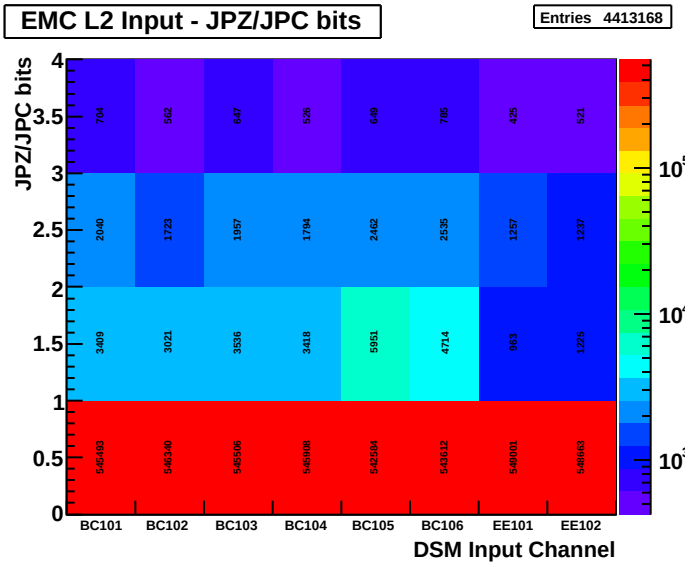

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

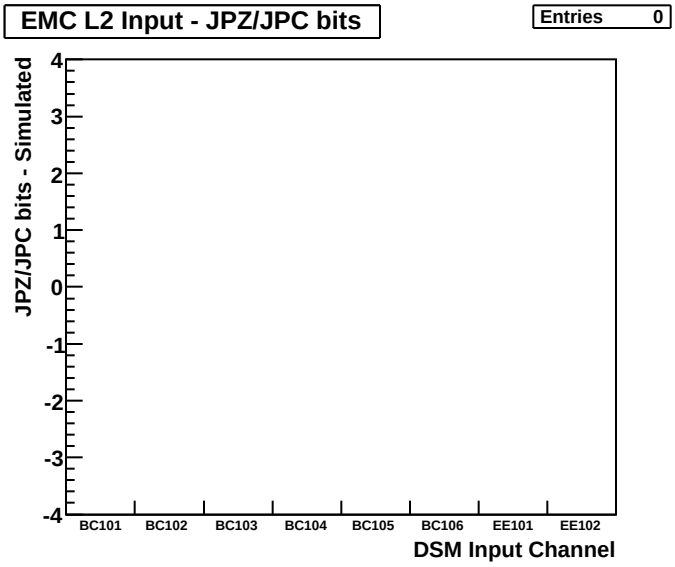

EMC L2 Input - JPX/JPA bits
Entries 4413168
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1691 1099 823 603 563 794 397 984
3851 2748 2307 1942 1834 2181 1140 2179
5051 4384 3863 3681 3693 3499 1065 1071
541053 542415 544653 545420 545556 545172 548044 547412

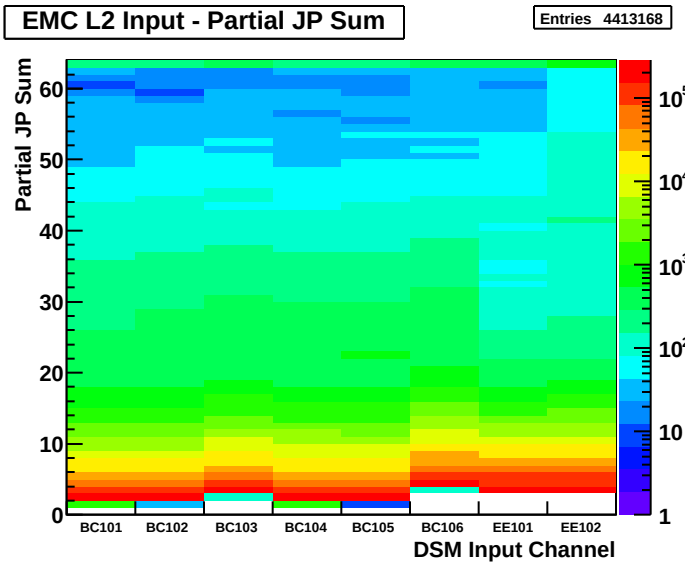

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

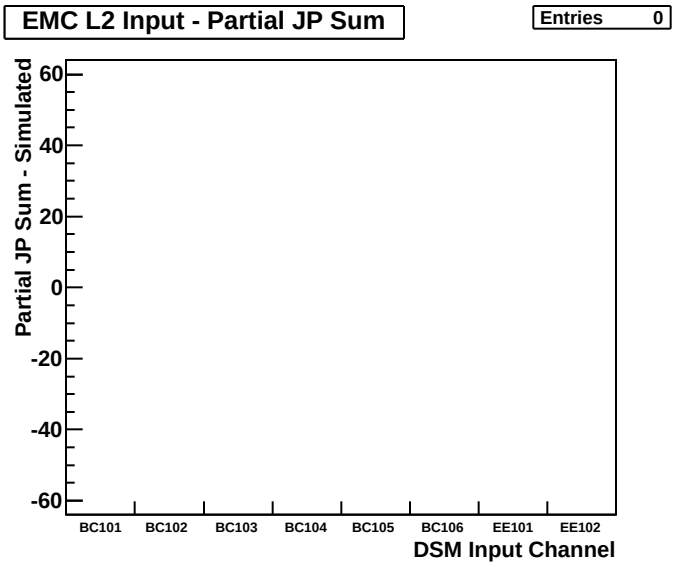

EMC L2 Input - JPY/JPB bits
Entries 4413168
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1012 639 500 493 592 784 439 601
2758 1856 1931 1754 2341 2470 1217 1519
4196 3199 3244 3352 5778 4182 791 1084
543680 543952 543891 546047 542935 544210 549199 549442

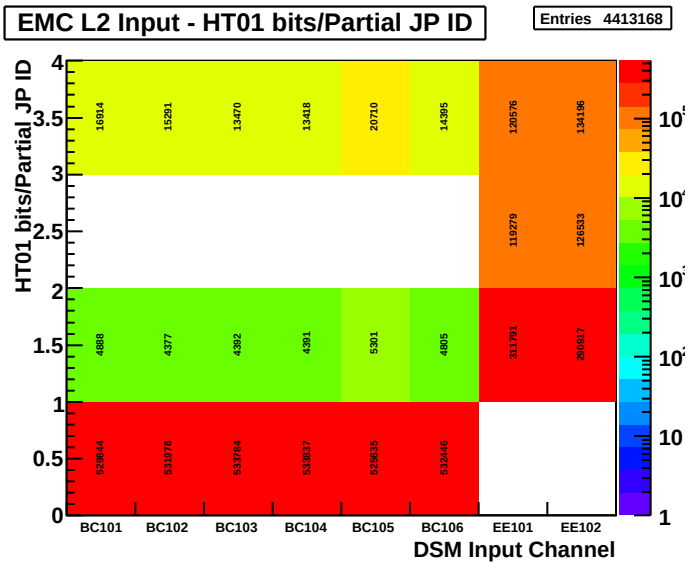

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

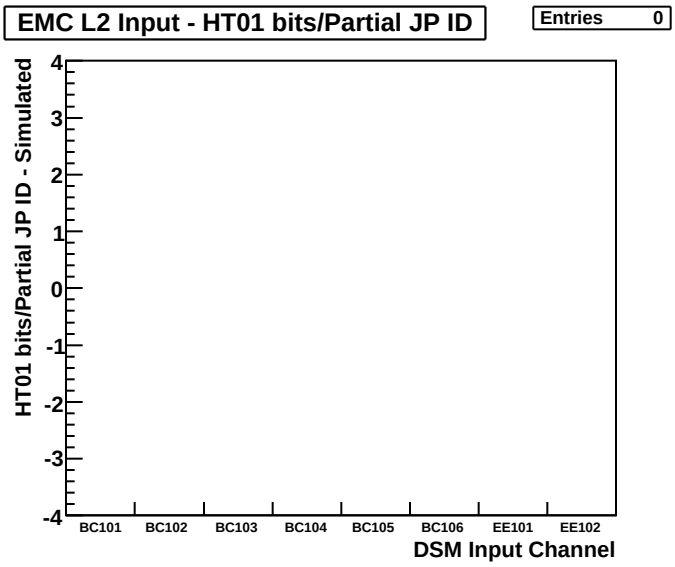

EMC L2 Input - JPZ/JPC bits
Entries 4413168
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
704 562 647 526 649 785 425 521
2040 1723 1957 1794 2462 2535 1257 1237
3409 3021 3536 3418 5951 4714 963 1225
545493 546340 545506 545908 542584 543612 549001 548663

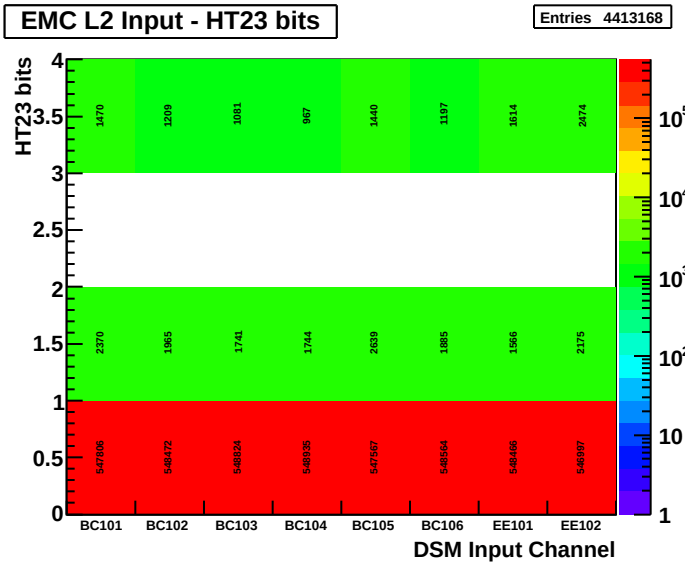

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

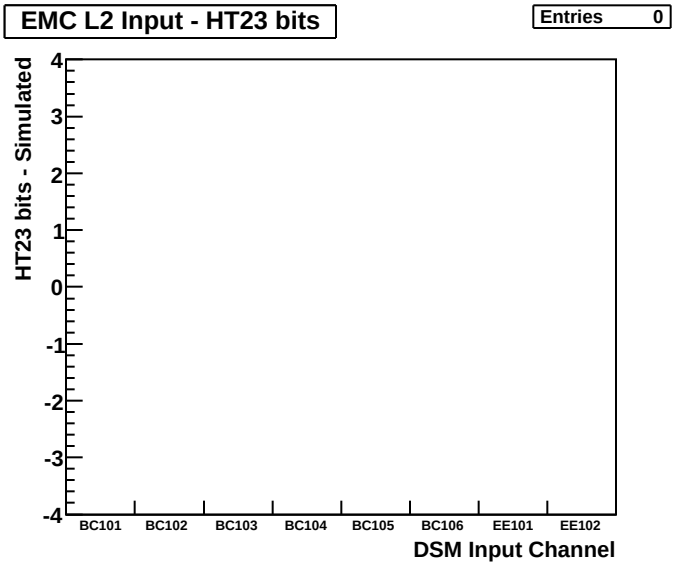

EMC L2 Input - Partial JP Sum
Entries 4413168
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

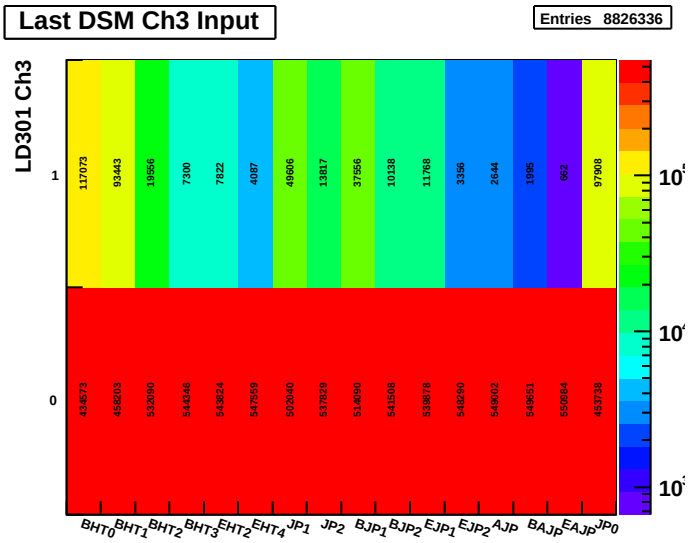

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

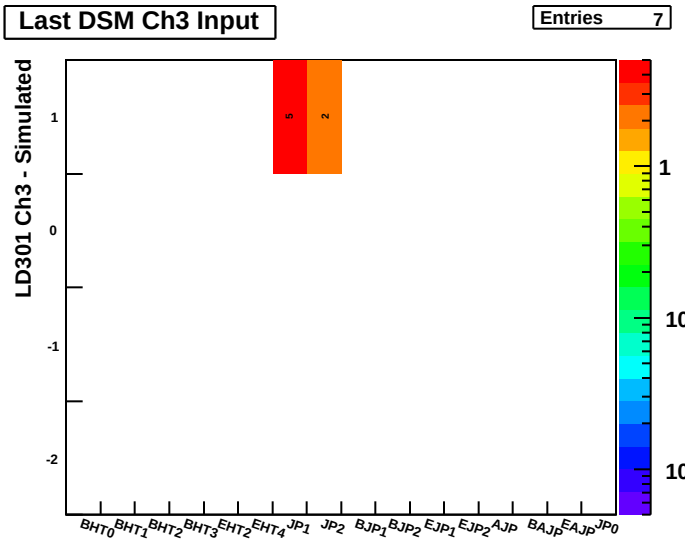

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4413168
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

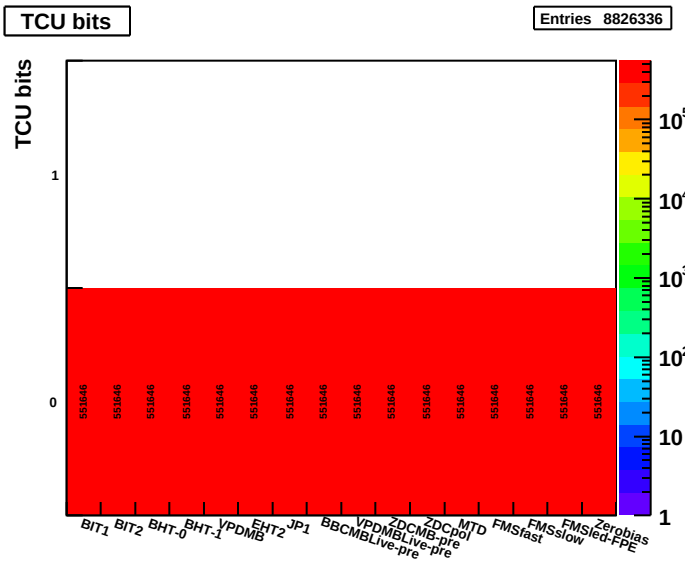

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

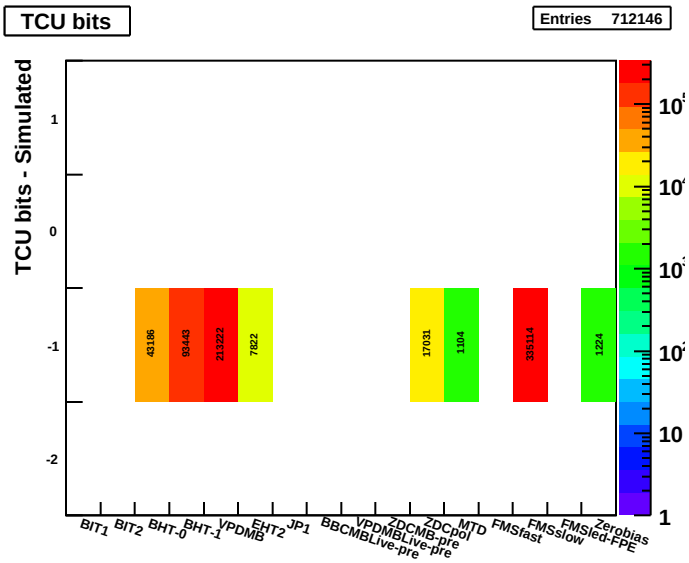

EMC L2 Input - HT23 bits
Entries 4413168
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

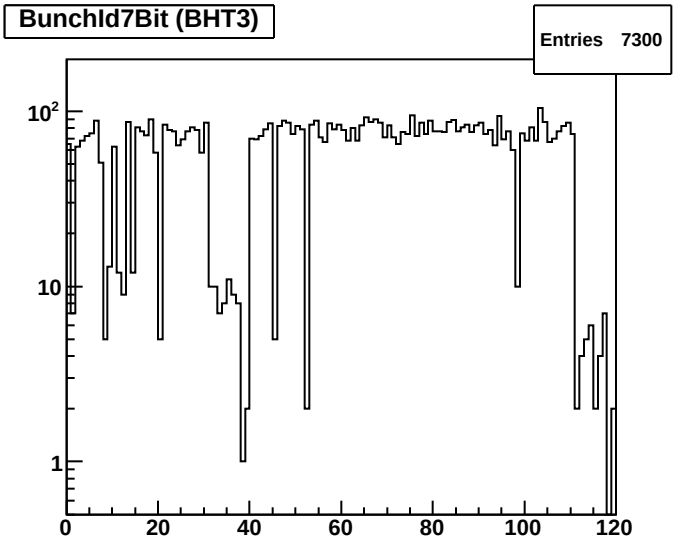

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

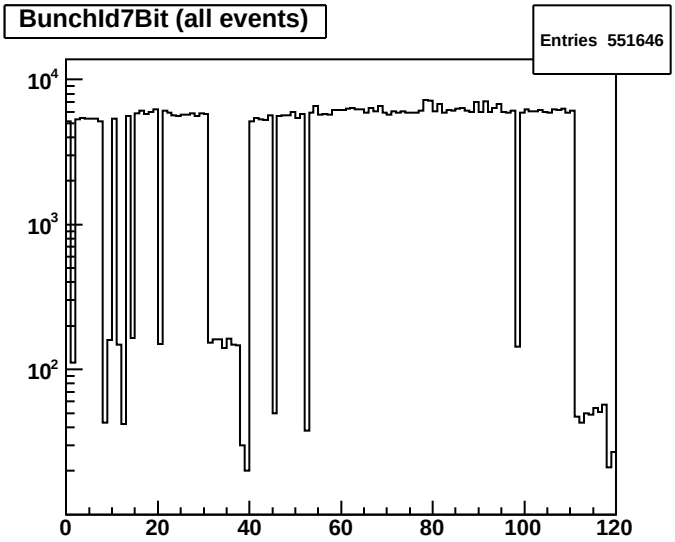

Last DSM Ch3 Input
Entries 8826336
LD301 Ch3
117073 93443 19556 7300 7822 4087 49606 13817 37556 10138 11768 3356 2644 1995 662 97908
434573 458203 532090 544346 543824 547559 502040 537629 514090 541508 539878 548290 549002 549651 550984 453738
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

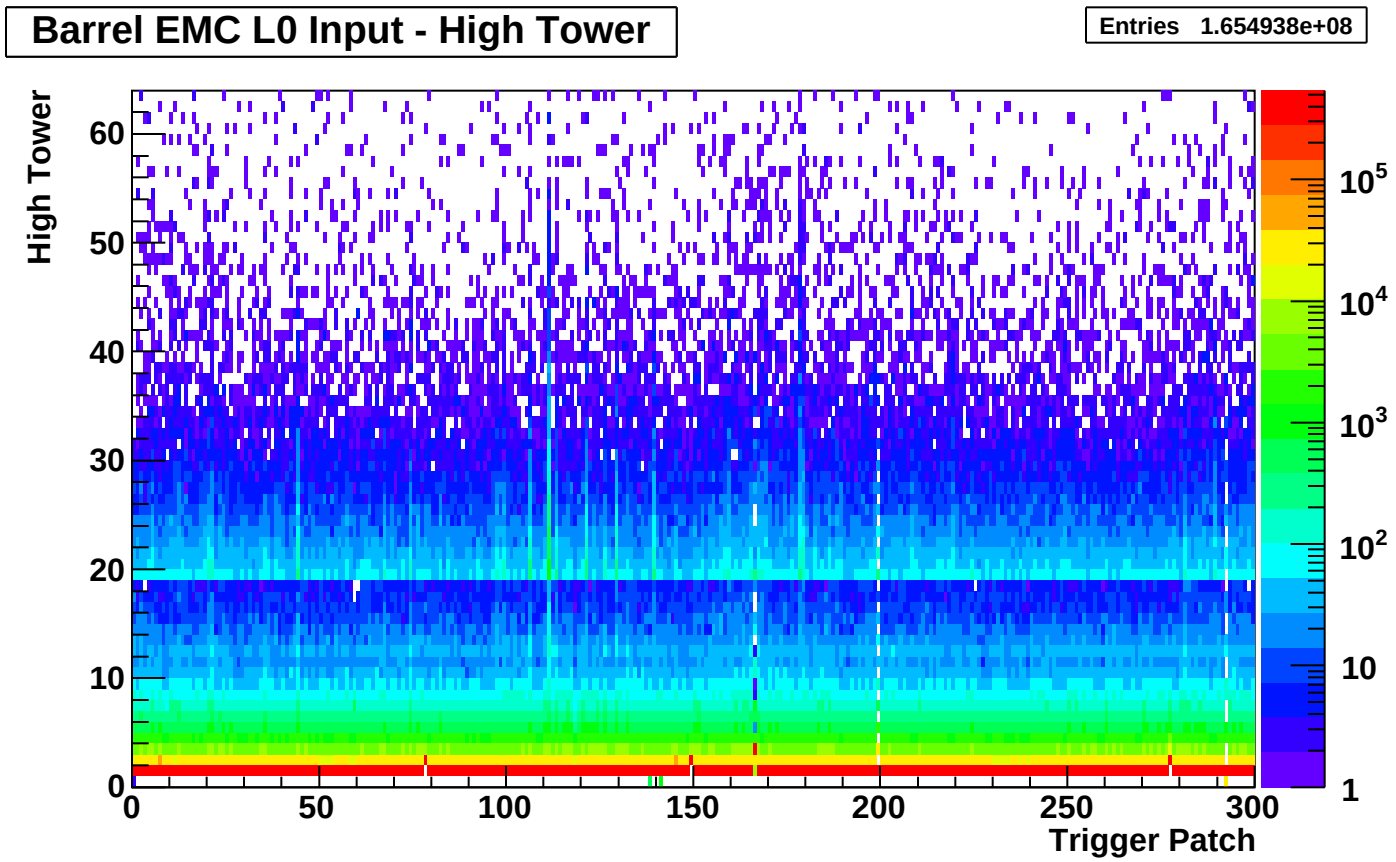

Last DSM Ch3 Input
Entries 7
LD301 Ch3 - Simulated
5 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

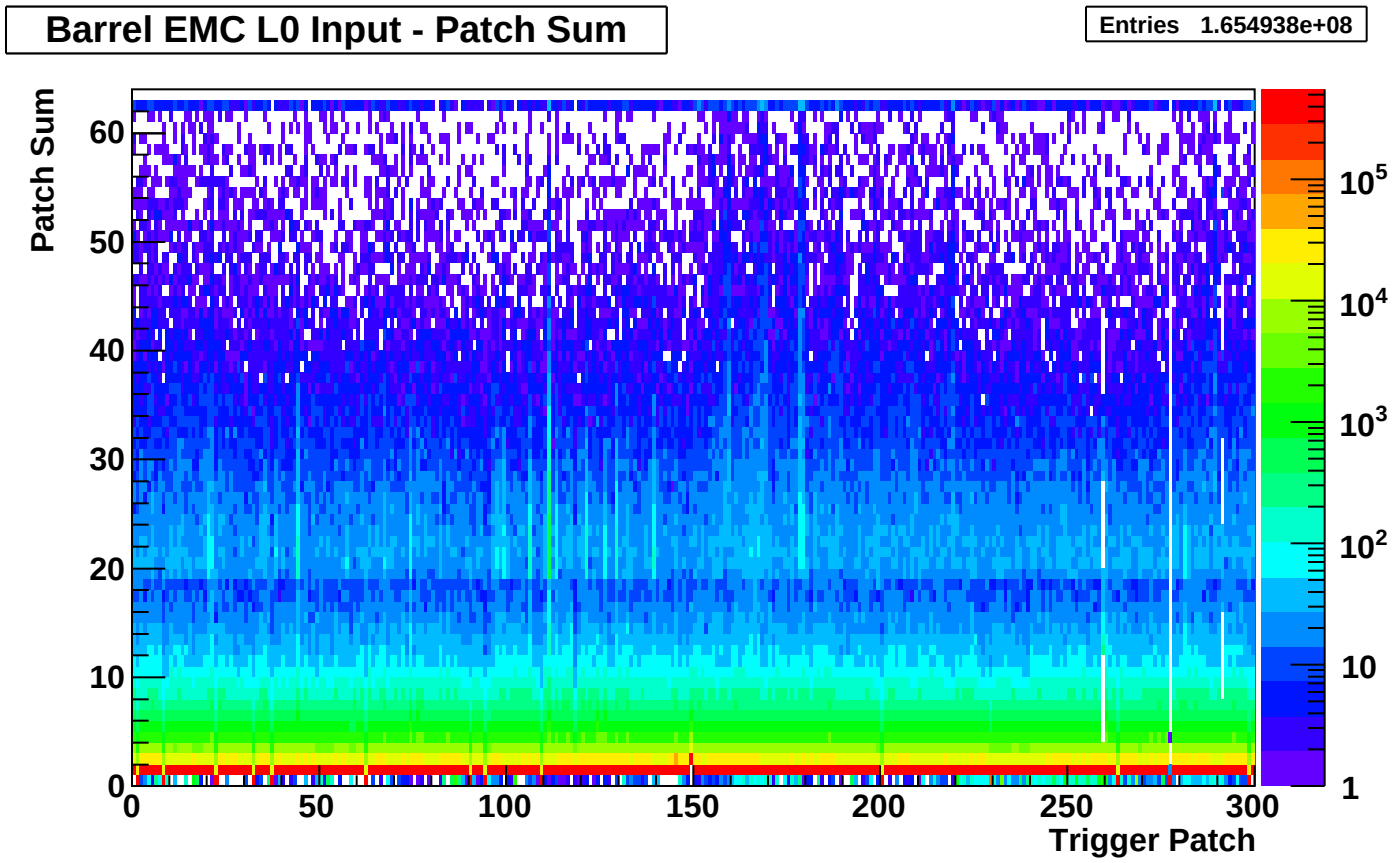

TCU bits
Entries 8826336
TCU bits
551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646 551646
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

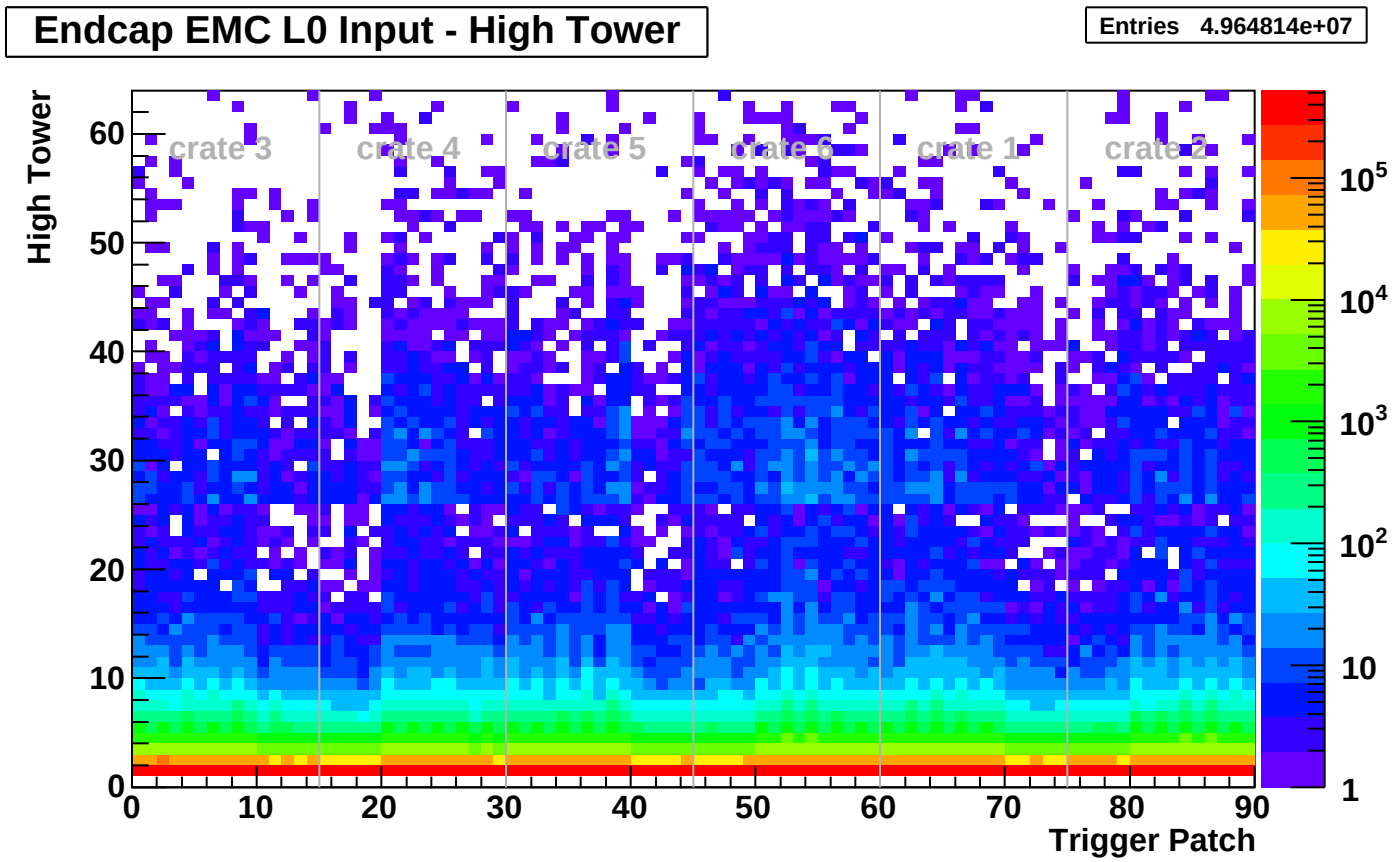

TCU bits
Entries 712146
TCU bits - Simulated
43186 93443 213222 7822 17031 1104 335114 1224
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

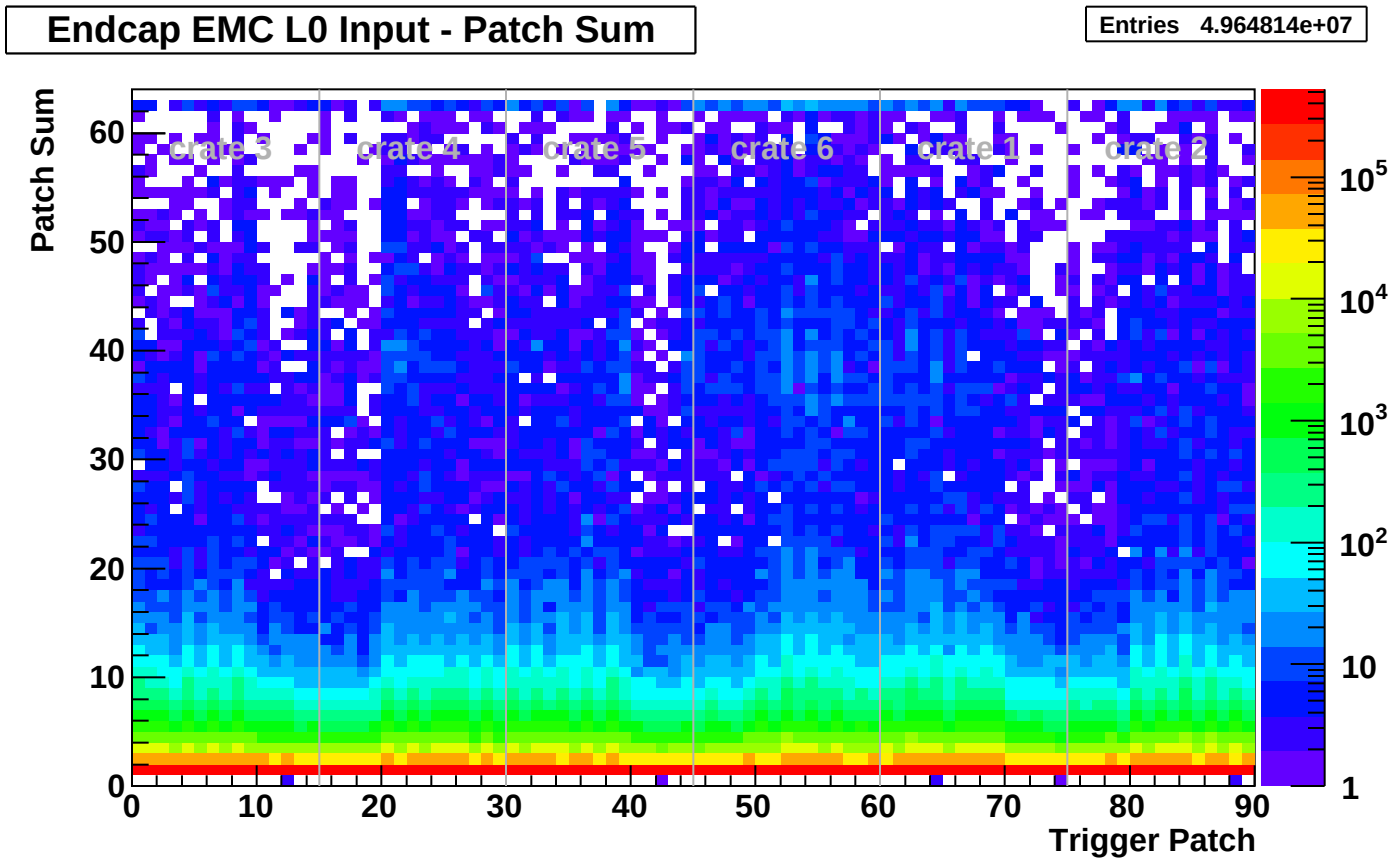

BunchId7Bit (BHT3)
Entries 7300

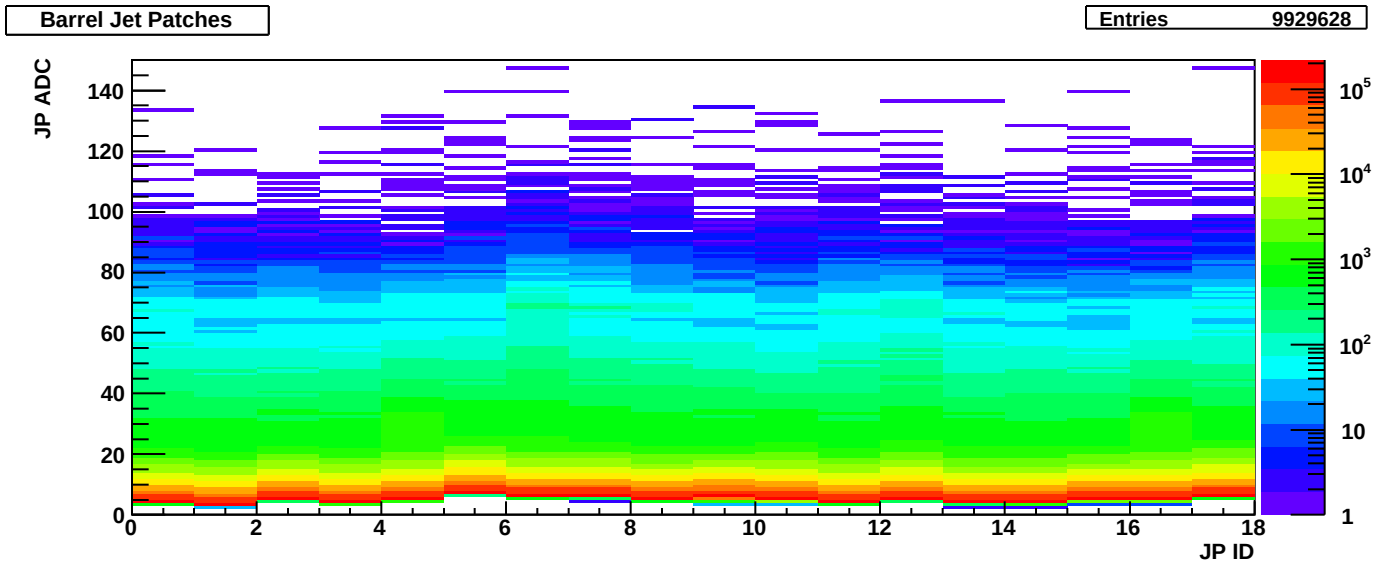

BunchId7Bit (all events)
Entries 551646

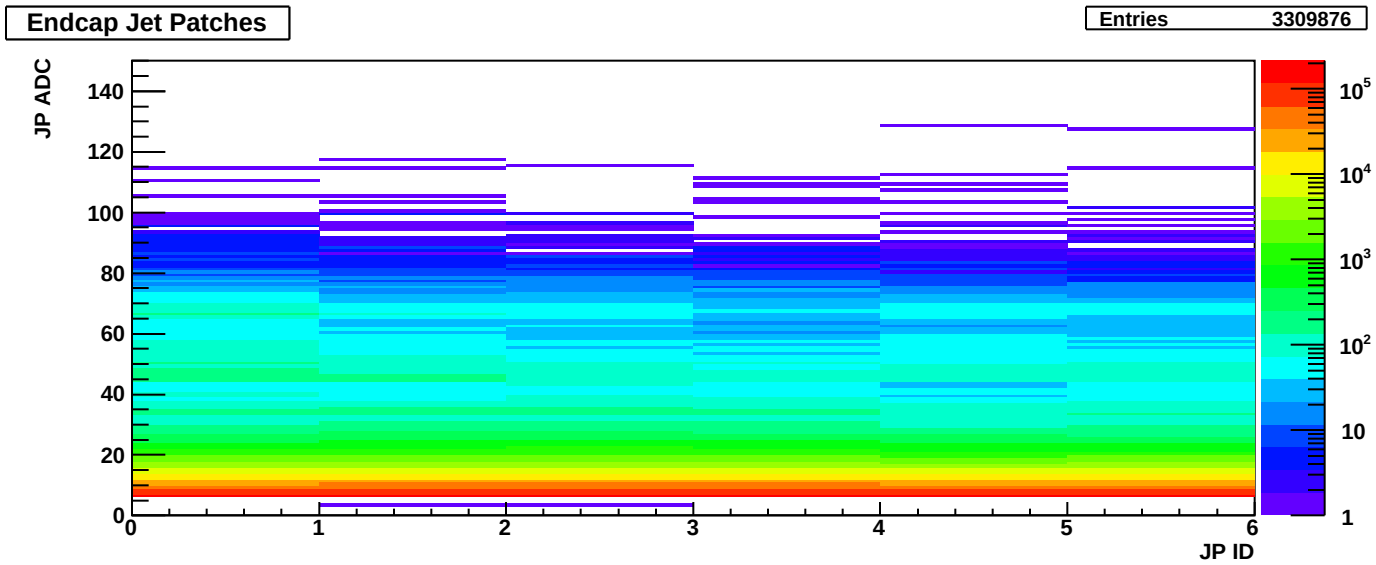

Barrel EMC L0 Input - High Tower
Entries 1.654938e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

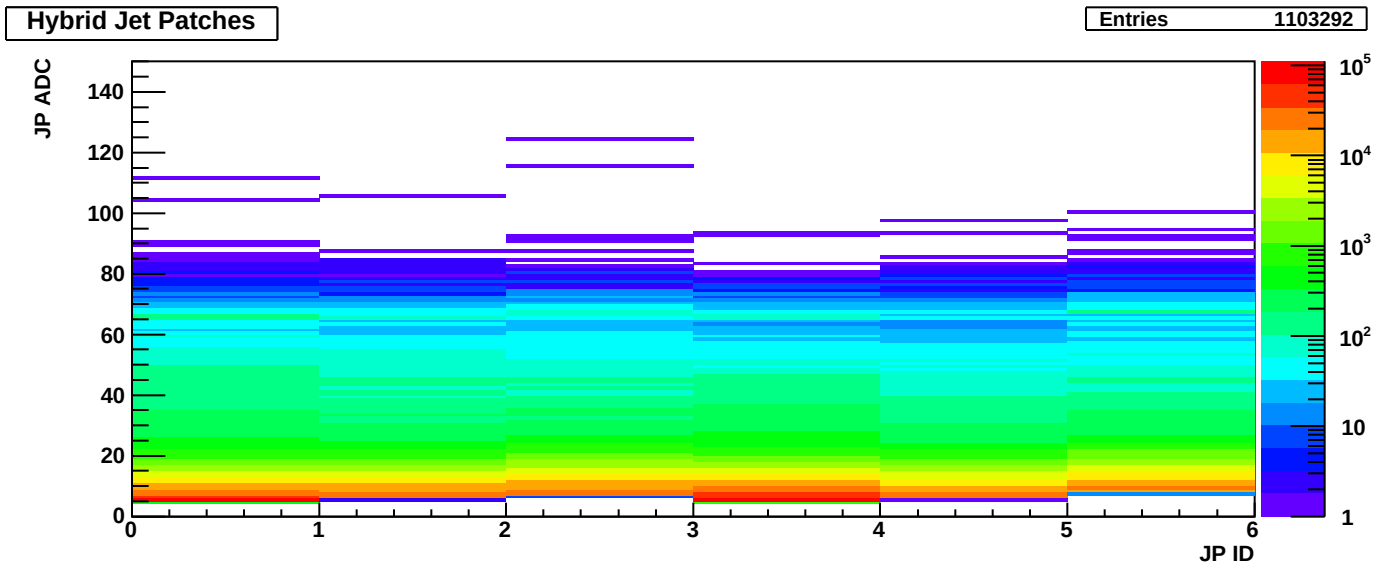

Barrel EMC L0 Input - Patch Sum
Entries 1.654938e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

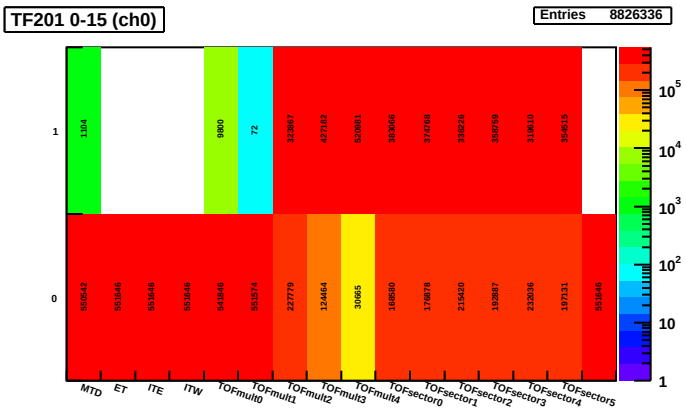

Endcap EMC L0 Input - High Tower
Entries 4.964814e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

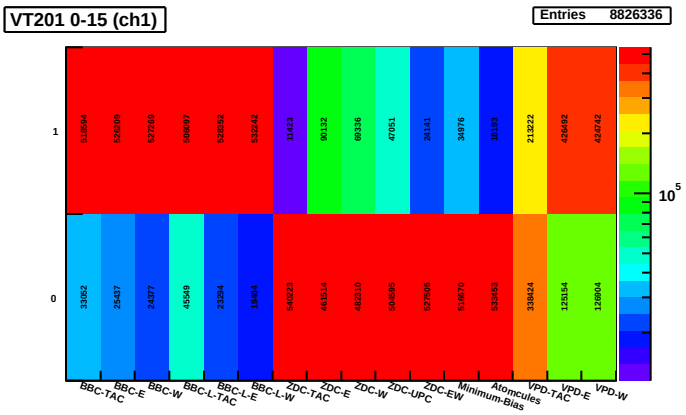

Endcap EMC L0 Input - Patch Sum
Entries 4.964814e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

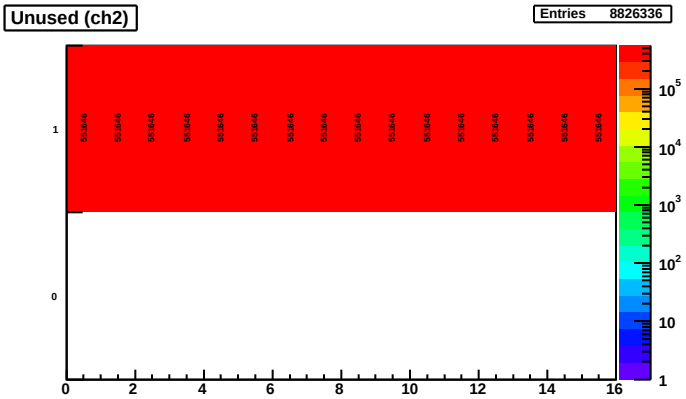

Barrel Jet Patches
Entries 9929628
JP ADC
JP ID

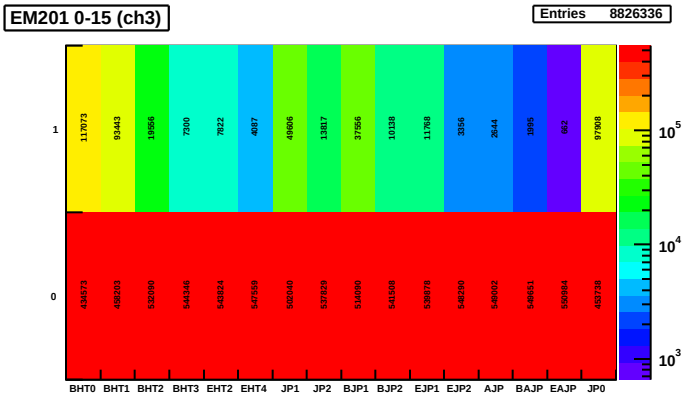

Endcap Jet Patches
Entries 3309876
JP ADC
JP ID

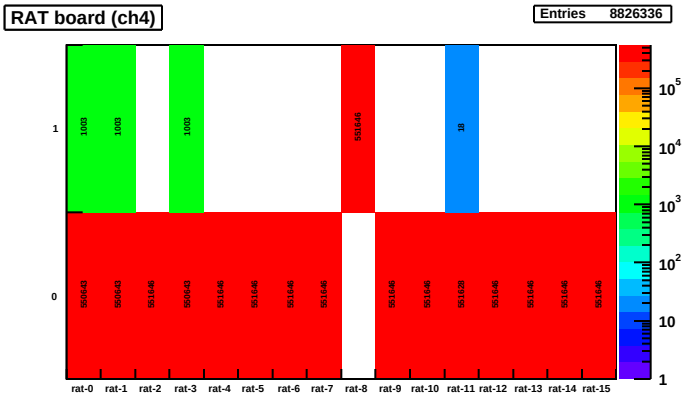

Hybrid Jet Patches
Entries 1103292
JP ADC
JP ID

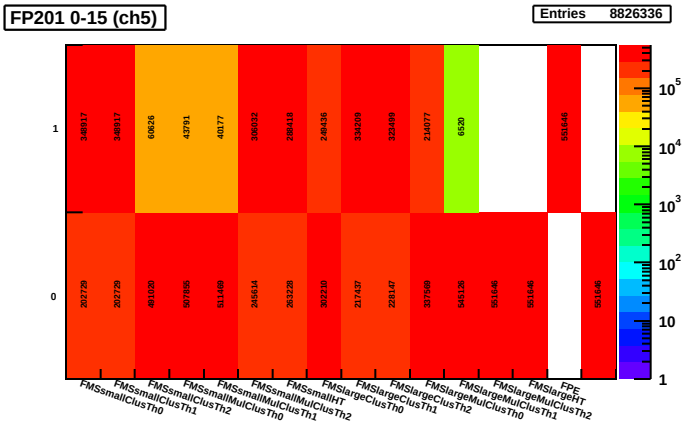

TF201 0-15 (ch0)
Entries 8826336
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

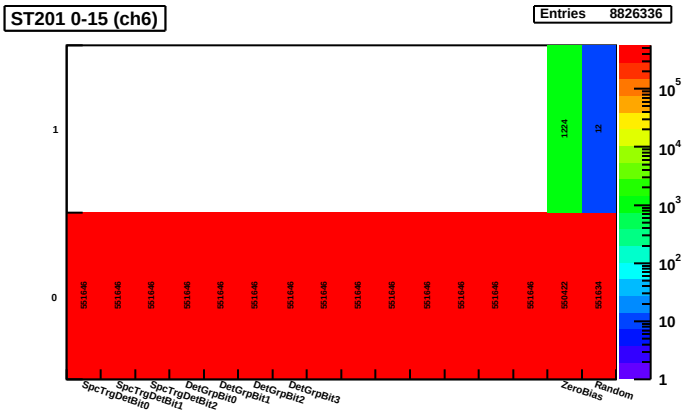

VT201 0-15 (ch1)
Entries 8826336
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

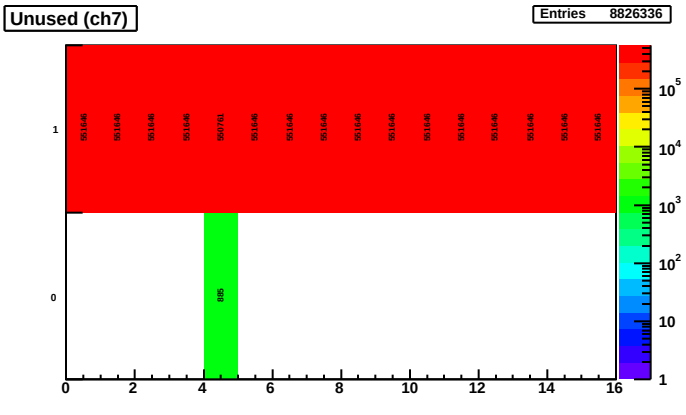

Unused (ch2)
Entries 8826336

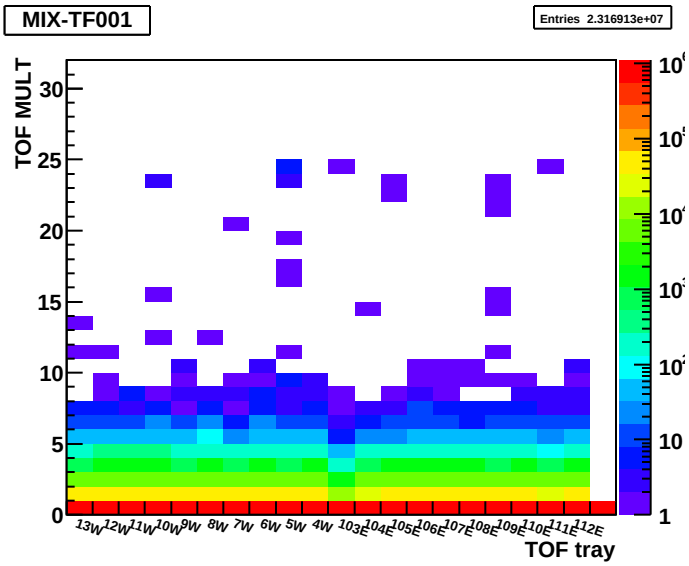

EM201 0-15 (ch3)
Entries 8826336
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

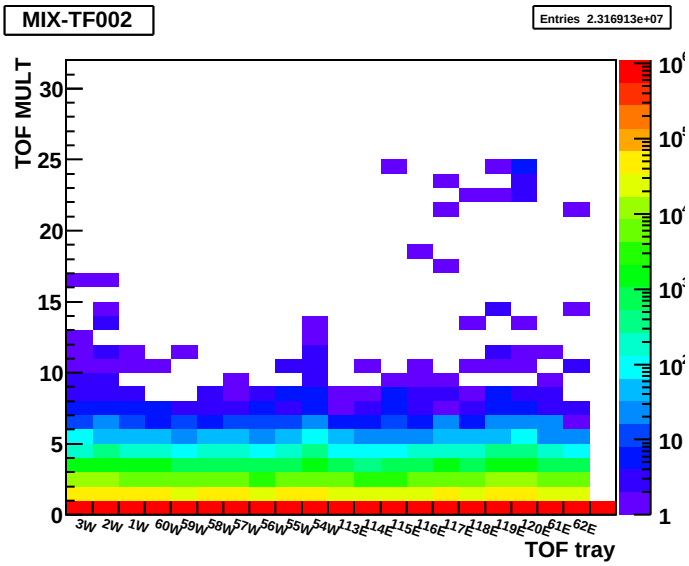

RAT board (ch4)
Entries 8826336
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

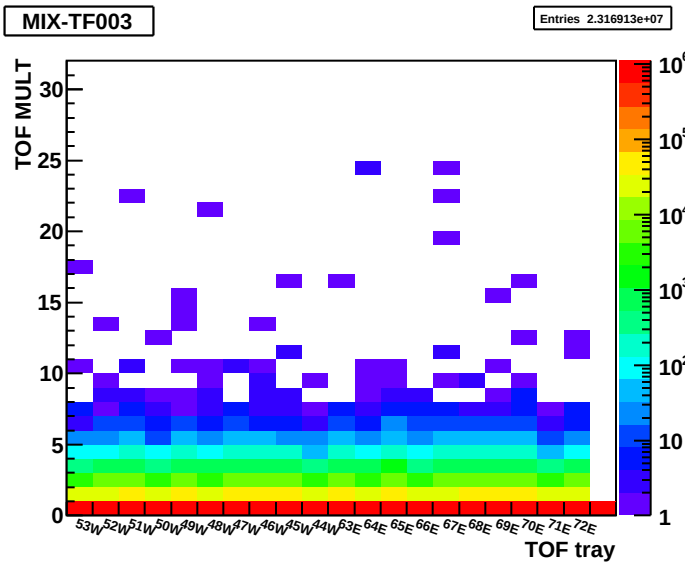

FP201 0-15 (ch5)
Entries 8826336
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

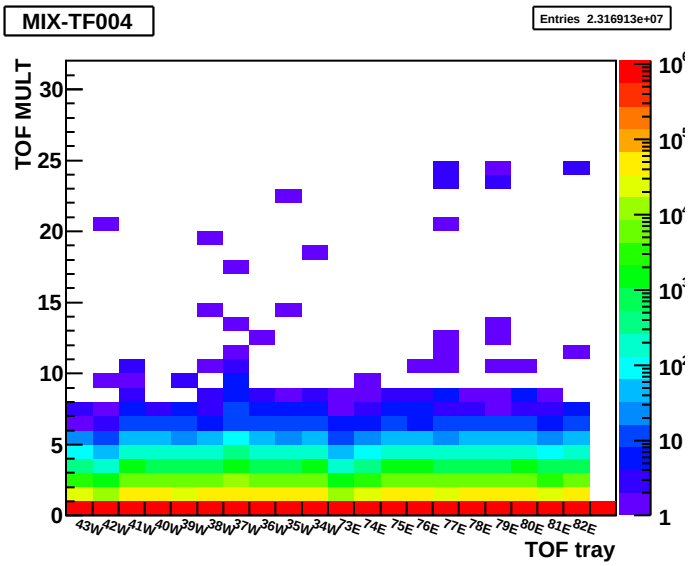

ST201 0-15 (ch6)
Entries 8826336
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

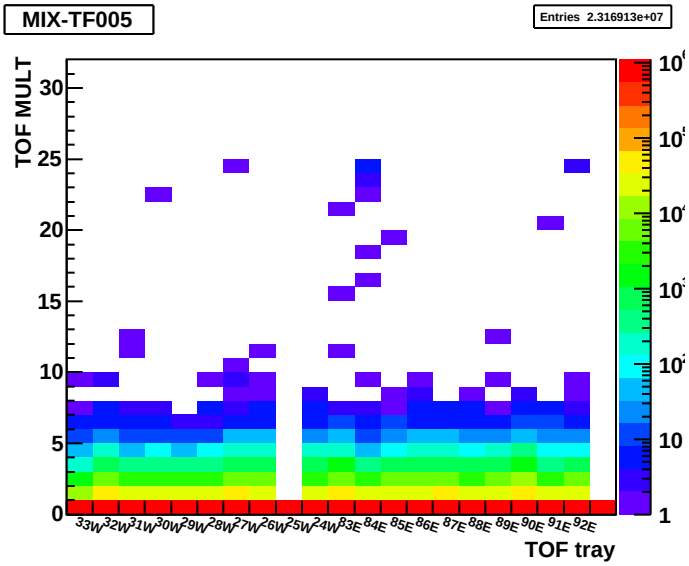

Unused (ch7)
Entries 8826336

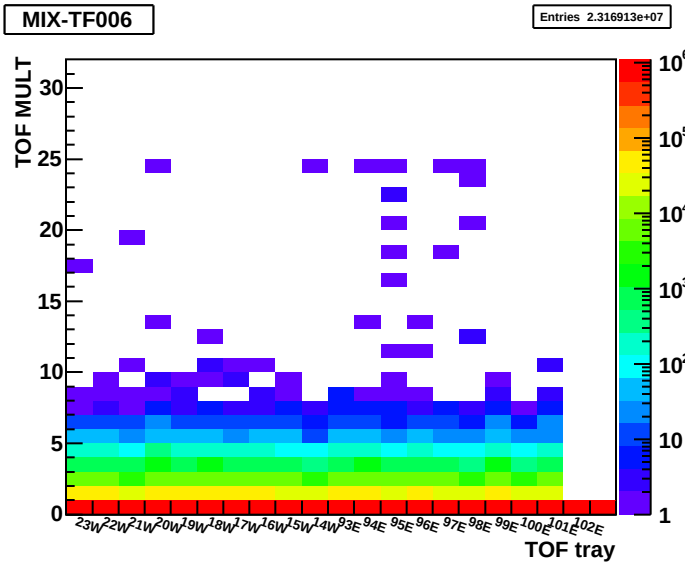

MIX-TF001
Entries 2.316913e+07
TOF MULT
TOF tray

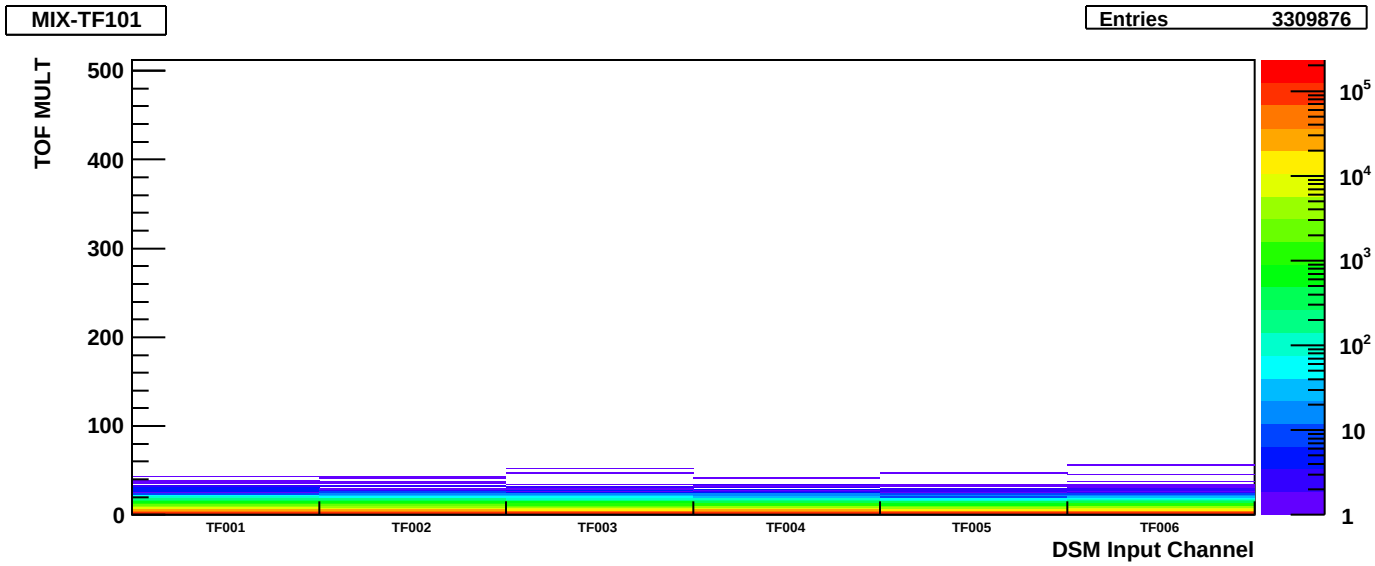

MIX-TF002
Entries 2.316913e+07
TOF MULT
TOF tray

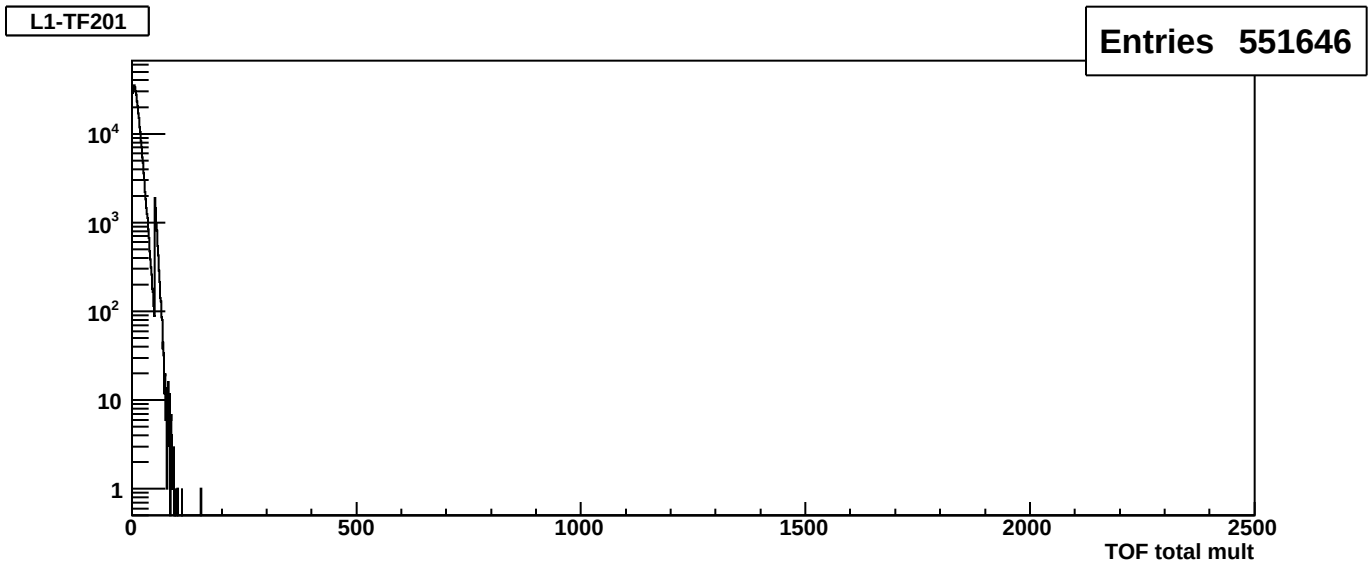

MIX-TF003
Entries 2.316913e+07
TOF MULT
TOF tray

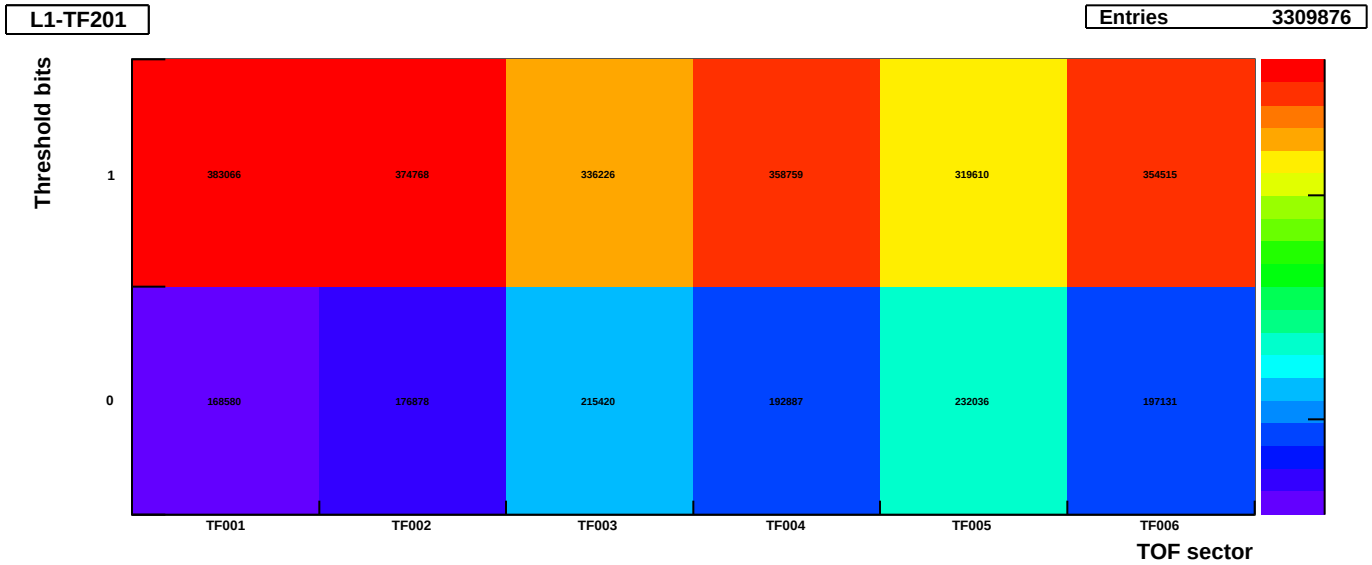

MIX-TF004
Entries 2.316913e+07
TOF MULT
TOF tray

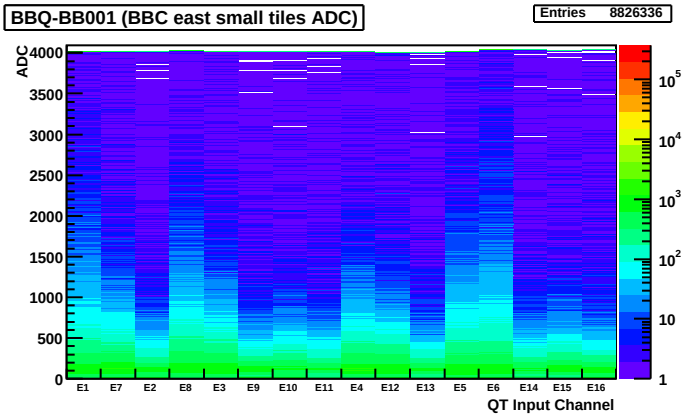

MIX-TF005
Entries 2.316913e+07
TOF MULT
TOF tray

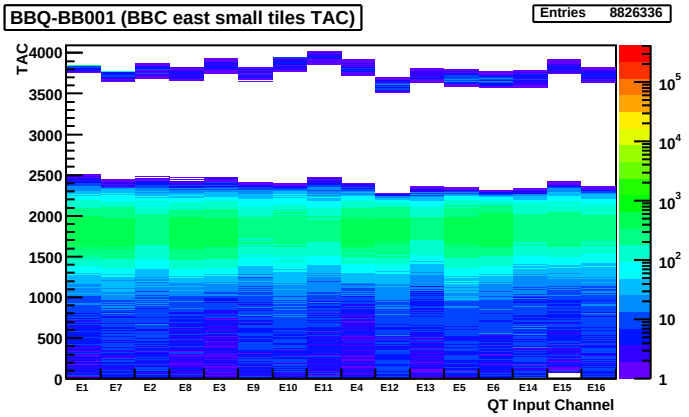

MIX-TF006
Entries 2.316913e+07
TOF MULT
TOF tray

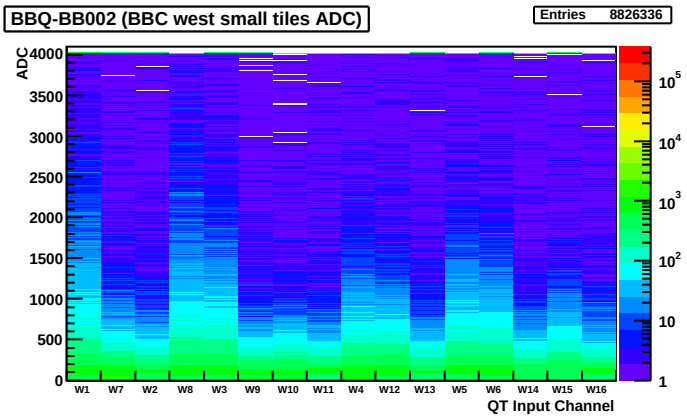

MIX-TF101
Entries 3309876
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

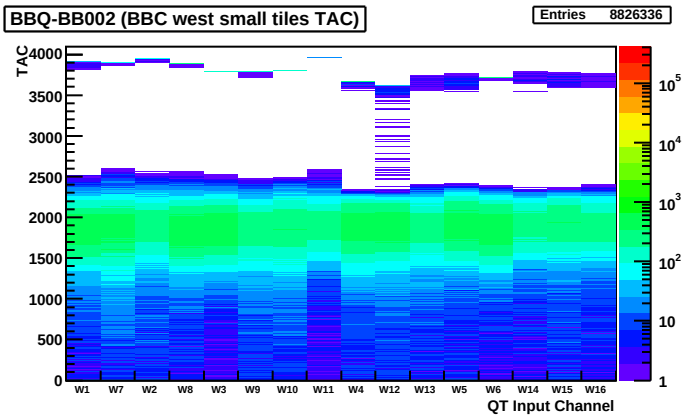

L1-TF201
Entries 551646
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

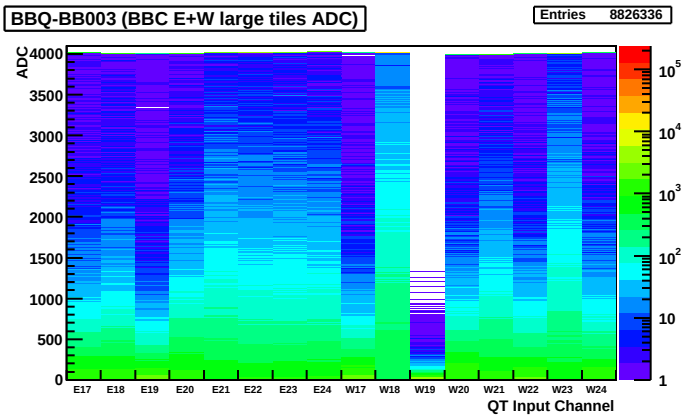

L1-TF201
Entries 3309876
Threshold bits
1
0
383066
374768
336226
358759
319610
354515
168580
176878
215420
192887
232036
197131
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

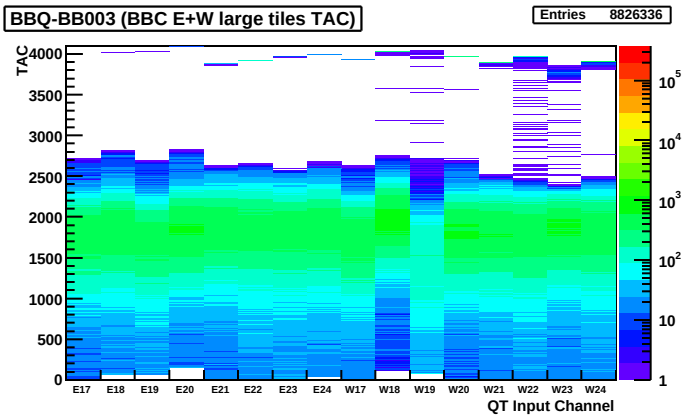

BBQ-BB001 (BBC east small tiles ADC)
Entries 8826336
ADC
QT Input Channel

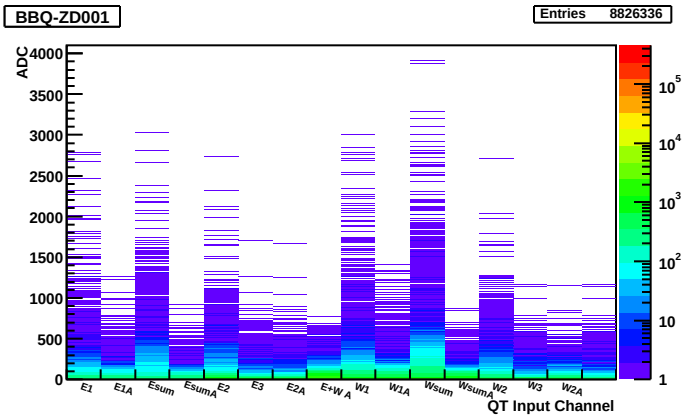

BBQ-BB001 (BBC east small tiles TAC)
Entries 8826336
TAC
QT Input Channel

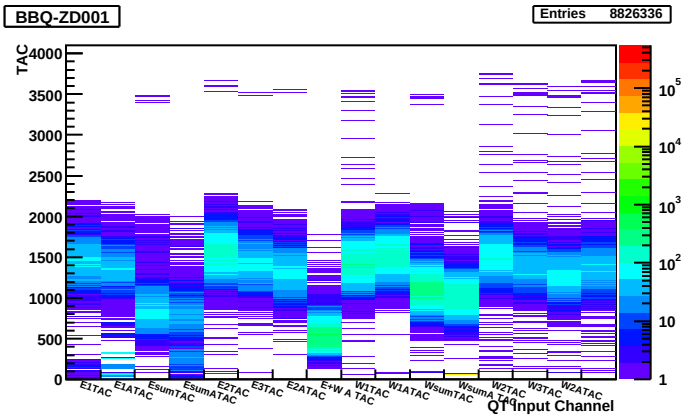

BBQ-BB002 (BBC west small tiles ADC)
Entries 8826336
ADC
QT Input Channel

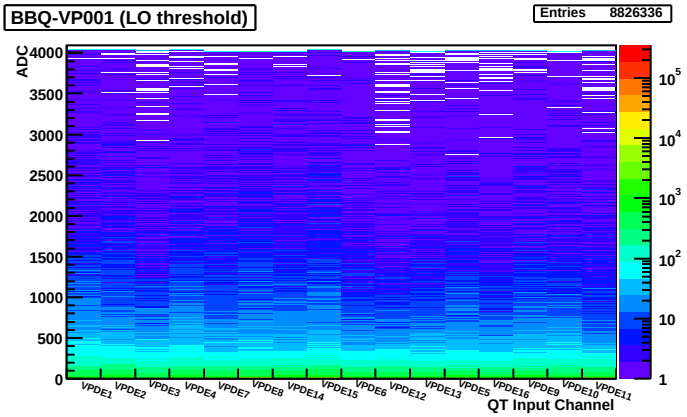

BBQ-BB002 (BBC west small tiles TAC)
Entries 8826336
TAC
QT Input Channel

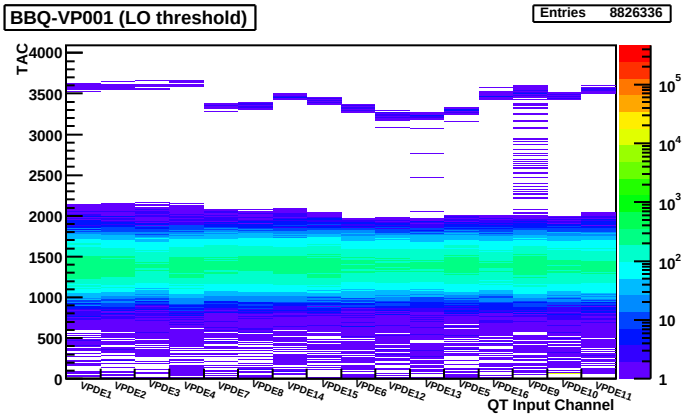

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8826336
ADC
QT Input Channel

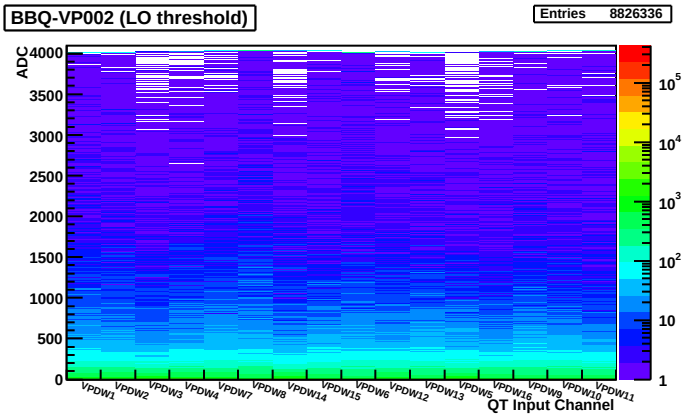

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8826336
TAC
QT Input Channel

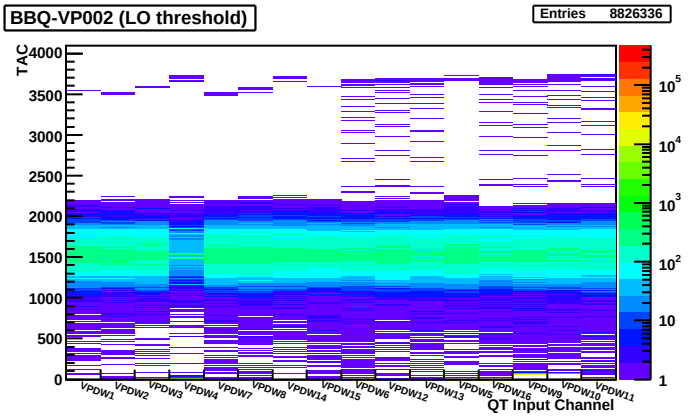

BBQ-ZD001
Entries 8826336
ADC
QT Input Channel

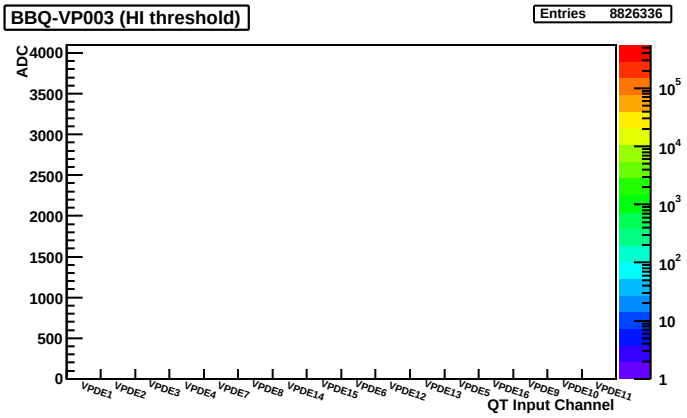

BBQ-ZD001
Entries 8826336
TAC
QT Input Channel

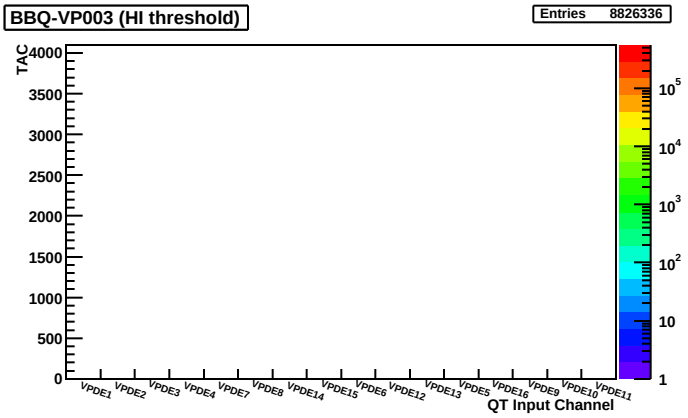

BBQ-VP001 (LO threshold)
Entries 8826336
ADC
QT Input Channel

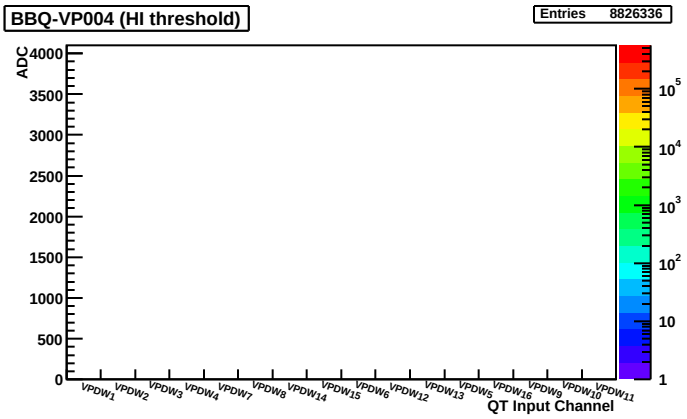

BBQ-VP001 (LO threshold)
Entries 8826336
TAC
QT Input Channel

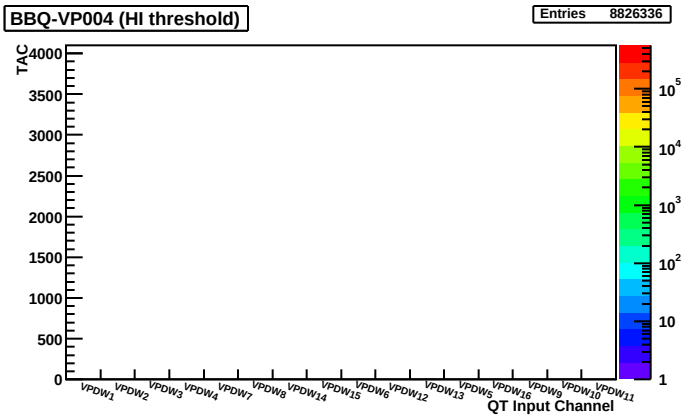

BBQ-VP002 (LO threshold)
Entries 8826336
ADC
QT Input Channel

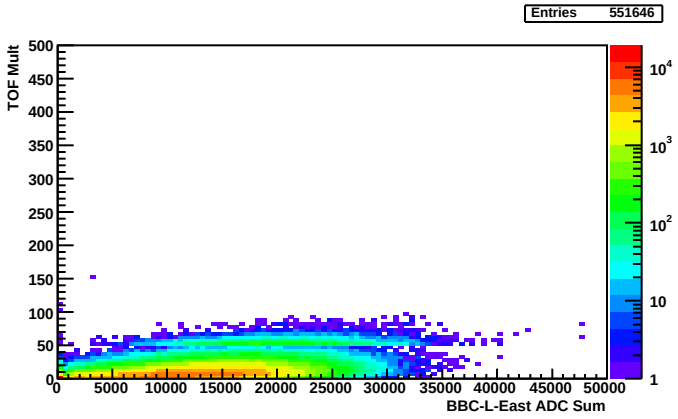

BBQ-VP002 (LO threshold)
Entries 8826336
TAC
QT Input Channel

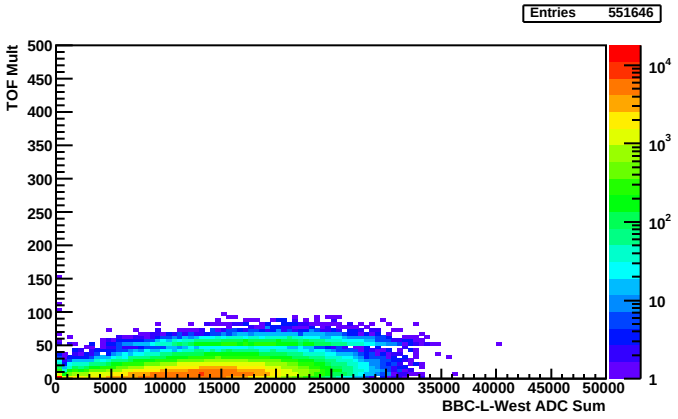

BBQ-VP003 (HI threshold)
Entries 8826336
ADC
QT Input Channel

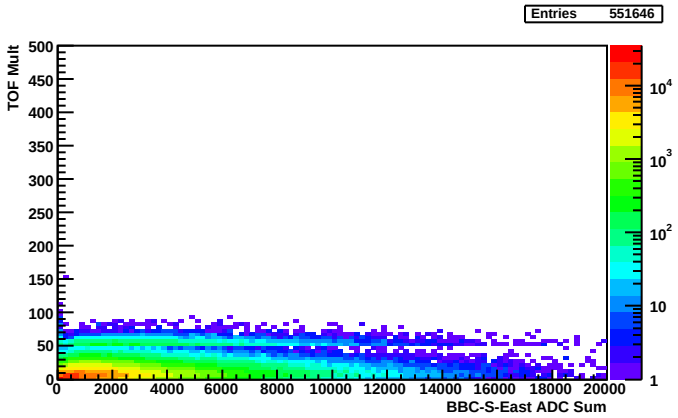

BBQ-VP003 (HI threshold)
Entries 8826336
TAC
QT Input Channel

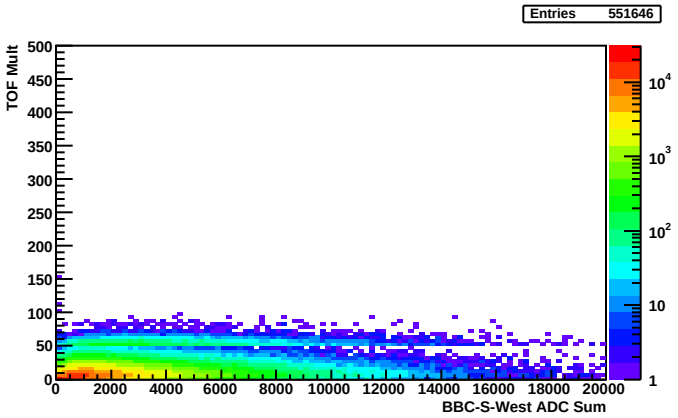

BBQ-VP004 (HI threshold)
Entries 8826336
ADC
QT Input Channel

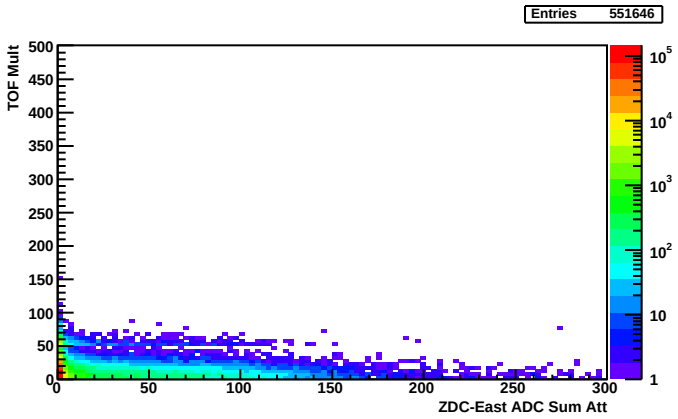

BBQ-VP004 (HI threshold)
Entries 8826336
TAC
QT Input Channel

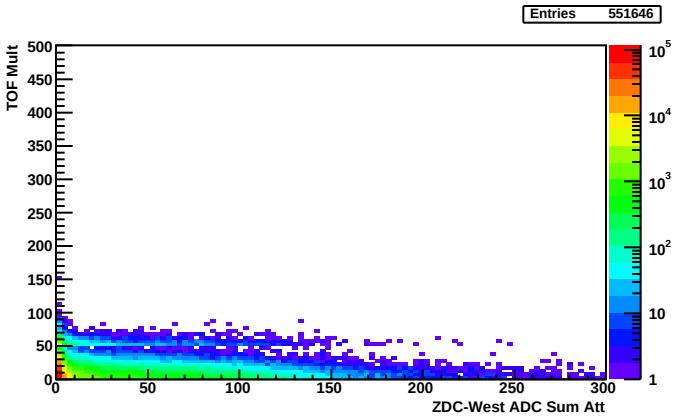

Entries 551646
TOF Mult
BBC-L-East ADC Sum

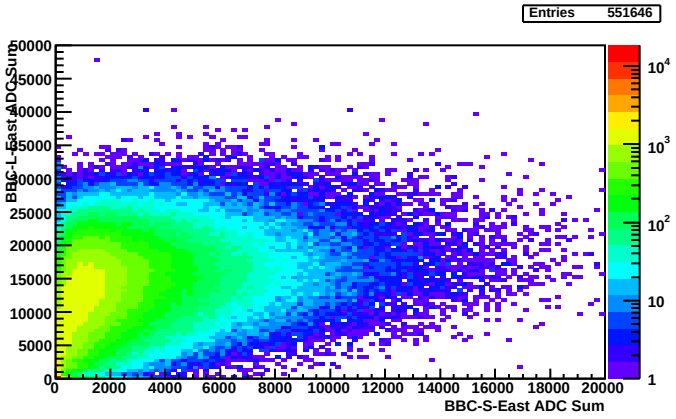

Entries 551646
TOF Mult
BBC-L-West ADC Sum

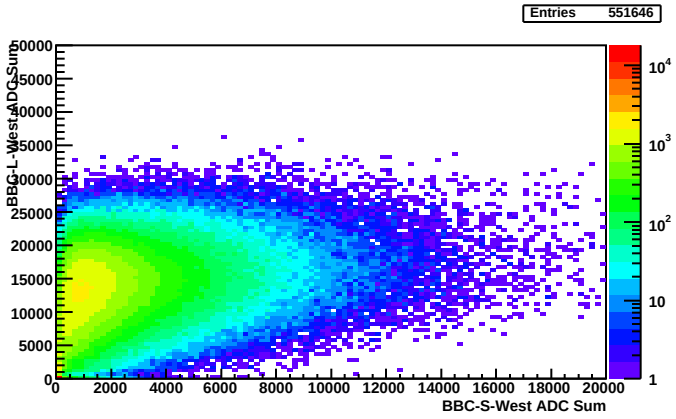

Entries 551646
TOF Mult
BBC-S-East ADC Sum

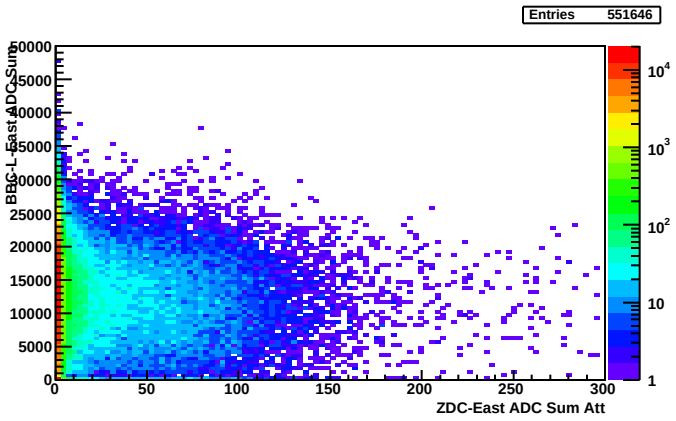

Entries 551646
TOF Mult
BBC-S-West ADC Sum

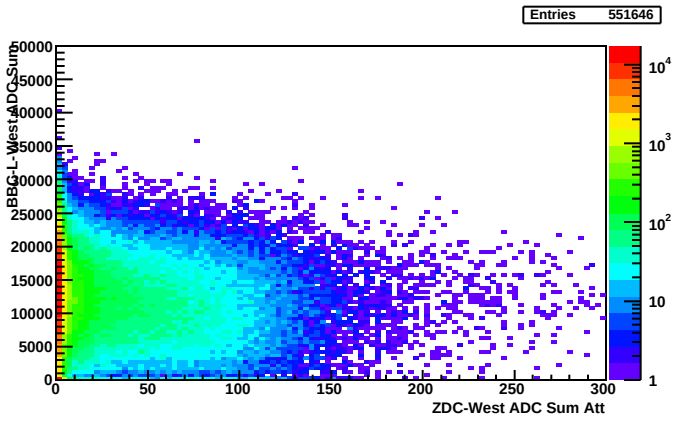

Entries 551646
TOF Mult
ZDC-East ADC Sum Att

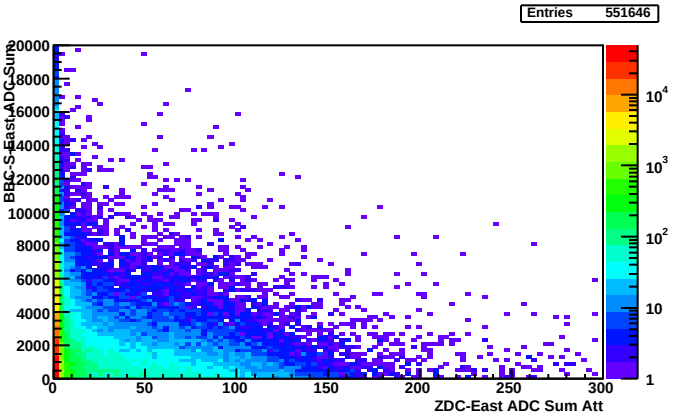

Entries 551646
TOF Mult
ZDC-West ADC Sum Att

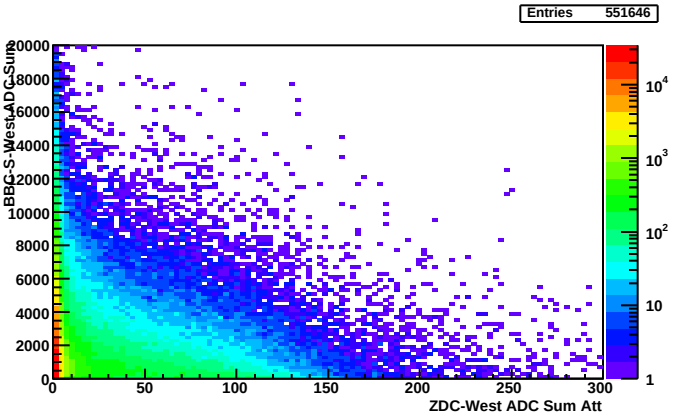

Entries 551646
BBC-L-East ADC Sum
BBC-S-East ADC Sum

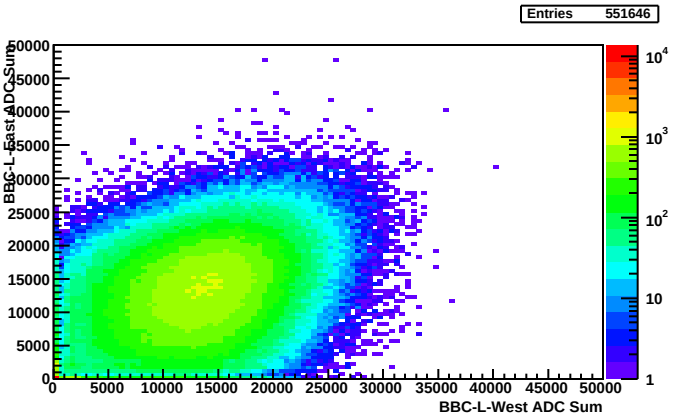

Entries 551646
BBC-L-West ADC Sum
BBC-S-West ADC Sum

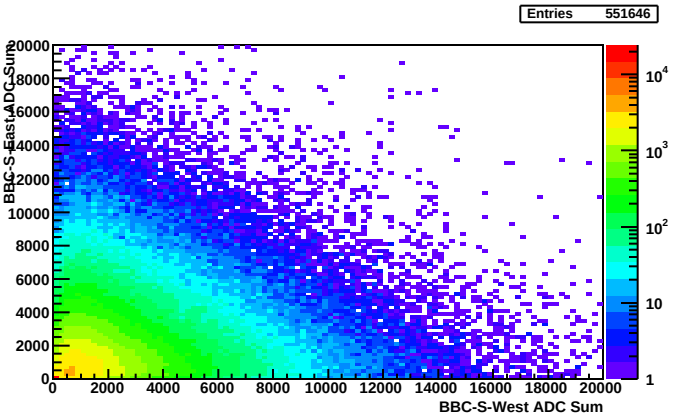

Entries 551646
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

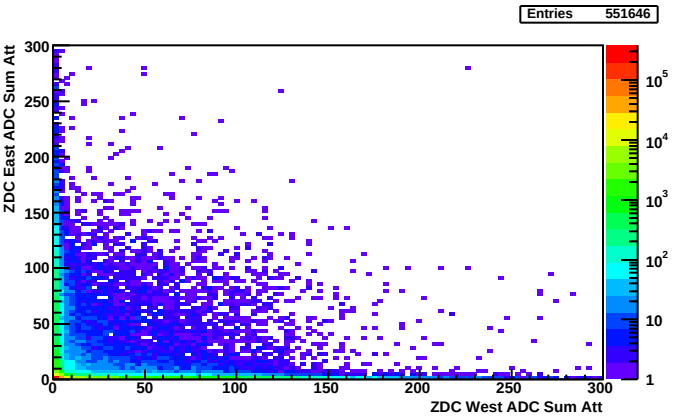

Entries 551646
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

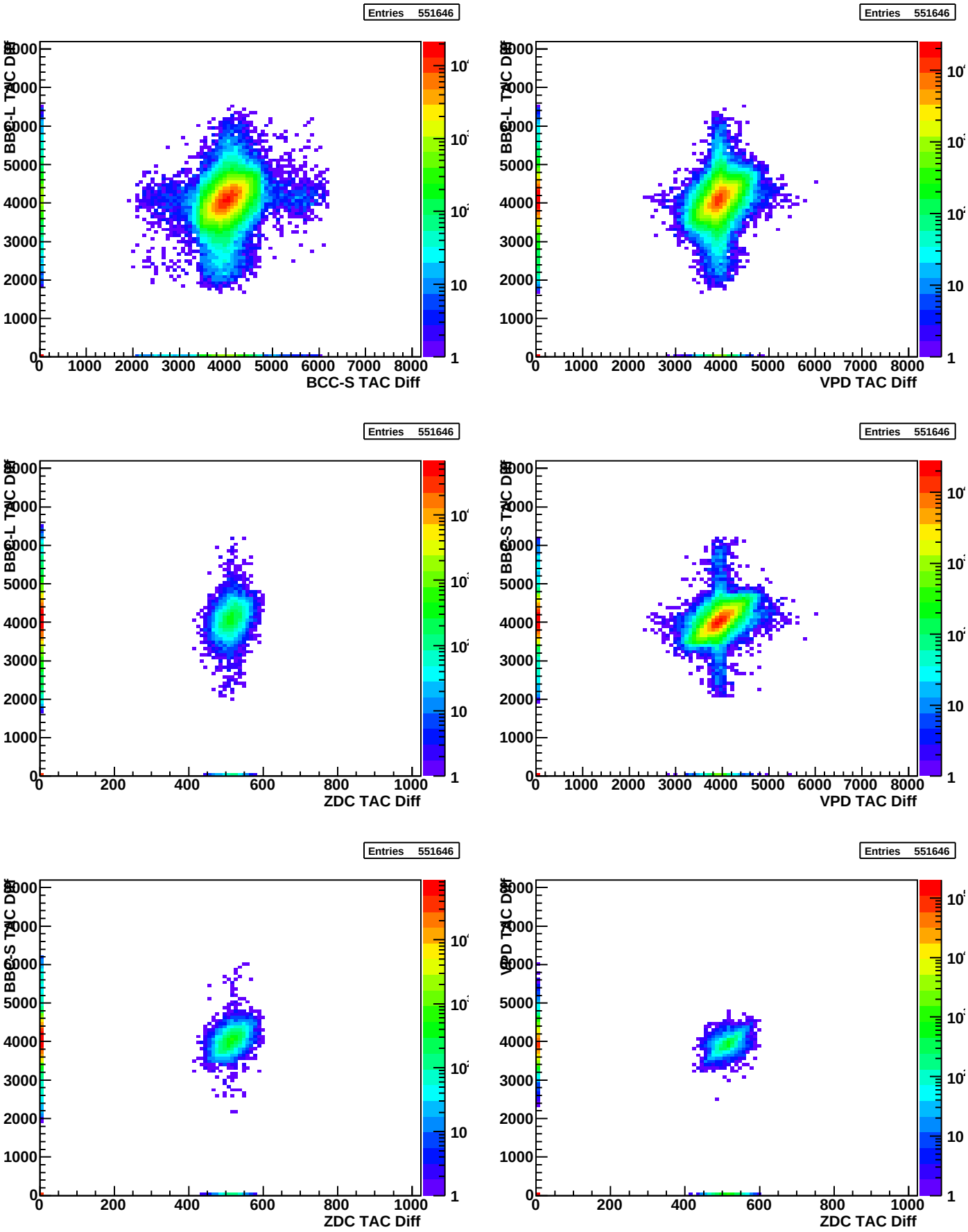

Entries 551646
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

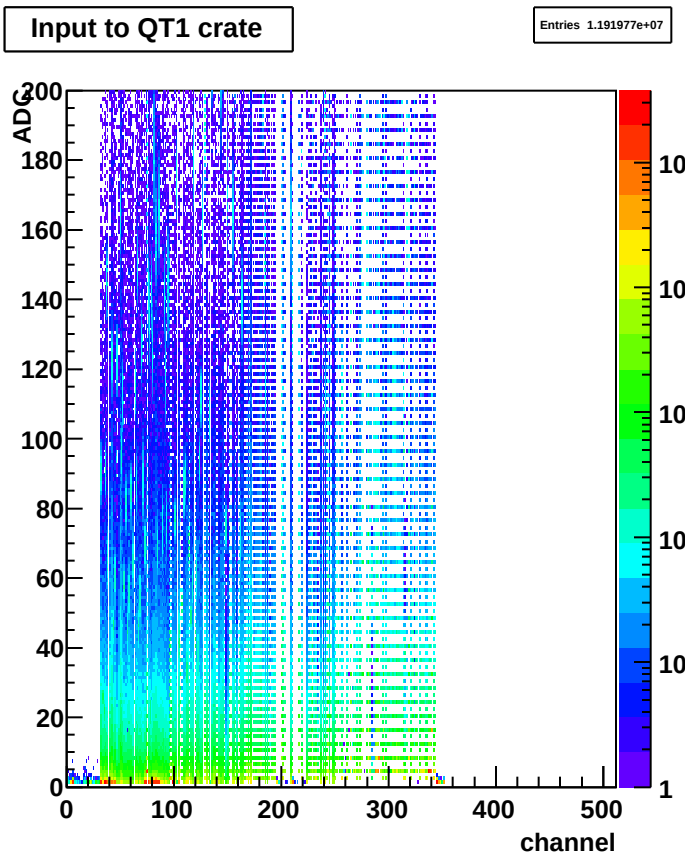

Entries 551646
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

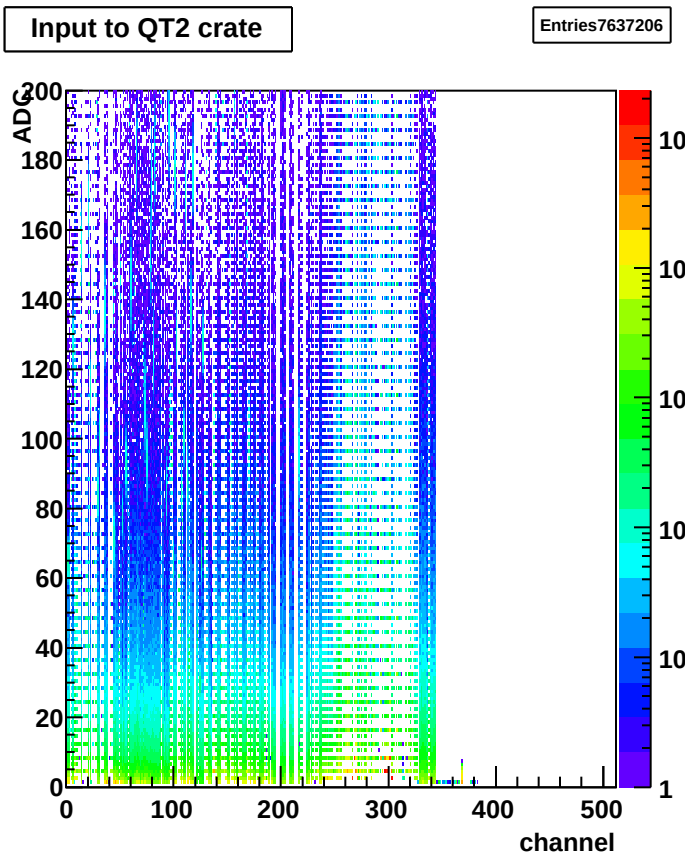

Entries 551646
BBC-L-East ADC Sum
BBC-L-West ADC Sum

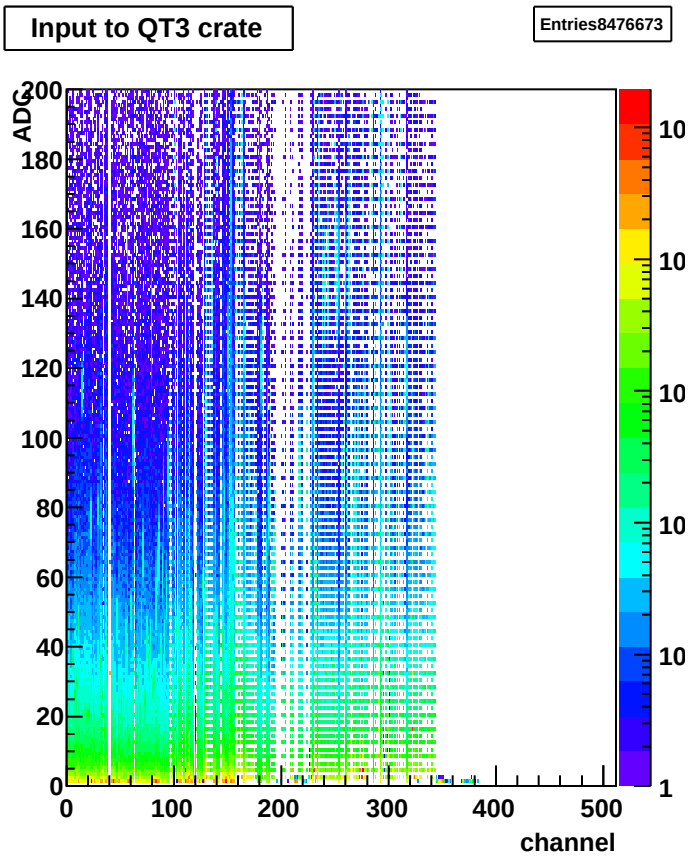

Entries 551646
BBC-S-East ADC Sum
BBC-S-West ADC Sum

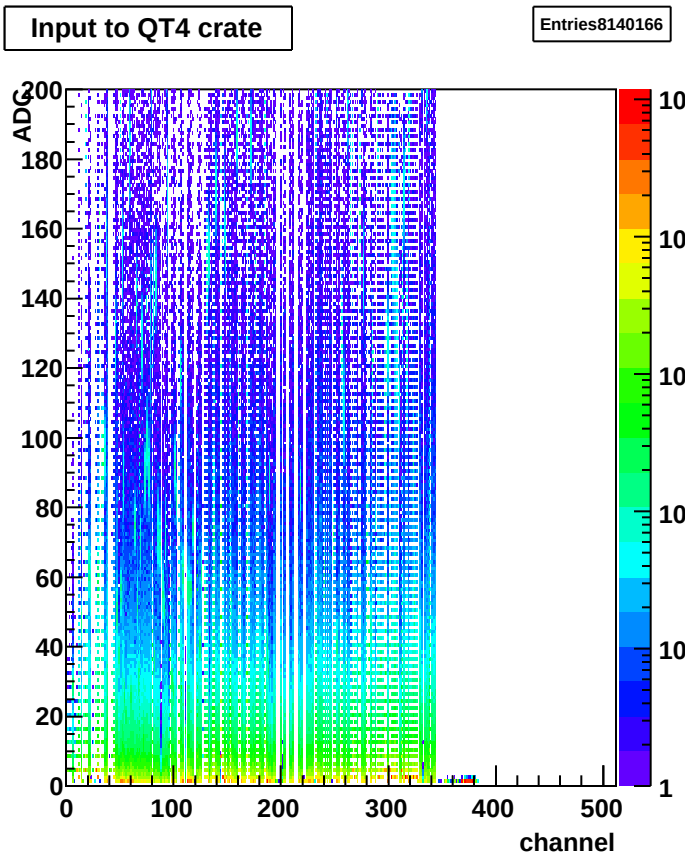

Entries 551646
ZDC East ADC Sum Att
ZDC West ADC Sum Att

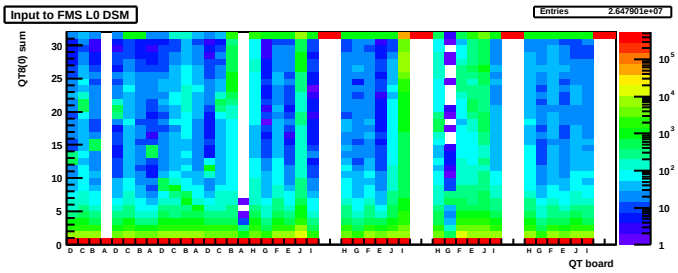

Entries 551646
BBC-L TAC Diff
BCC-S TAC Diff
Entries 551646
BBC-L TAC Diff
VPD TAC Diff
Entries 551646
BBC-L TAC Diff
ZDC TAC Diff
Entries 551646
BBC-S TAC Diff
VPD TAC Diff
Entries 551646
BBC-S TAC Diff
ZDC TAC Diff
Entries 551646
VPD TAC Diff
ZDC TAC Diff

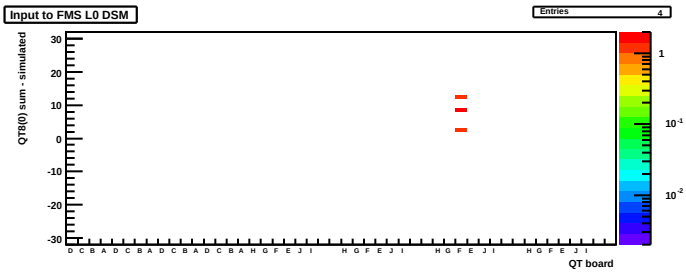

Input to QT1 crate
Entries 1.191977e+07
ADC
channel

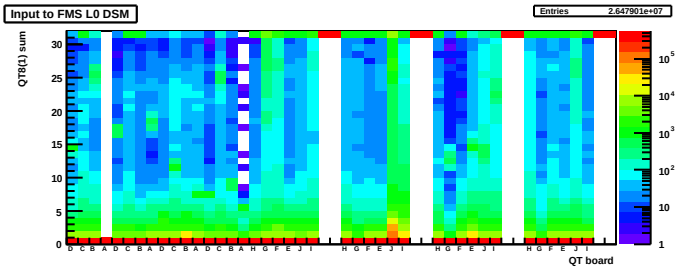

Input to QT2 crate
Entries7637206
ADC
channel

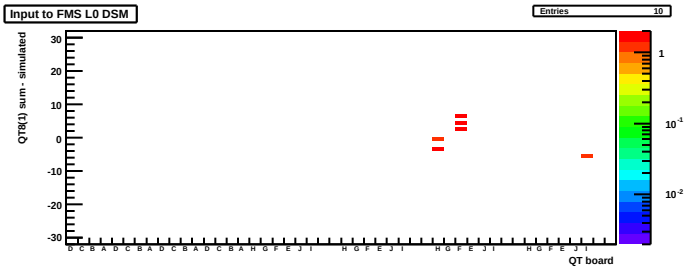

Input to QT3 crate
Entries8476673
ADC
channel

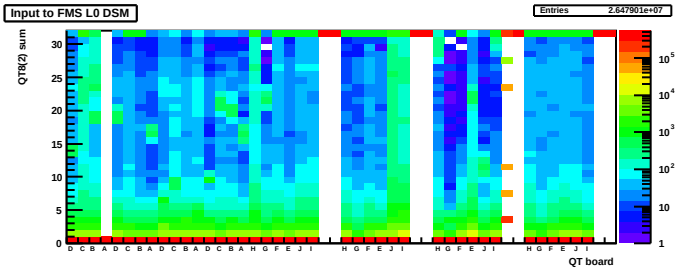

Input to QT4 crate
Entries8140166
ADC
channel

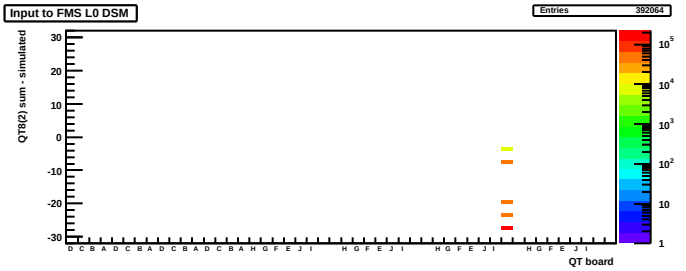

Input to FMS L0 DSM
Entries 2.647901e+07
QT8(0) sum
QT board

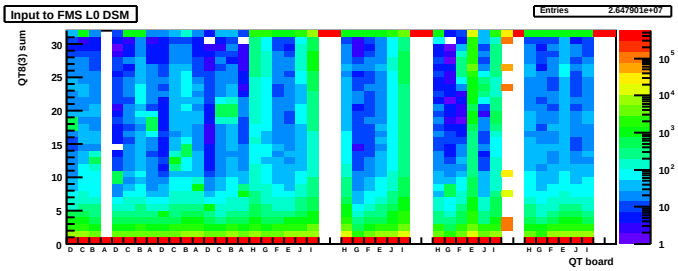

Input to FMS L0 DSM
Entries 4
QT8(0) sum - simulated
QT board

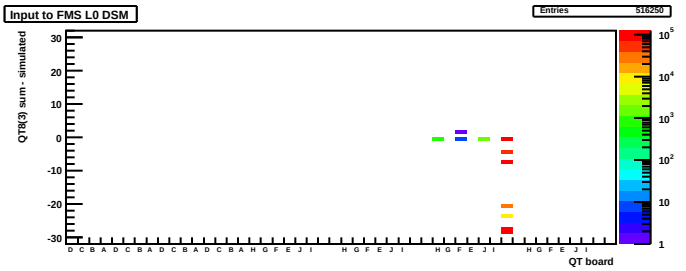

Input to FMS L0 DSM
Entries 2.647901e+07
QT8(1) sum
QT board

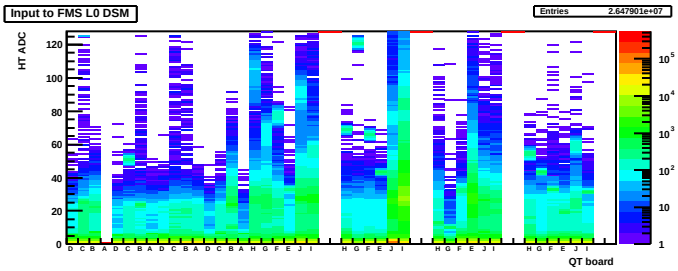

Input to FMS L0 DSM
Entries 10
QT8(1) sum - simulated
QT board

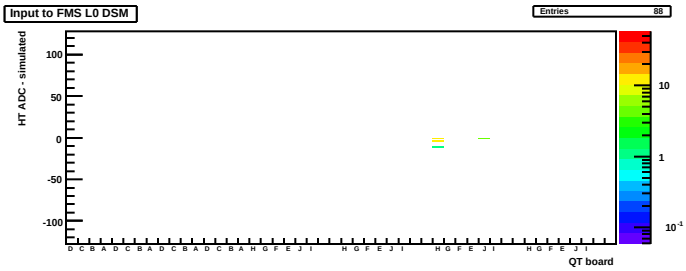

Input to FMS L0 DSM
Entries 2.647901e+07
QT8(2) sum
QT board

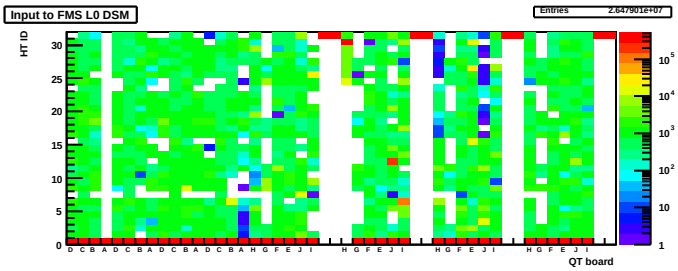

Input to FMS L0 DSM
Entries 392064
QT8(2) sum - simulated
QT board

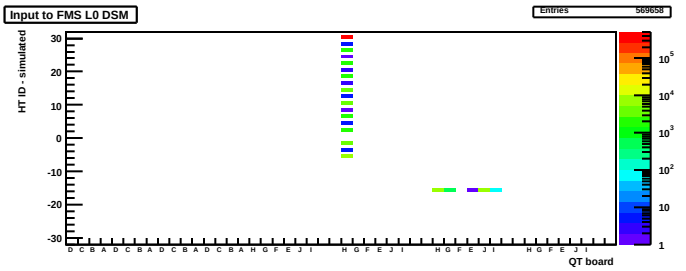

Input to FMS L0 DSM
Entries 2.647901e+07
QT8(3) sum
QT board

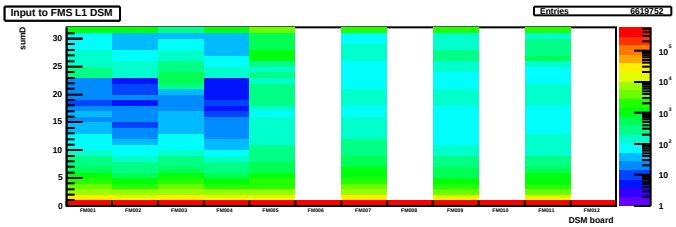

Input to FMS L0 DSM
Entries 516250
QT8(3) sum - simulated
QT board

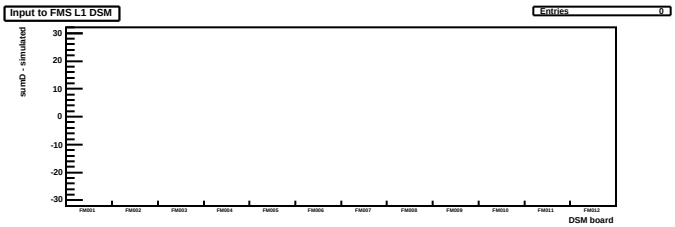

Input to FMS L0 DSM
Entries 2.647901e+07
HT ADC
QT board

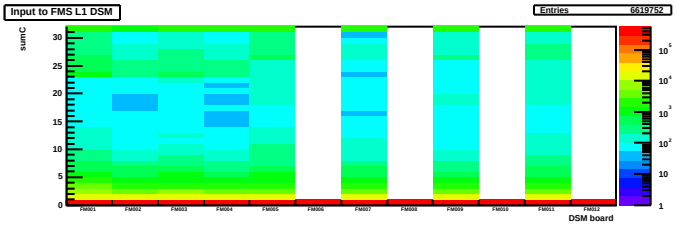

Input to FMS L0 DSM
Entries 88
HT ADC - simulated
QT board

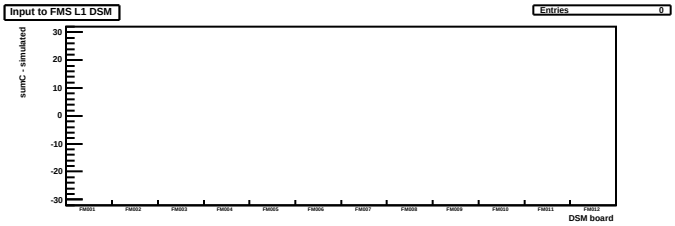

Input to FMS L0 DSM
Entries 2.647901e+07
HT ID
QT board

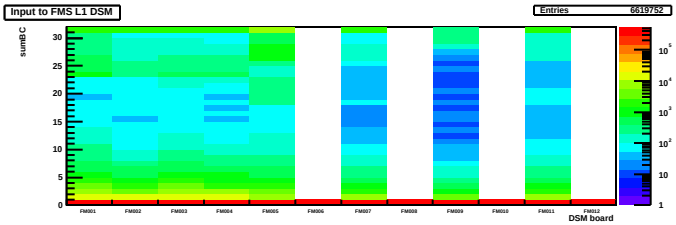

Input to FMS L0 DSM
Entries 569658
HT ID - simulated
QT board

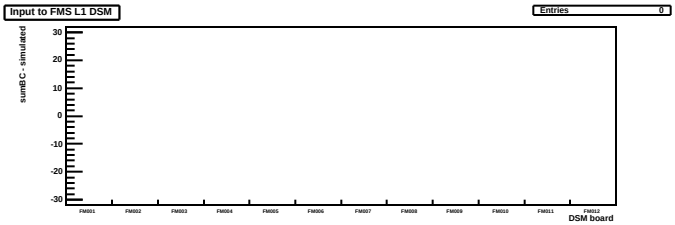

Input to FMS L1 DSM
Entries 6619752
sumD
DSM board

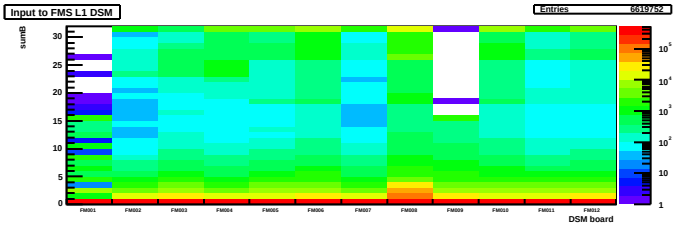

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

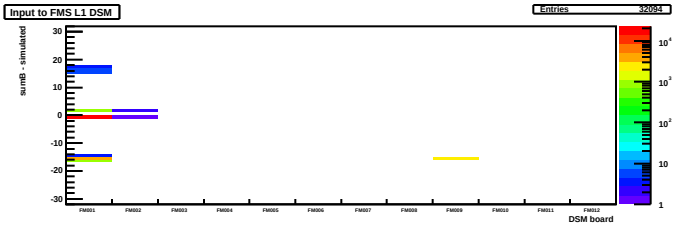

Input to FMS L1 DSM
Entries 6619752
sumC
DSM board

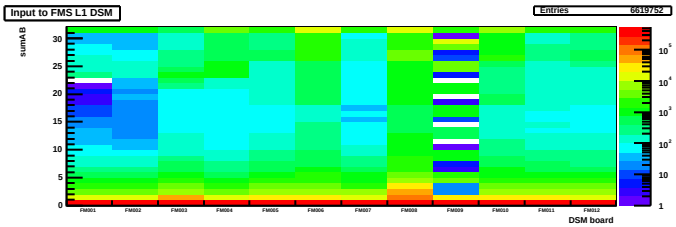

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

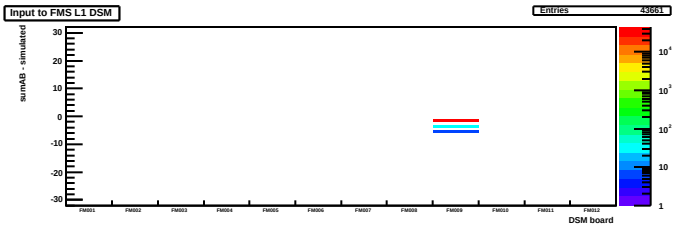

Input to FMS L1 DSM
Entries 6619752
sumBC
DSM board

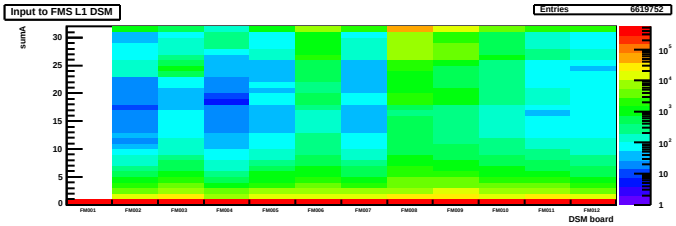

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

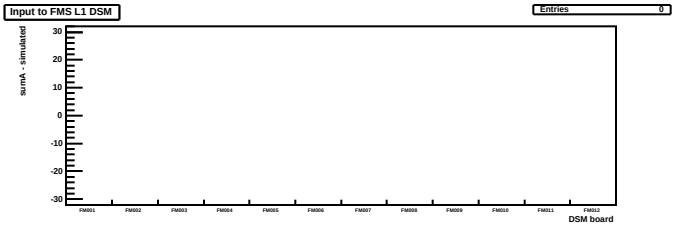

Input to FMS L1 DSM
Entries 6619752
sumB
DSM board

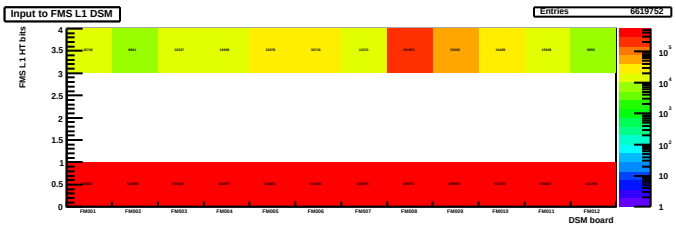

Input to FMS L1 DSM
Entries 32094
sumB - simulated
DSM board

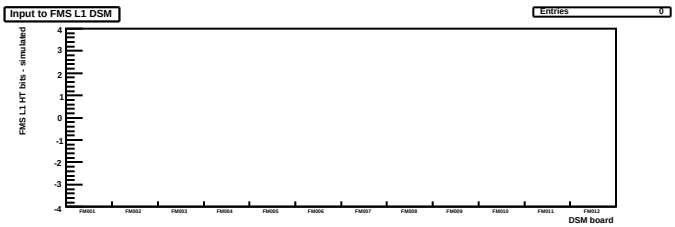

Input to FMS L1 DSM
Entries 6619752
sumAB
DSM board

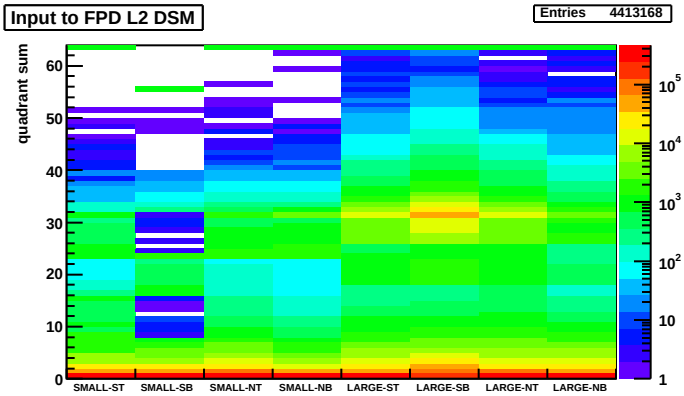

Input to FMS L1 DSM
Entries 43661
sumAB - simulated
DSM board

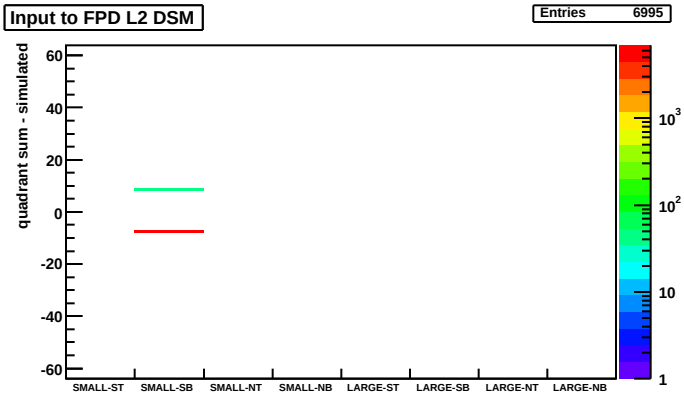

Input to FMS L1 DSM
Entries 6619752
sumA
DSM board

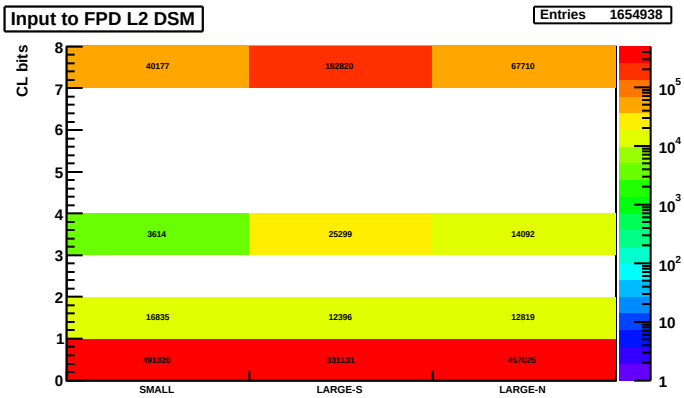

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

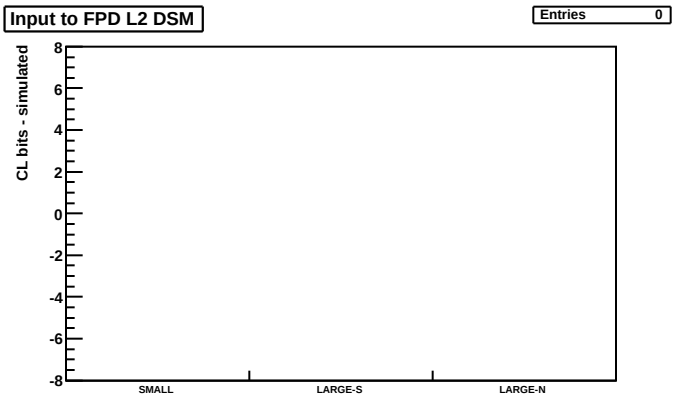

Input to FMS L1 DSM
Entries 6619752
FMS L1 HT bits
DSM board

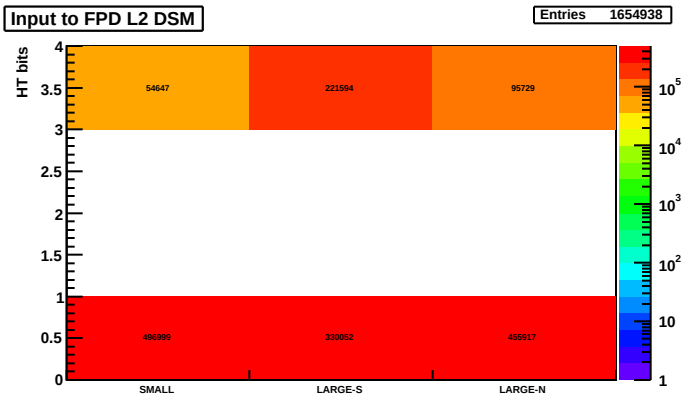

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

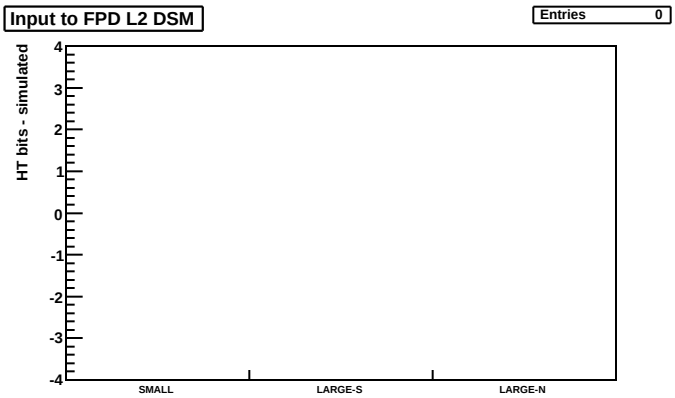

Input to FPD L2 DSM
Entries 4413168
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

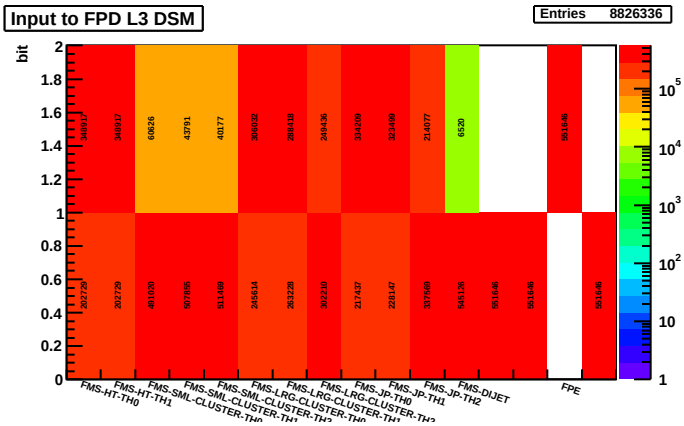

Input to FPD L2 DSM
Entries 6995
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

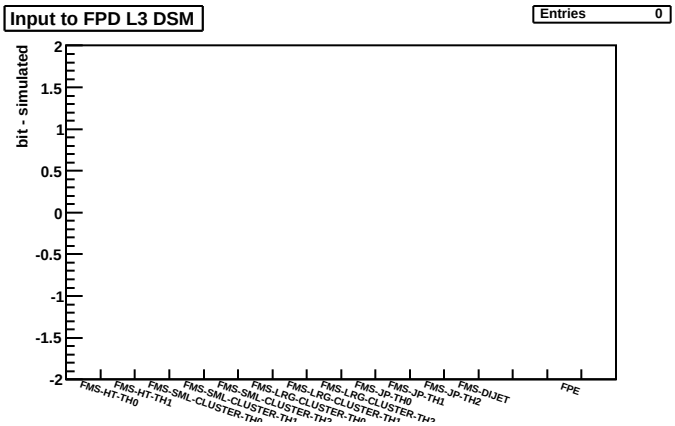

Input to FPD L2 DSM
Entries 1654938
CL bits
40177
182820
67710
3614
25299
14092
16835
12396
12819
491020
331131
457025
SMALL
LARGE-S
LARGE-N

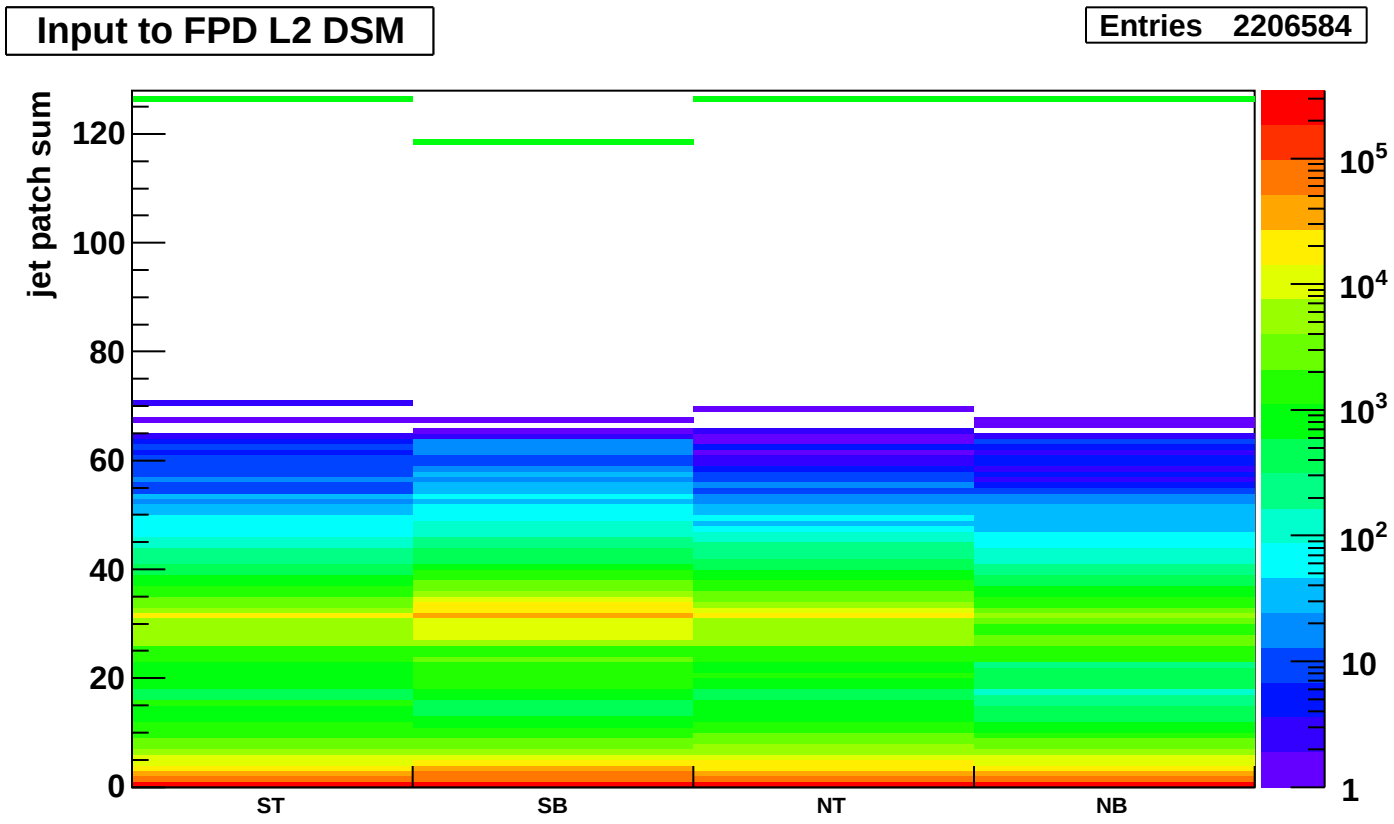

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

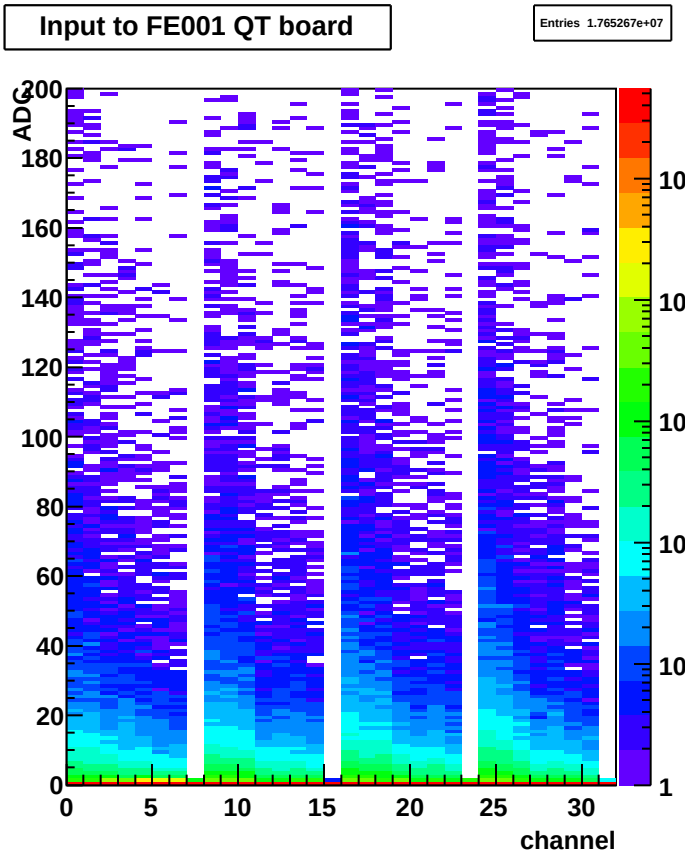

Input to FPD L2 DSM
Entries 1654938
HT bits
54647
221594
95729
496999
330052
455917
SMALL
LARGE-S
LARGE-N

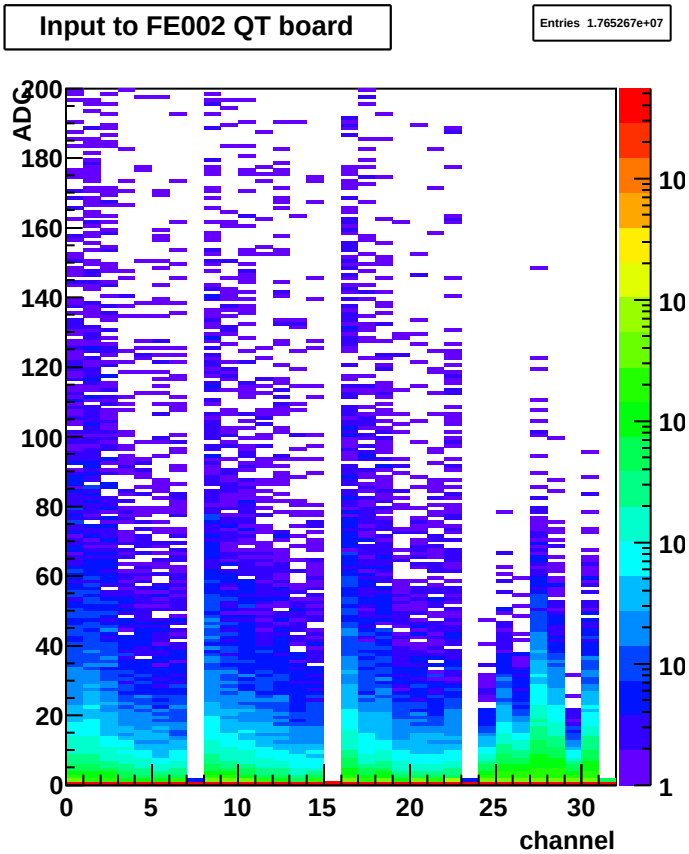

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

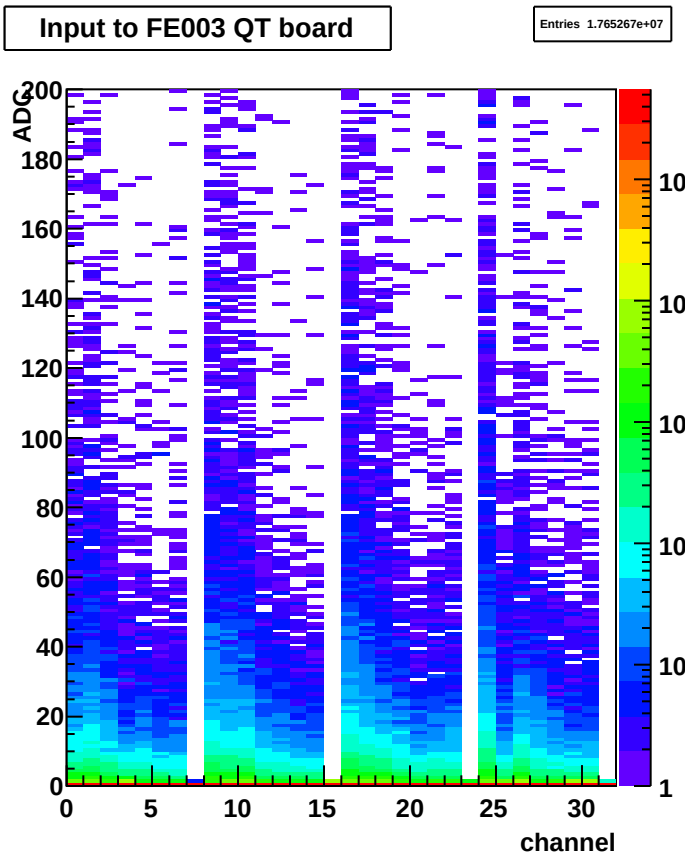

Input to FPD L3 DSM
Entries 8826336
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

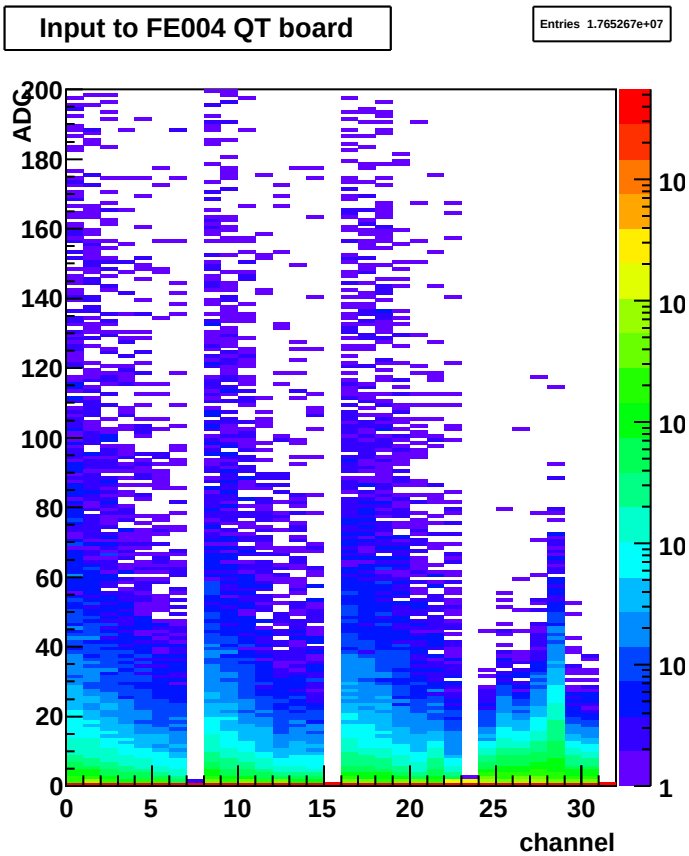

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

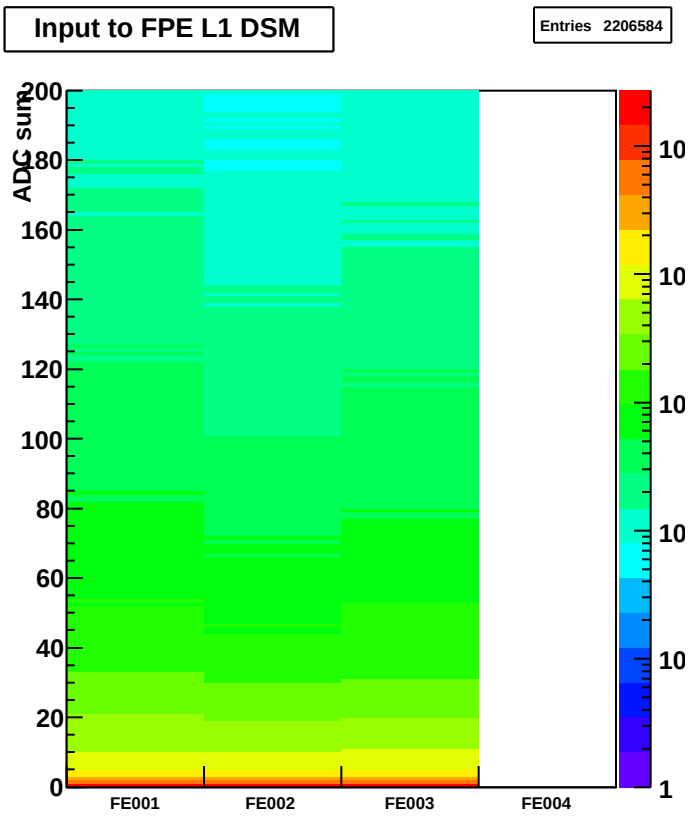

Input to FPD L2 DSM
Entries 2206584
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

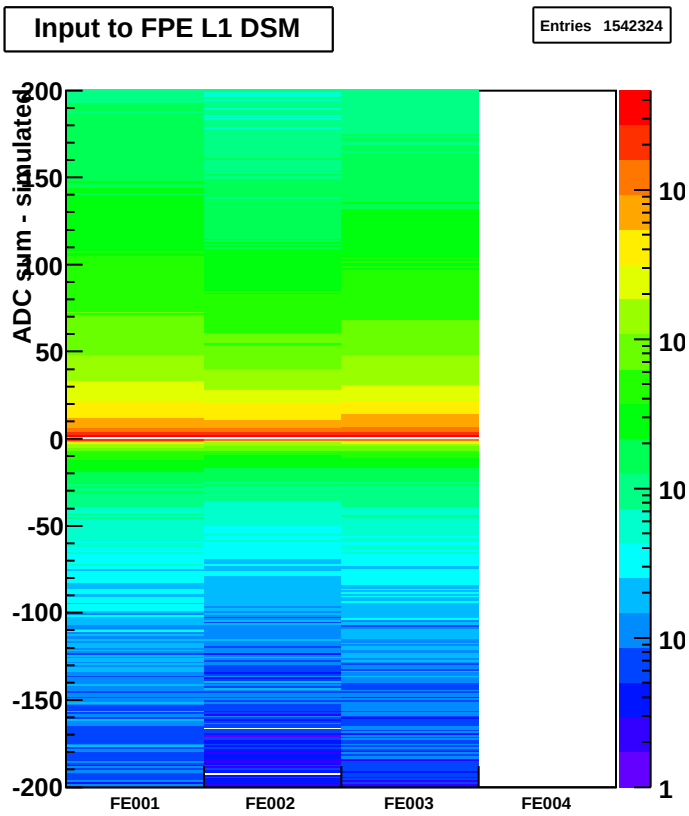

Input to FE001 QT board
Entries 1.765267e+07
ADC
channel

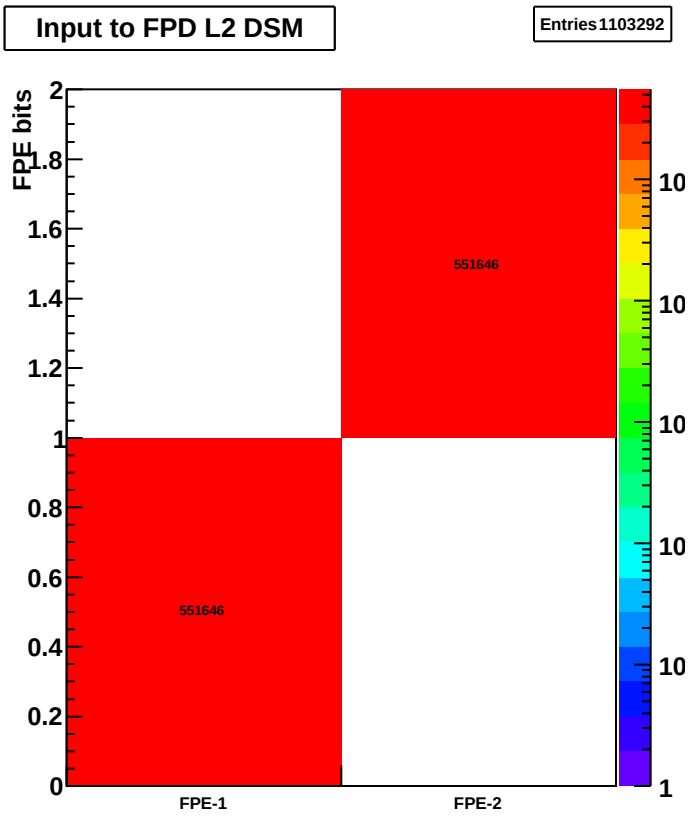

Input to FE002 QT board
Entries 1.765267e+07
ADC
channel

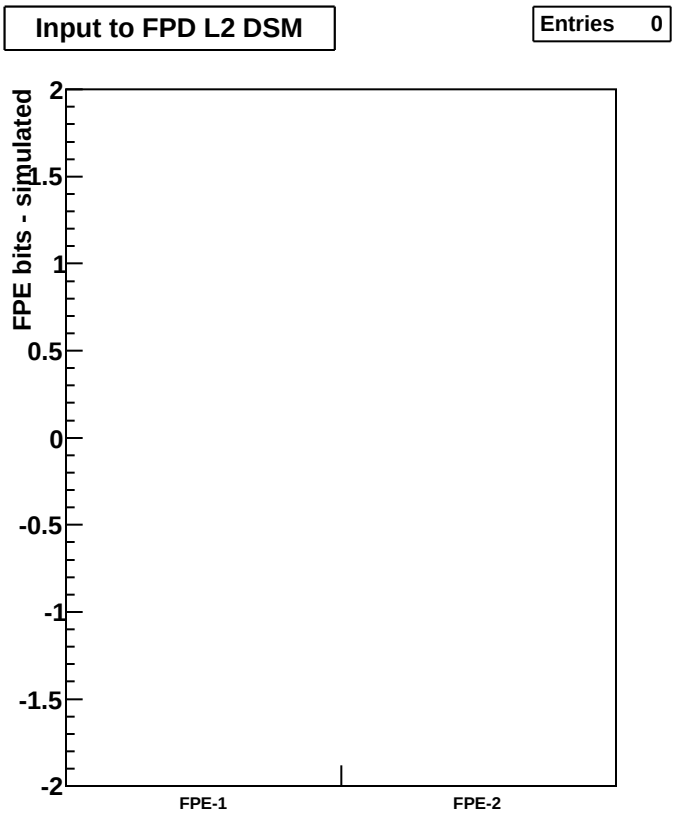

Input to FE003 QT board
Entries 1.765267e+07
ADC
channel


Input to FE004 QT board
Entries 1.765267e+07
ADC
channel


Input to FPE L1 DSM
Entries 2206584
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 1542324
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1103292
FPE bits
551646
551646
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2