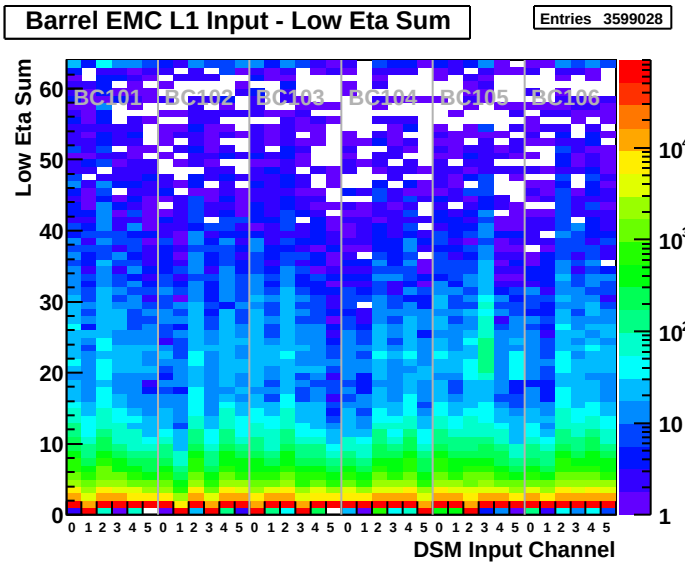
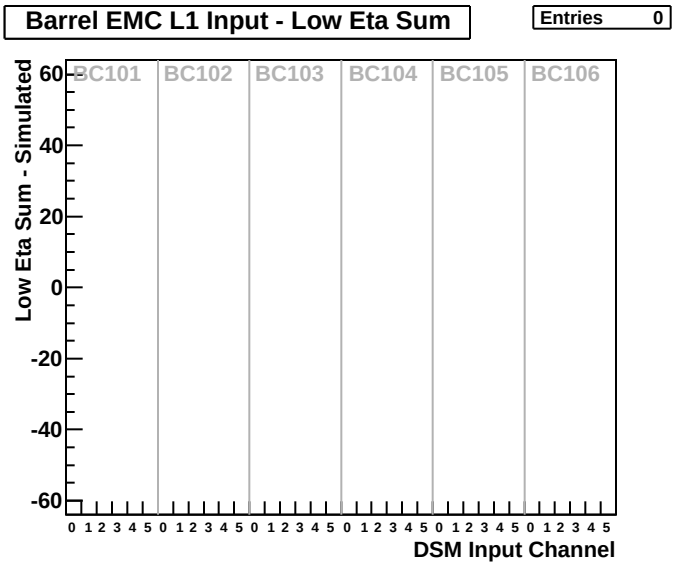
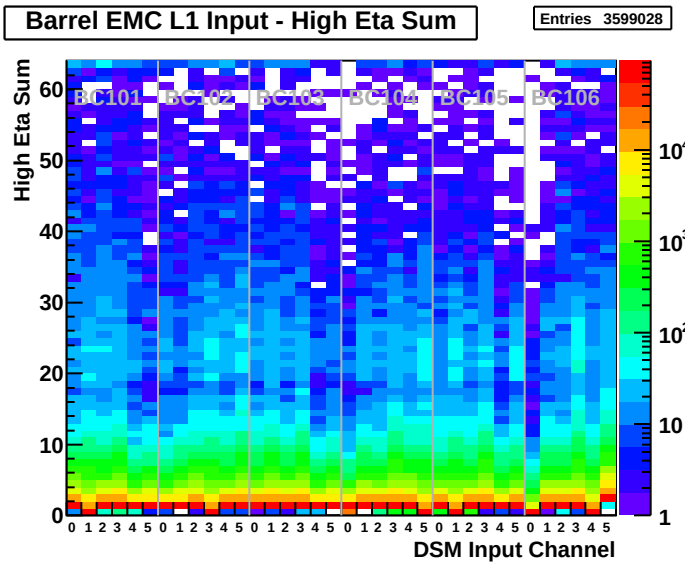
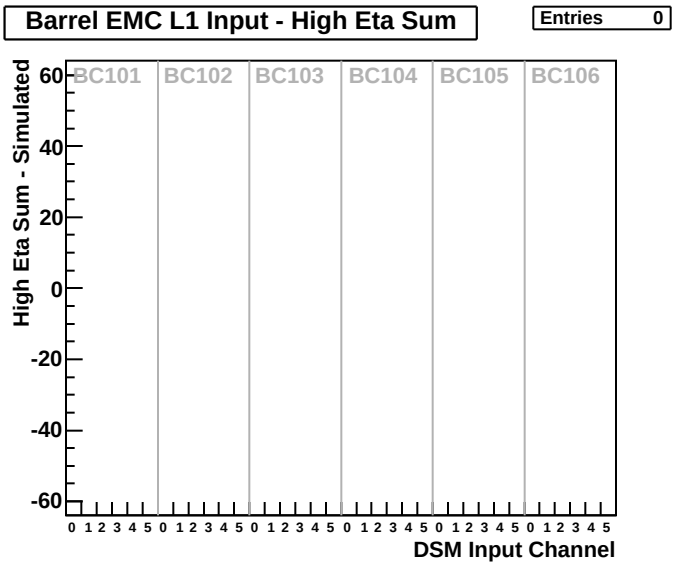
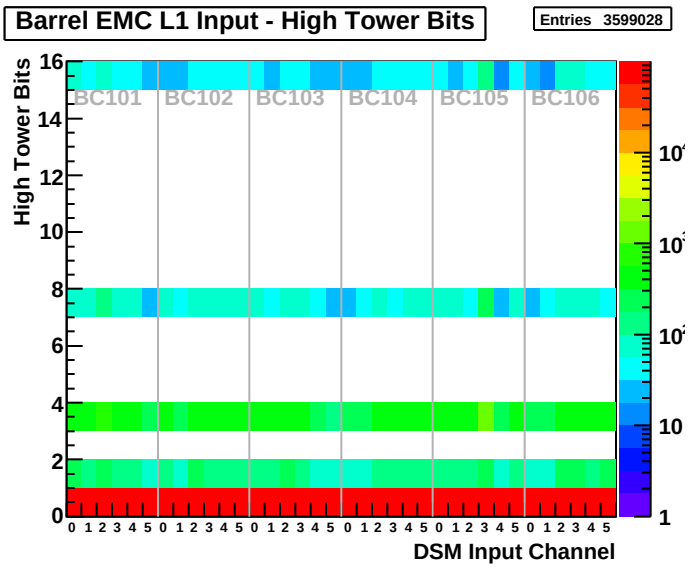
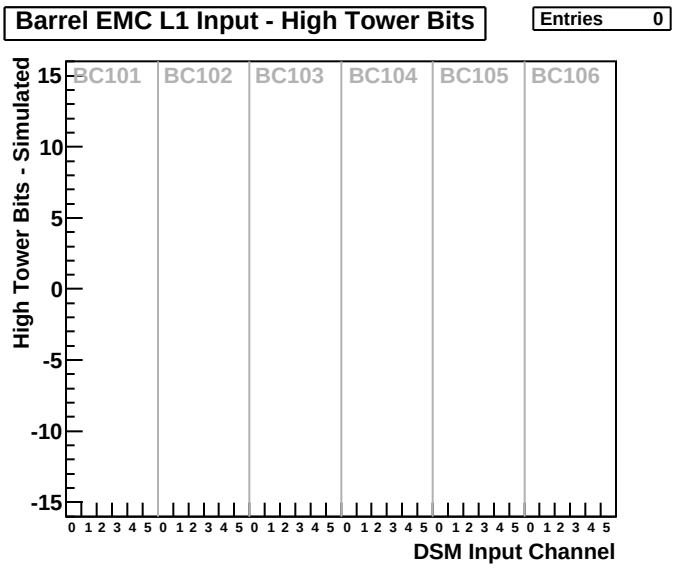

Barrel EMC L1 Input - Low Eta Sum
Entries 3599028
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

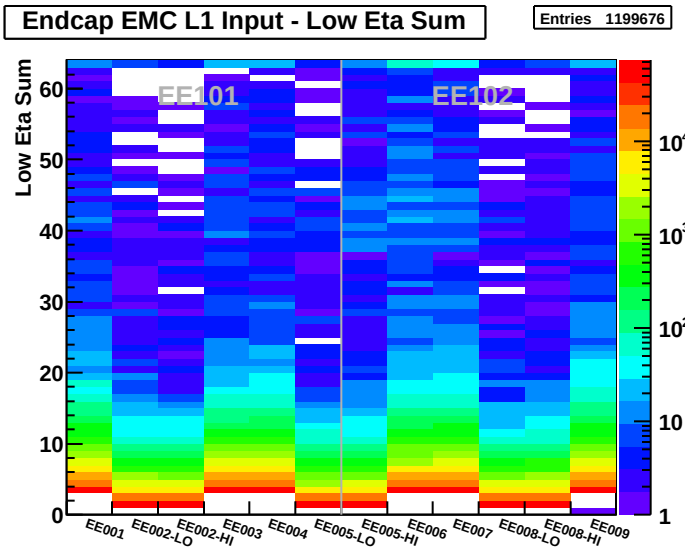

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

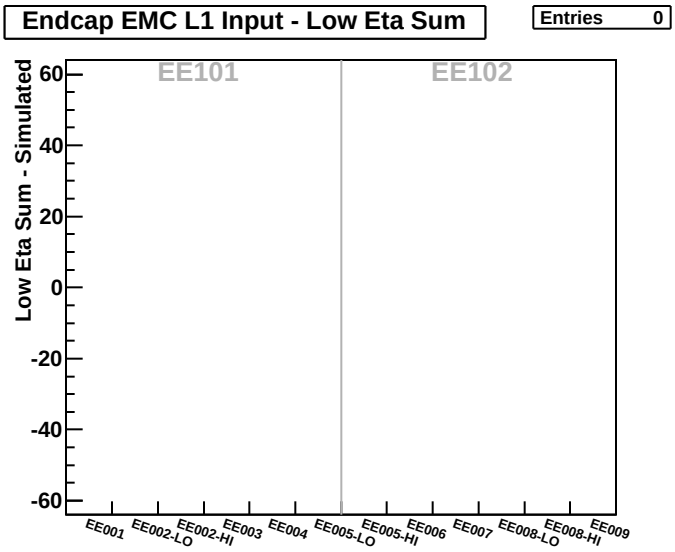

Barrel EMC L1 Input - High Eta Sum
Entries 3599028
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

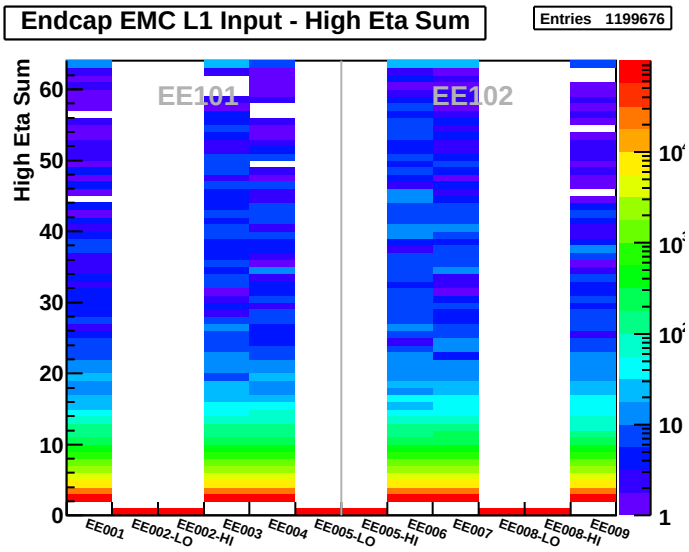

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

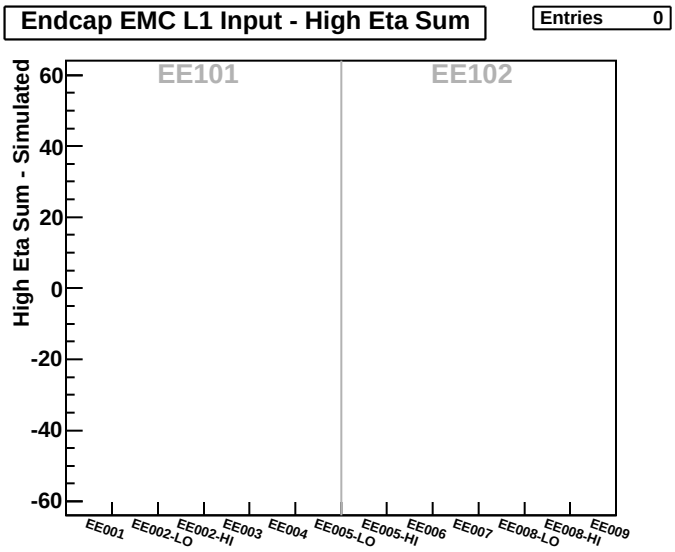

Barrel EMC L1 Input - High Tower Bits
Entries 3599028
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

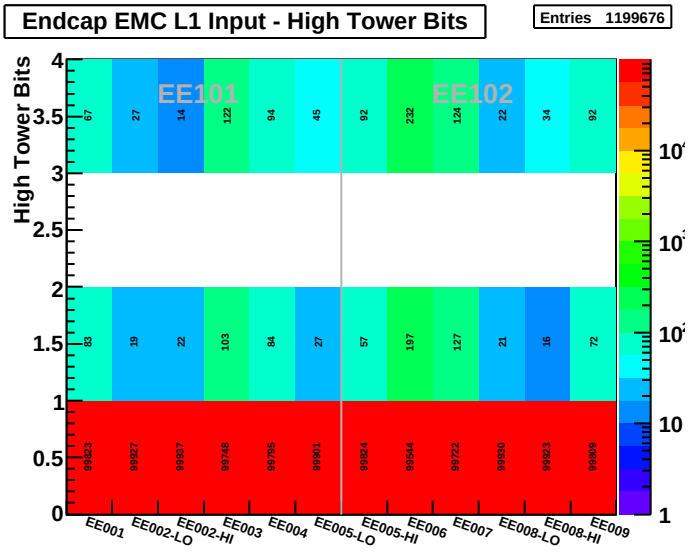

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

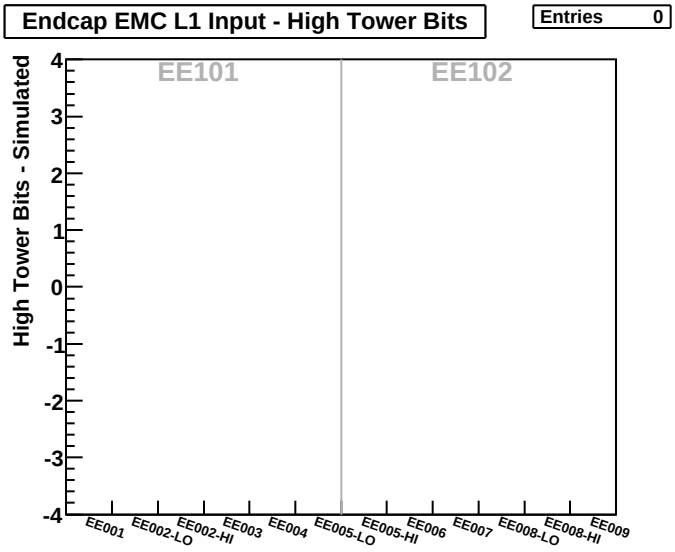

Endcap EMC L1 Input - Low Eta Sum
Entries 1199676
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

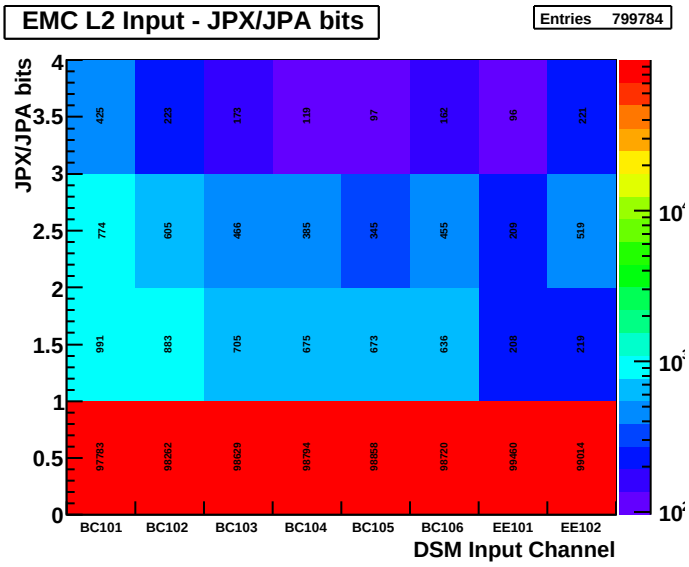

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

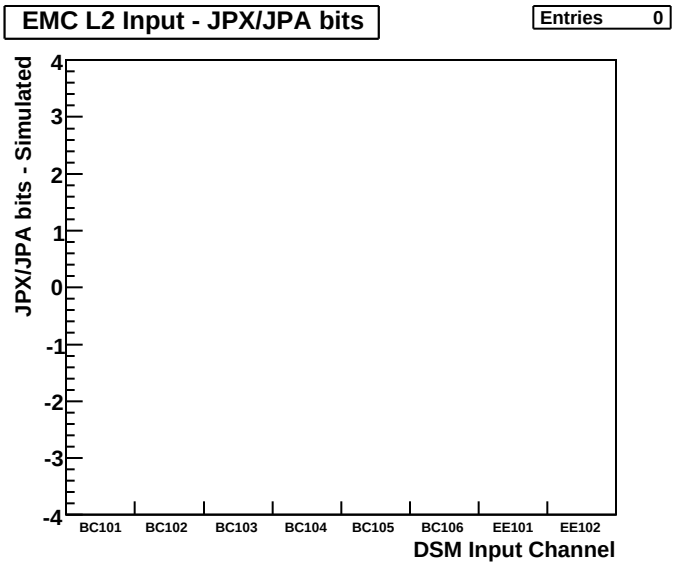

Endcap EMC L1 Input - High Eta Sum
Entries 1199676
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

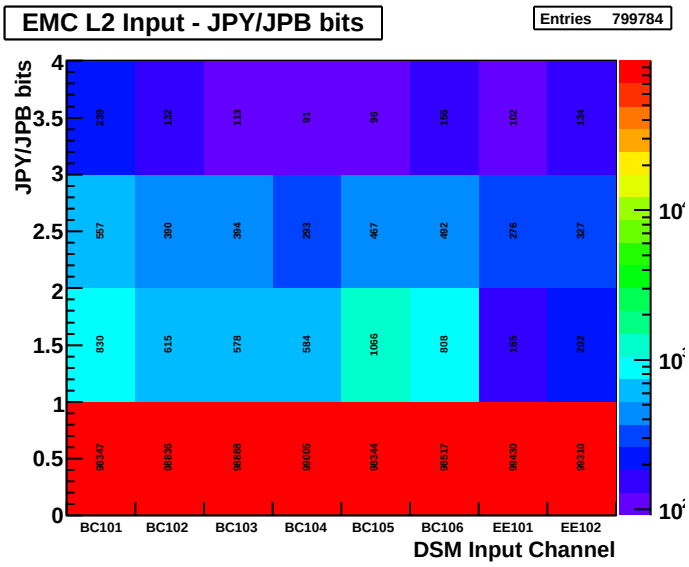

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

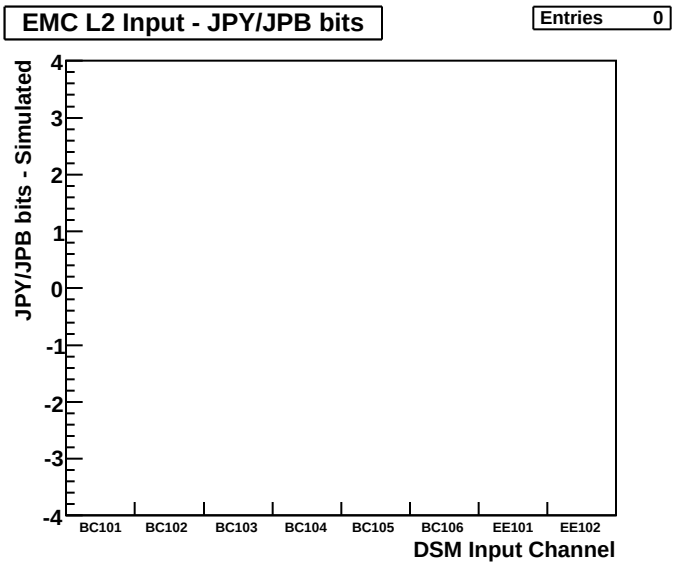

Endcap EMC L1 Input - High Tower Bits
Entries 1199676
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

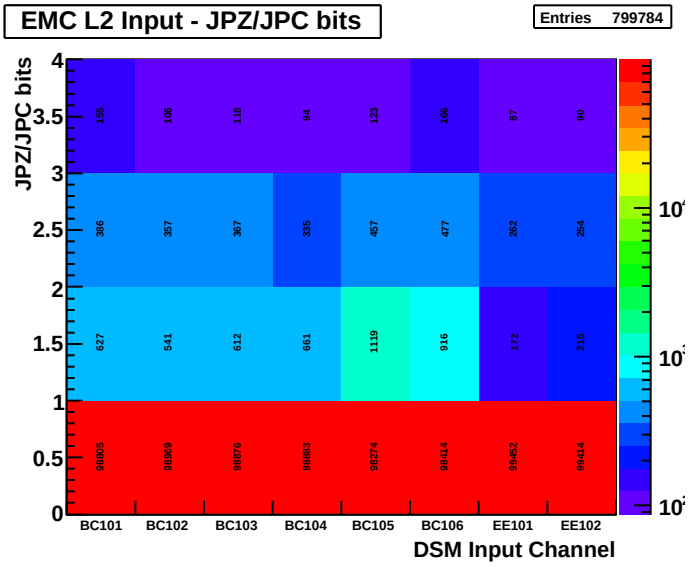

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

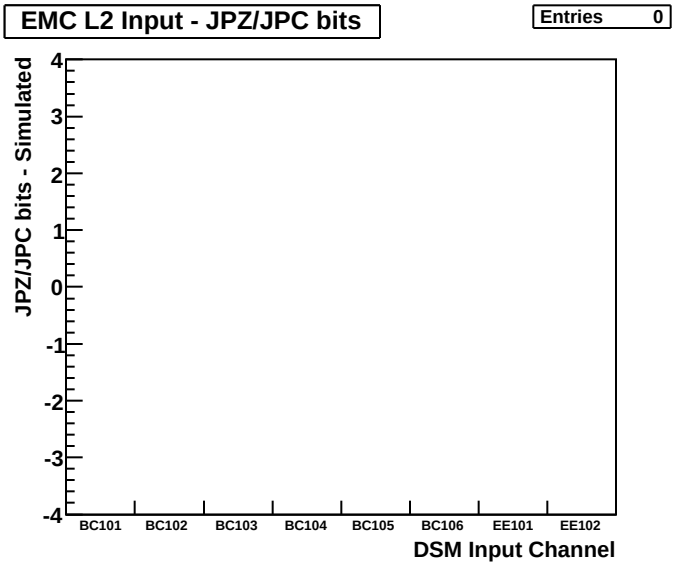

EMC L2 Input - JPX/JPA bits
Entries 799784
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
425 223 173 119 97 162 96 221
774 605 466 385 345 455 209 519
991 883 705 675 673 636 208 219
97783 98262 98629 98794 98858 98720 99460 99014

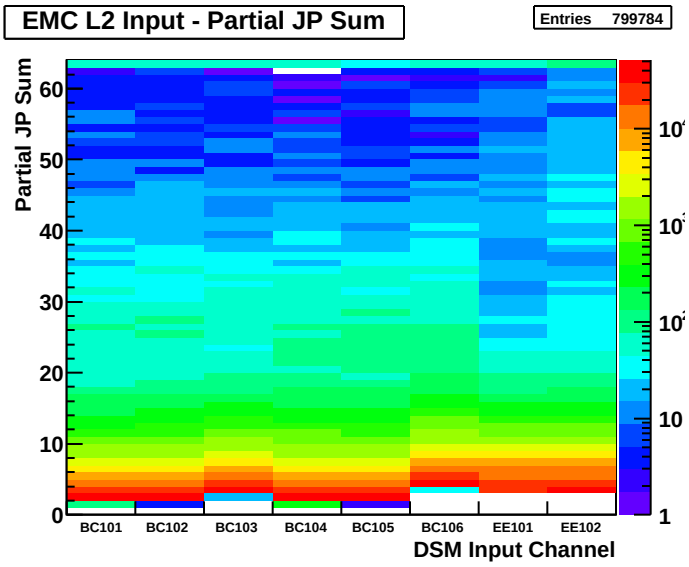

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

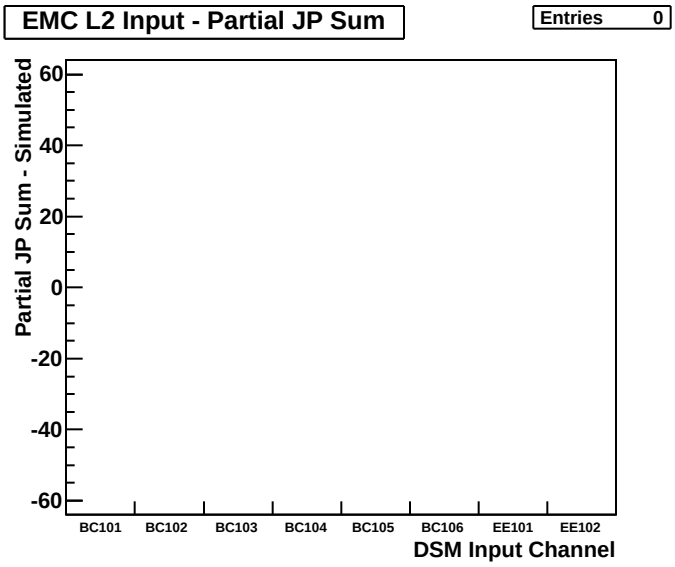

EMC L2 Input - JPY/JPB bits
Entries 799784
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
238 132 113 91 96 156 102 134
557 390 394 293 467 492 276 327
830 615 578 584 1066 808 165 202
98347 98836 98888 99005 98344 98517 99430 99310

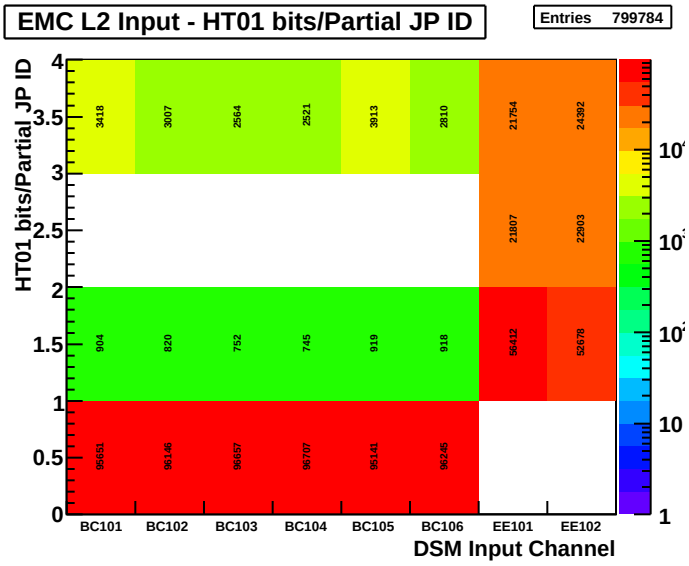

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

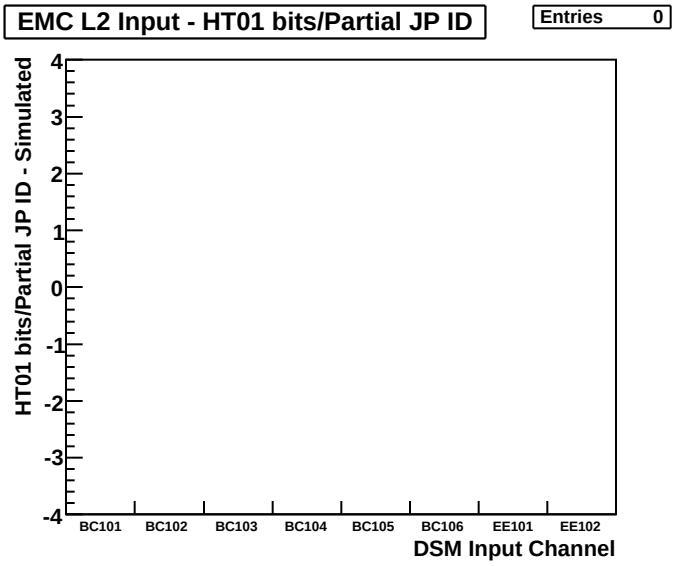

EMC L2 Input - JPZ/JPC bits
Entries 799784
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
155 106 118 94 123 166 87 90
386 357 367 335 457 477 262 254
627 541 612 661 1119 916 172 215
98805 98869 98876 98883 98274 98414 99452 99414

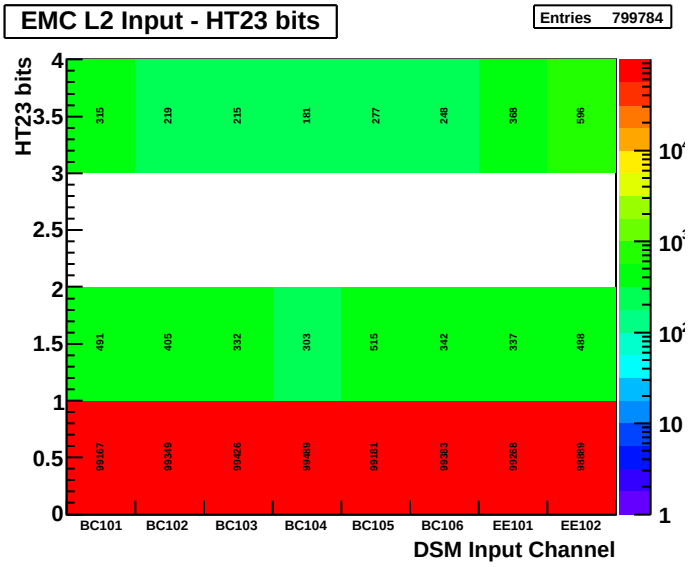

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

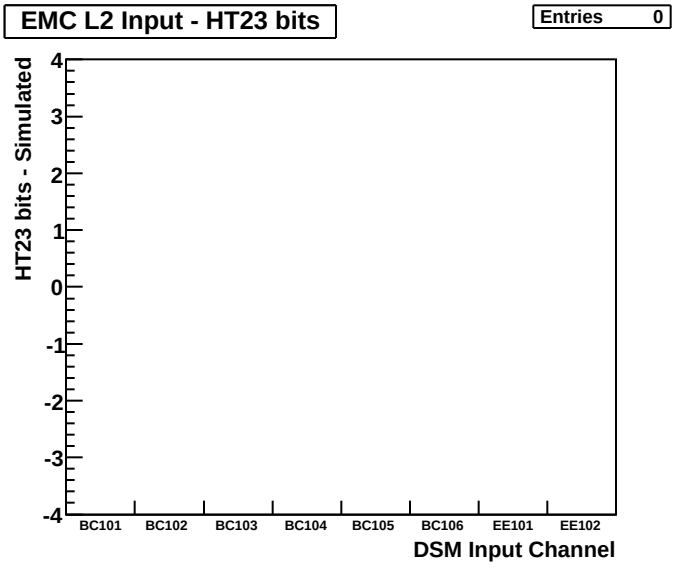

EMC L2 Input - Partial JP Sum
Entries 799784
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

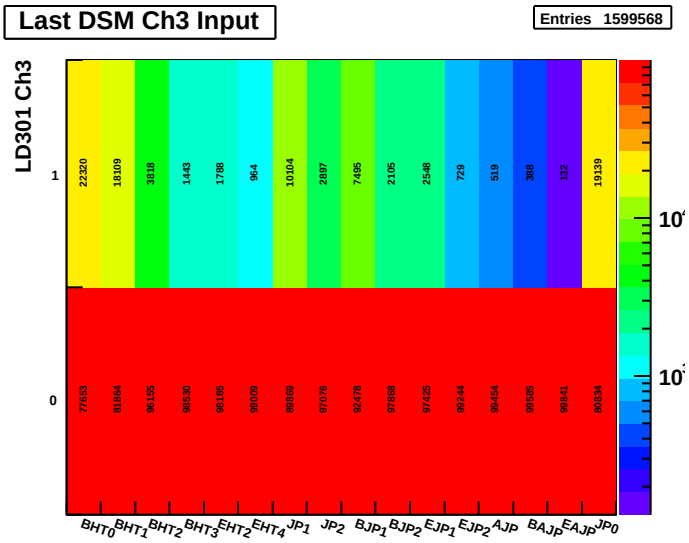

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

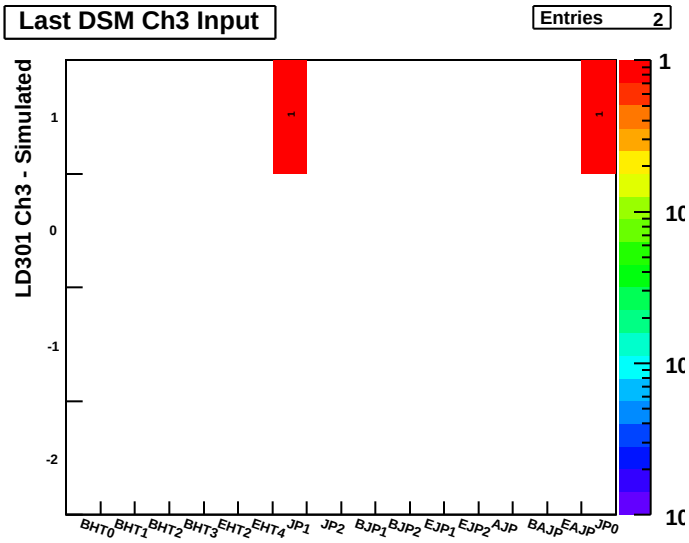

EMC L2 Input - HT01 bits/Partial JP ID
Entries 799784
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

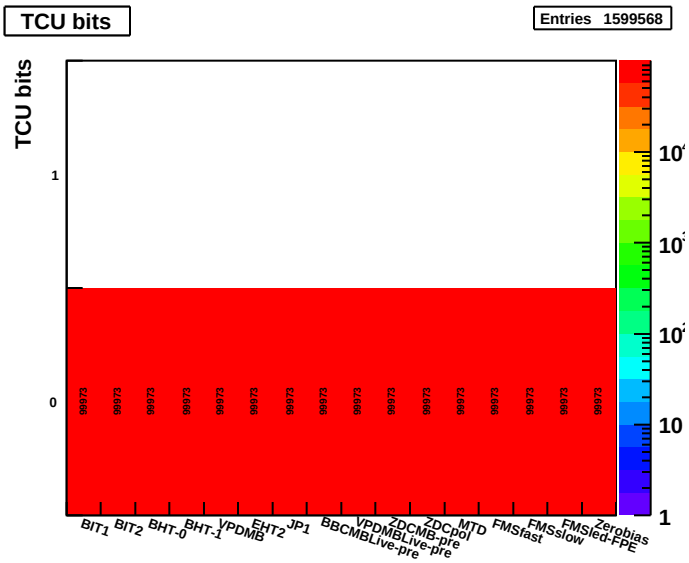

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

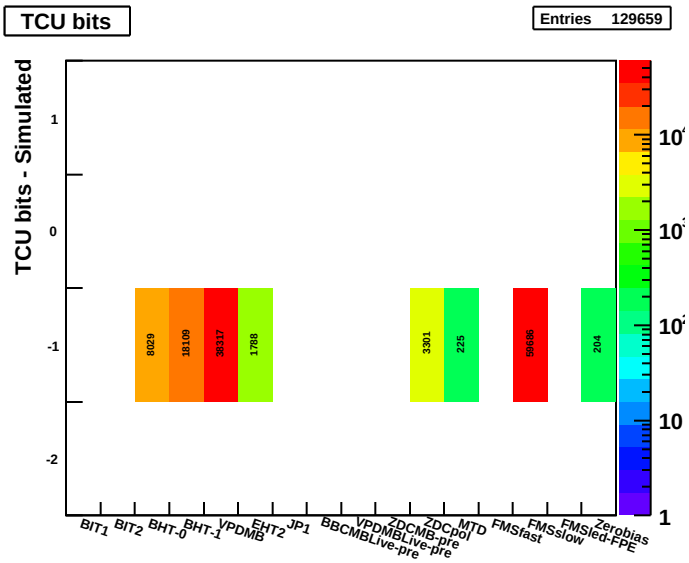

EMC L2 Input - HT23 bits
Entries 799784
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

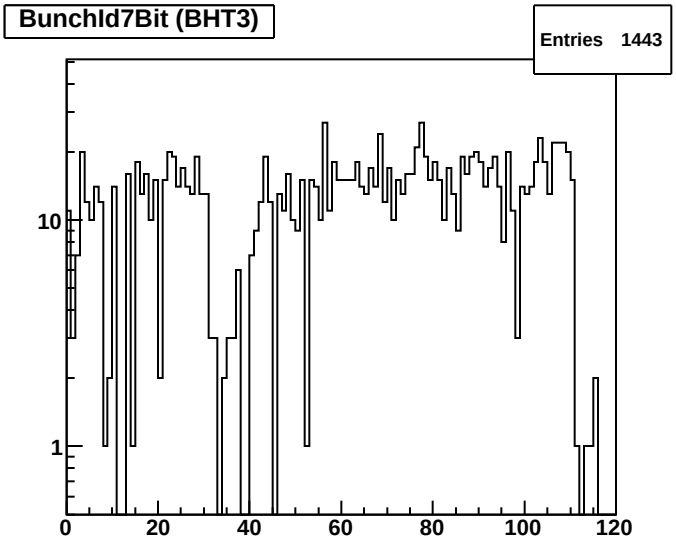

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

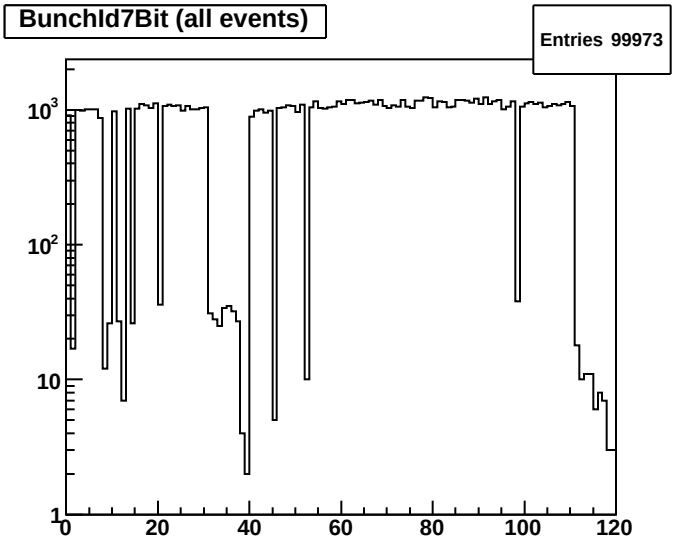

Last DSM Ch3 Input
Entries 1599568
LD301 Ch3
22320 18109 3818 1443 1788 964 10104 2897 7495 2105 2548 729 519 388 132 19139
77653 81864 95155 98530 98185 96009 89869 97076 92478 97868 97425 98244 99454 99565 99841 80834
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

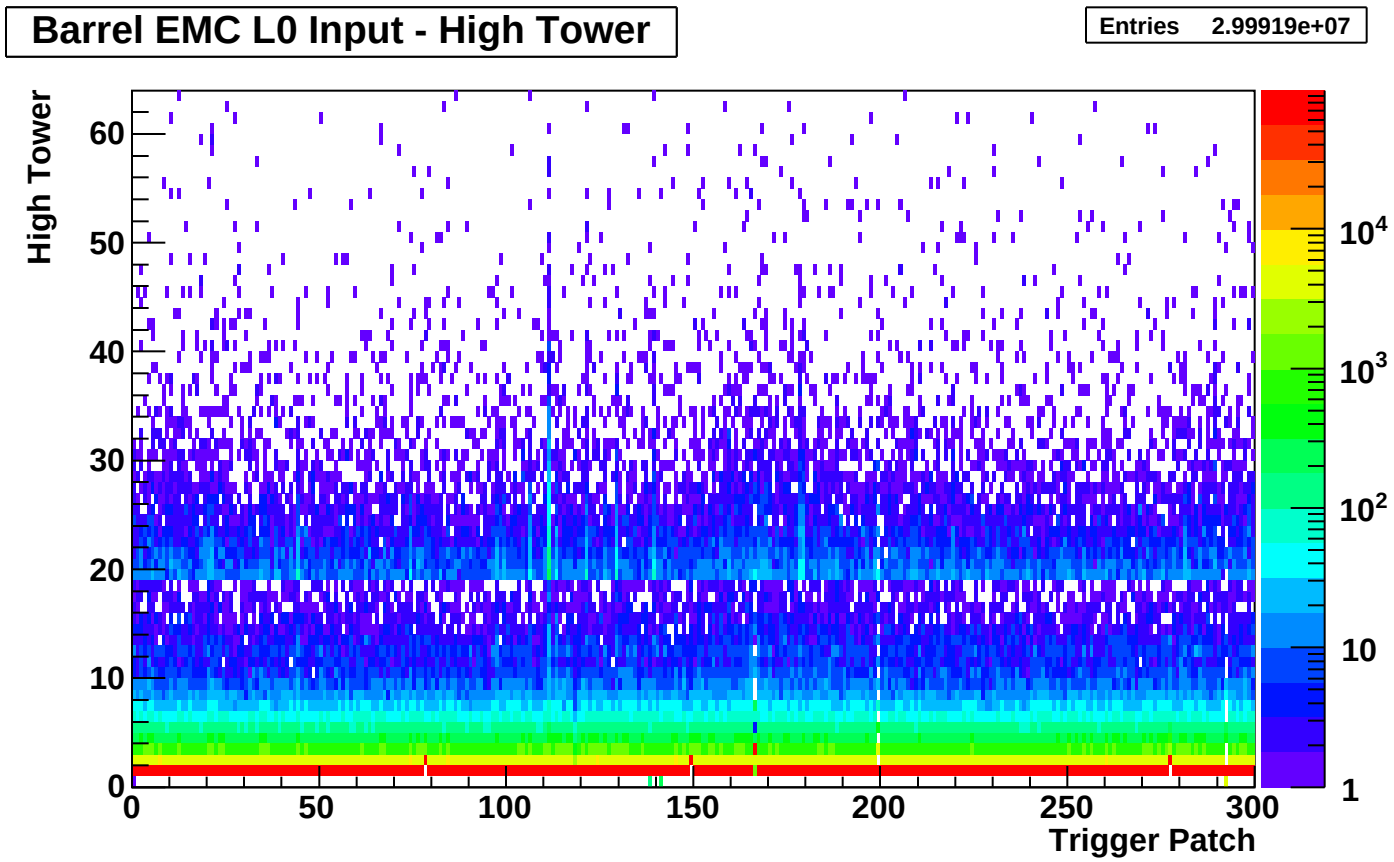

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

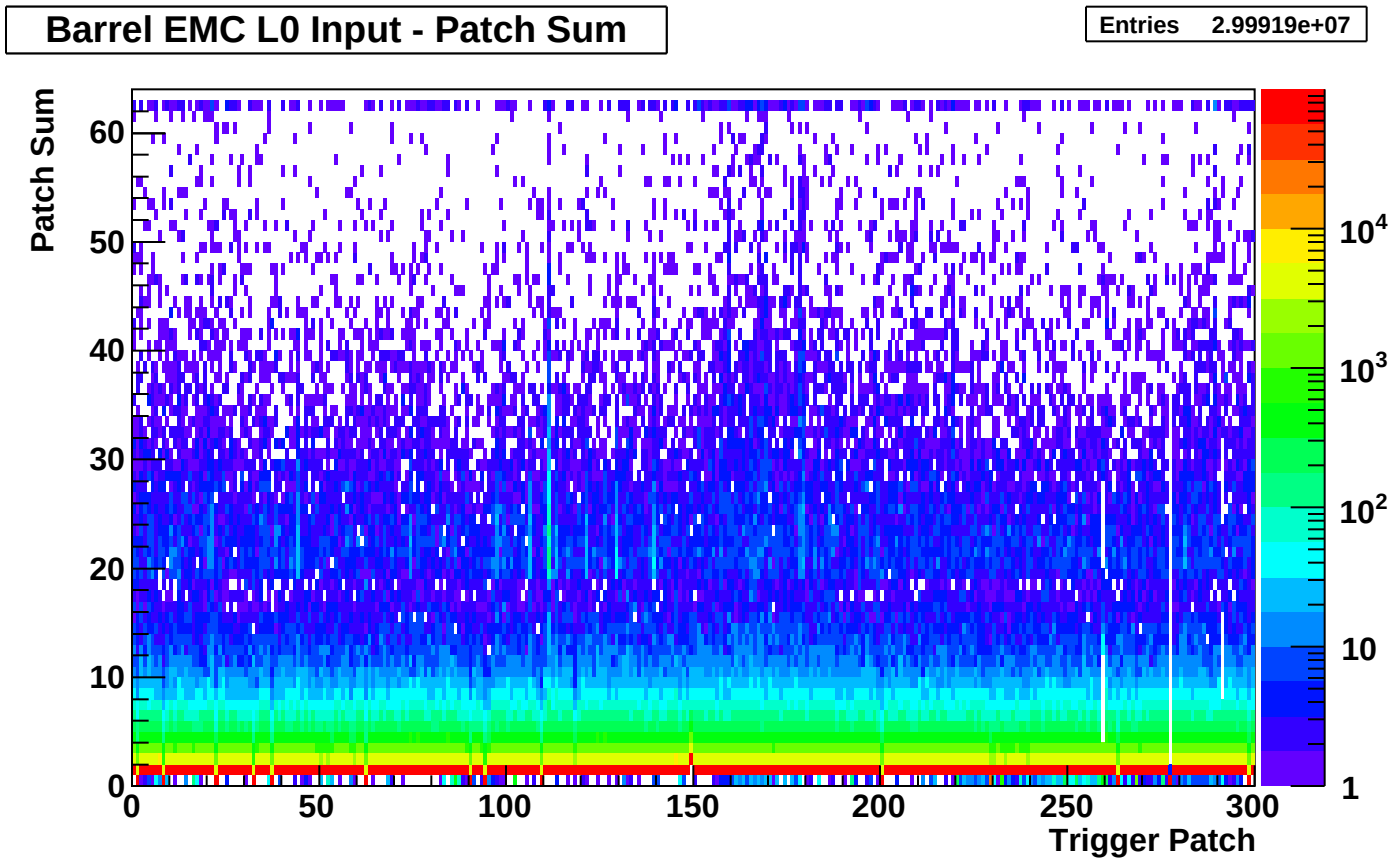

TCU bits
Entries 1599568
TCU bits
99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973 99973
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

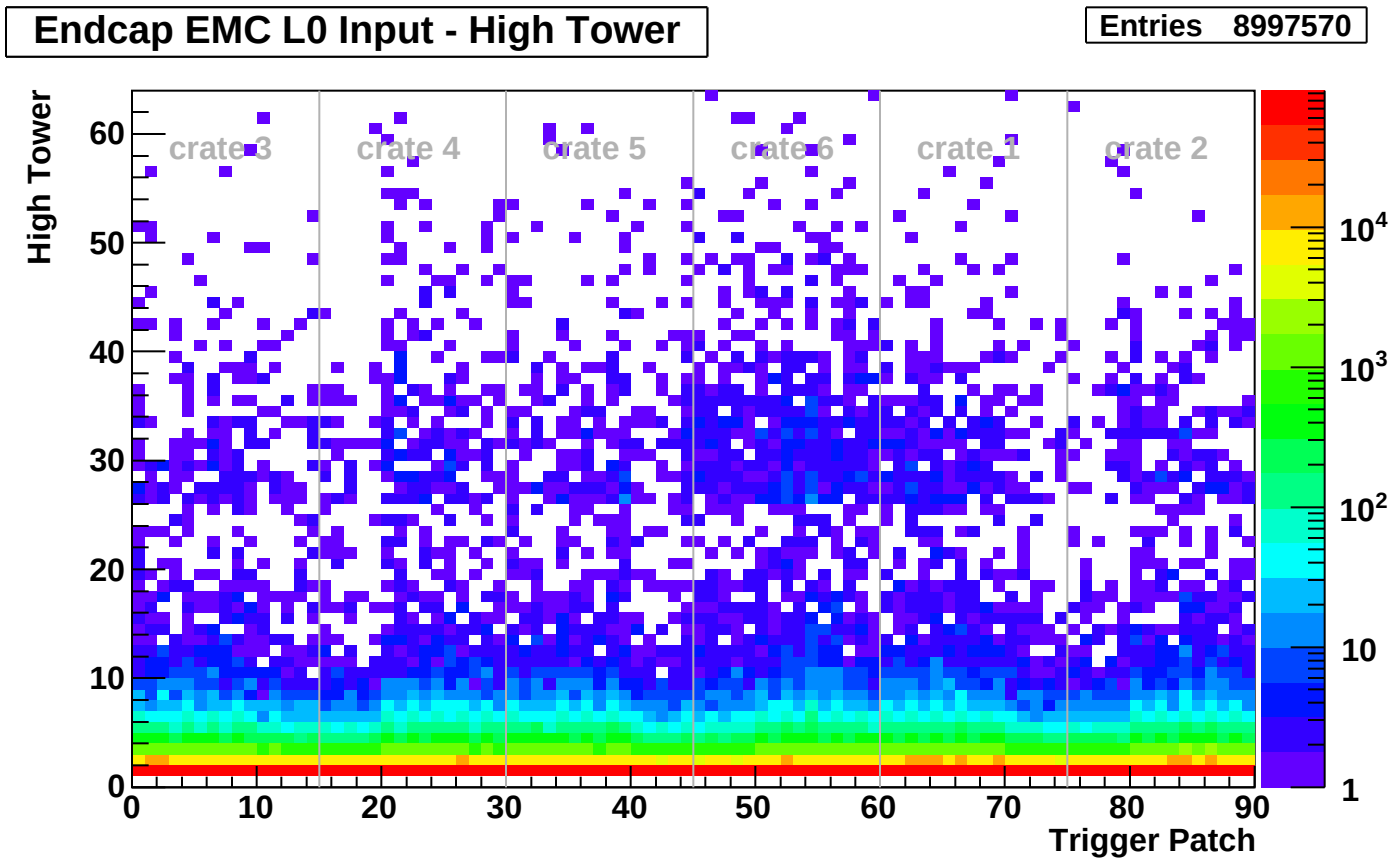

TCU bits
Entries 129659
TCU bits - Simulated
8029 18109 38317 1788 3301 225 59686 204
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

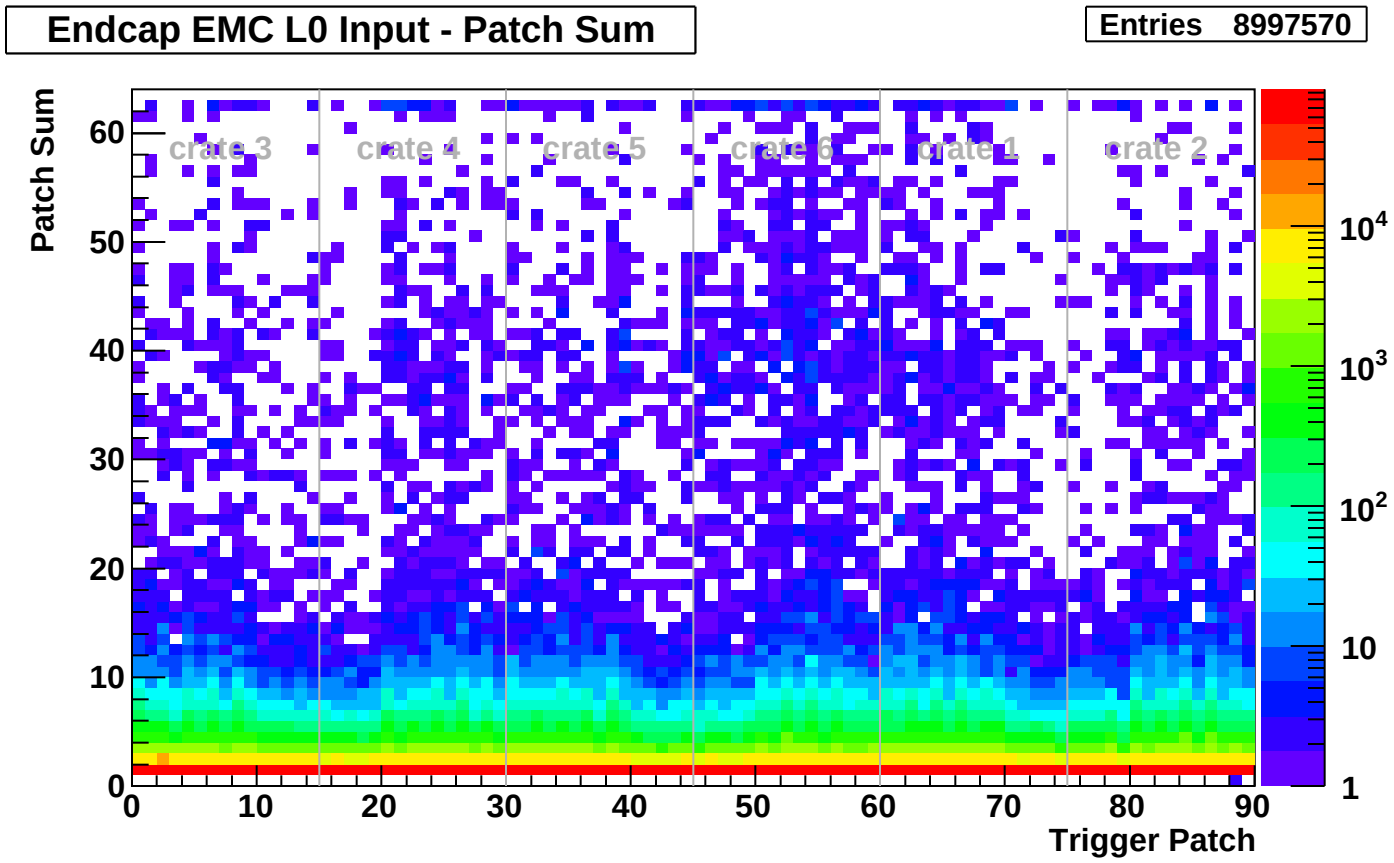

BunchId7Bit (BHT3)
Entries 1443

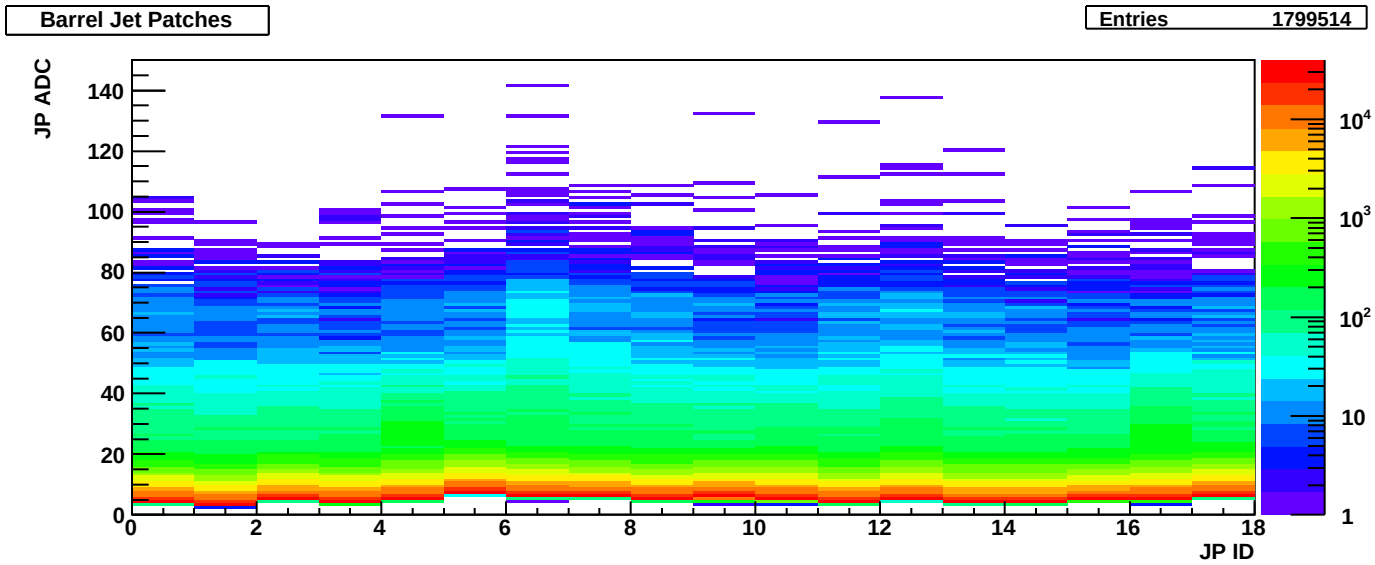

BunchId7Bit (all events)
Entries 99973

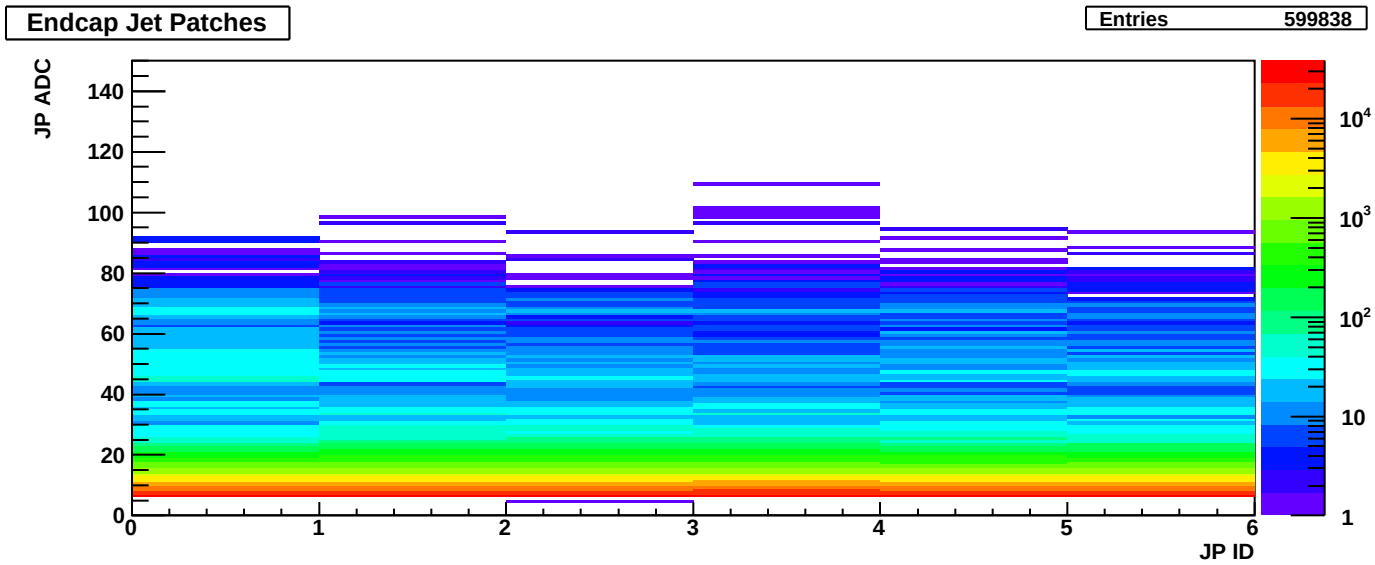

Barrel EMC L0 Input - High Tower
Entries 2.99919e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

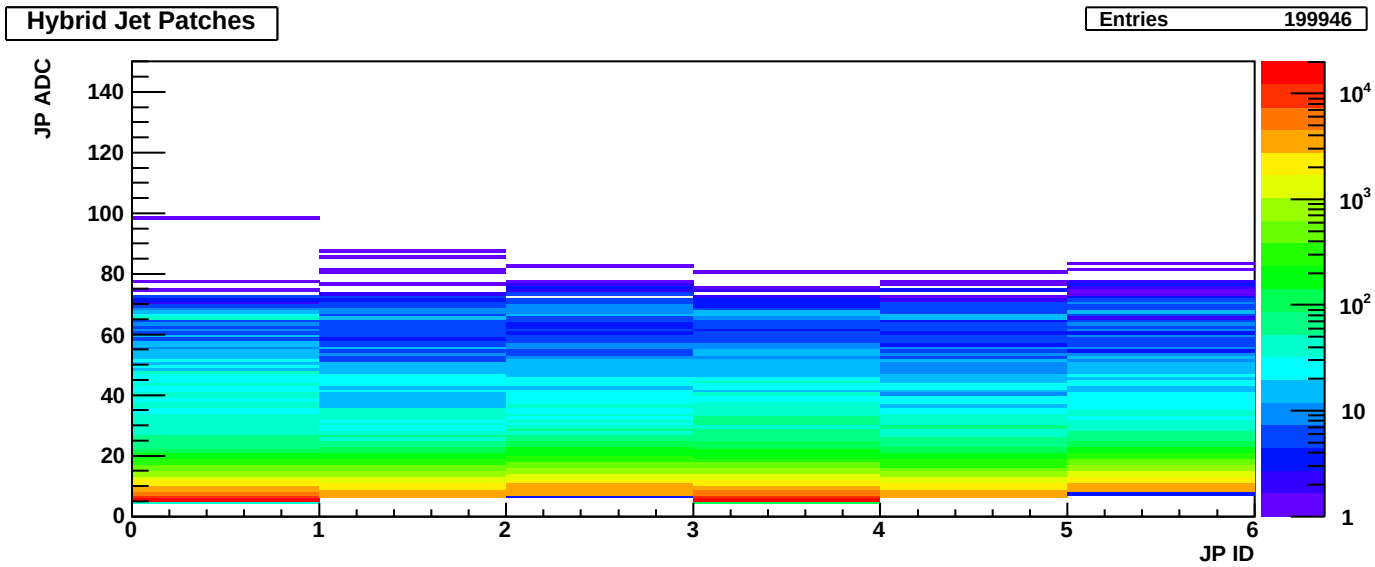

Barrel EMC L0 Input - Patch Sum
Entries 2.99919e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

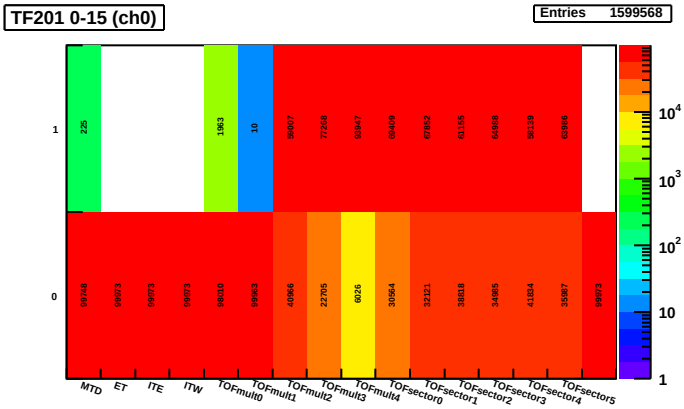

Endcap EMC L0 Input - High Tower
Entries 8997570
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

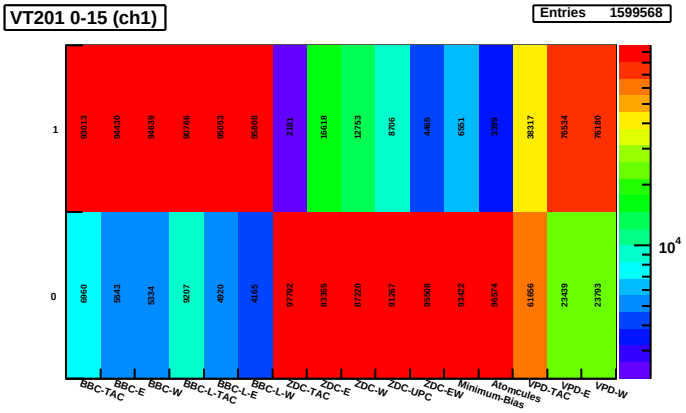

Endcap EMC L0 Input - Patch Sum
Entries 8997570
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

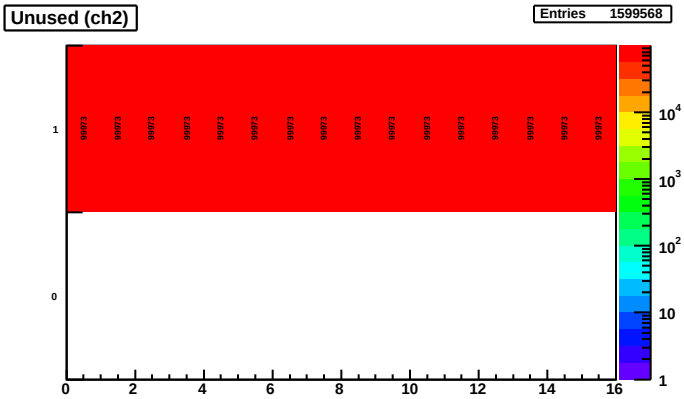

Barrel Jet Patches
Entries 1799514
JP ADC
JP ID

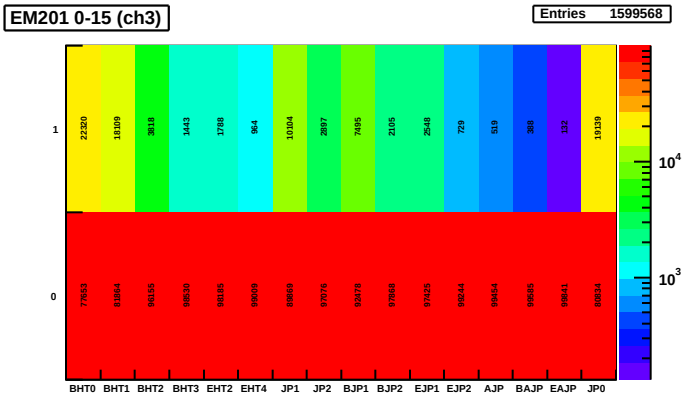

Endcap Jet Patches
Entries 599838
JP ADC
JP ID

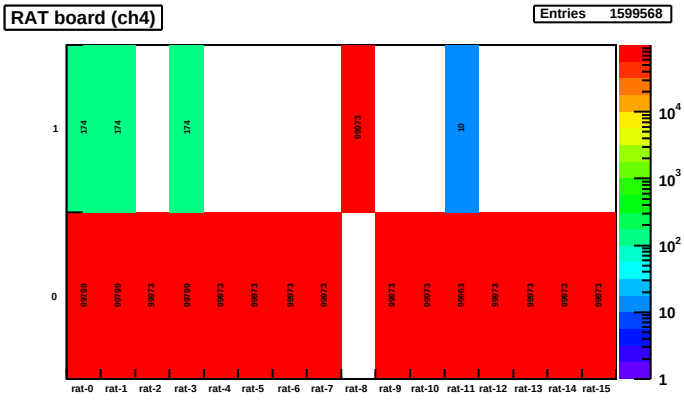

Hybrid Jet Patches
Entries 199946
JP ADC
JP ID

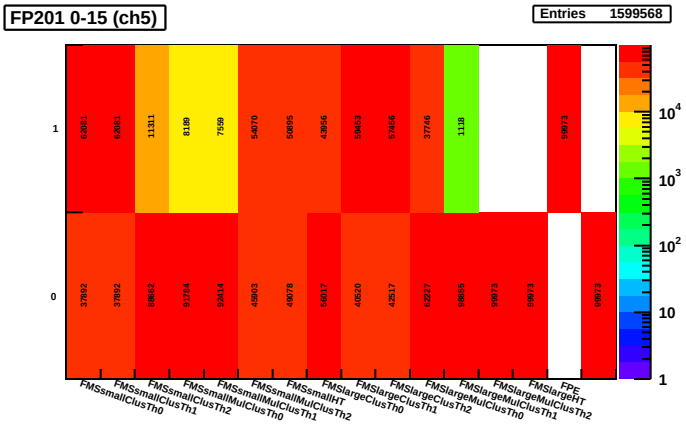

TF201 0-15 (ch0)
Entries 1599568
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

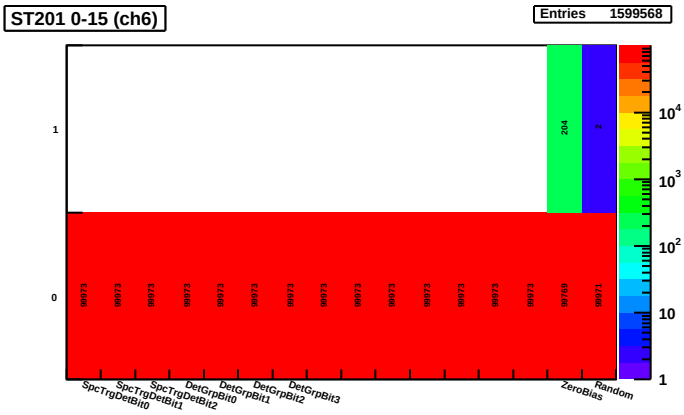

VT201 0-15 (ch1)
Entries 1599568
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

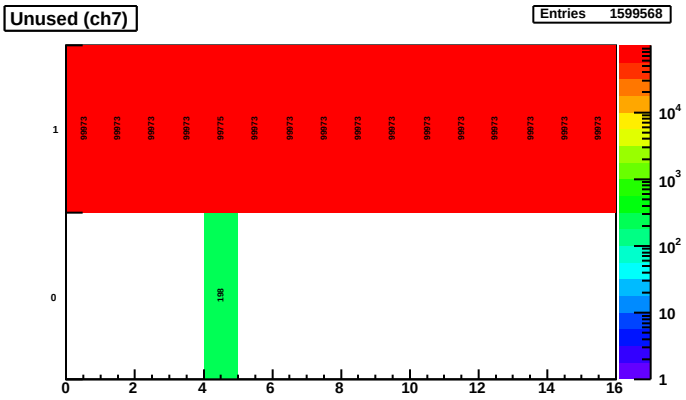

Unused (ch2)
Entries 1599568

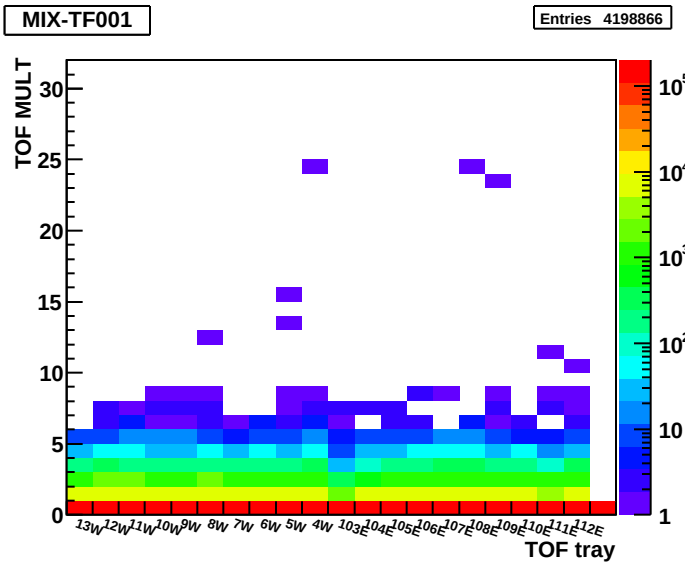

EM201 0-15 (ch3)
Entries 1599568
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

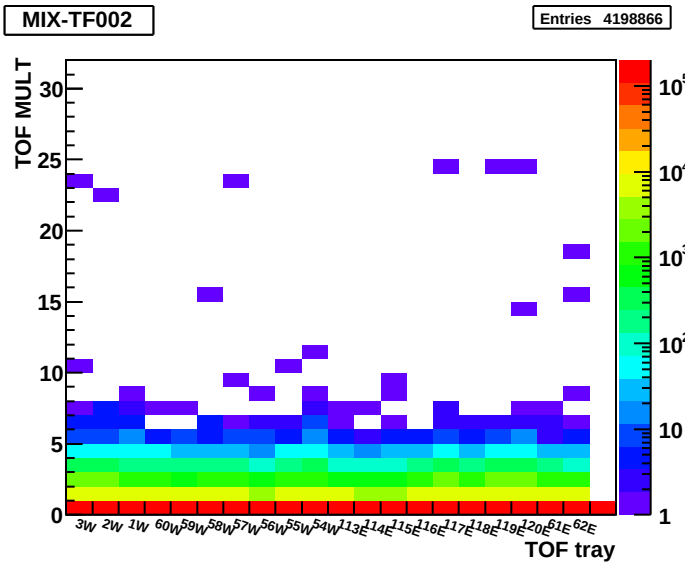

RAT board (ch4)
Entries 1599568
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

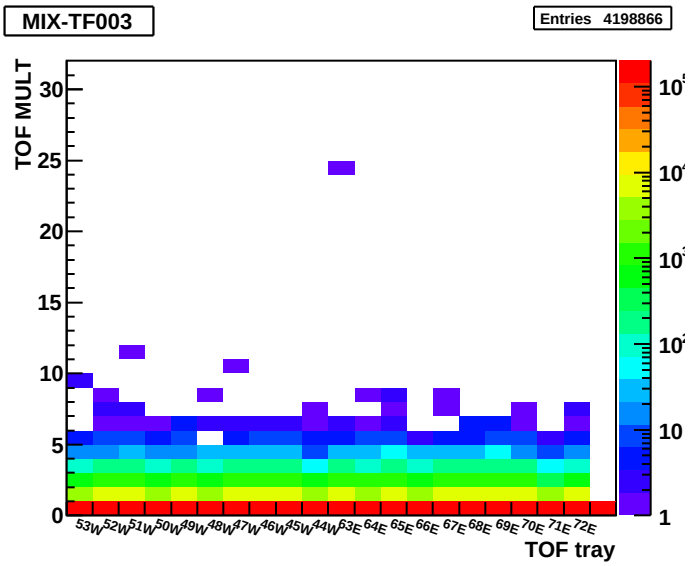

FP201 0-15 (ch5)
Entries 1599568
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

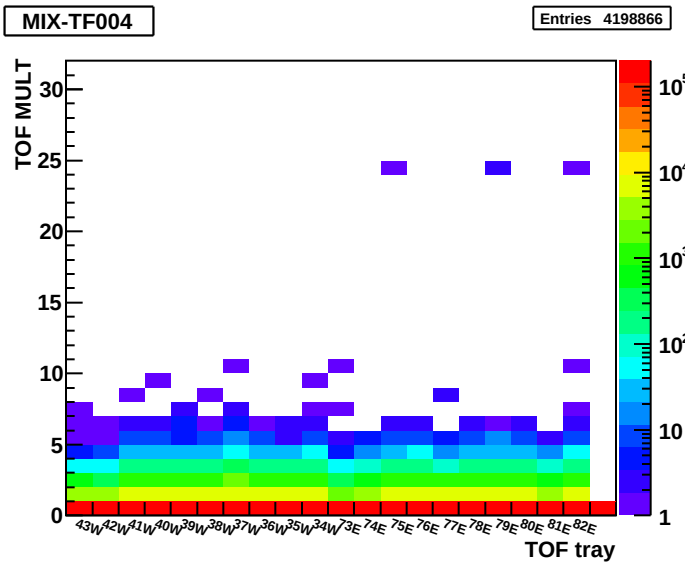

ST201 0-15 (ch6)
Entries 1599568
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

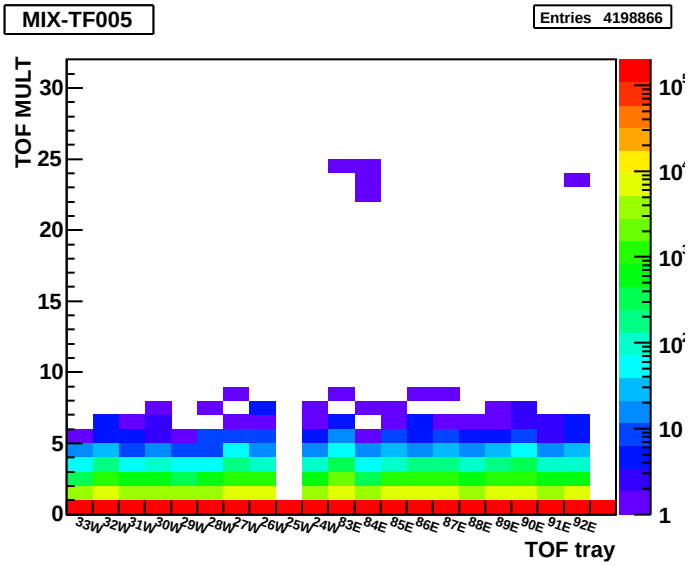

Unused (ch7)
Entries 1599568

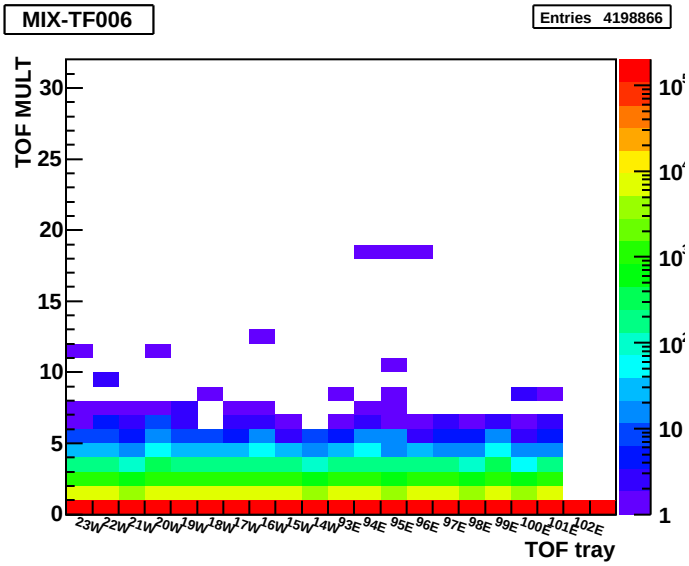

MIX-TF001
Entries 4198866
TOF MULT
TOF tray

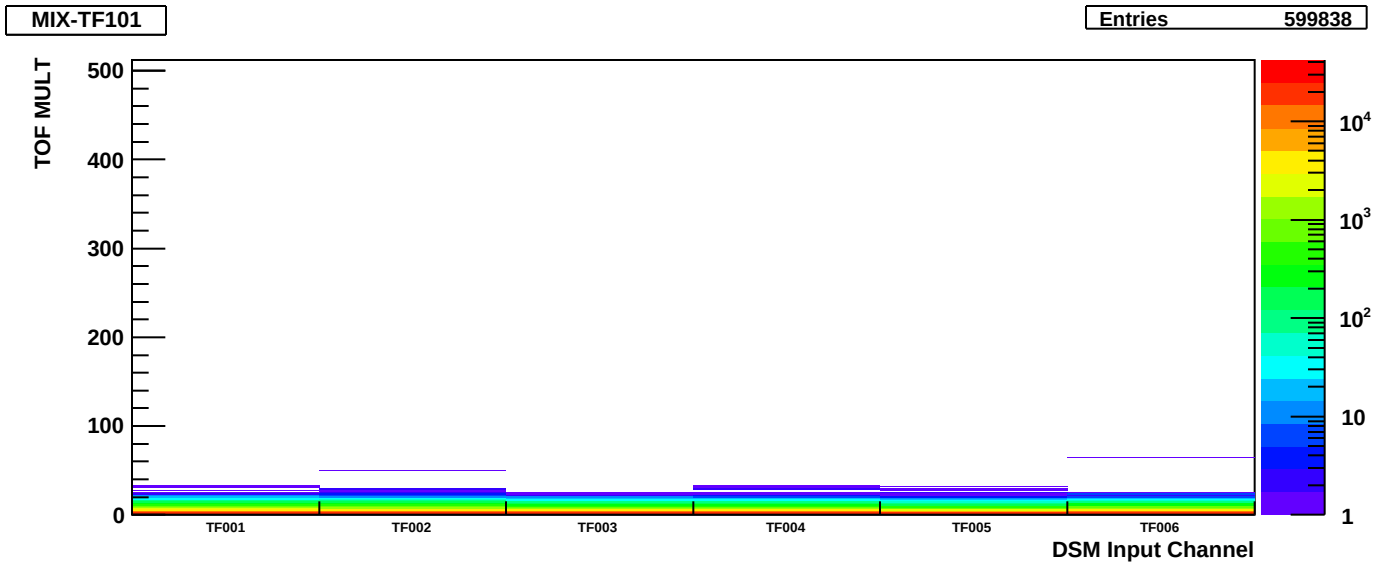

MIX-TF002
Entries 4198866
TOF MULT
TOF tray

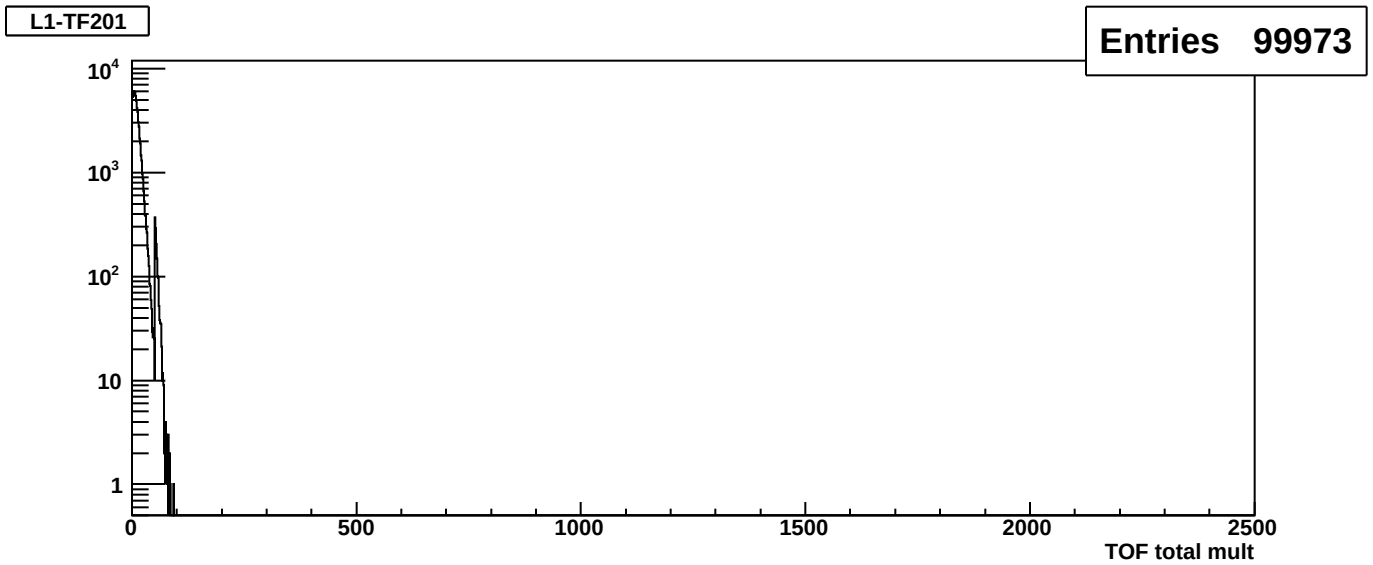

MIX-TF003
Entries 4198866
TOF MULT
TOF tray

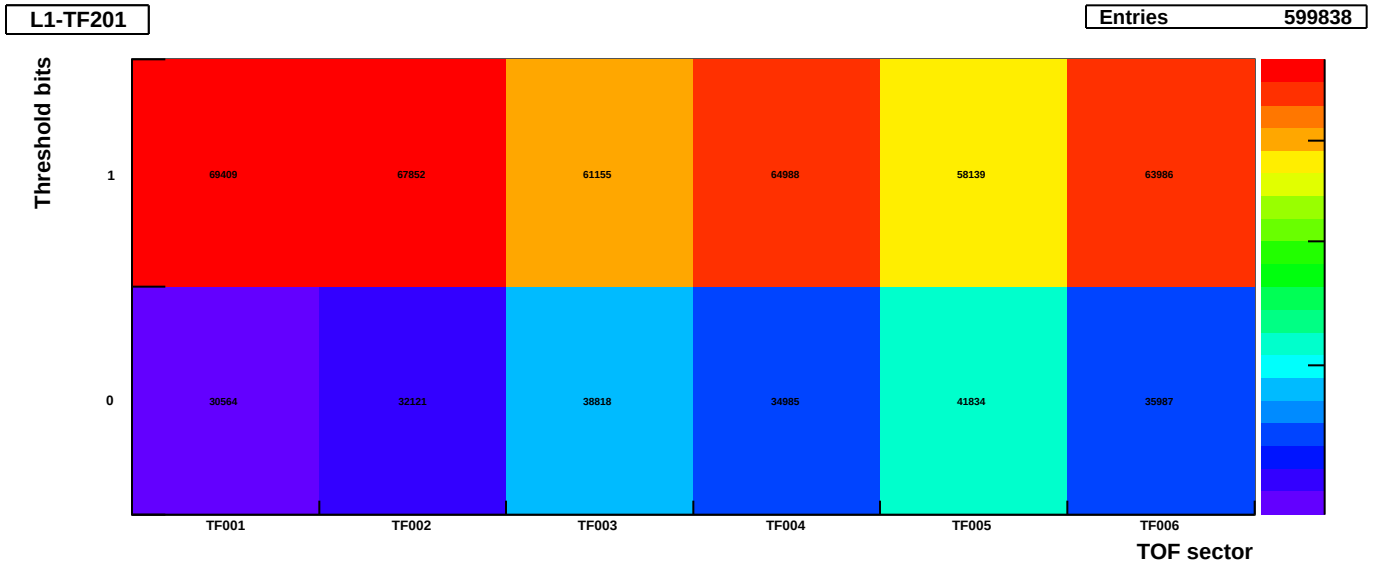

MIX-TF004
Entries 4198866
TOF MULT
TOF tray

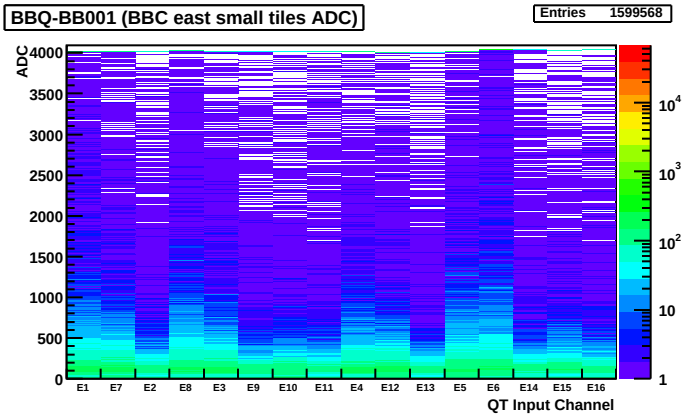

MIX-TF005
Entries 4198866
TOF MULT
TOF tray

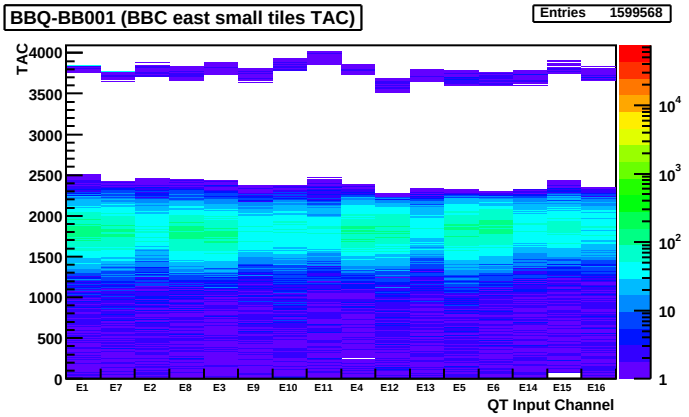

MIX-TF006
Entries 4198866
TOF MULT
TOF tray

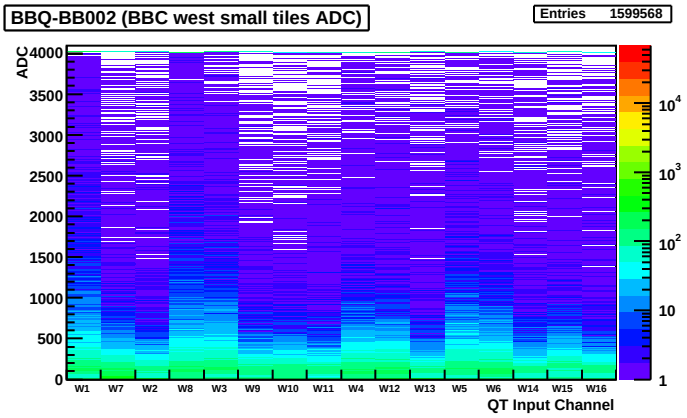

MIX-TF101
Entries 599838
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

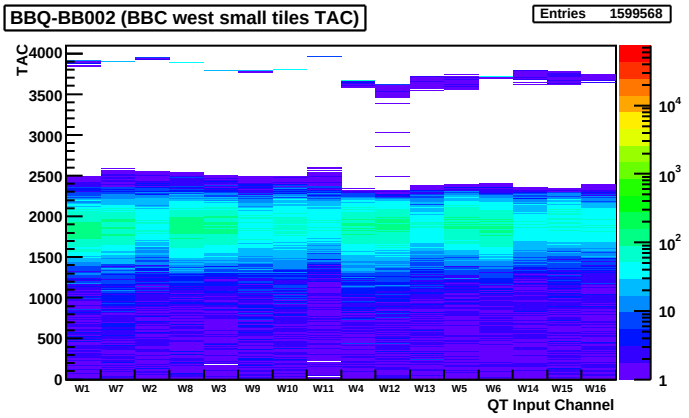

L1-TF201
Entries 99973
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

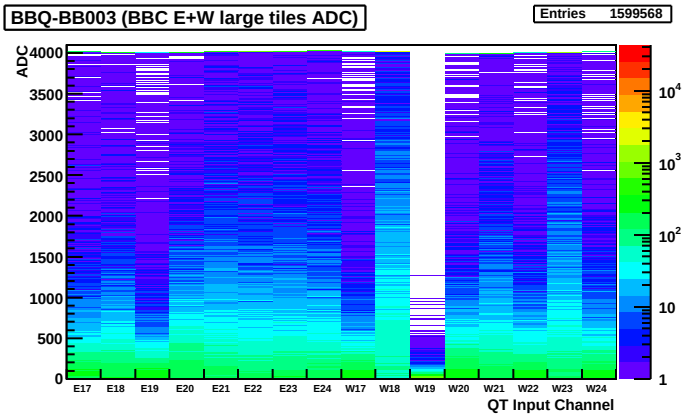

L1-TF201
Entries 599838
Threshold bits
1
0
69409
67852
61155
64988
58139
63986
30564
32121
38818
34985
41834
35987
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

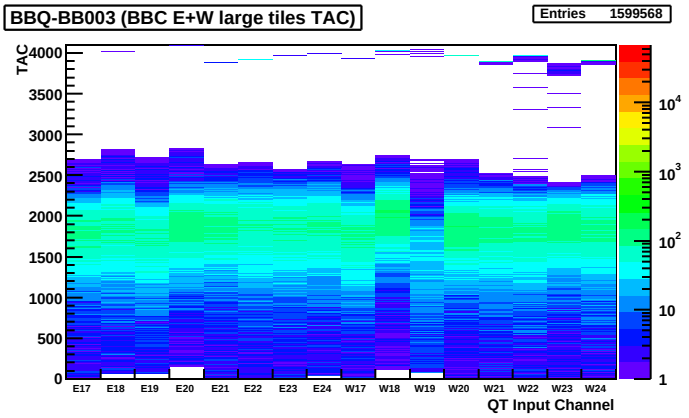

BBQ-BB001 (BBC east small tiles ADC)
Entries 1599568
ADC
QT Input Channel

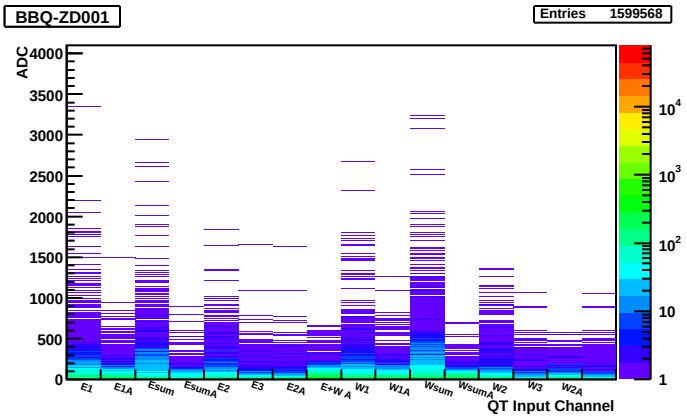

BBQ-BB001 (BBC east small tiles TAC)
Entries 1599568
TAC
QT Input Channel

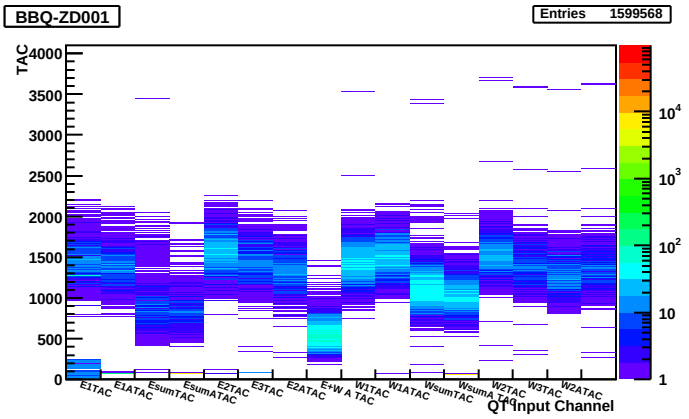

BBQ-BB002 (BBC west small tiles ADC)
Entries 1599568
ADC
QT Input Channel

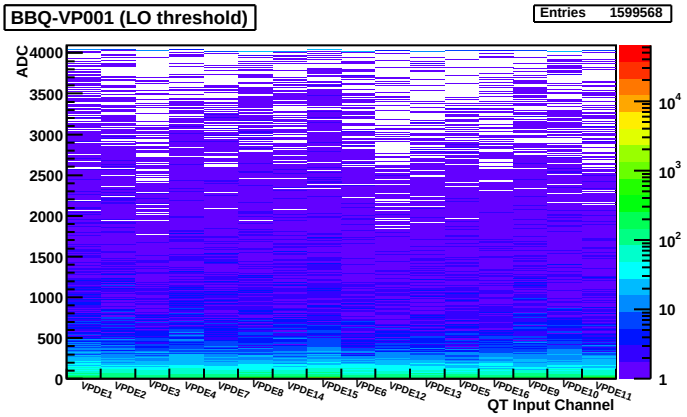

BBQ-BB002 (BBC west small tiles TAC)
Entries 1599568
TAC
QT Input Channel

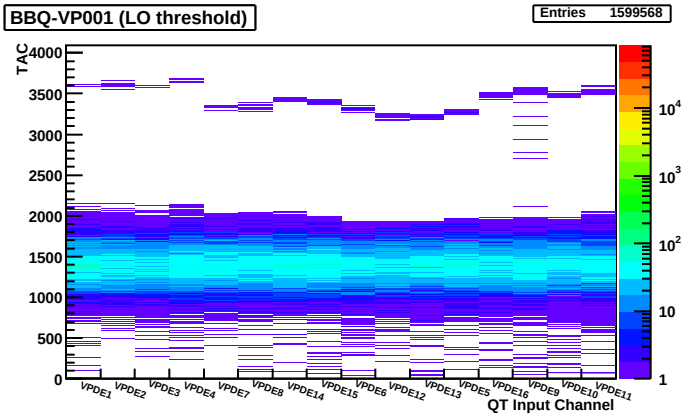

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1599568
ADC
QT Input Channel

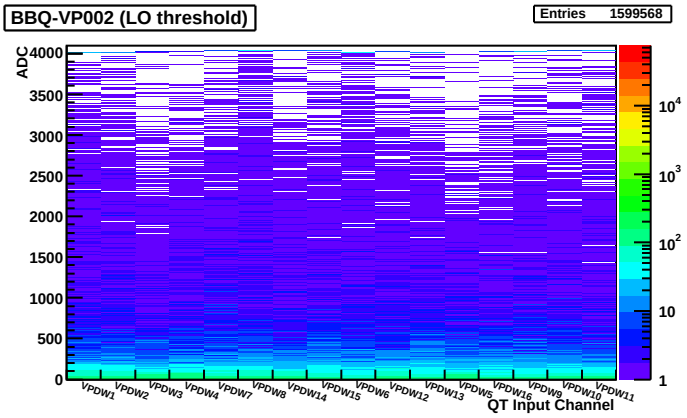

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1599568
TAC
QT Input Channel

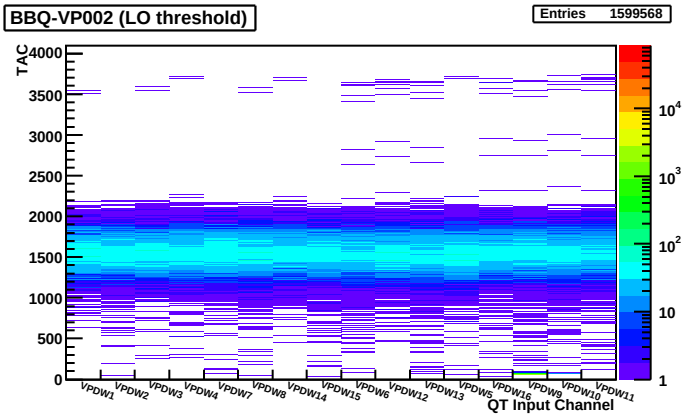

BBQ-ZD001
Entries 1599568
ADC
QT Input Channel

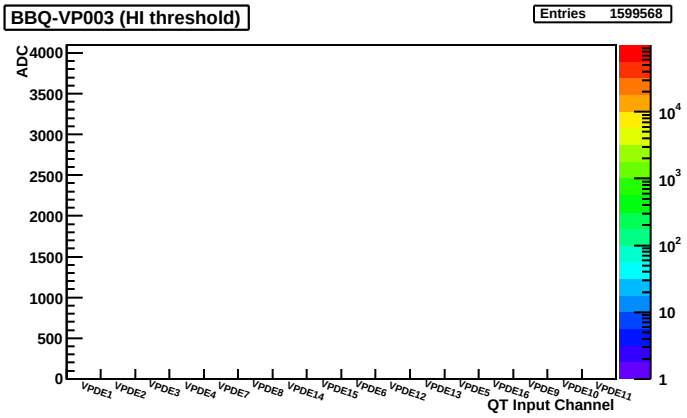

BBQ-ZD001
Entries 1599568
TAC
QT Input Channel

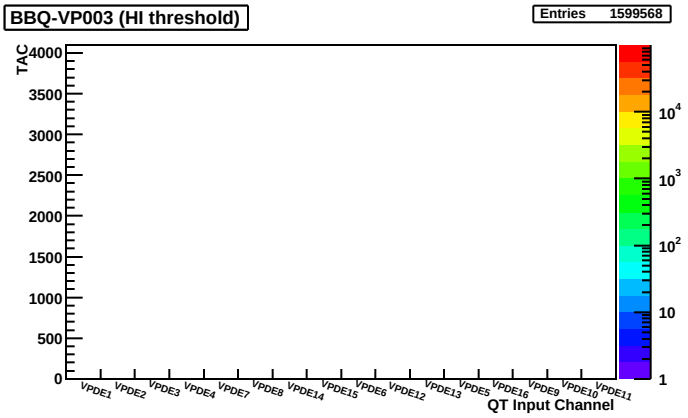

BBQ-VP001 (LO threshold)
Entries 1599568
ADC
QT Input Channel

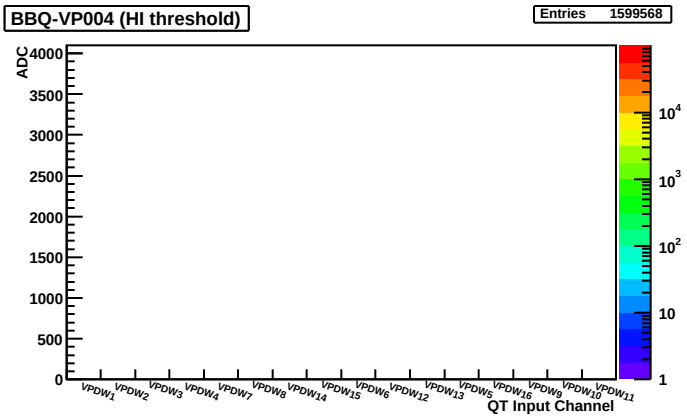

BBQ-VP001 (LO threshold)
Entries 1599568
TAC
QT Input Channel

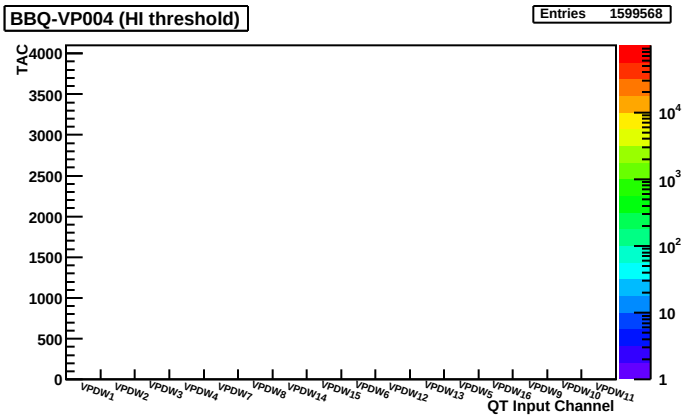

BBQ-VP002 (LO threshold)
Entries 1599568
ADC
QT Input Channel

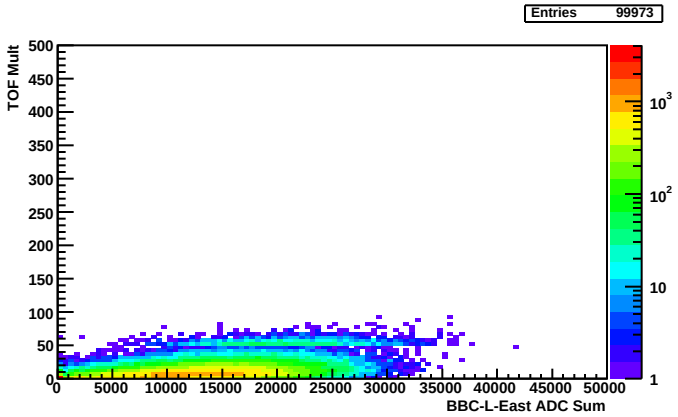

BBQ-VP002 (LO threshold)
Entries 1599568
TAC
QT Input Channel

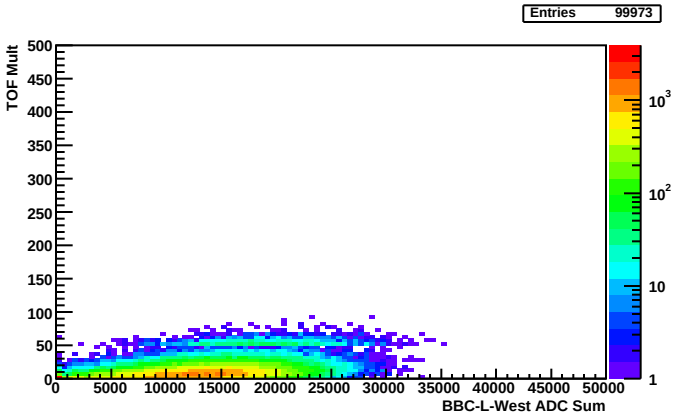

BBQ-VP003 (HI threshold)
Entries 1599568
ADC
QT Input Channel

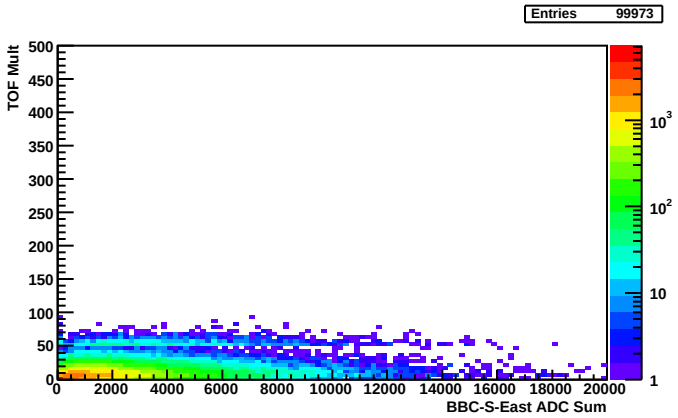

BBQ-VP003 (HI threshold)
Entries 1599568
TAC
QT Input Channel

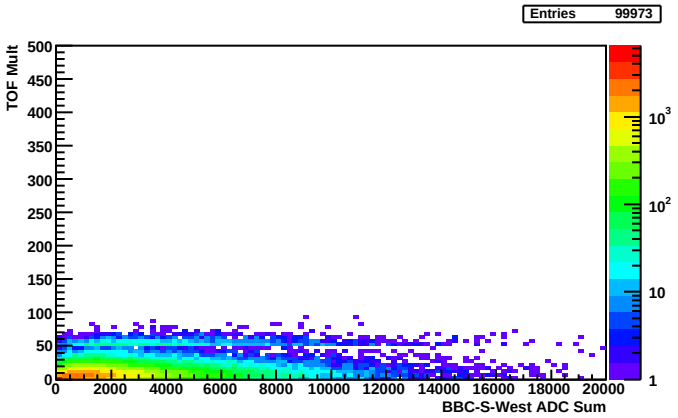

BBQ-VP004 (HI threshold)
Entries 1599568
ADC
QT Input Channel

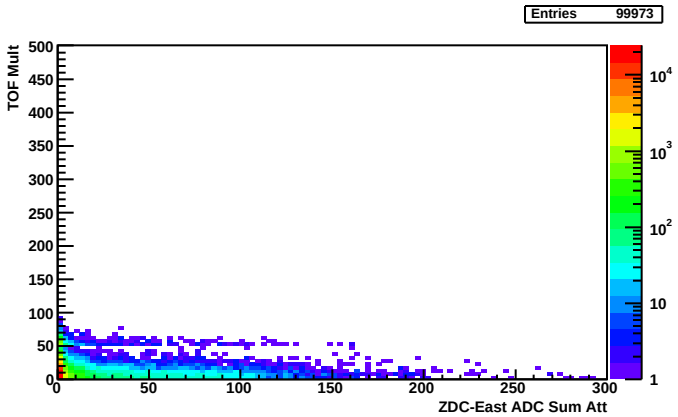

BBQ-VP004 (HI threshold)
Entries 1599568
TAC
QT Input Channel

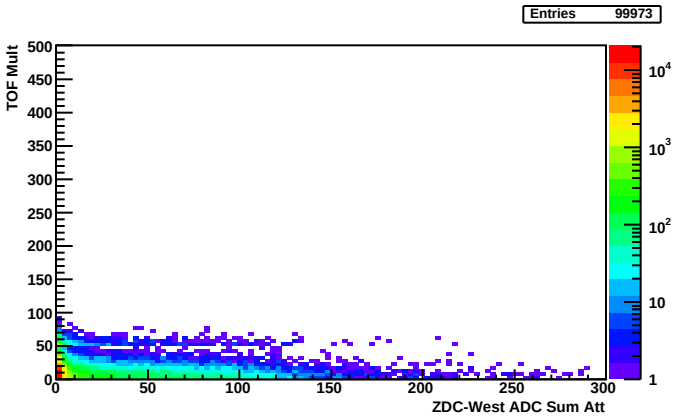

Entries 99973
TOF Mult
BBC-L-East ADC Sum

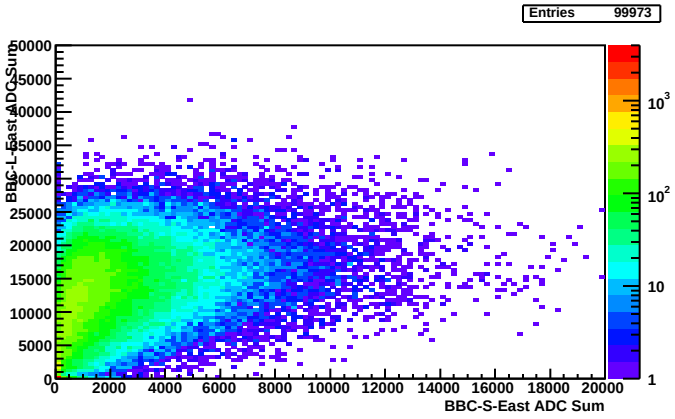

Entries 99973
TOF Mult
BBC-L-West ADC Sum

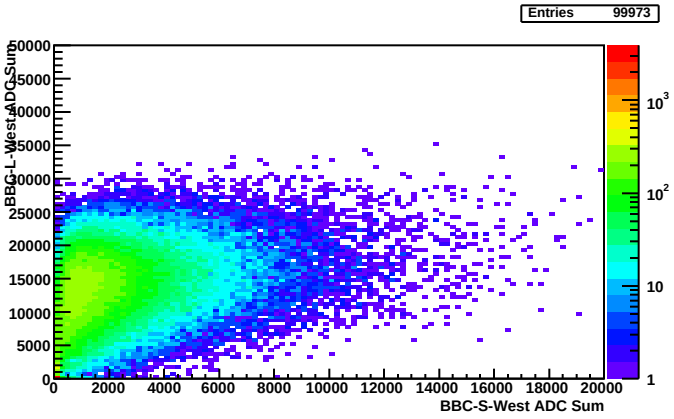

Entries 99973
TOF Mult
BBC-S-East ADC Sum

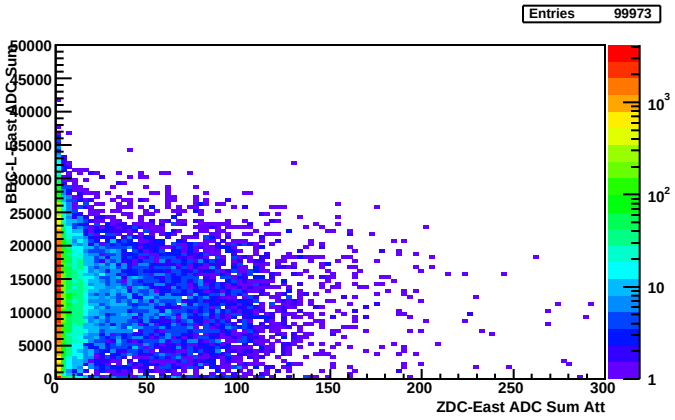

Entries 99973
TOF Mult
BBC-S-West ADC Sum

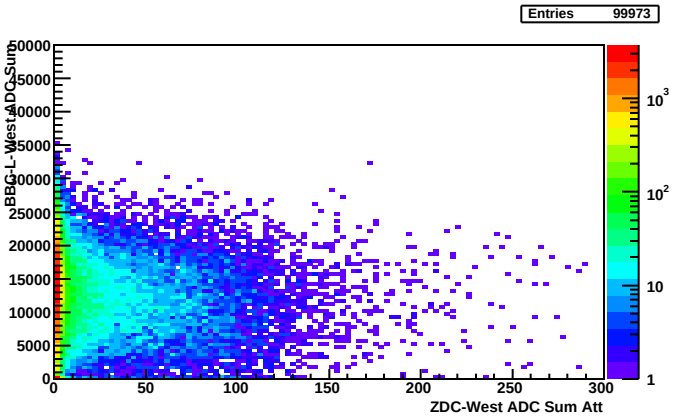

Entries 99973
TOF Mult
ZDC-East ADC Sum Att

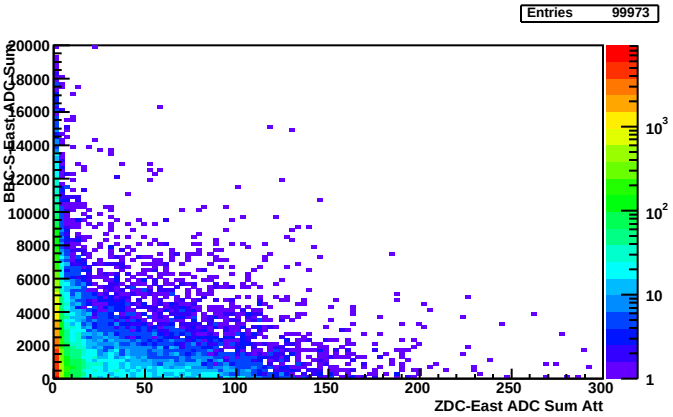

Entries 99973
TOF Mult
ZDC-West ADC Sum Att

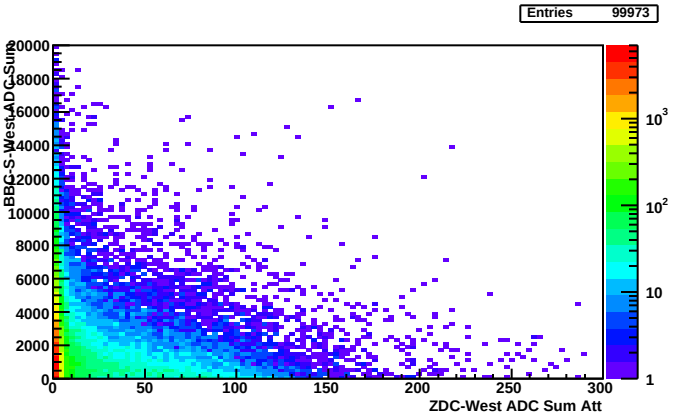

Entries 99973
BBC-L-East ADC Sum
BBC-S-East ADC Sum

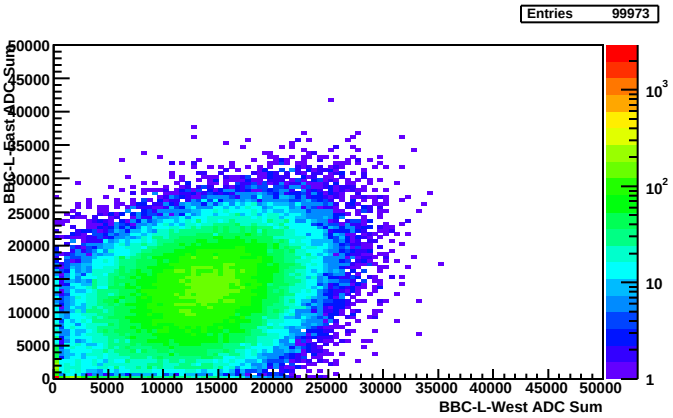

Entries 99973
BBC-L-West ADC Sum
BBC-S-West ADC Sum

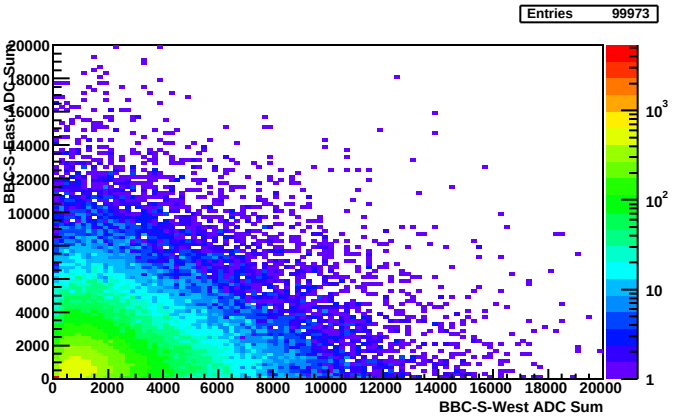

Entries 99973
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

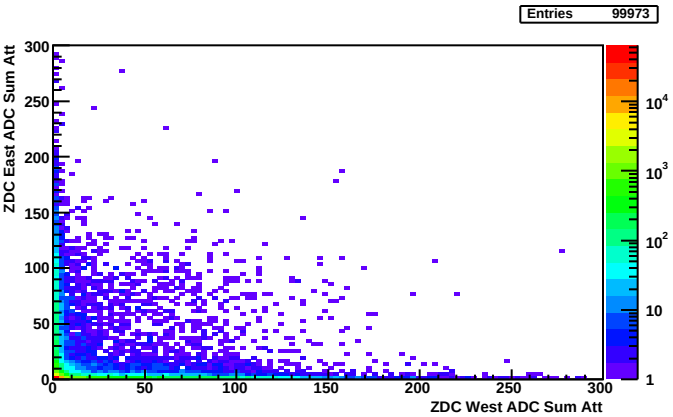

Entries 99973
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

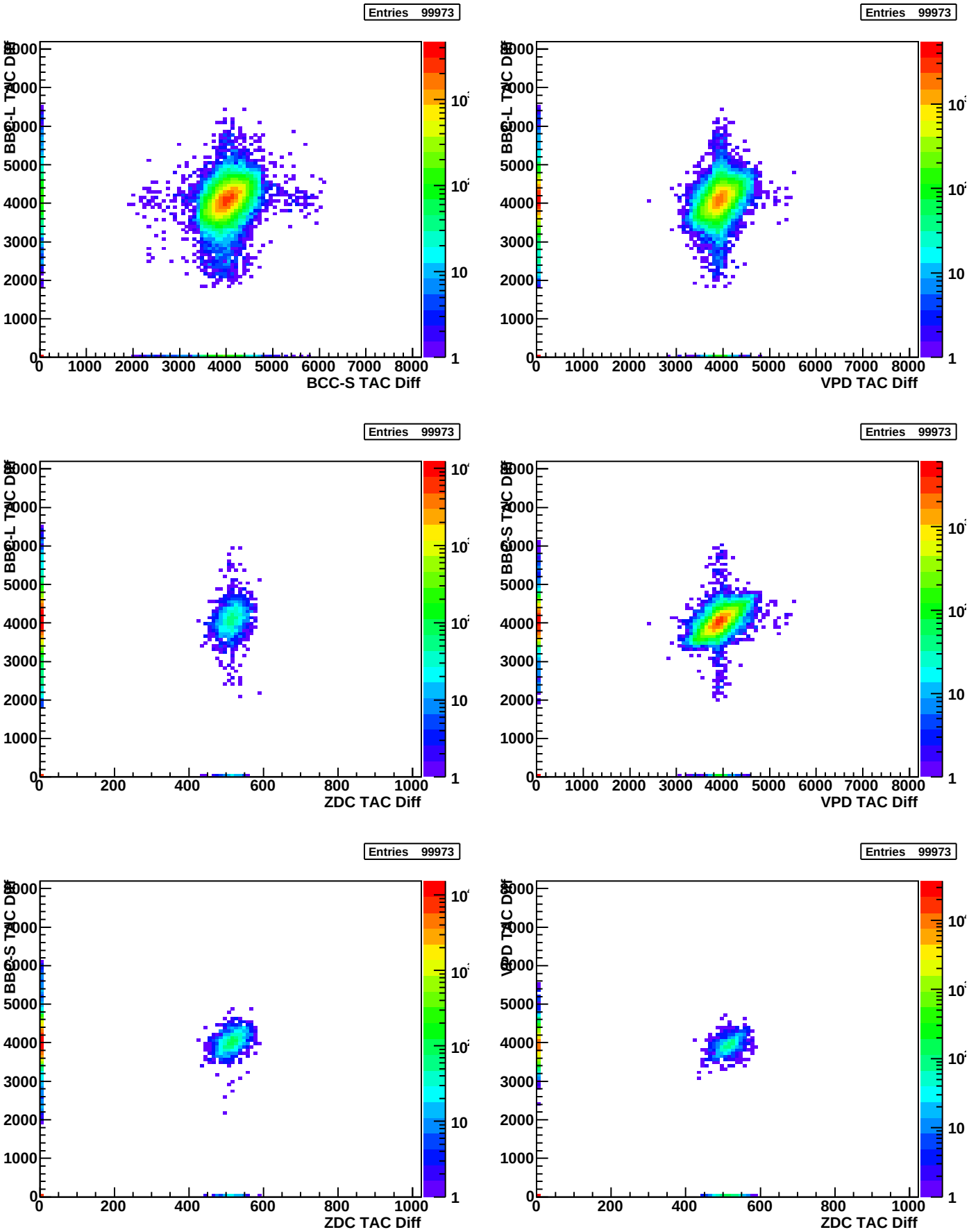

Entries 99973
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

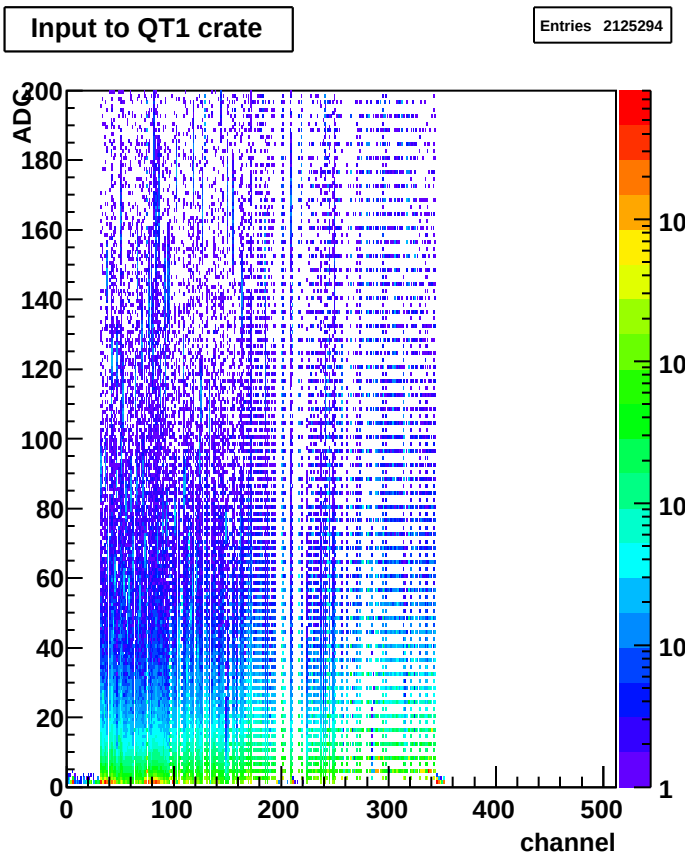

Entries 99973
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

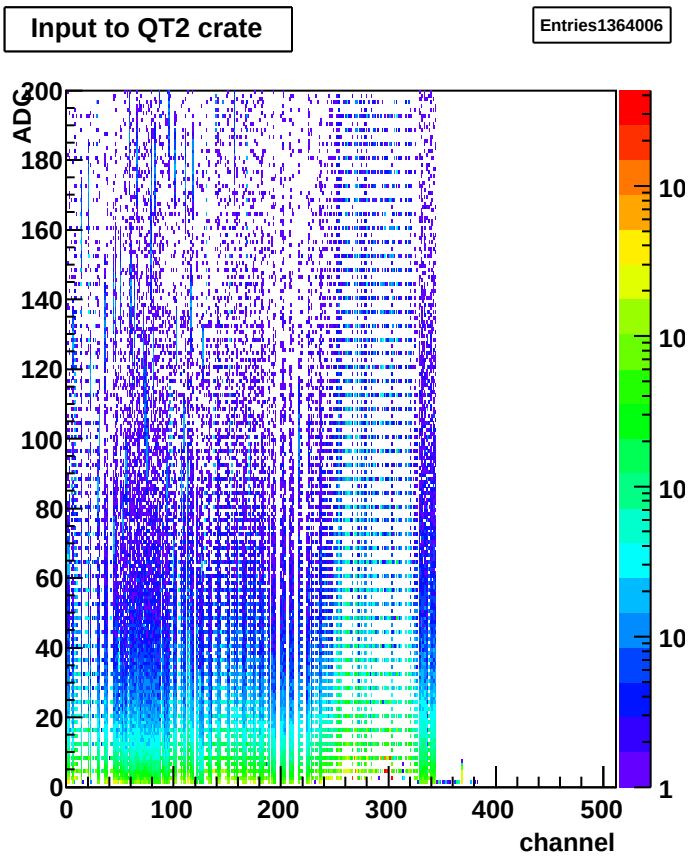

Entries 99973
BBC-L-East ADC Sum
BBC-L-West ADC Sum

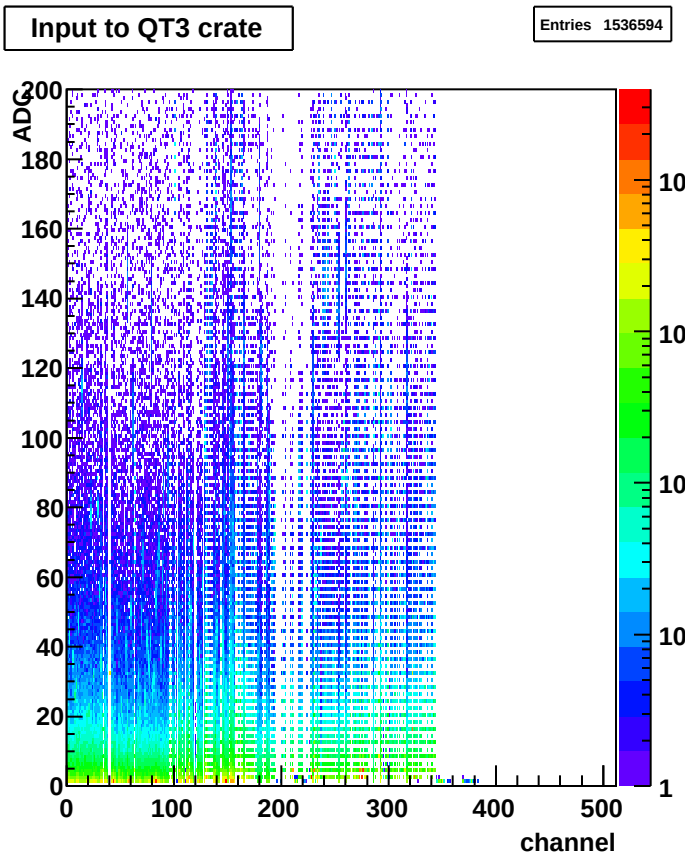

Entries 99973
BBC-S-East ADC Sum
BBC-S-West ADC Sum

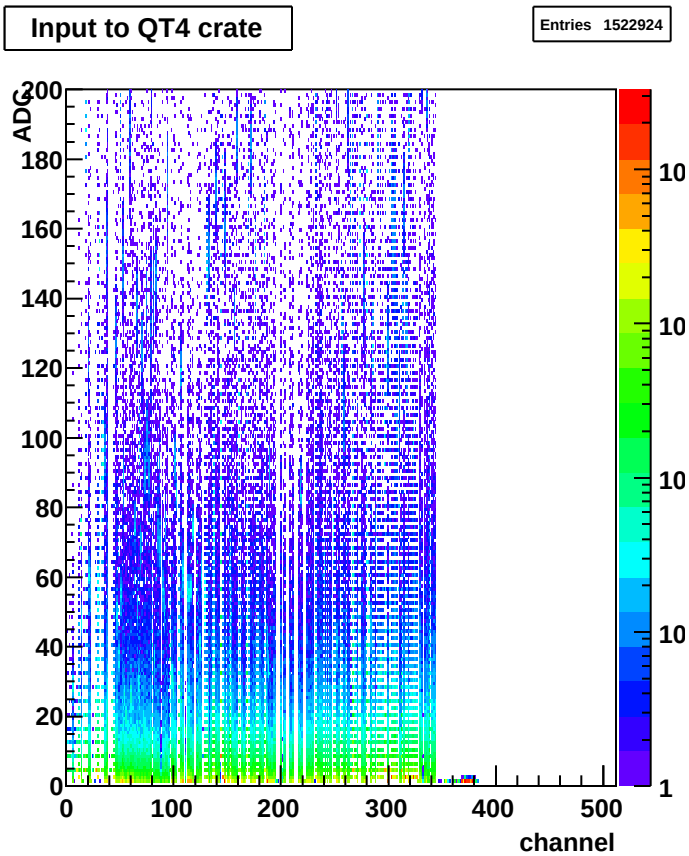

Entries 99973
ZDC East ADC Sum Att
ZDC West ADC Sum Att

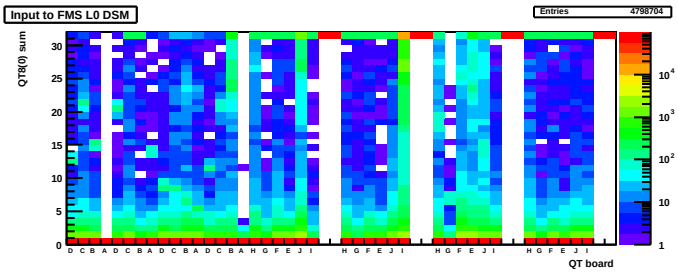

Entries 99973
BBC-L TAC Diff
BCC-S TAC Diff
Entries 99973
BBC-L TAC Diff
VPD TAC Diff
Entries 99973
BBC-L TAC Diff
ZDC TAC Diff
Entries 99973
BBC-S TAC Diff
VPD TAC Diff
Entries 99973
BBC-S TAC Diff
ZDC TAC Diff
Entries 99973
VPD TAC Diff
ZDC TAC Diff

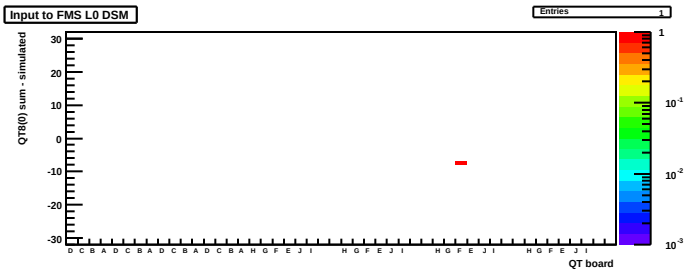

Input to QT1 crate
Entries 2125294
ADC
channel

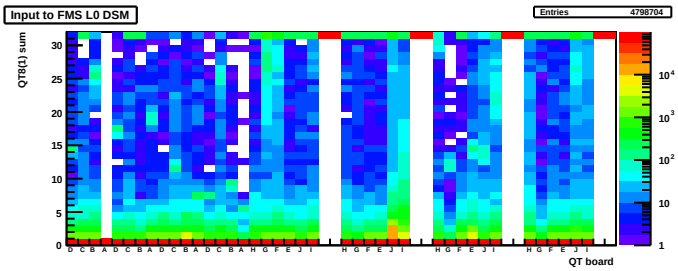

Input to QT2 crate
Entries1364006
ADC
channel

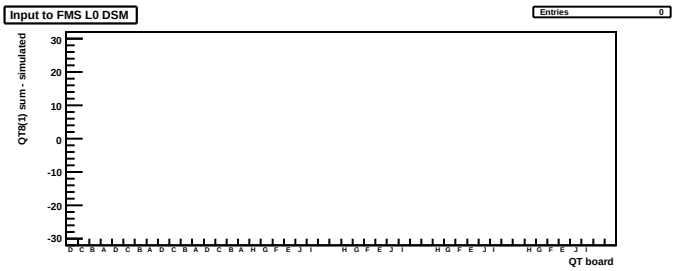

Input to QT3 crate
Entries 1536594
ADC
channel

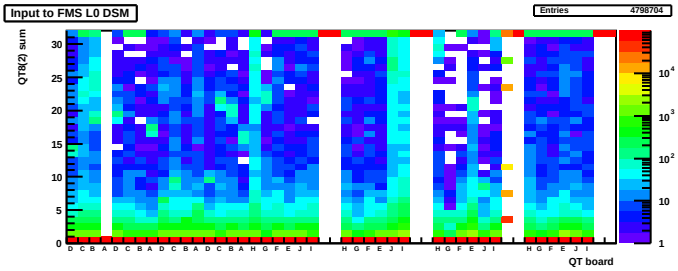

Input to QT4 crate
Entries 1522924
ADC
channel

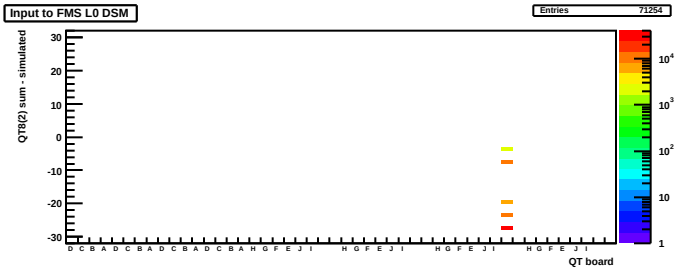

Input to FMS L0 DSM
Entries 4798704
QT8(0) sum
QT board

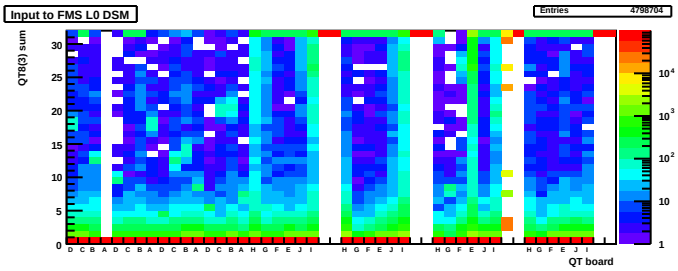

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

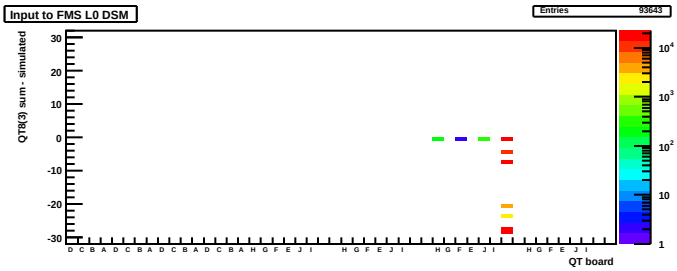

Input to FMS L0 DSM
Entries 4798704
QT8(1) sum
QT board

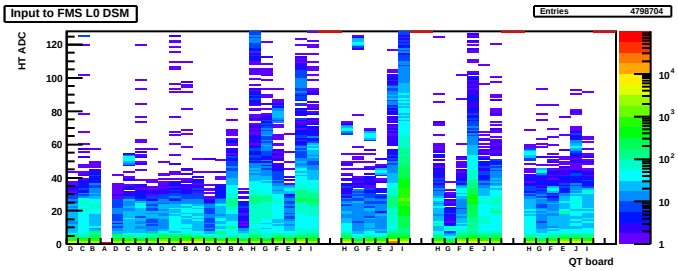

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

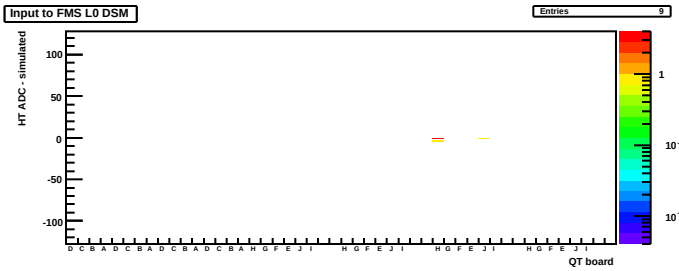

Input to FMS L0 DSM
Entries 4798704
QT8(2) sum
QT board

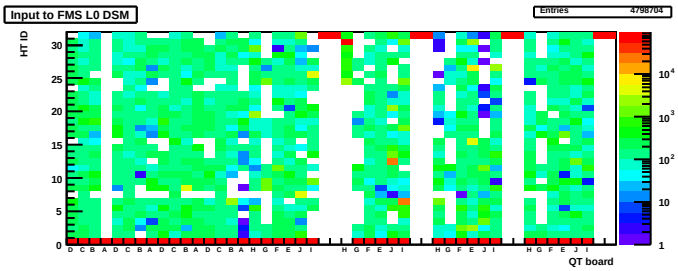

Input to FMS L0 DSM
Entries 71254
QT8(2) sum - simulated
QT board

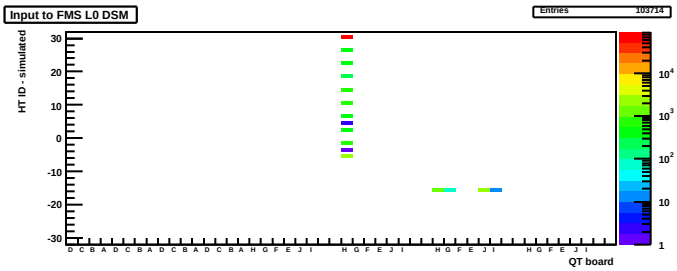

Input to FMS L0 DSM
Entries 4798704
QT8(3) sum
QT board

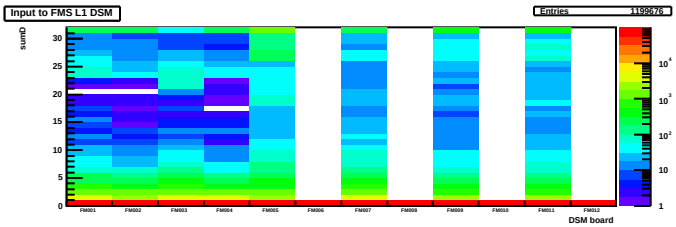

Input to FMS L0 DSM
Entries 93643
QT8(3) sum - simulated
QT board

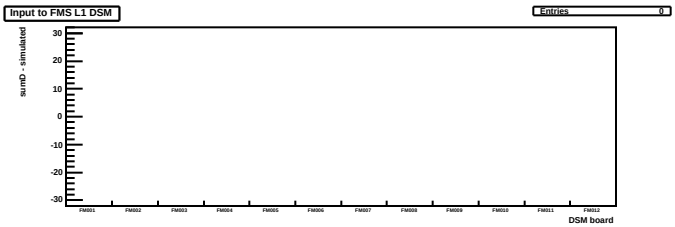

Input to FMS L0 DSM
Entries 4798704
HT ADC
QT board

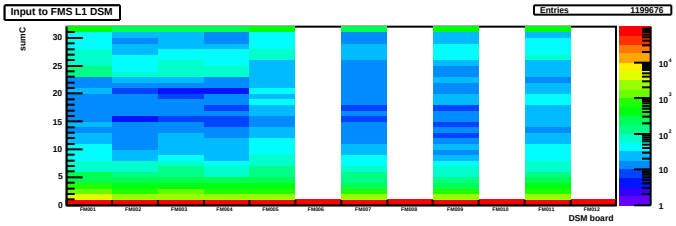

Input to FMS L0 DSM
Entries 9
HT ADC - simulated
QT board

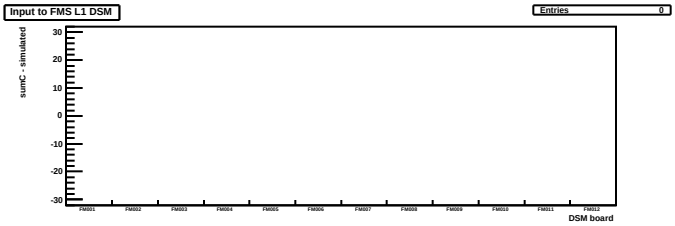

Input to FMS L0 DSM
Entries 4798704
HT ID
QT board

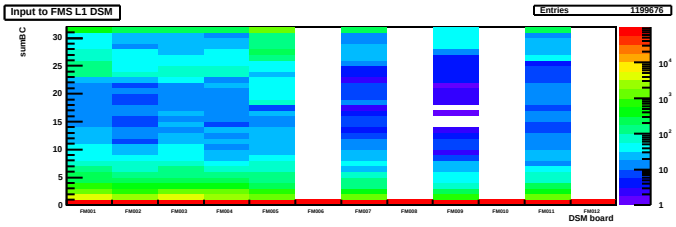

Input to FMS L0 DSM
Entries 103714
HT ID - simulated
QT board

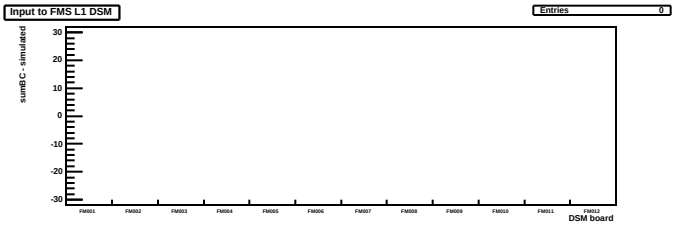

Input to FMS L1 DSM
Entries 1199676
sumD
DSM board

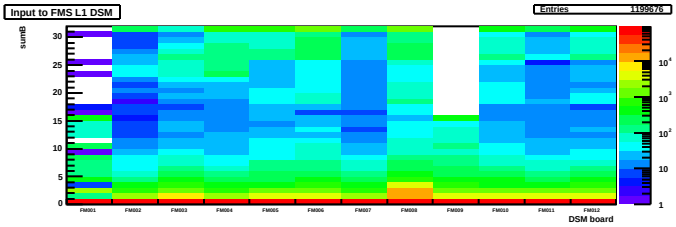

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

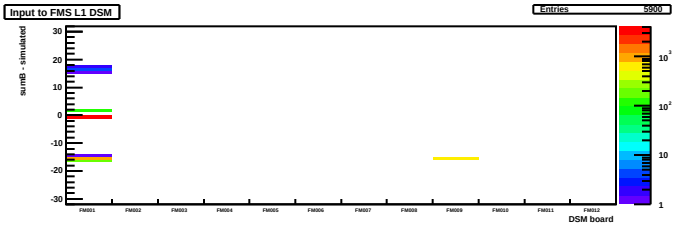

Input to FMS L1 DSM
Entries 1199676
sumC
DSM board

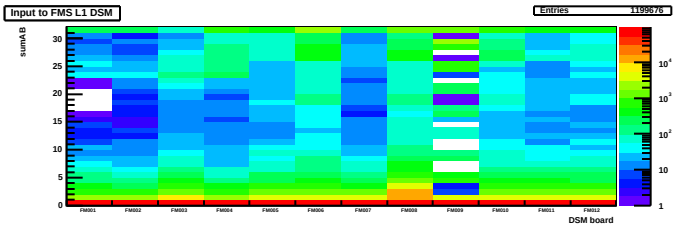

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

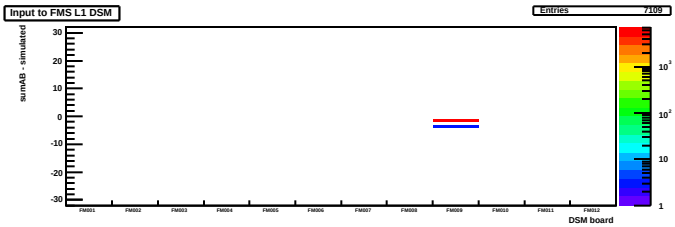

Input to FMS L1 DSM
Entries 1199676
sumBC
DSM board

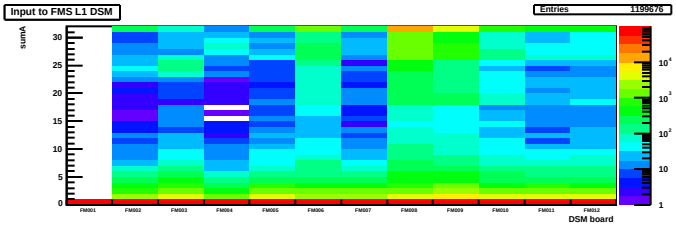

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

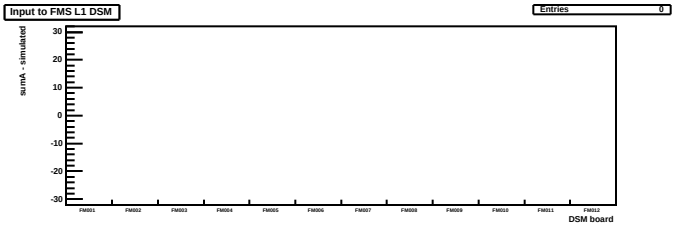

Input to FMS L1 DSM
Entries 1199676
sumB
DSM board

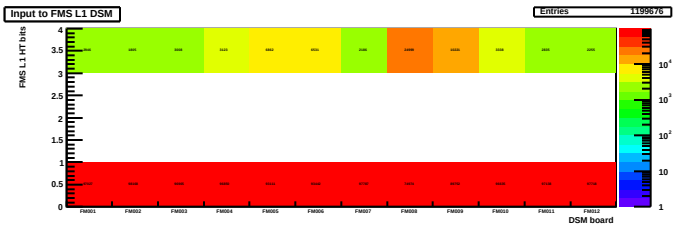

Input to FMS L1 DSM
Entries 5900
sumB - simulated
DSM board

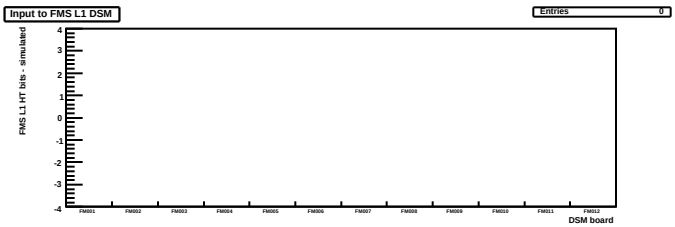

Input to FMS L1 DSM
Entries 1199676
sumAB
DSM board

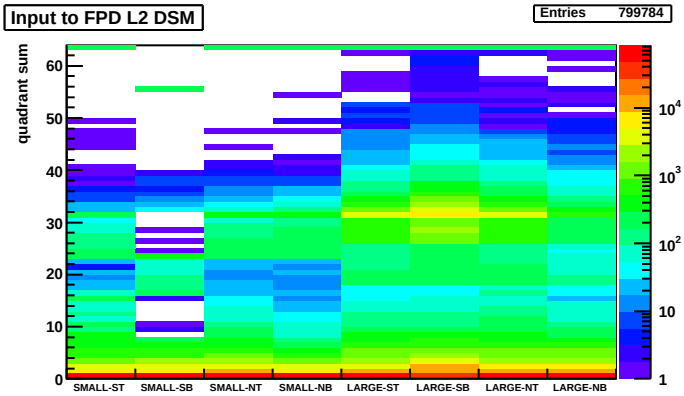

Input to FMS L1 DSM
Entries 7109
sumAB - simulated
DSM board

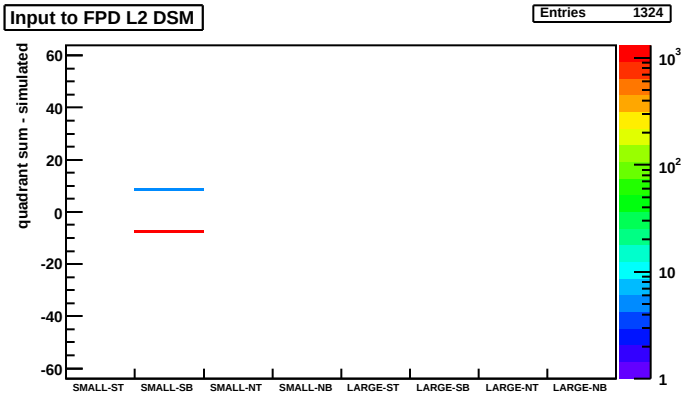

Input to FMS L1 DSM
Entries 1199676
sumA
DSM board

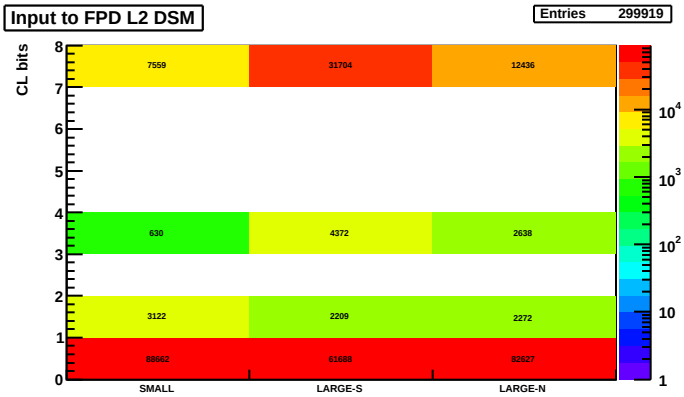

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

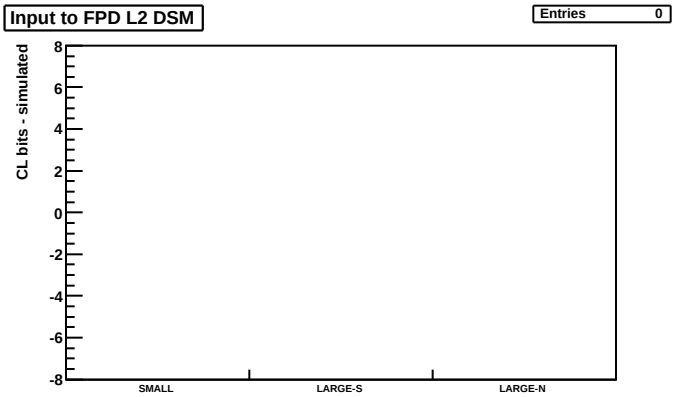

Input to FMS L1 DSM
Entries 1199676
FMS L1 HT bits
DSM board

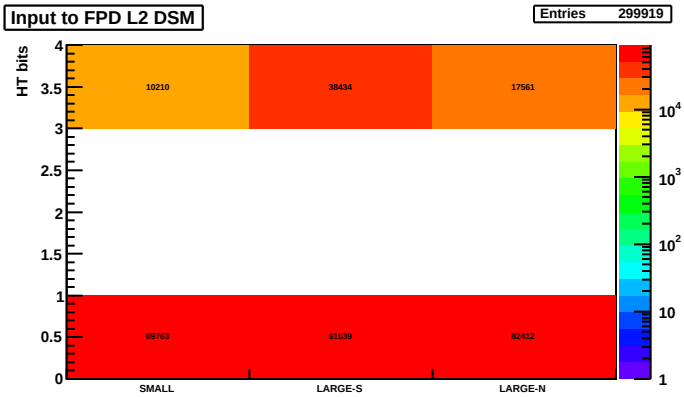

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

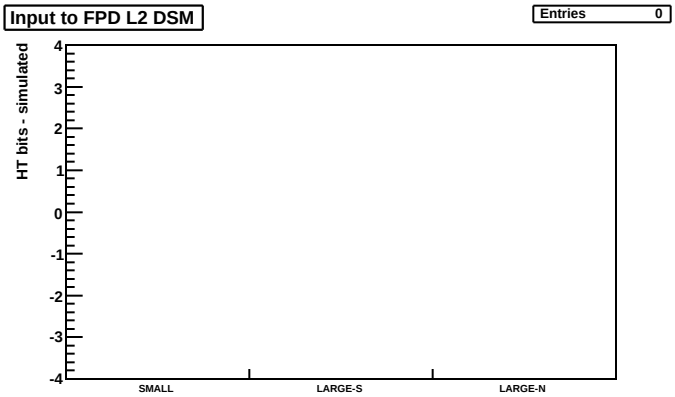

Input to FPD L2 DSM
Entries 799784
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

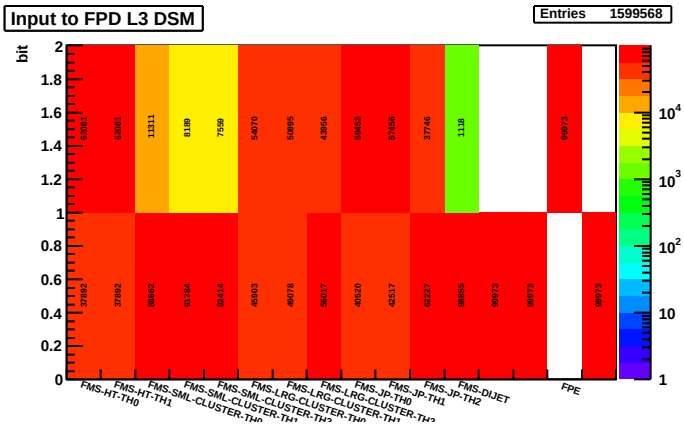

Input to FPD L2 DSM
Entries 1324
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

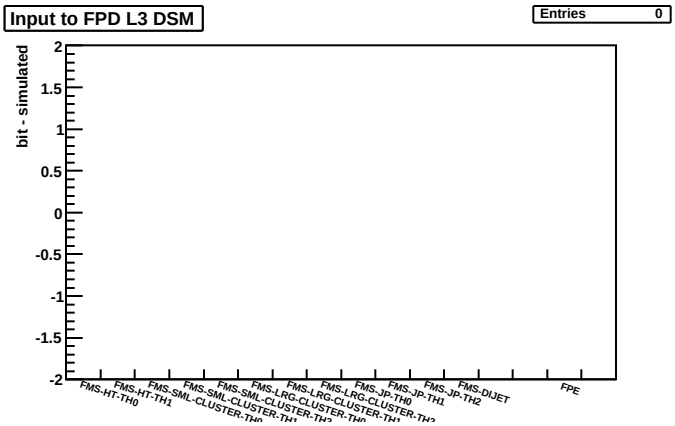

Input to FPD L2 DSM
Entries 299919
CL bits
7559
31704
12436
630
4372
2638
3122
2209
2272
88662
61688
82627
SMALL
LARGE-S
LARGE-N

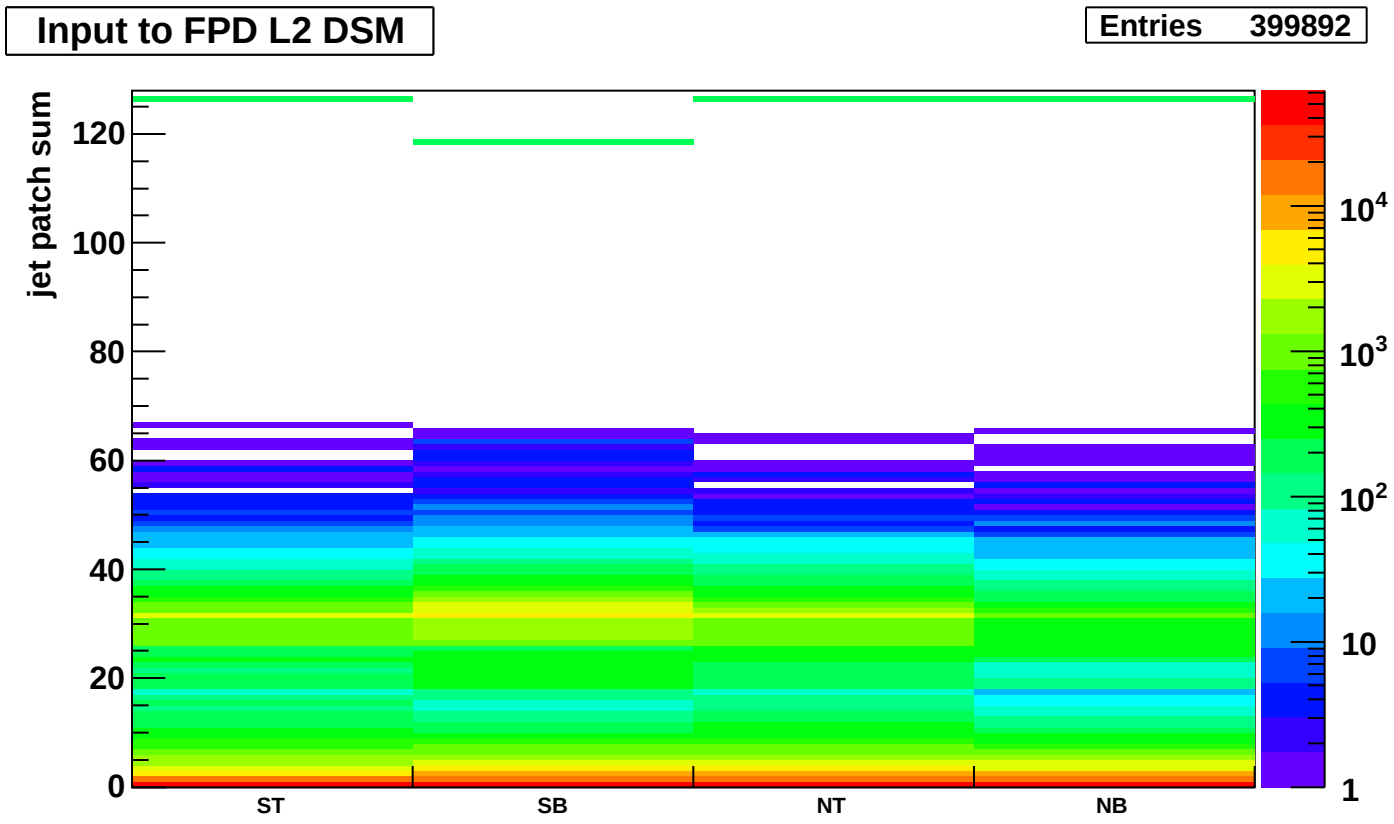

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

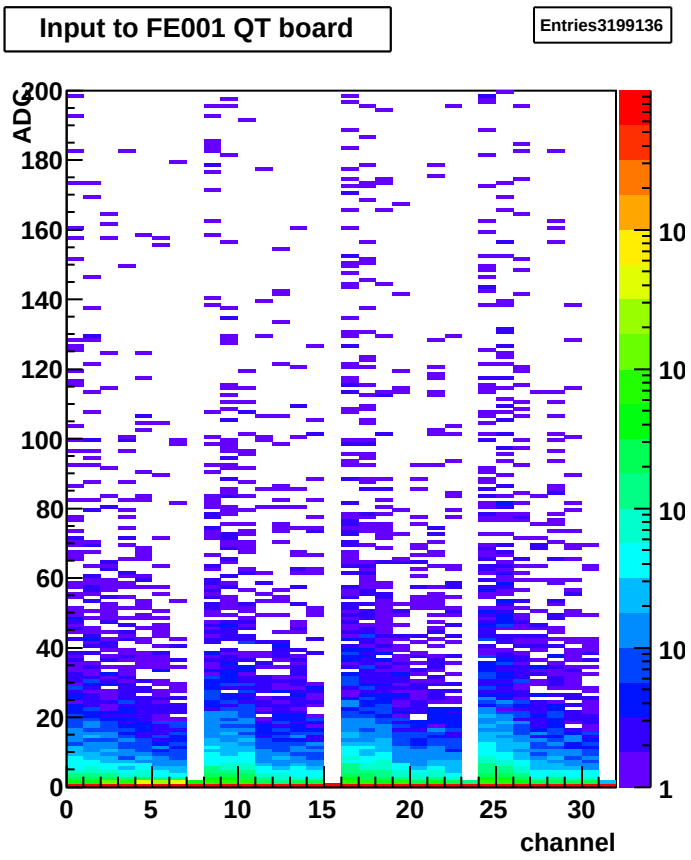

Input to FPD L2 DSM
Entries 299919
HT bits
10210
38434
17561
89763
61539
82412
SMALL
LARGE-S
LARGE-N

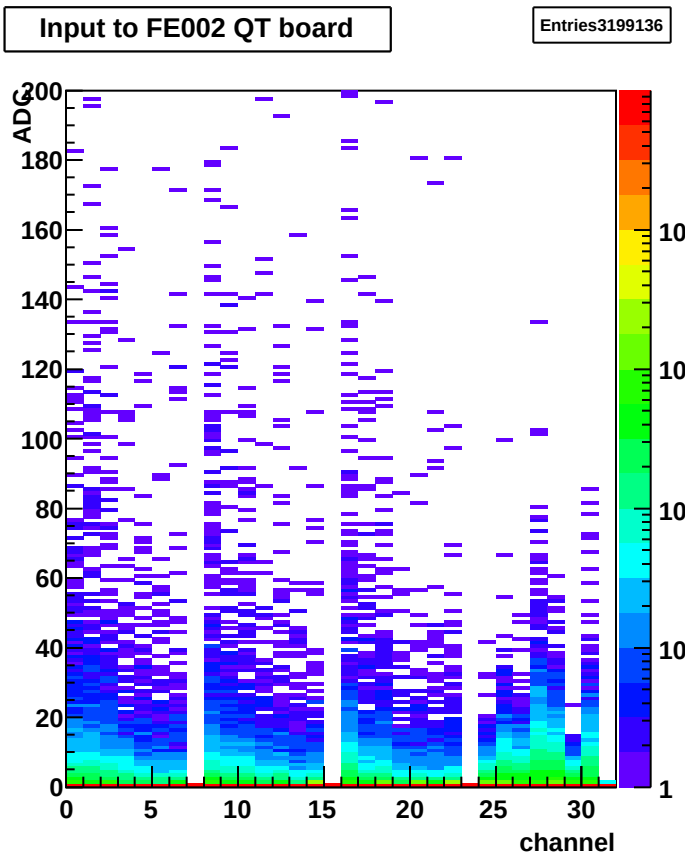

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

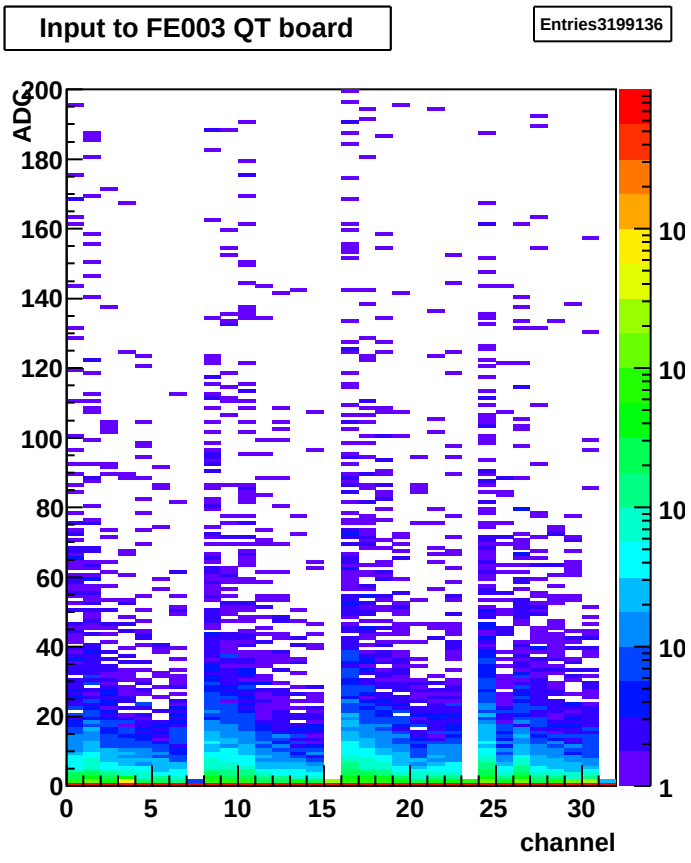

Input to FPD L3 DSM
Entries 1599568
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

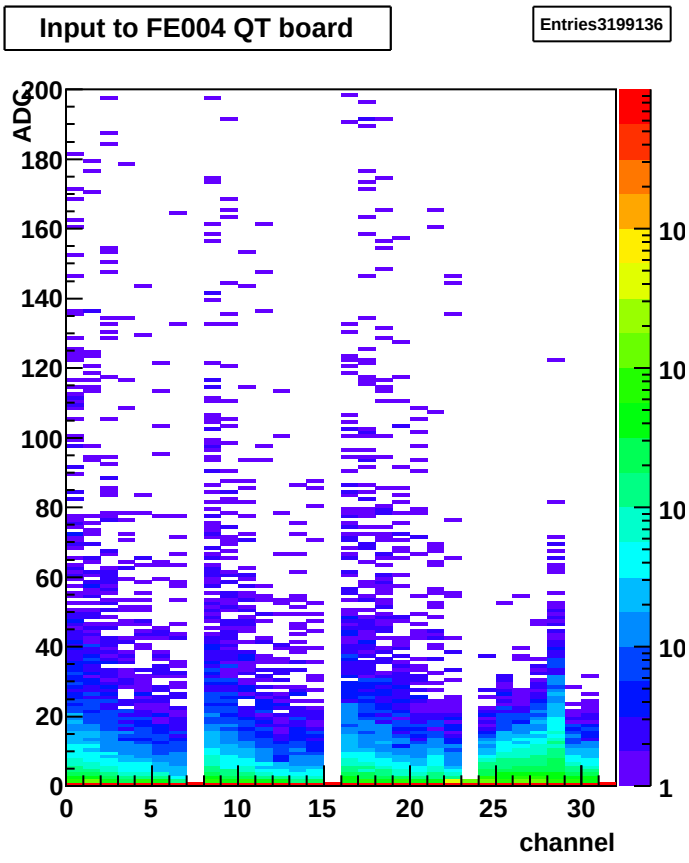

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

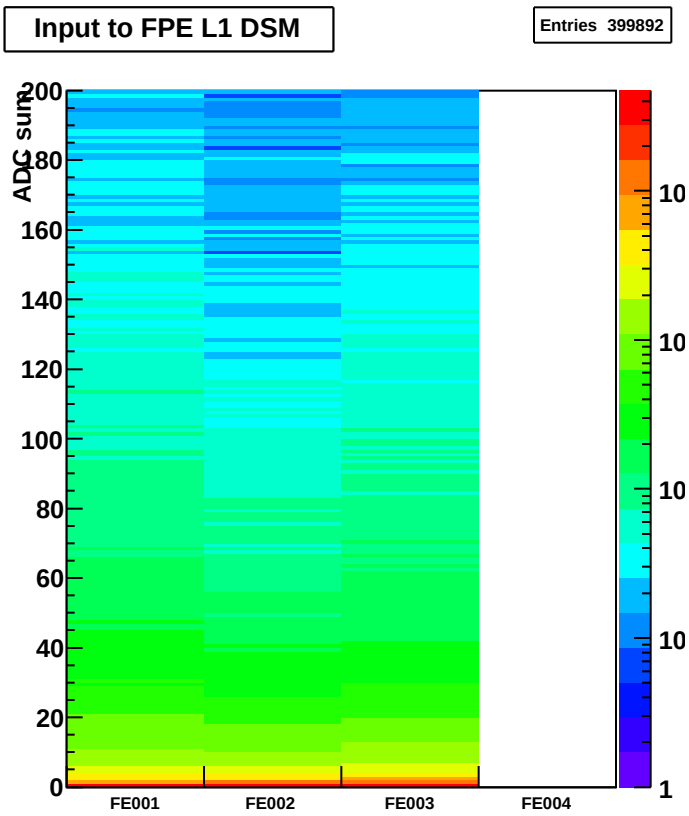

Input to FPD L2 DSM
Entries 399892
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

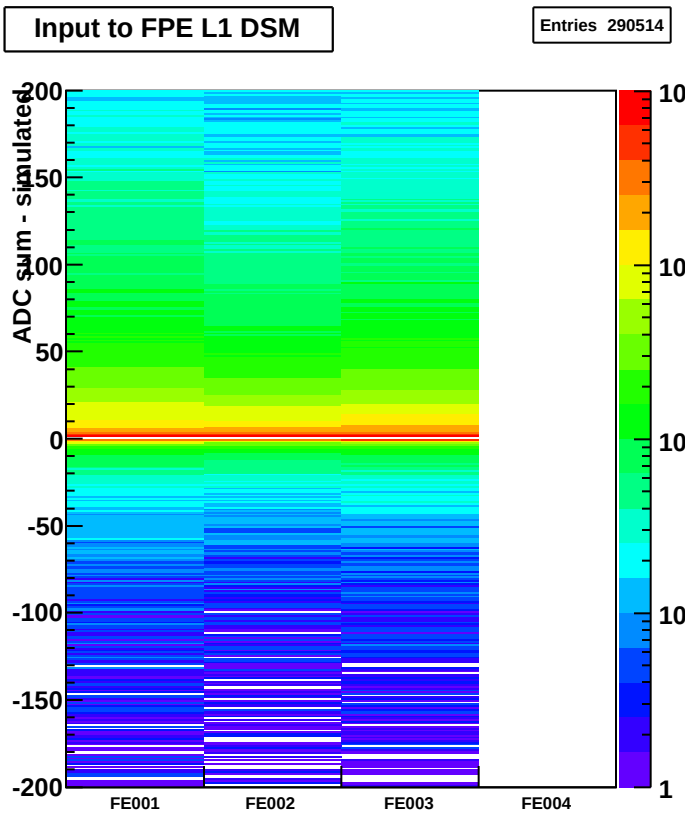

Input to FE001 QT board
Entries3199136
ADC
channel

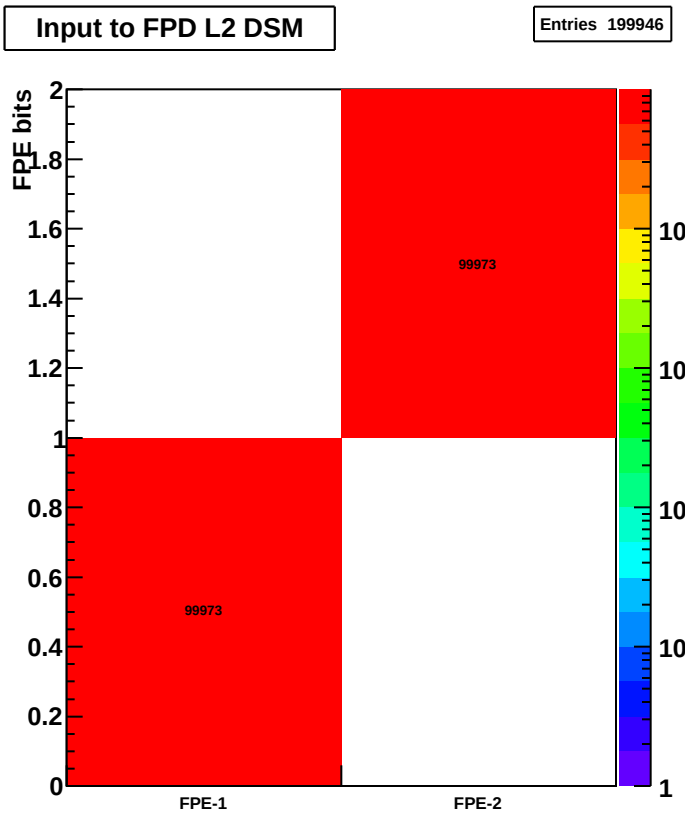

Input to FE002 QT board
Entries3199136
ADC
channel

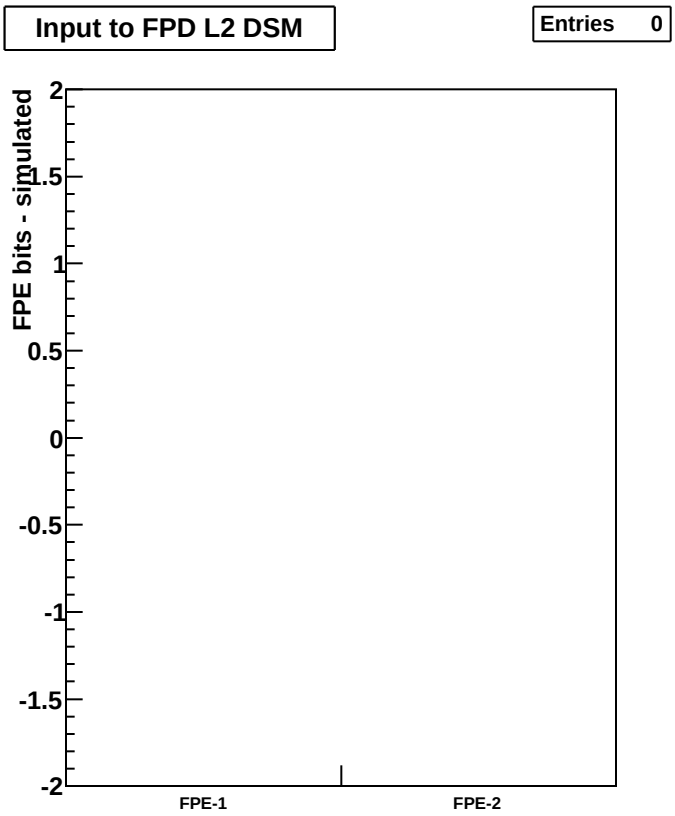

Input to FE003 QT board
Entries3199136
ADC
channel


Input to FE004 QT board
Entries3199136
ADC
channel


Input to FPE L1 DSM
Entries 399892
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 290514
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 199946
FPE bits
99973
99973
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2