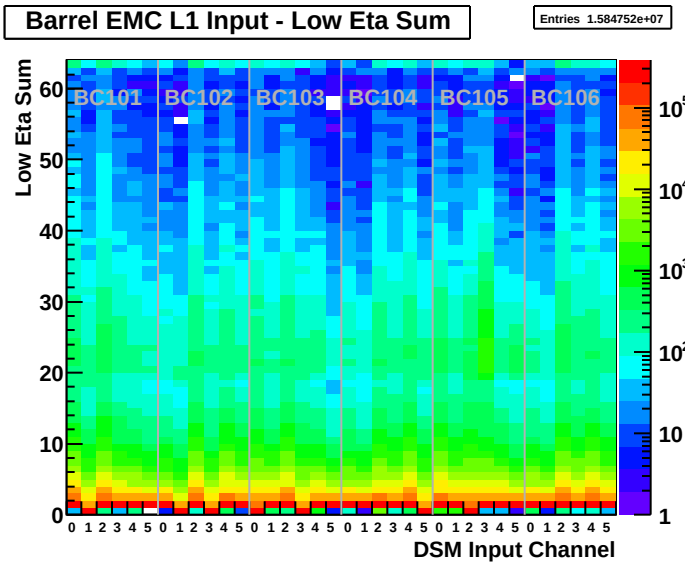
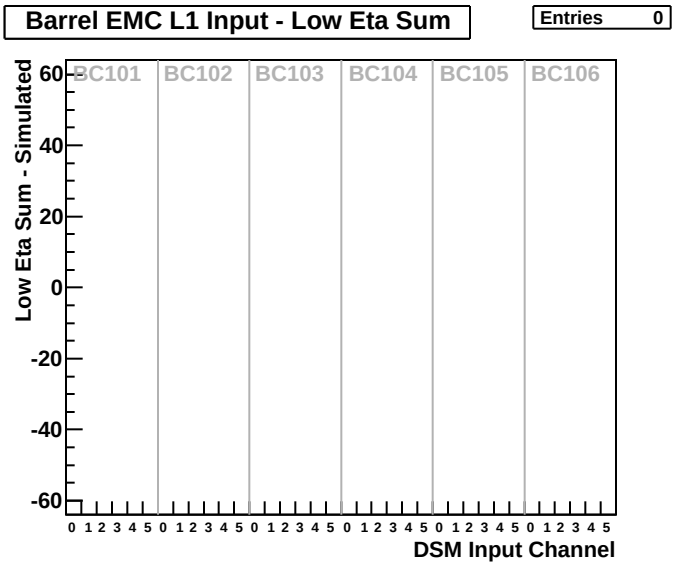
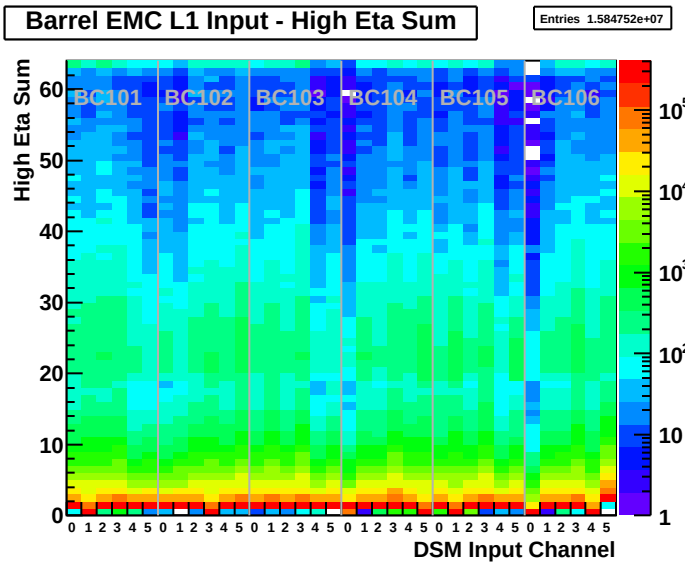
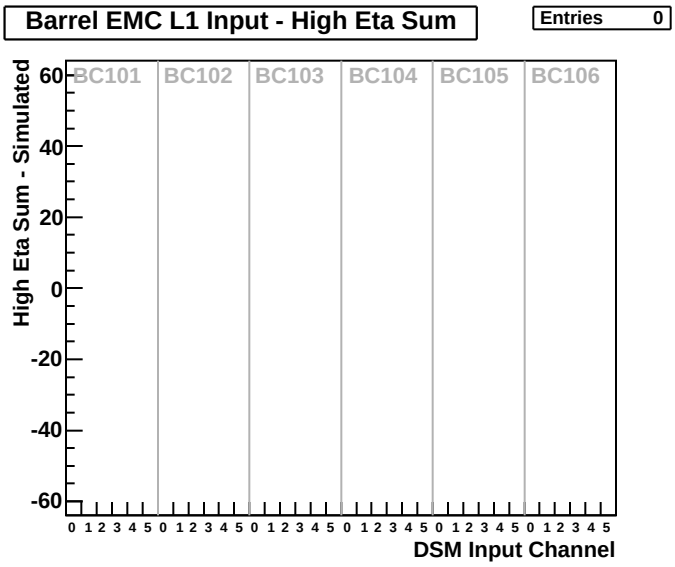
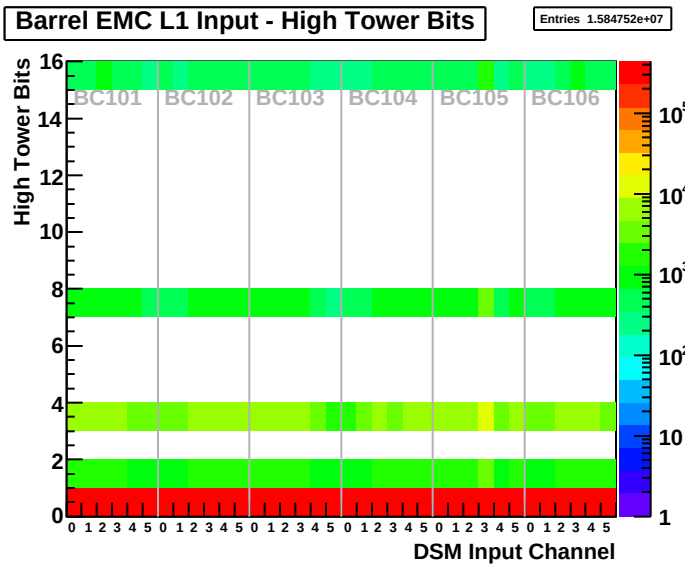
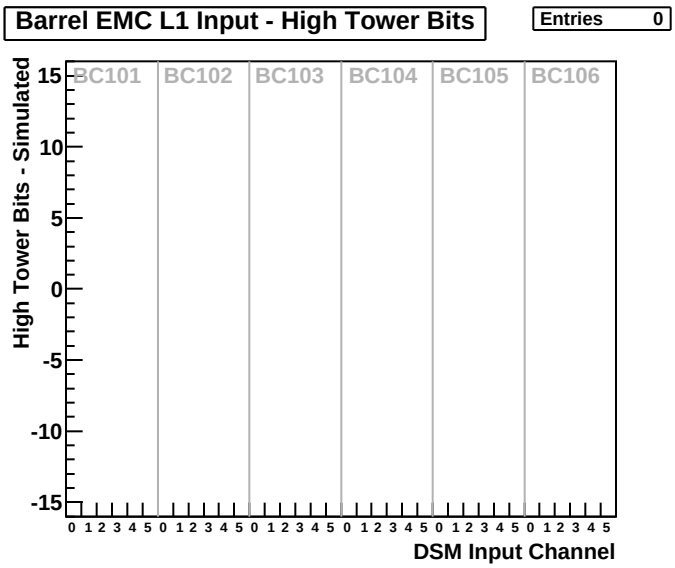

Barrel EMC L1 Input - Low Eta Sum
Entries 1.584752e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

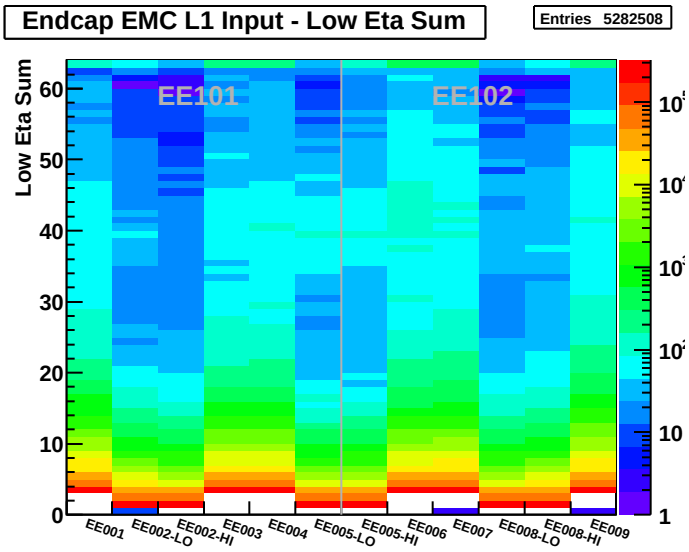

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

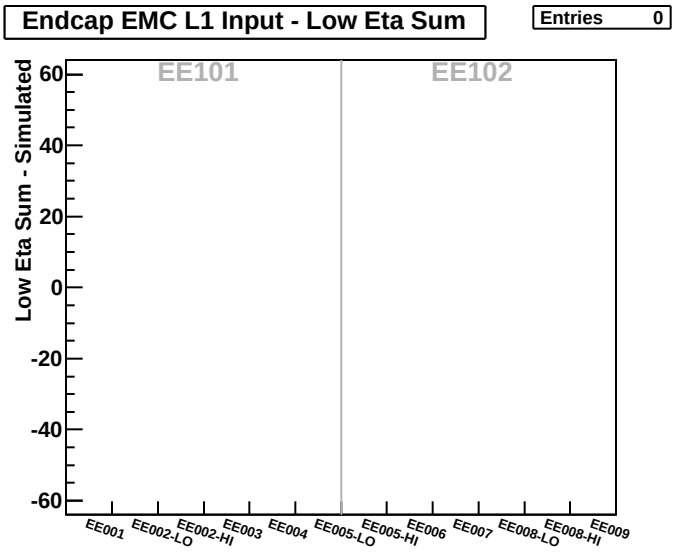

Barrel EMC L1 Input - High Eta Sum
Entries 1.584752e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

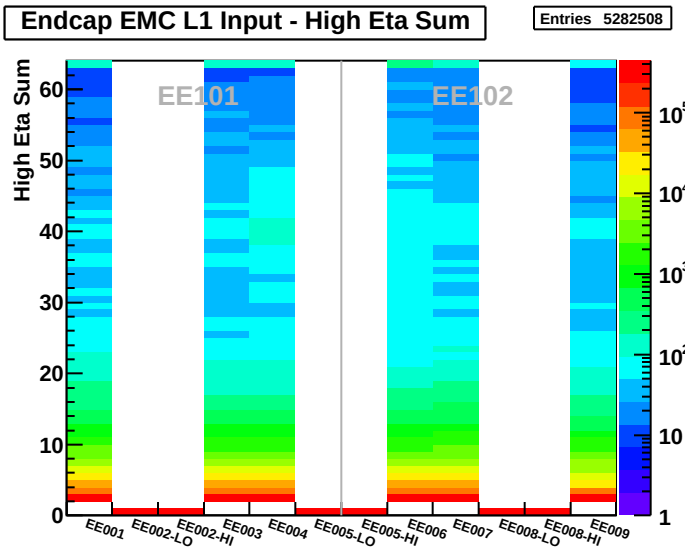

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

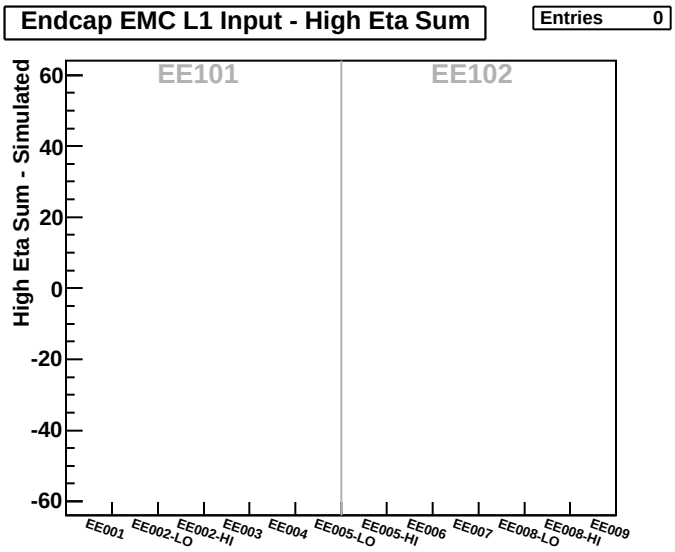

Barrel EMC L1 Input - High Tower Bits
Entries 1.584752e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

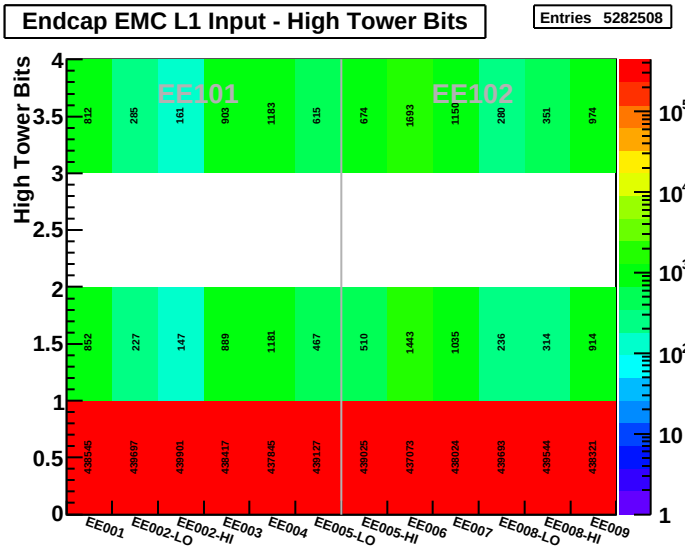

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

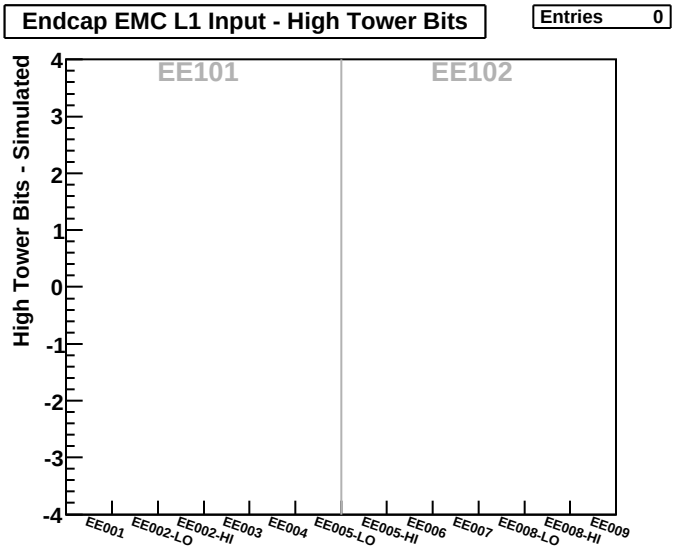

Endcap EMC L1 Input - Low Eta Sum
Entries 5282508
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

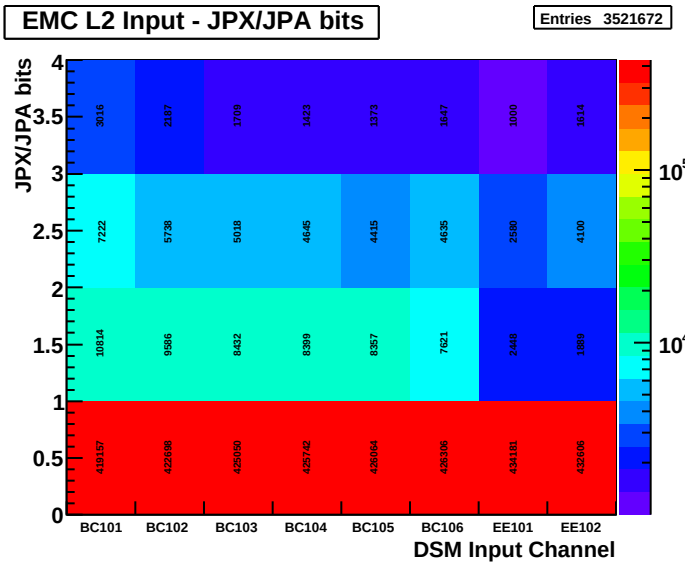

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

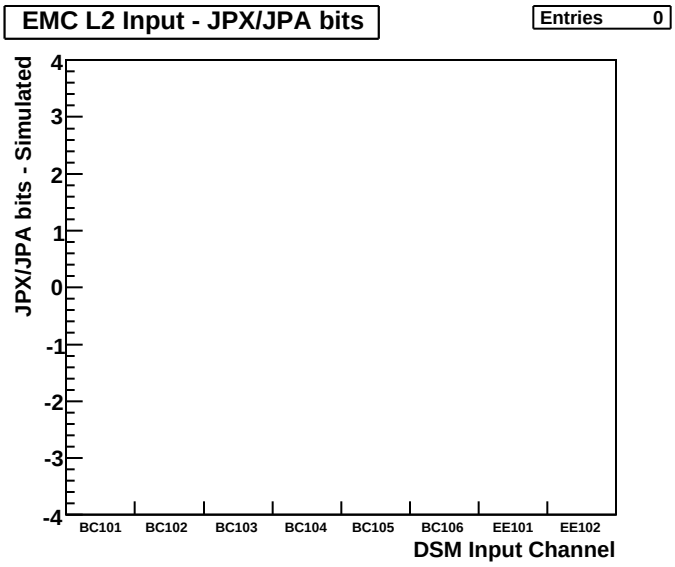

Endcap EMC L1 Input - High Eta Sum
Entries 5282508
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

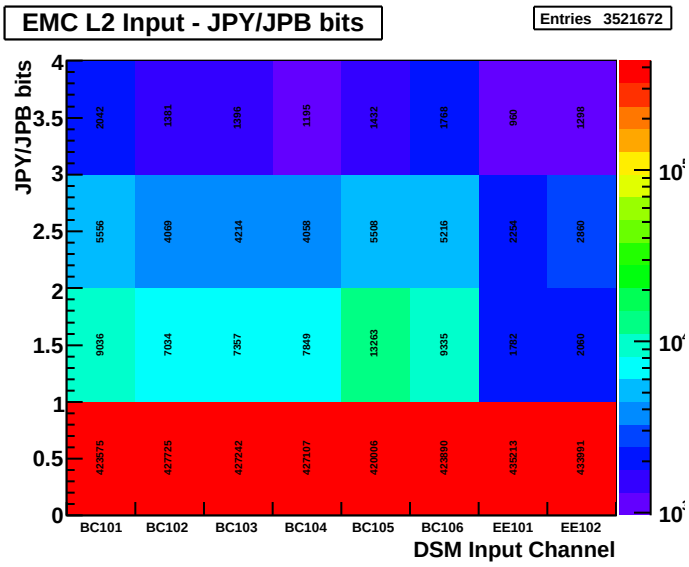

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

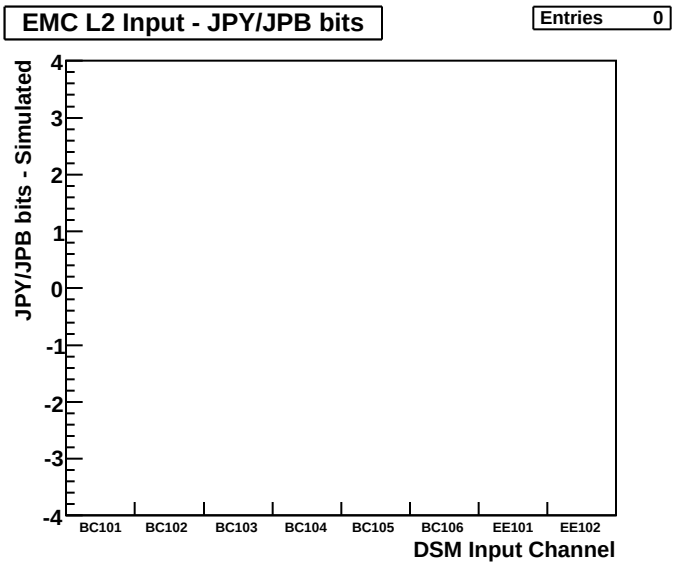

Endcap EMC L1 Input - High Tower Bits
Entries 5282508
High Tower Bits
EE101
EE102
812 285 161 903 1183 615 674 1593 1150 280 351 974
852 227 147 889 1181 467 510 1443 1035 236 314 914
438545 439697 439901 438417 437845 439127 439025 437073 438024 439693 439544 438321
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

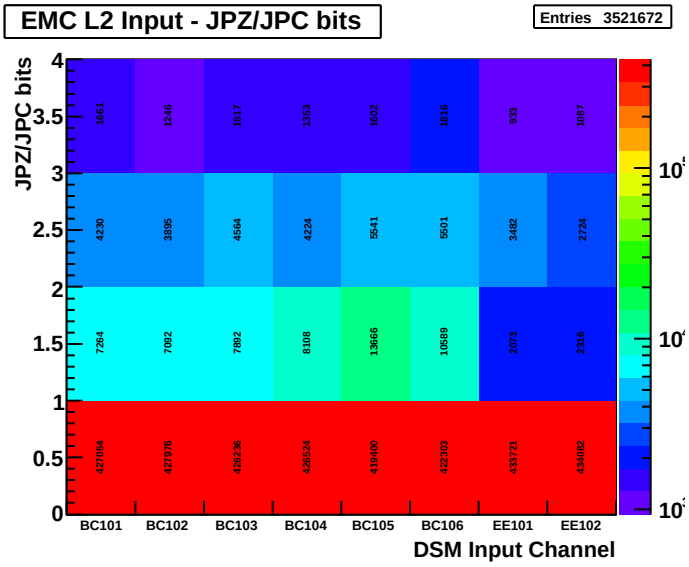

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

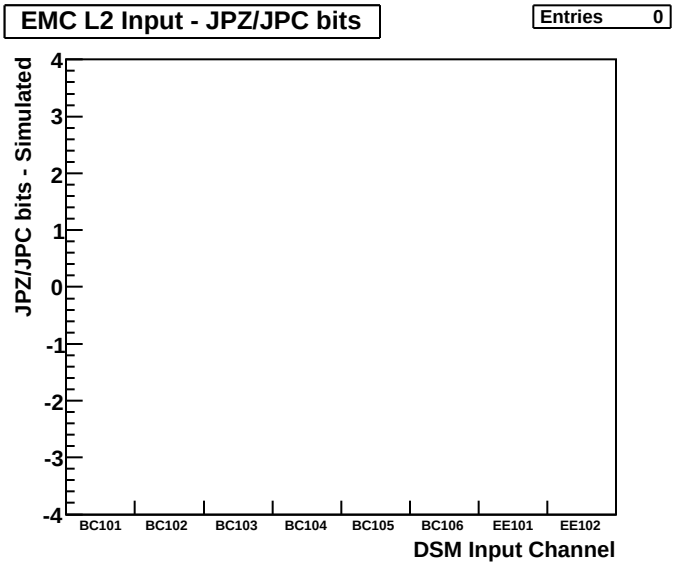

EMC L2 Input - JPX/JPA bits
Entries 3521672
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3016 2187 1709 1423 1373 1647 1000 1514
7222 5738 5018 4645 4415 4635 2580 4100
10814 9586 8432 8399 8357 7621 2448 1889
419157 422698 425050 425742 426064 426306 434181 432606

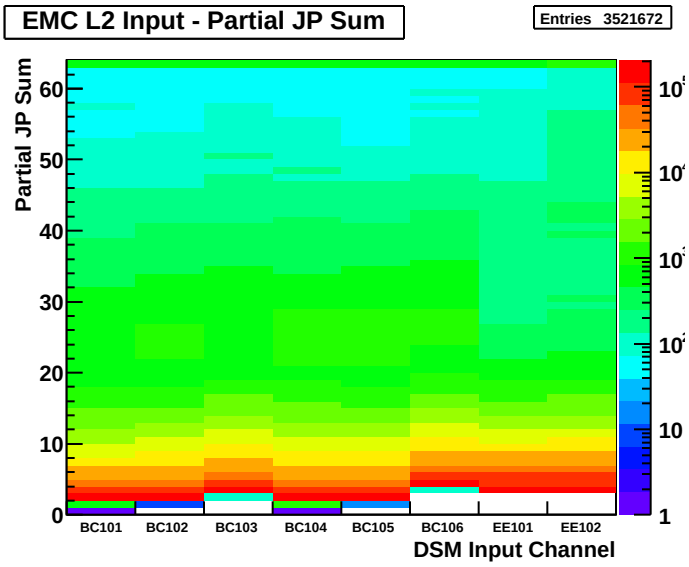

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

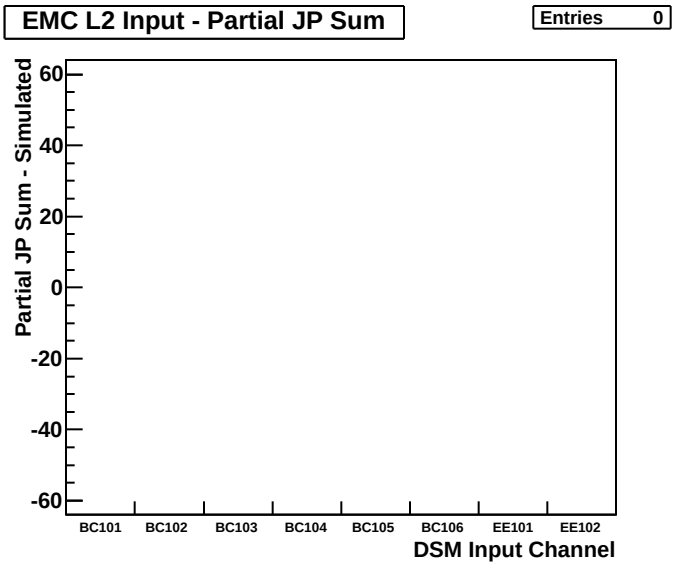

EMC L2 Input - JPY/JPB bits
Entries 3521672
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2042 1381 1396 1195 1432 1768 960 1298
5556 4069 4214 4058 5508 5216 2254 2860
9036 7034 7357 7849 13263 9335 1782 2060
425575 427725 427242 427107 426006 423890 435213 433991

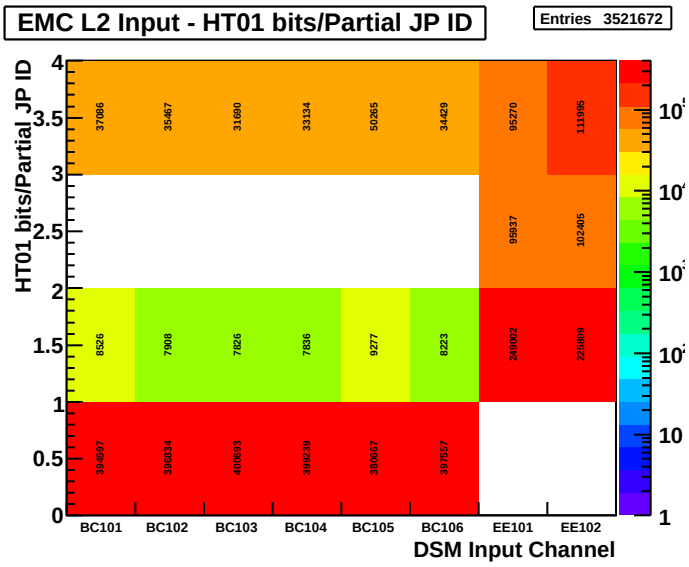

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

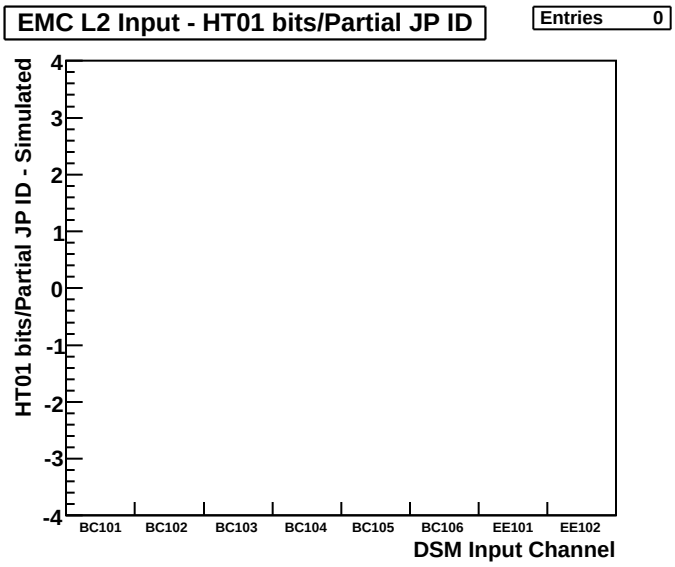

EMC L2 Input - JPZ/JPC bits
Entries 3521672
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1661 1246 1517 1353 1602 1616 933 1037
4230 3895 4564 4224 5541 5501 3482 2724
7264 7092 7892 8108 13666 10589 2073 2316
427054 427976 426236 426524 419400 422303 433721 434082

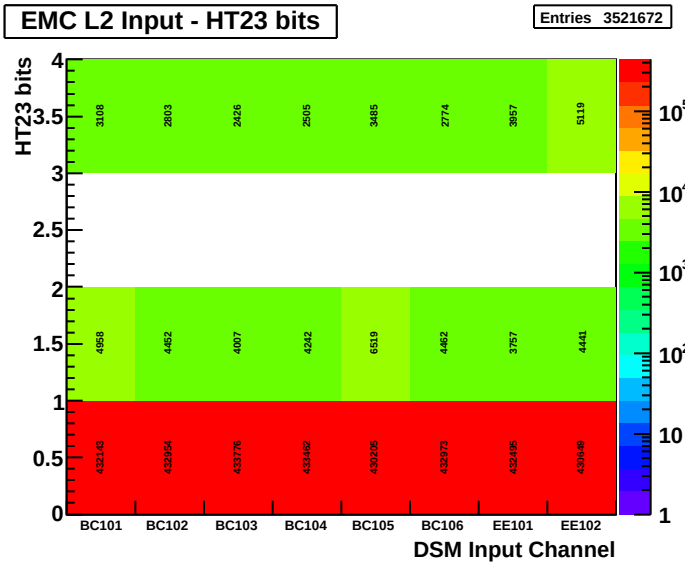

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

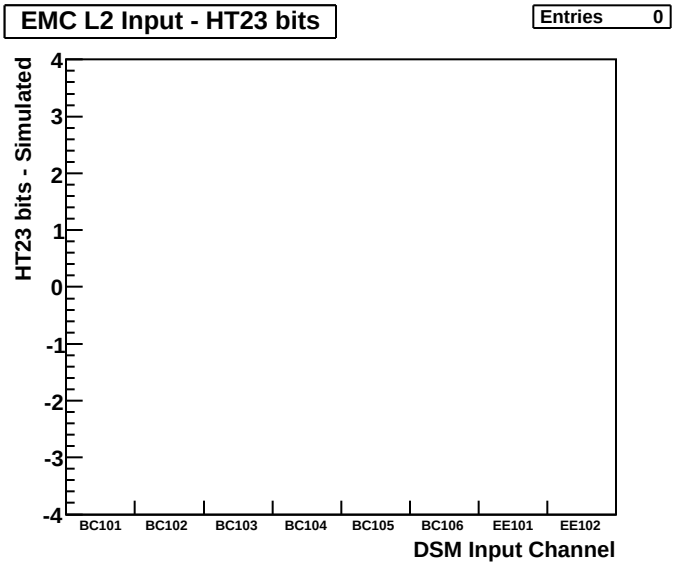

EMC L2 Input - Partial JP Sum
Entries 3521672
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

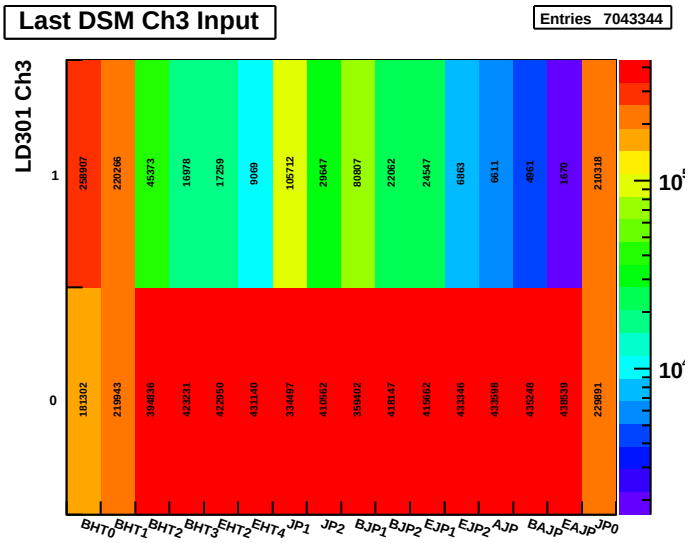

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

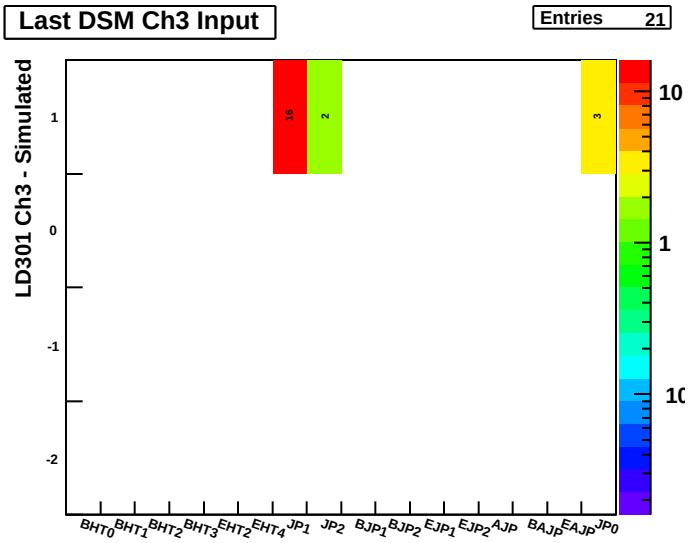

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3521672
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

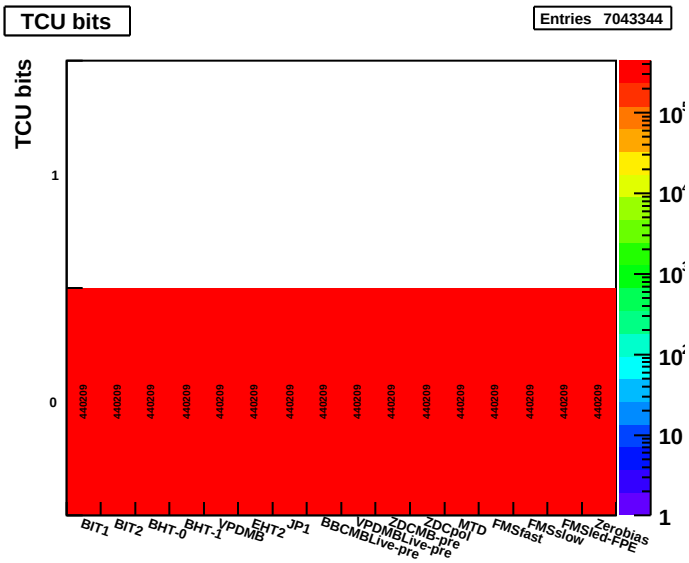

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

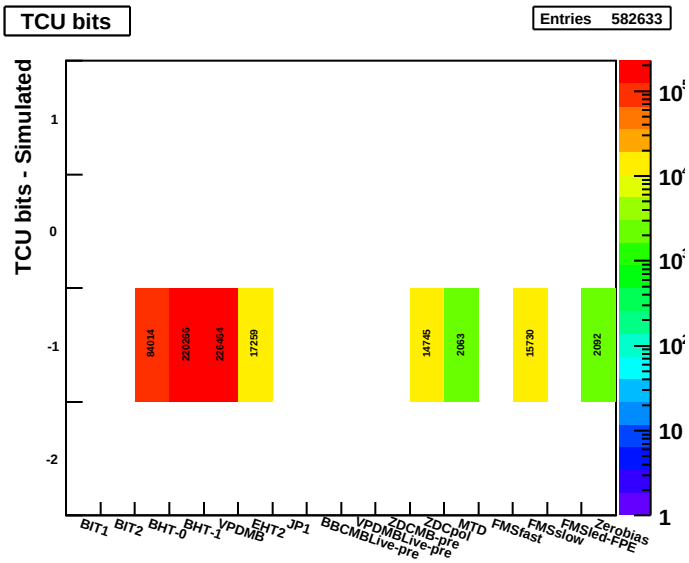

EMC L2 Input - HT23 bits
Entries 3521672
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

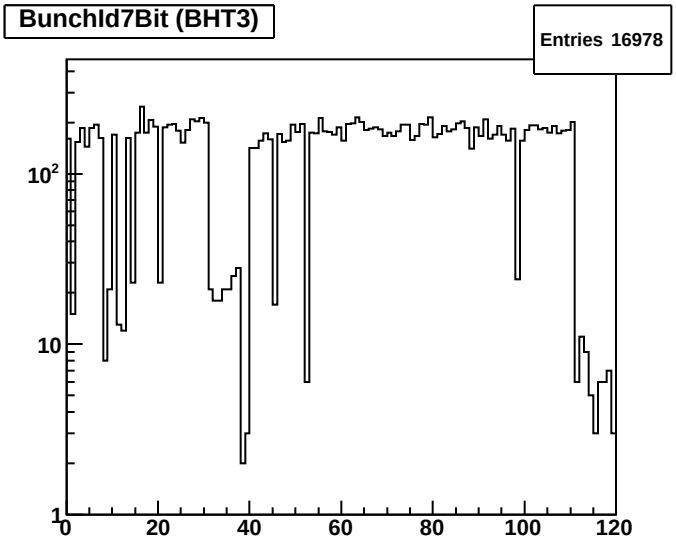

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

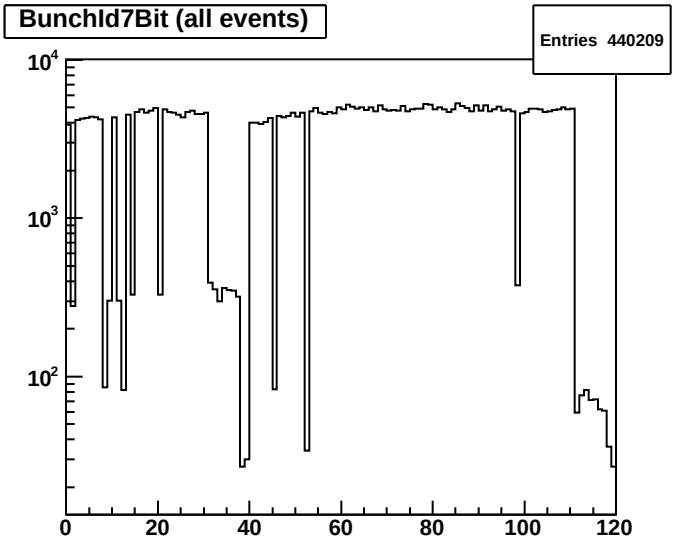

Last DSM Ch3 Input
Entries 7043344
LD301 Ch3
258907 220266 45373 16978 17259 9069 105712 29647 80807 22062 24547 6863 6611 4861 1670 210318
181302 219943 394836 423231 422950 431140 334497 410562 359402 418147 415662 433346 433598 435248 438539 229891
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

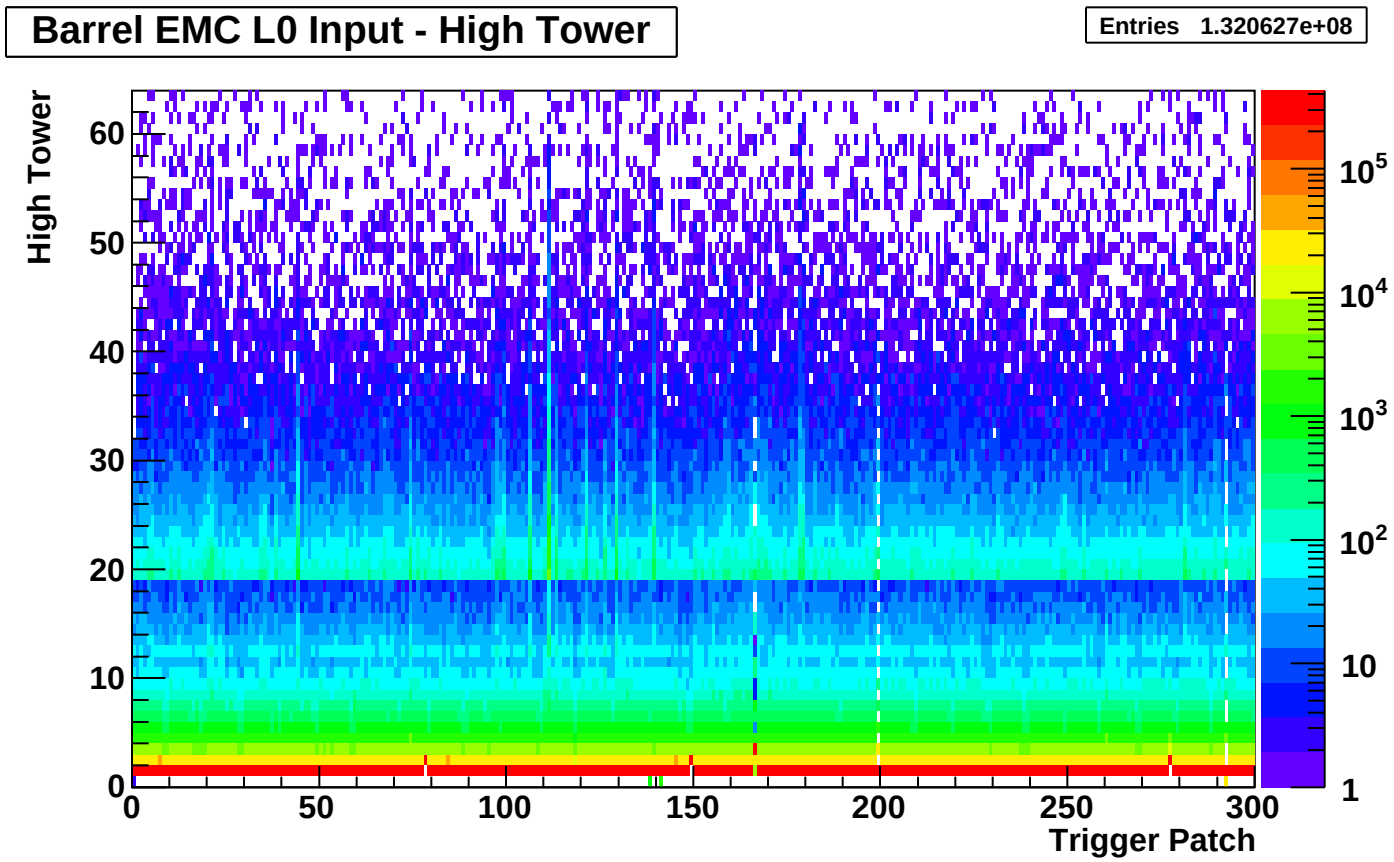

Last DSM Ch3 Input
Entries 21
LD301 Ch3 - Simulated
16 2 3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

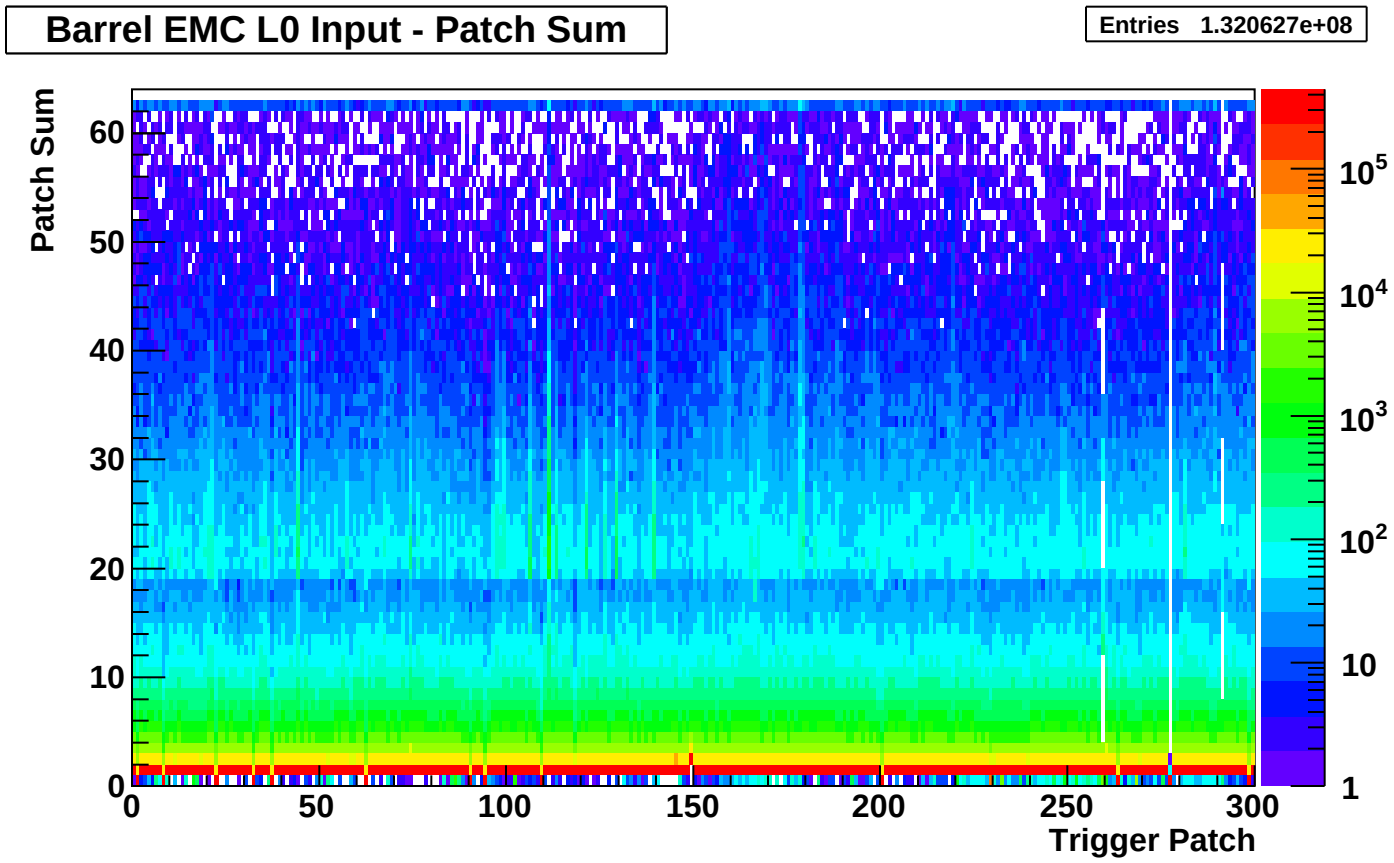

TCU bits
Entries 7043344
TCU bits
440209
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

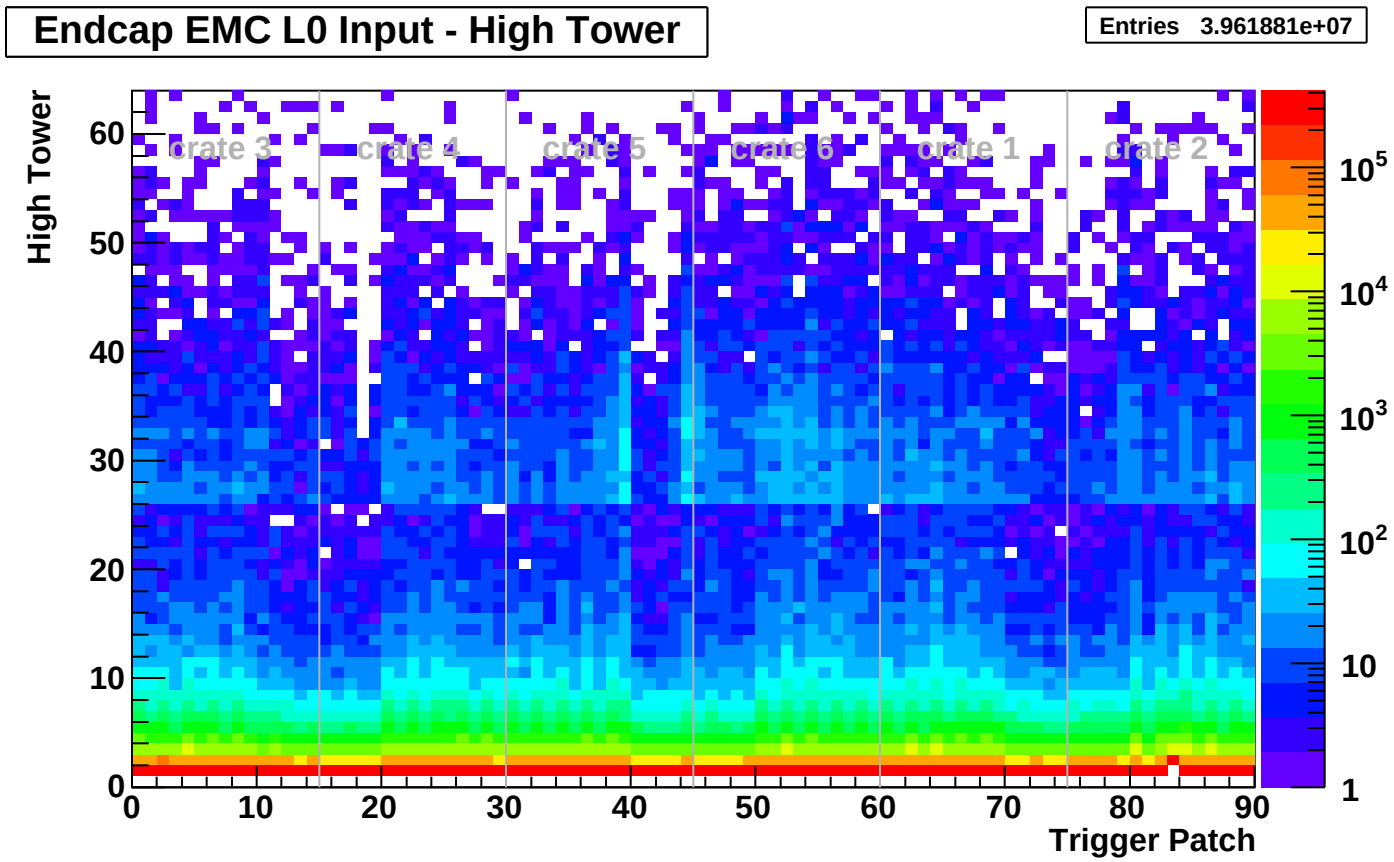

TCU bits
Entries 582633
TCU bits - Simulated
84014 220286 226464 17259 14745 2063 15730 2092
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

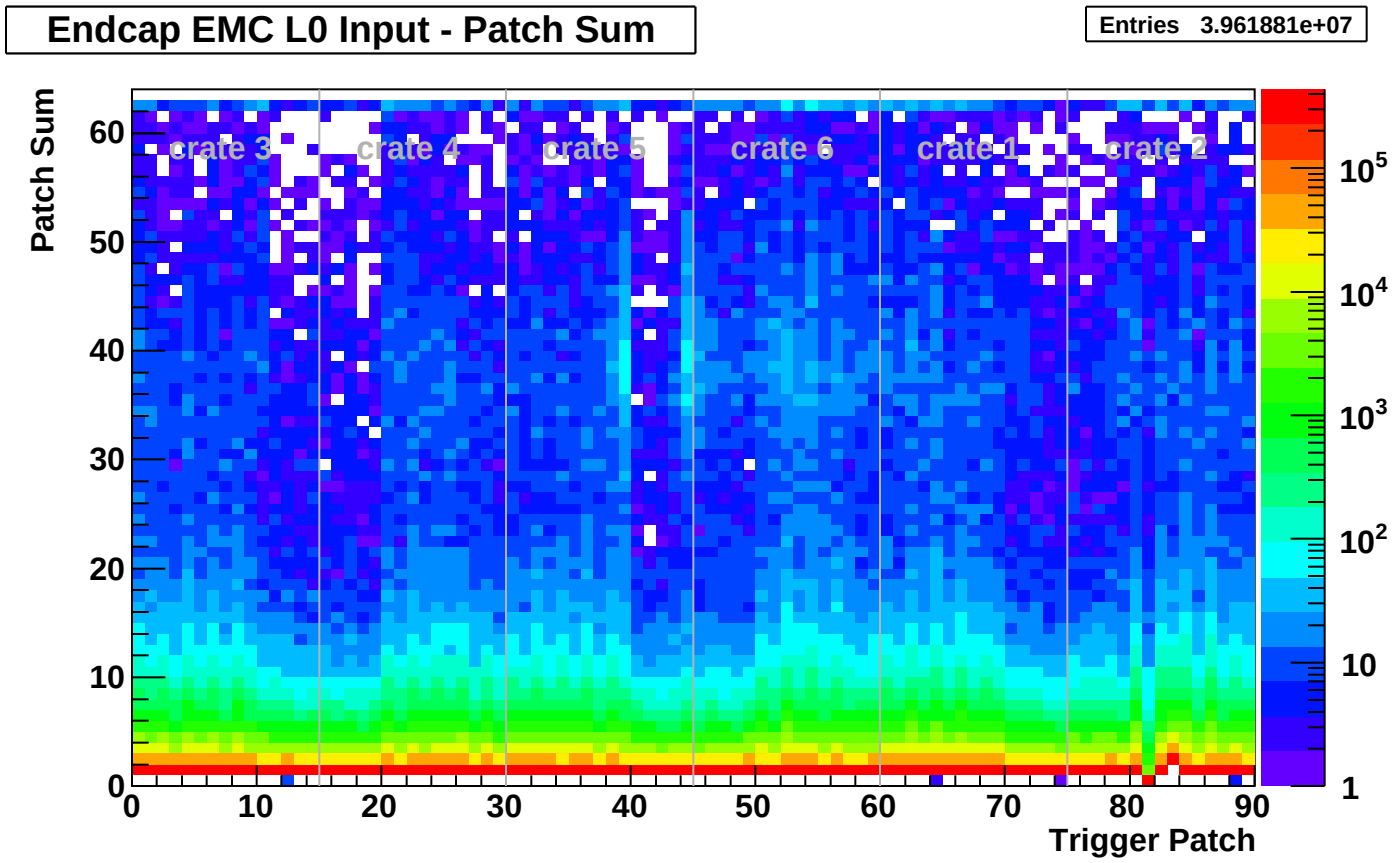

BunchId7Bit (BHT3)
Entries 16978

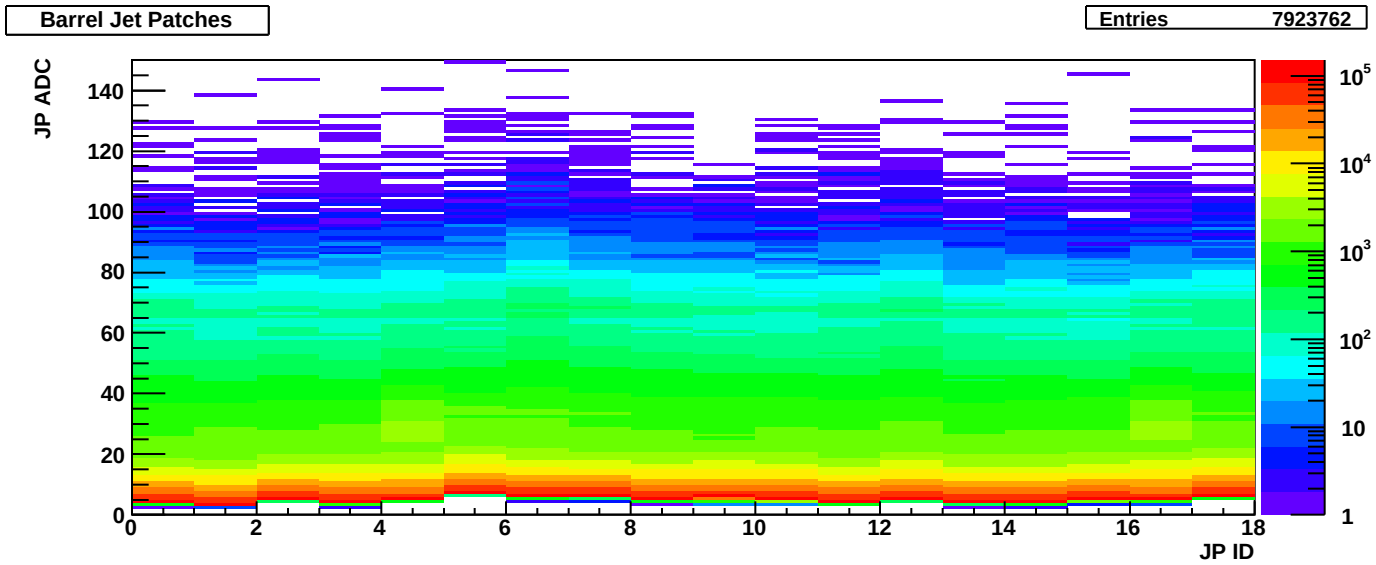

BunchId7Bit (all events)
Entries 440209

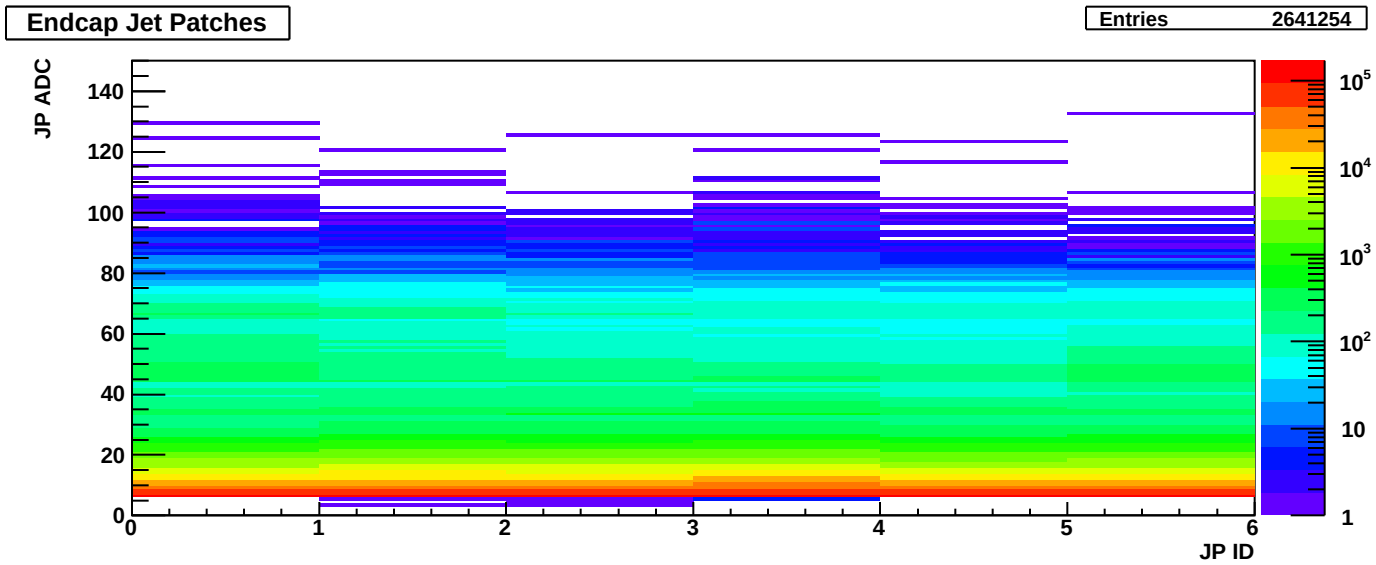

Barrel EMC L0 Input - High Tower
Entries 1.320627e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

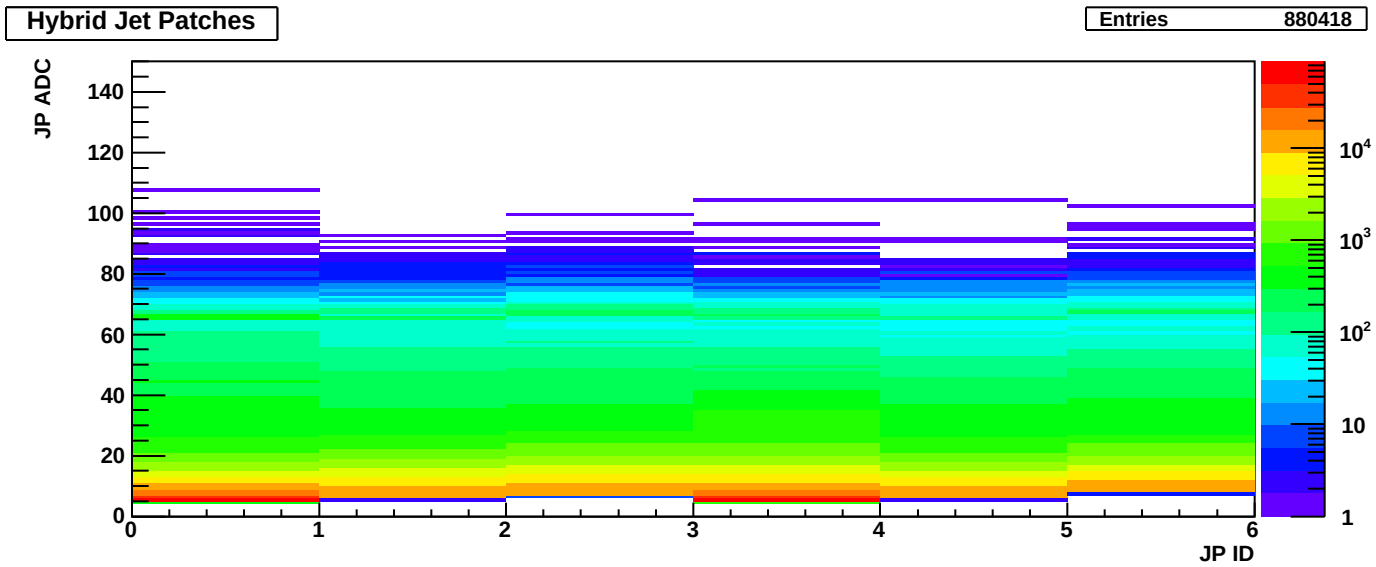

Barrel EMC L0 Input - Patch Sum
Entries 1.320627e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

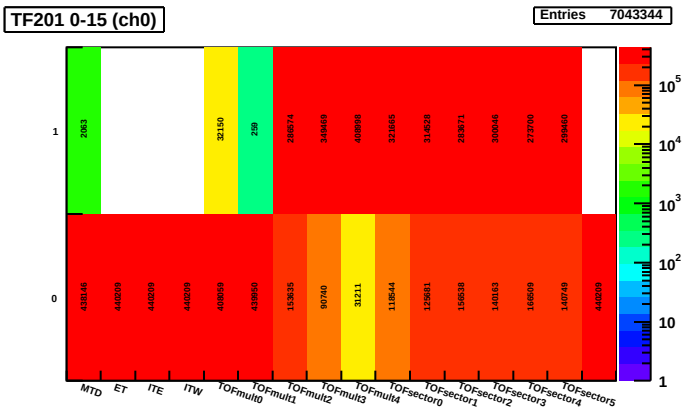

Endcap EMC L0 Input - High Tower
Entries 3.961881e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

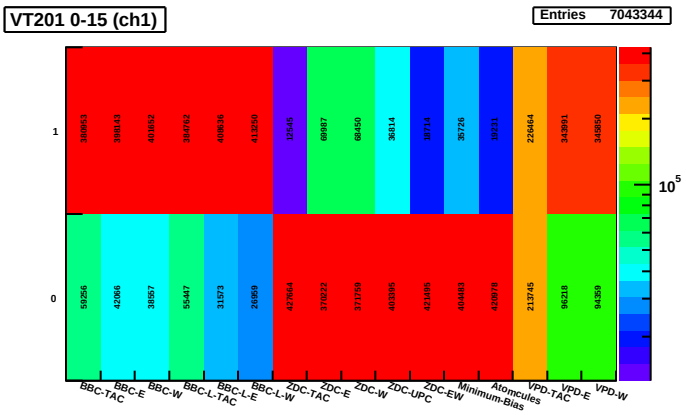

Endcap EMC L0 Input - Patch Sum
Entries 3.961881e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

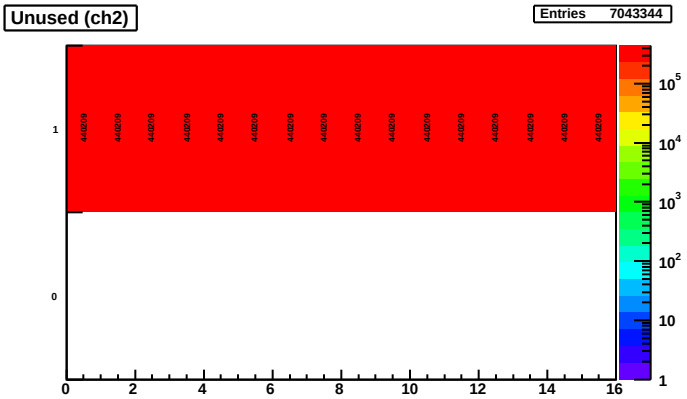

Barrel Jet Patches
Entries 7923762
JP ADC
JP ID

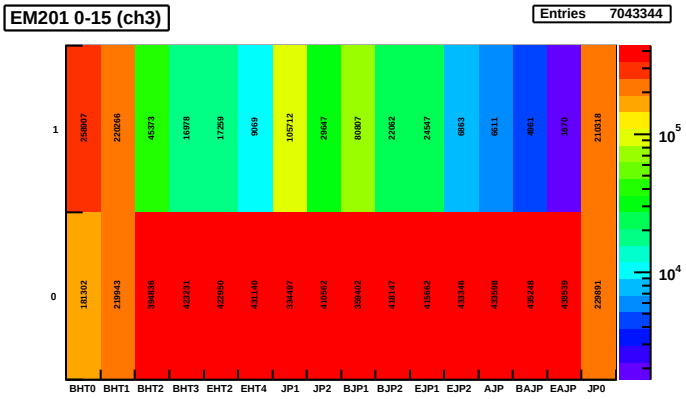

Endcap Jet Patches
Entries 2641254
JP ADC
JP ID

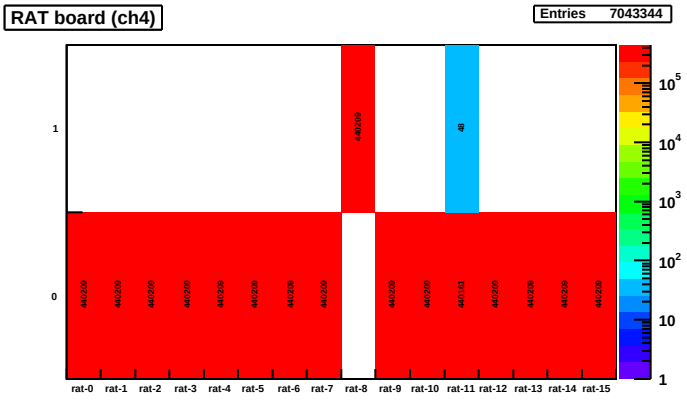

Hybrid Jet Patches
Entries 880418
JP ADC
JP ID

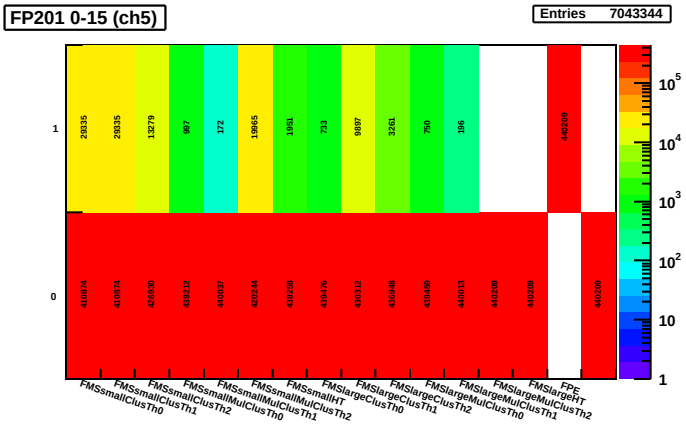

TF201 0-15 (ch0)
Entries 7043344
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

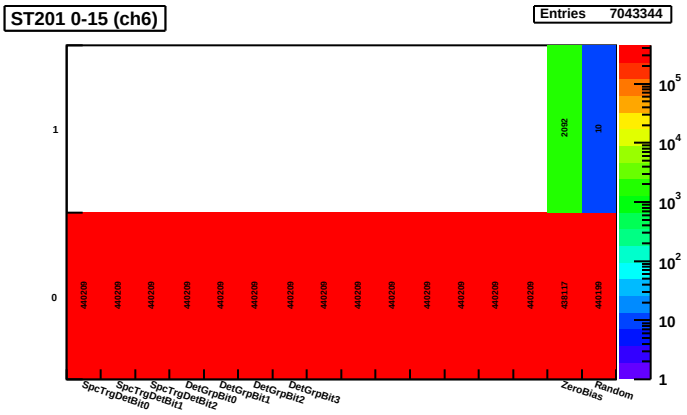

VT201 0-15 (ch1)
Entries 7043344
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

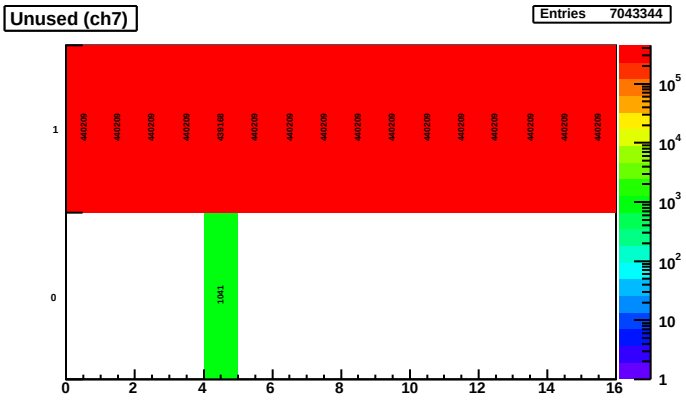

Unused (ch2)
Entries 7043344

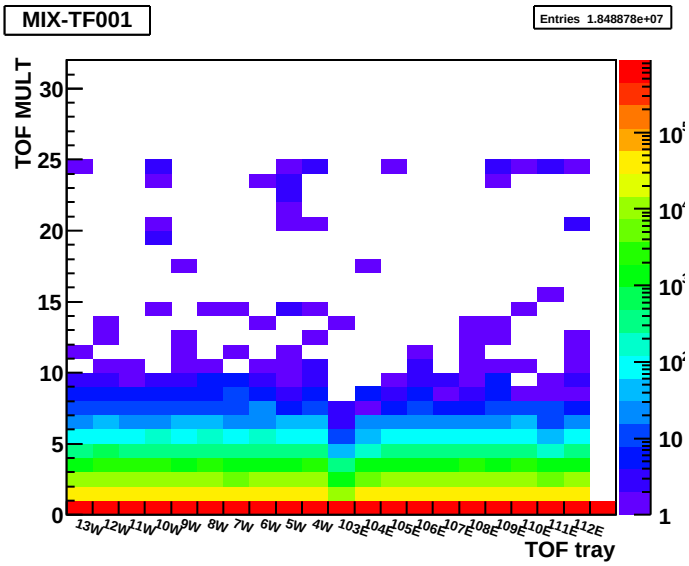

EM201 0-15 (ch3)
Entries 7043344
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

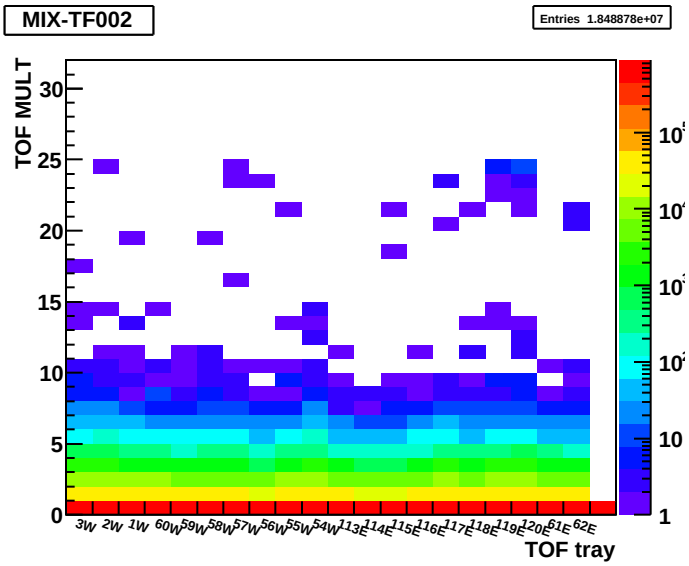

RAT board (ch4)
Entries 7043344
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

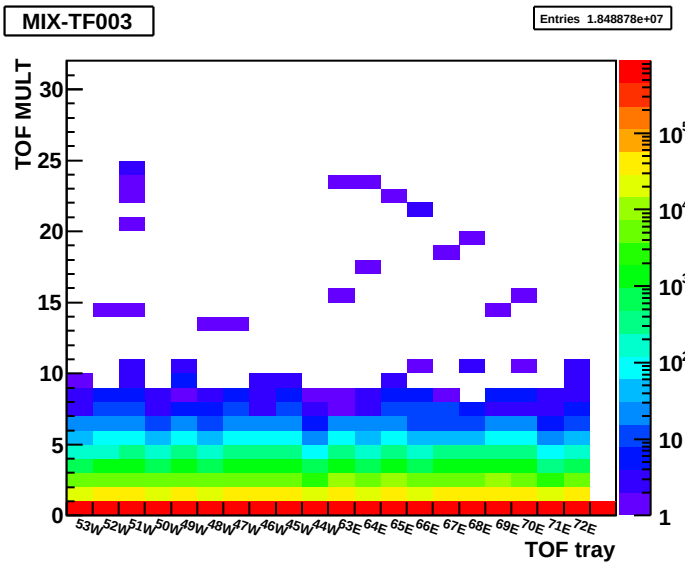

FP201 0-15 (ch5)
Entries 7043344
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

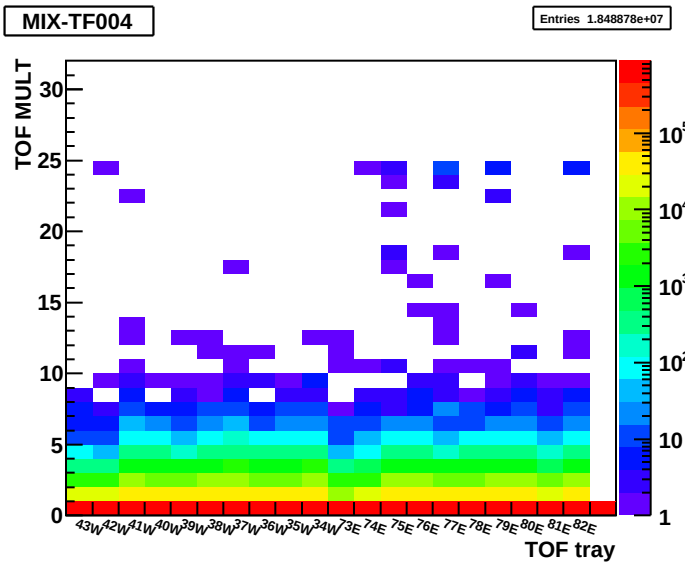

ST201 0-15 (ch6)
Entries 7043344
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

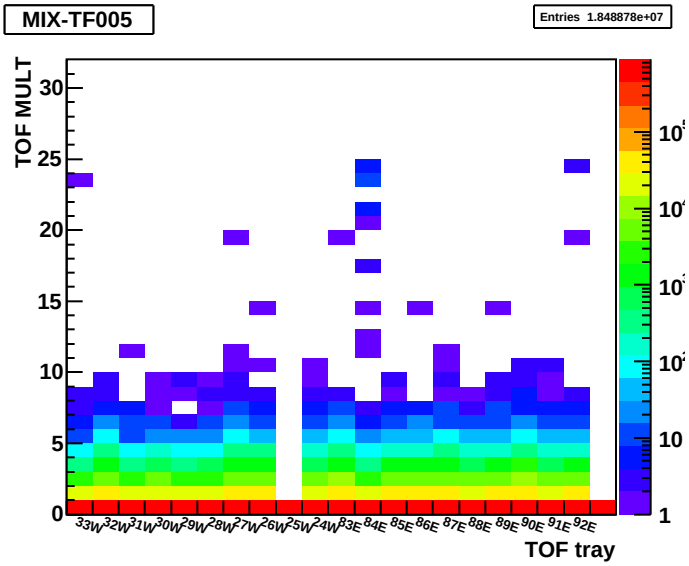

Unused (ch7)
Entries 7043344

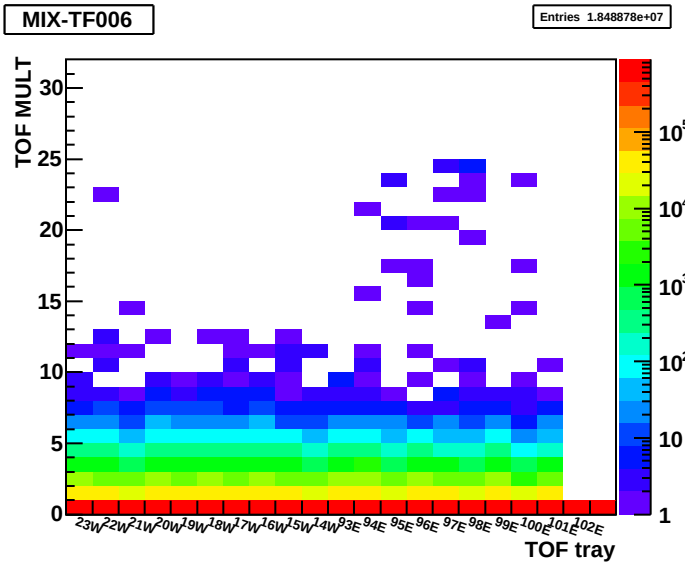

MIX-TF001
Entries 1.848878e+07
TOF MULT
TOF tray

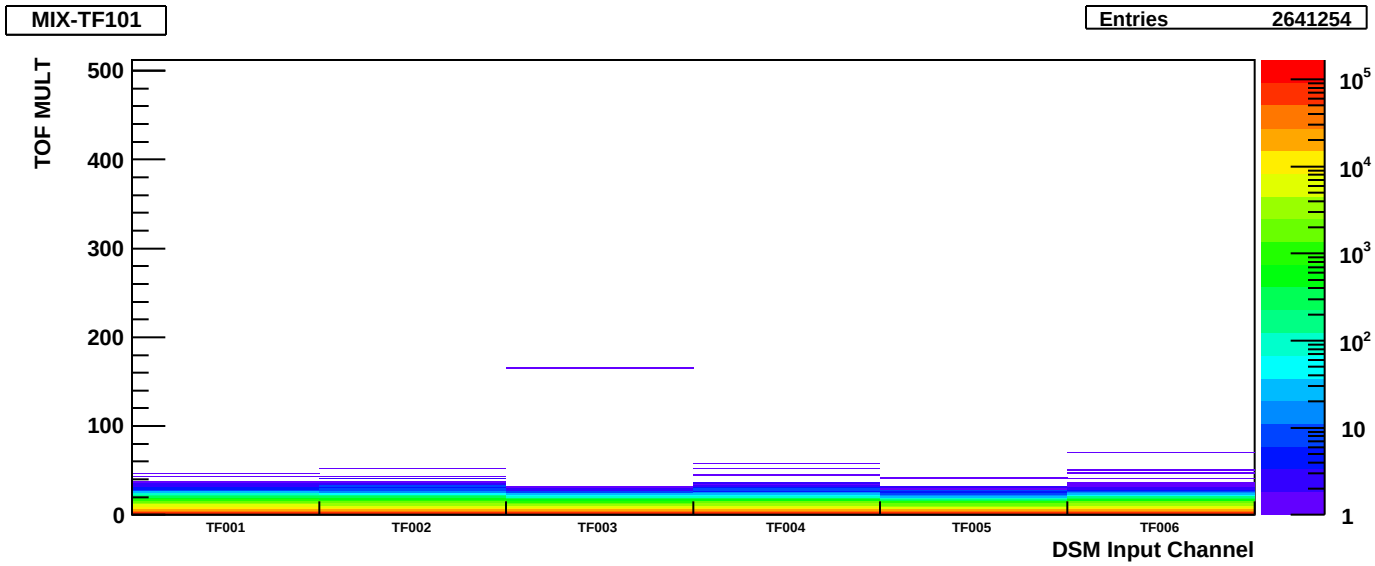

MIX-TF002
Entries 1.848878e+07
TOF MULT
TOF tray

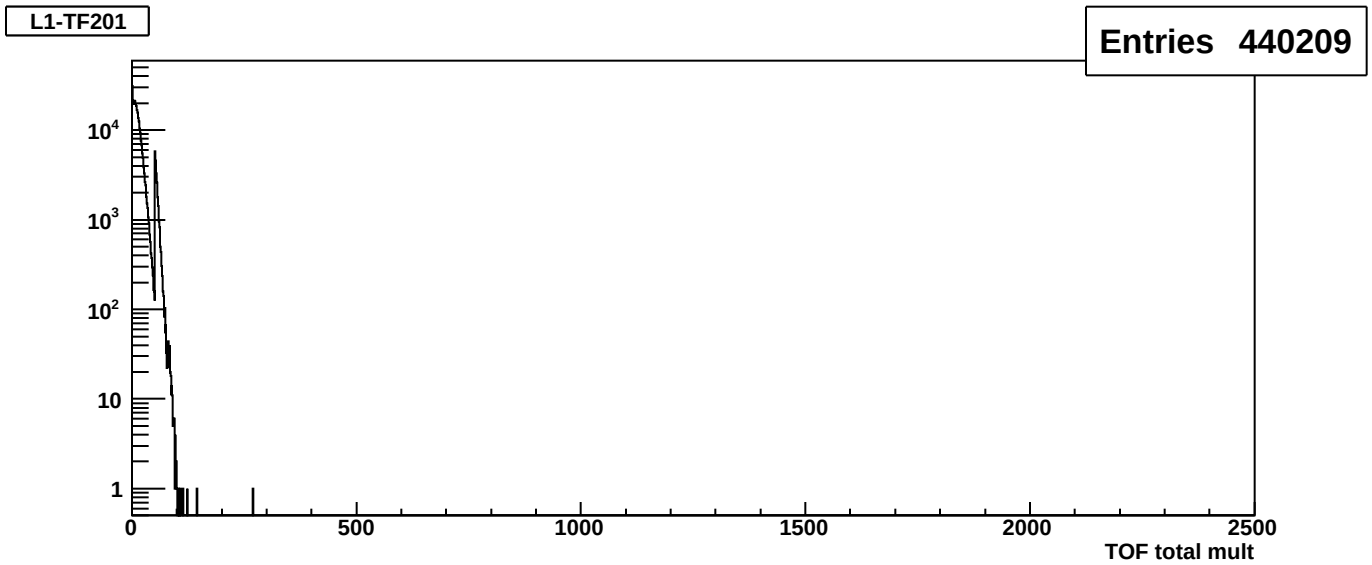

MIX-TF003
Entries 1.848878e+07
TOF MULT
TOF tray

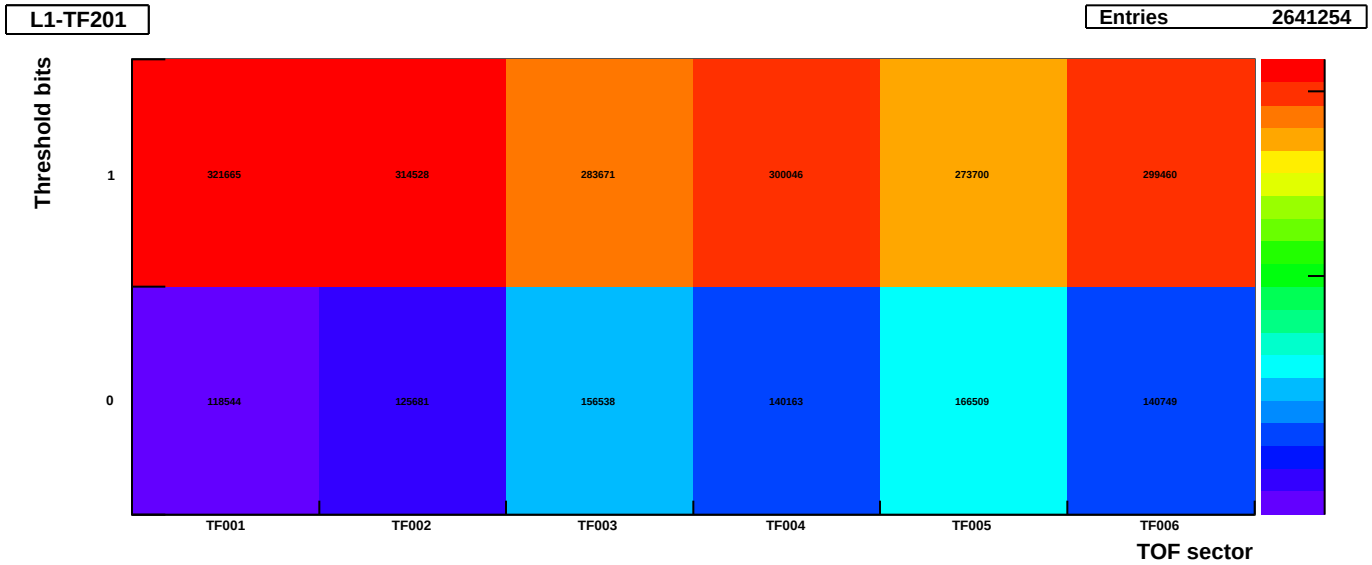

MIX-TF004
Entries 1.848878e+07
TOF MULT
TOF tray

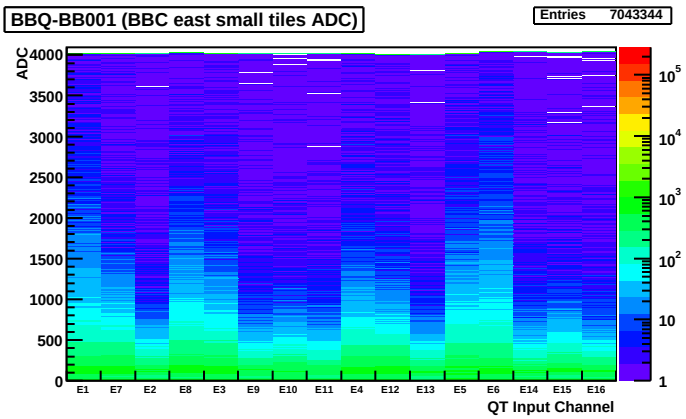

MIX-TF005
Entries 1.848878e+07
TOF MULT
TOF tray

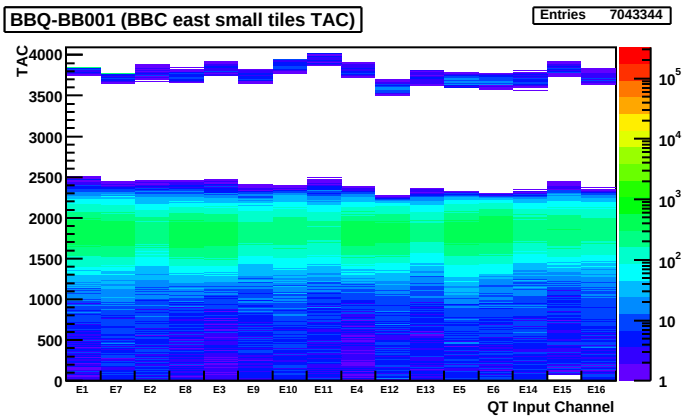

MIX-TF006
Entries 1.848878e+07
TOF MULT
TOF tray

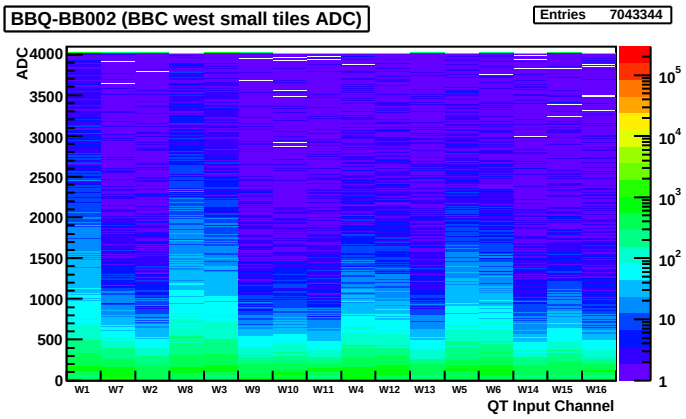

MIX-TF101
Entries 2641254
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

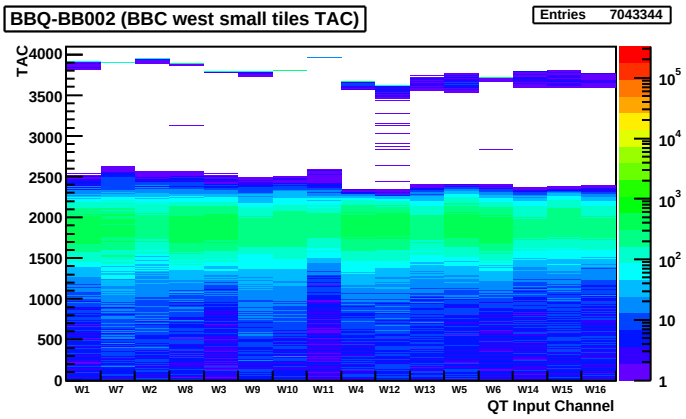

L1-TF201
Entries 440209
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

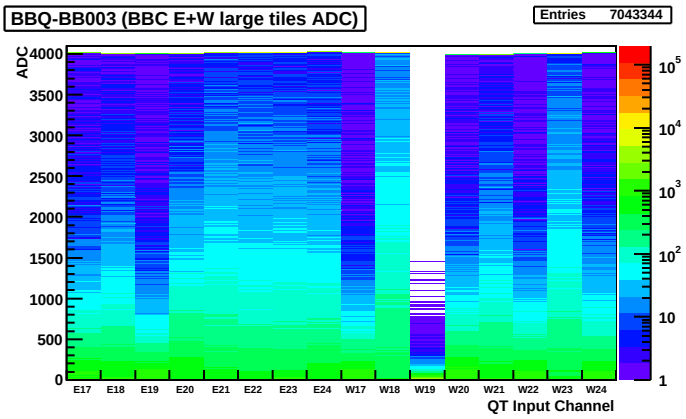

L1-TF201
Entries 2641254
Threshold bits
1
0
321665
314528
283671
300046
273700
299460
118544
125681
156538
140163
166509
140749
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

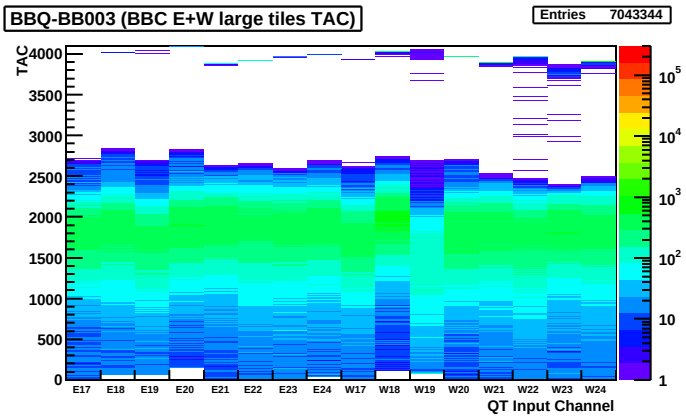

BBQ-BB001 (BBC east small tiles ADC)
Entries 7043344
ADC
QT Input Channel

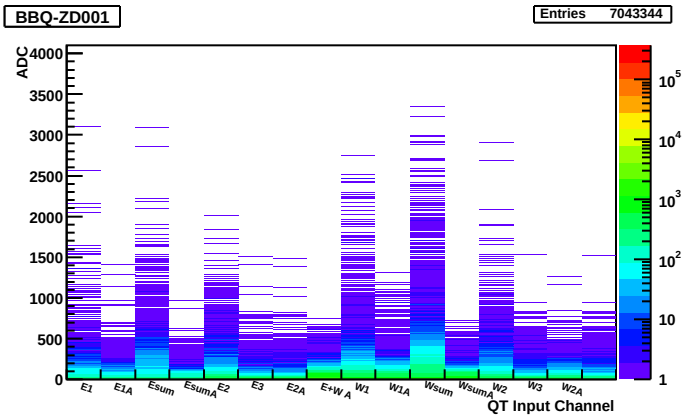

BBQ-BB001 (BBC east small tiles TAC)
Entries 7043344
TAC
QT Input Channel

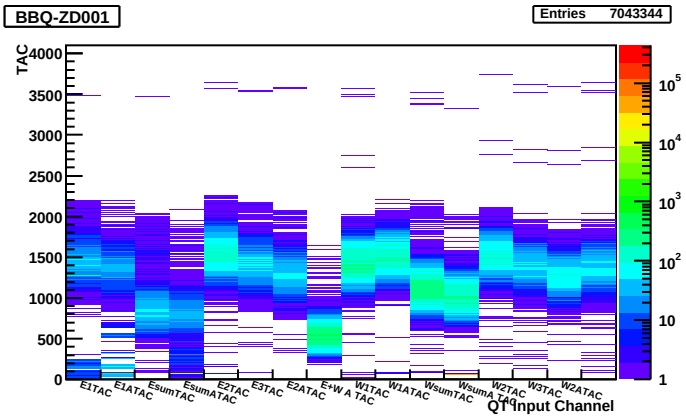

BBQ-BB002 (BBC west small tiles ADC)
Entries 7043344
ADC
QT Input Channel

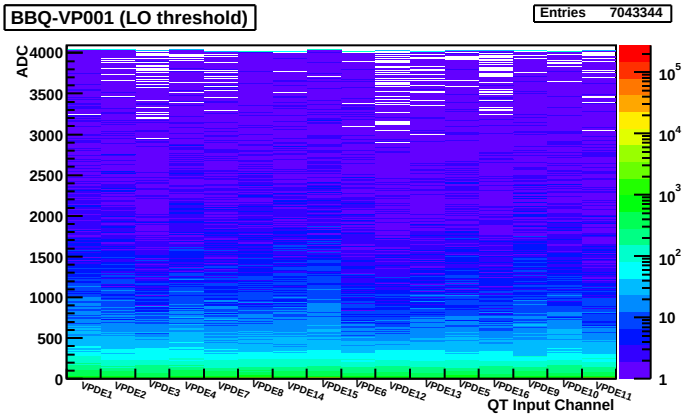

BBQ-BB002 (BBC west small tiles TAC)
Entries 7043344
TAC
QT Input Channel

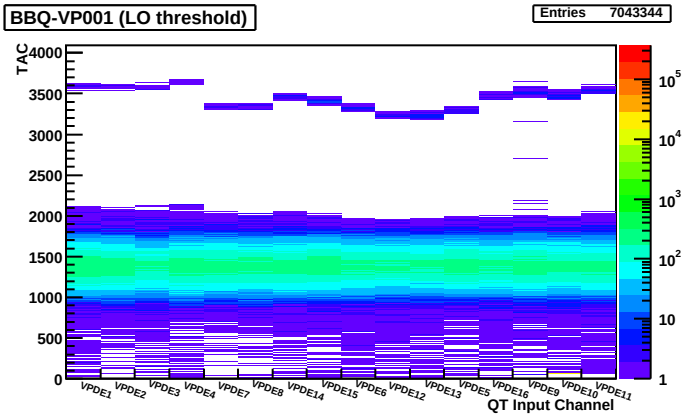

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7043344
ADC
QT Input Channel

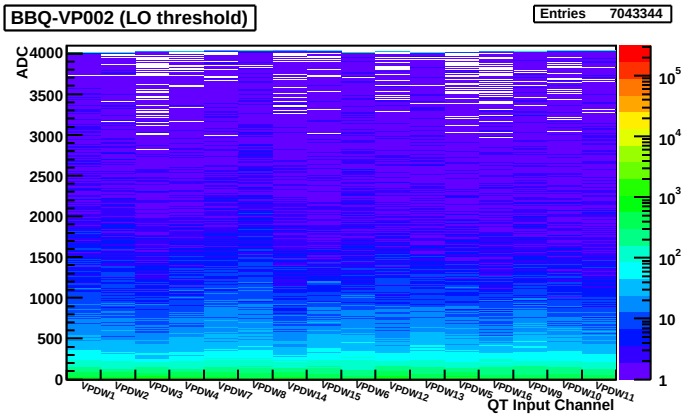

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7043344
TAC
QT Input Channel

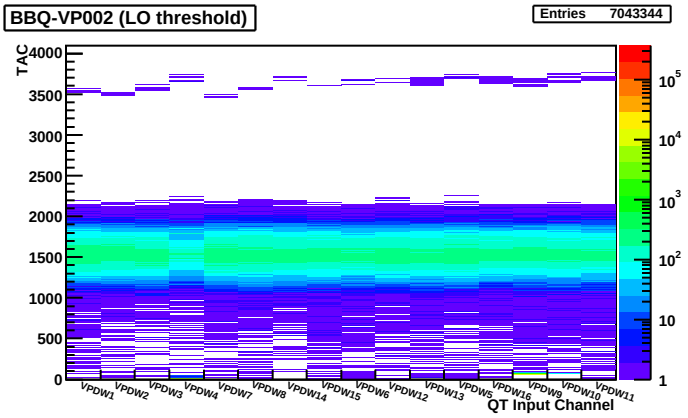

BBQ-ZD001
Entries 7043344
ADC
QT Input Channel

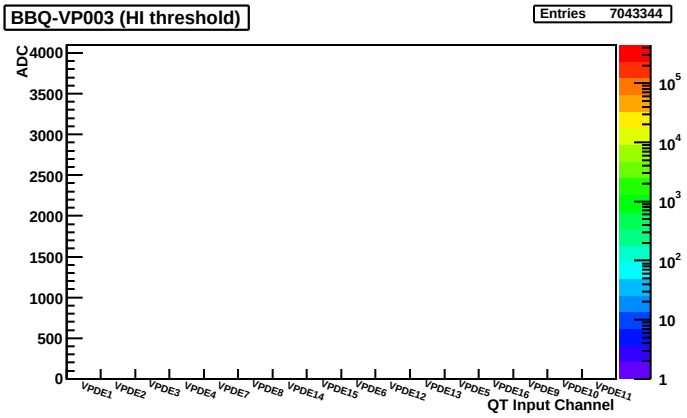

BBQ-ZD001
Entries 7043344
TAC
QT Input Channel

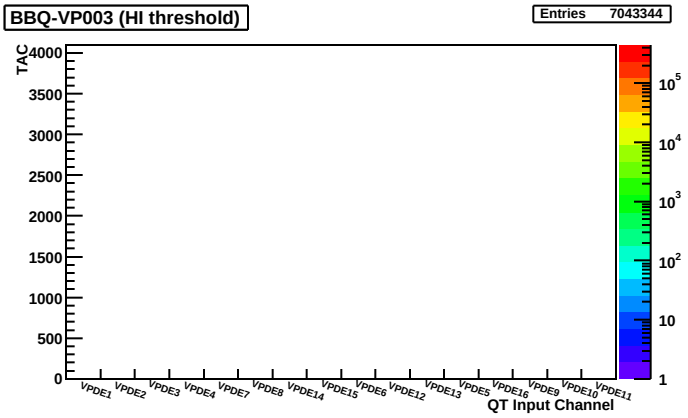

BBQ-VP001 (LO threshold)
Entries 7043344
ADC
QT Input Channel

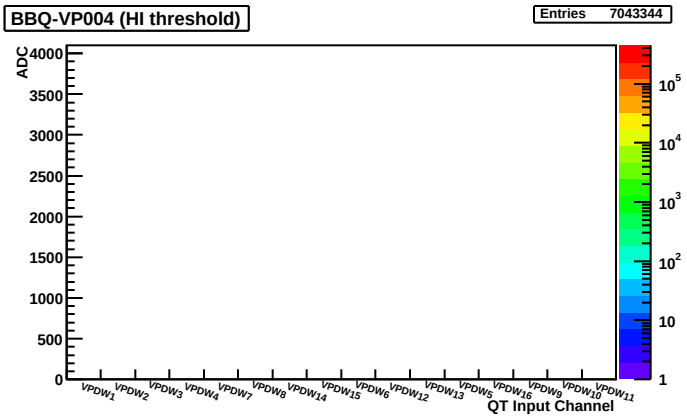

BBQ-VP001 (LO threshold)
Entries 7043344
TAC
QT Input Channel

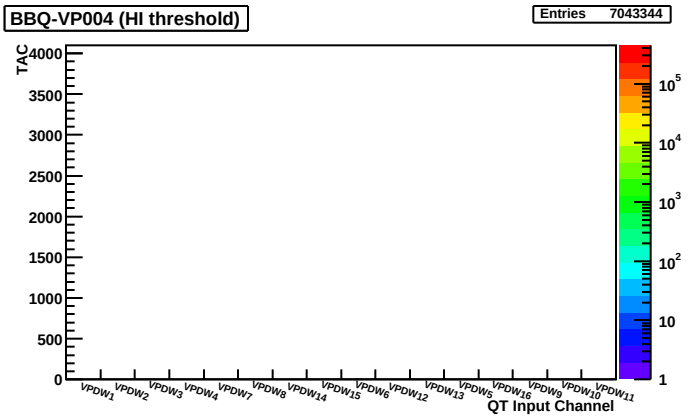

BBQ-VP002 (LO threshold)
Entries 7043344
ADC
QT Input Channel

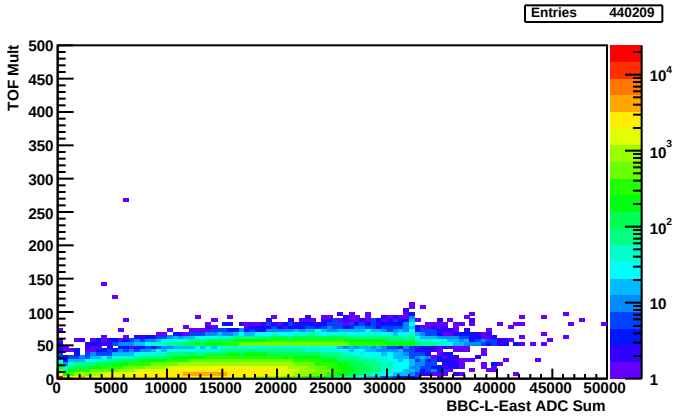

BBQ-VP002 (LO threshold)
Entries 7043344
TAC
QT Input Channel

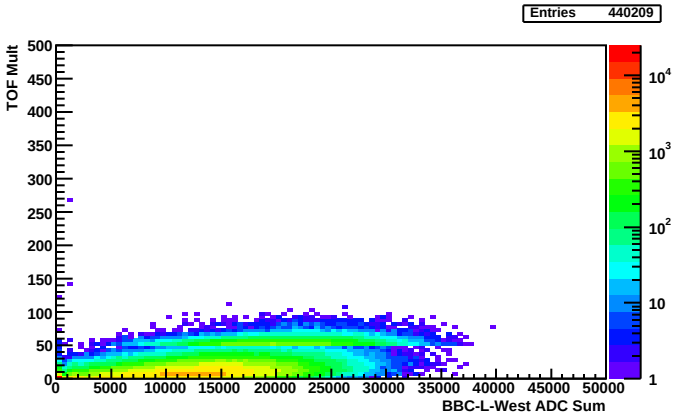

BBQ-VP003 (HI threshold)
Entries 7043344
ADC
QT Input Channel

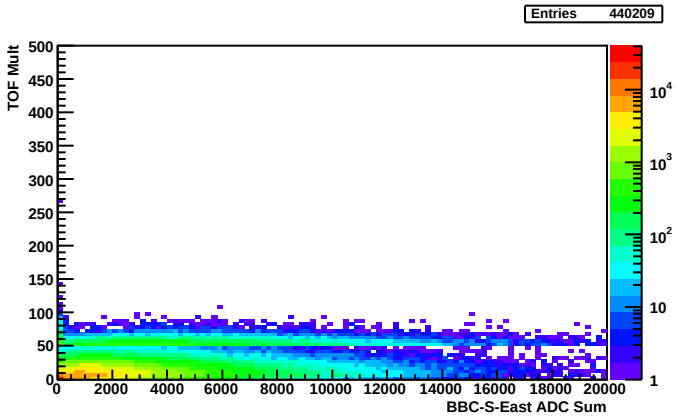

BBQ-VP003 (HI threshold)
Entries 7043344
TAC
QT Input Channel

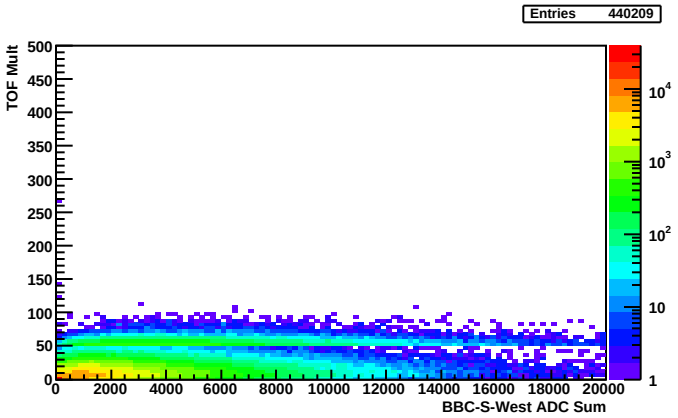

BBQ-VP004 (HI threshold)
Entries 7043344
ADC
QT Input Channel

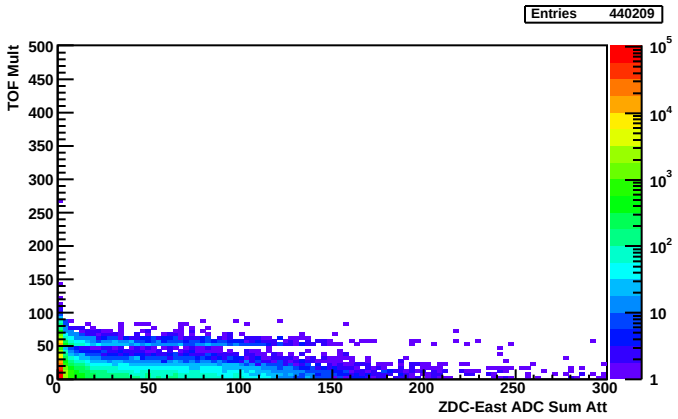

BBQ-VP004 (HI threshold)
Entries 7043344
TAC
QT Input Channel

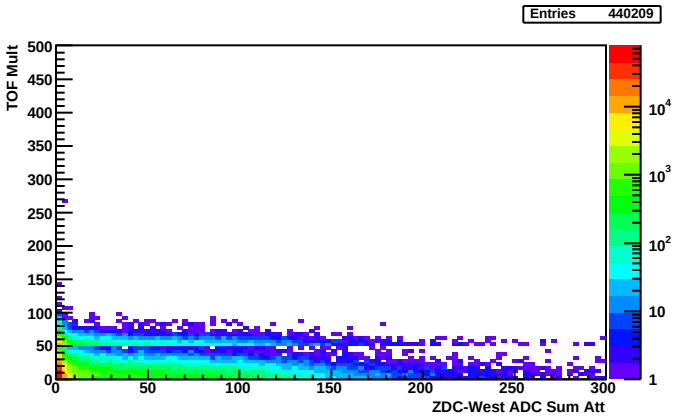

Entries 440209
TOF Mult
BBC-L-East ADC Sum

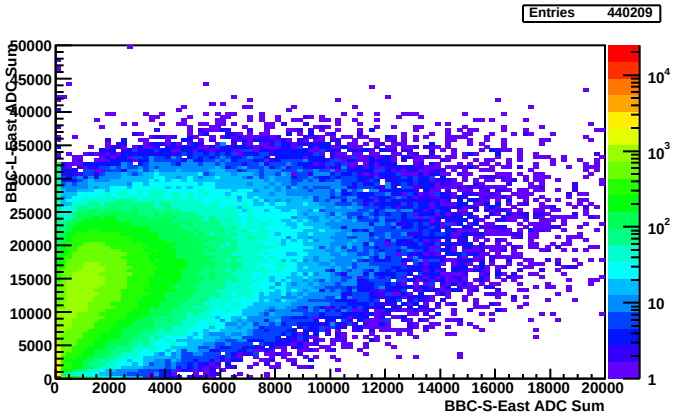

Entries 440209
TOF Mult
BBC-L-West ADC Sum

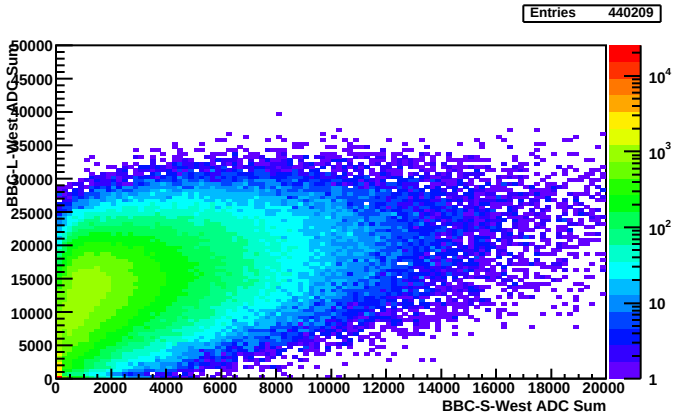

Entries 440209
TOF Mult
BBC-S-East ADC Sum

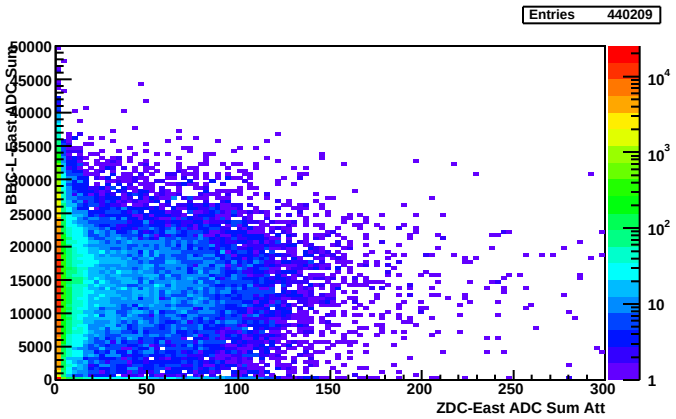

Entries 440209
TOF Mult
BBC-S-West ADC Sum

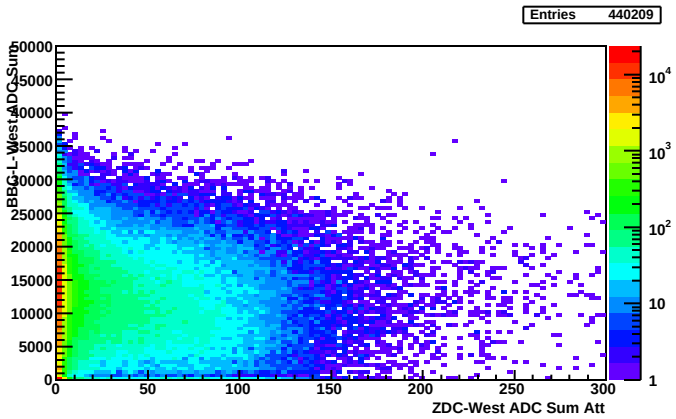

Entries 440209
TOF Mult
ZDC-East ADC Sum Att

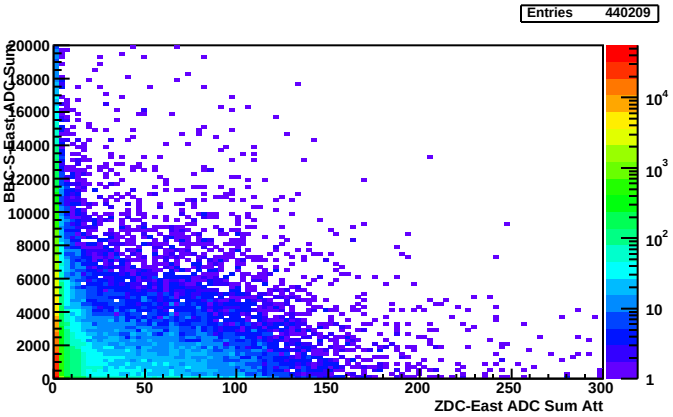

Entries 440209
TOF Mult
ZDC-West ADC Sum Att

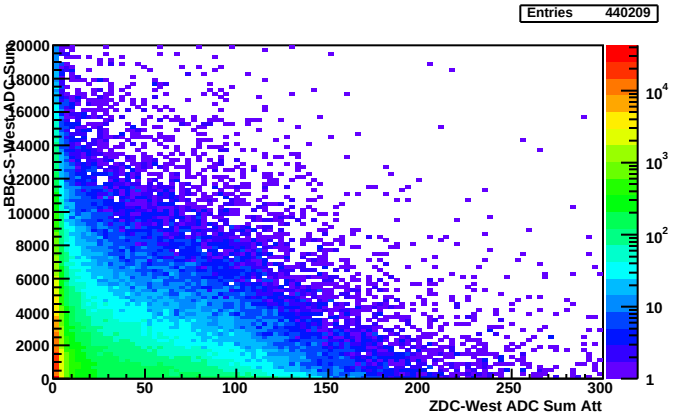

Entries 440209
BBC-L-East ADC Sum
BBC-S-East ADC Sum

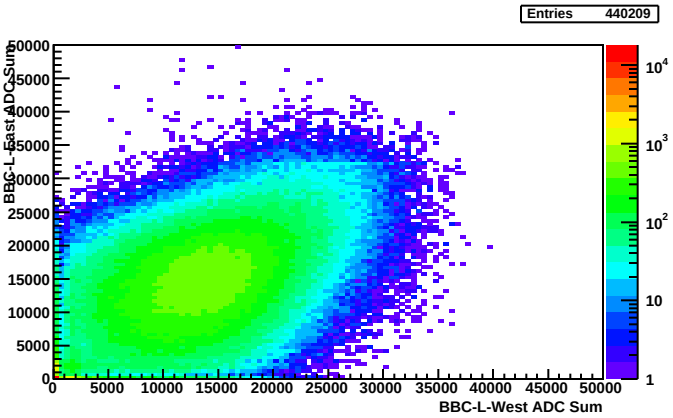

Entries 440209
BBC-L-West ADC Sum
BBC-S-West ADC Sum

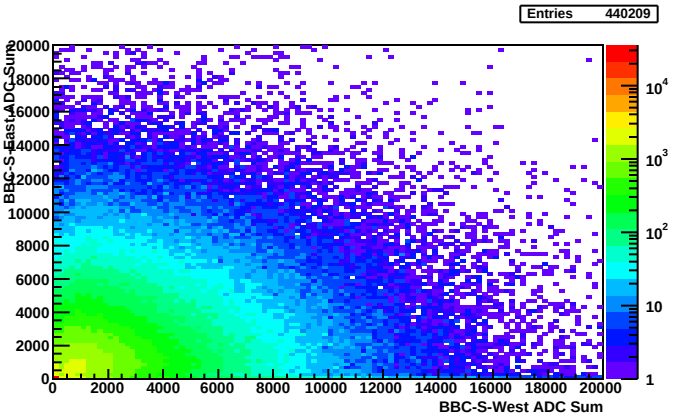

Entries 440209
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

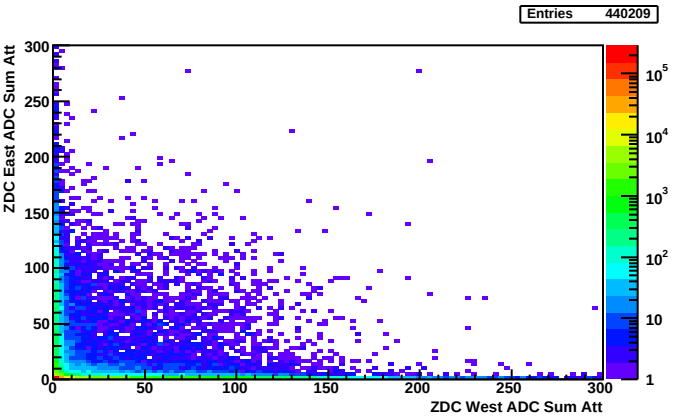

Entries 440209
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

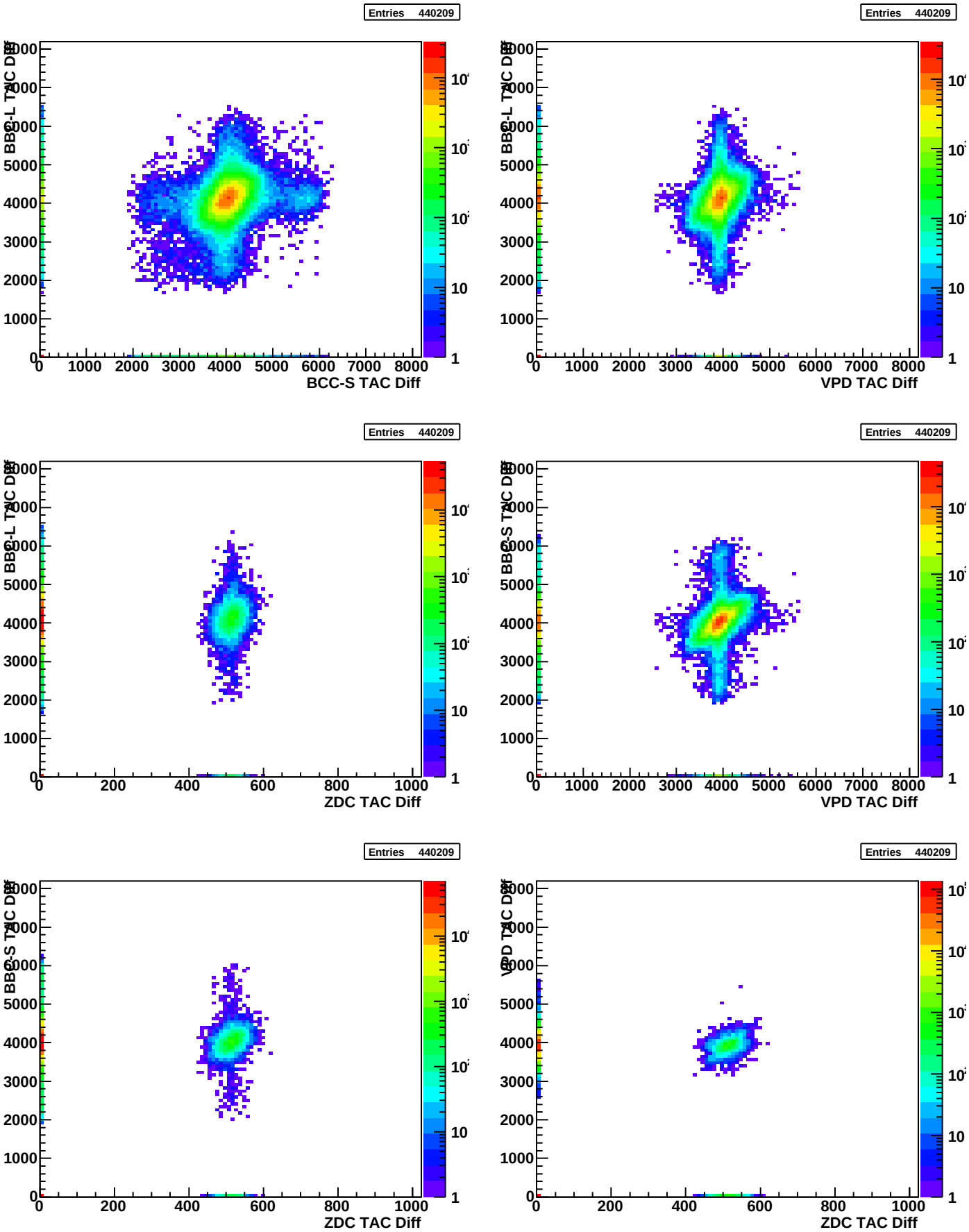

Entries 440209
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

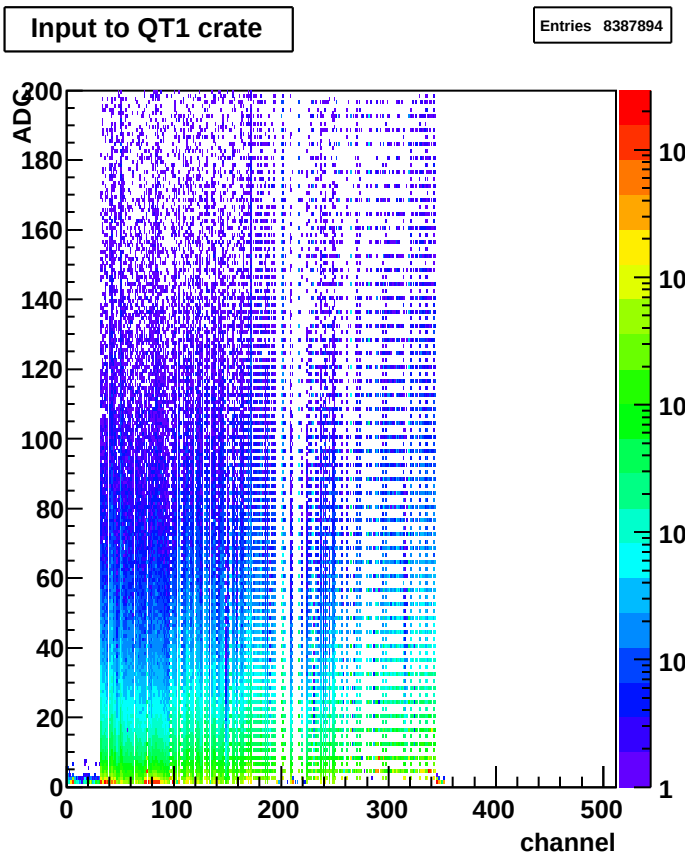

Entries 440209
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

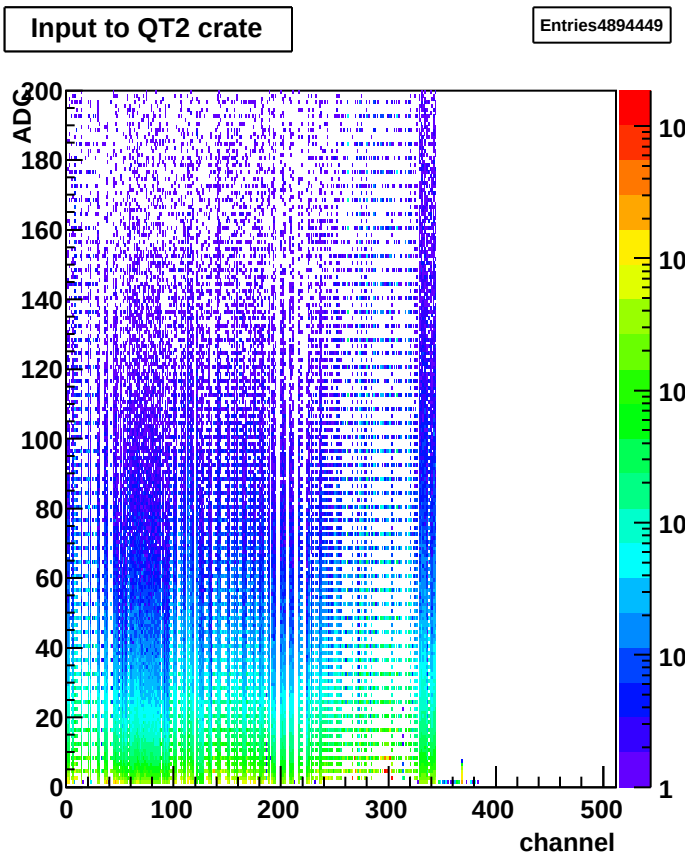

Entries 440209
BBC-L-East ADC Sum
BBC-L-West ADC Sum

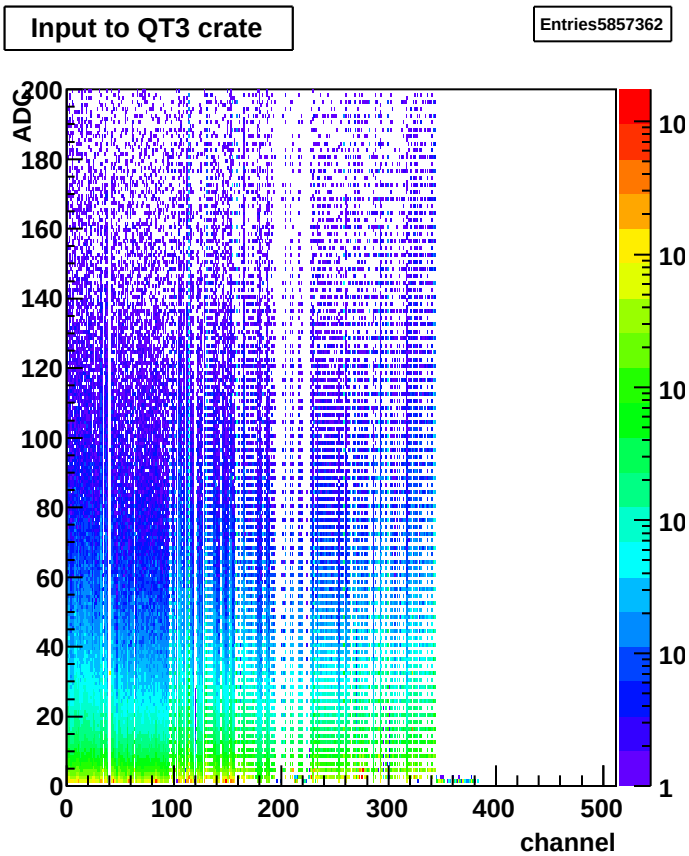

Entries 440209
BBC-S-East ADC Sum
BBC-S-West ADC Sum

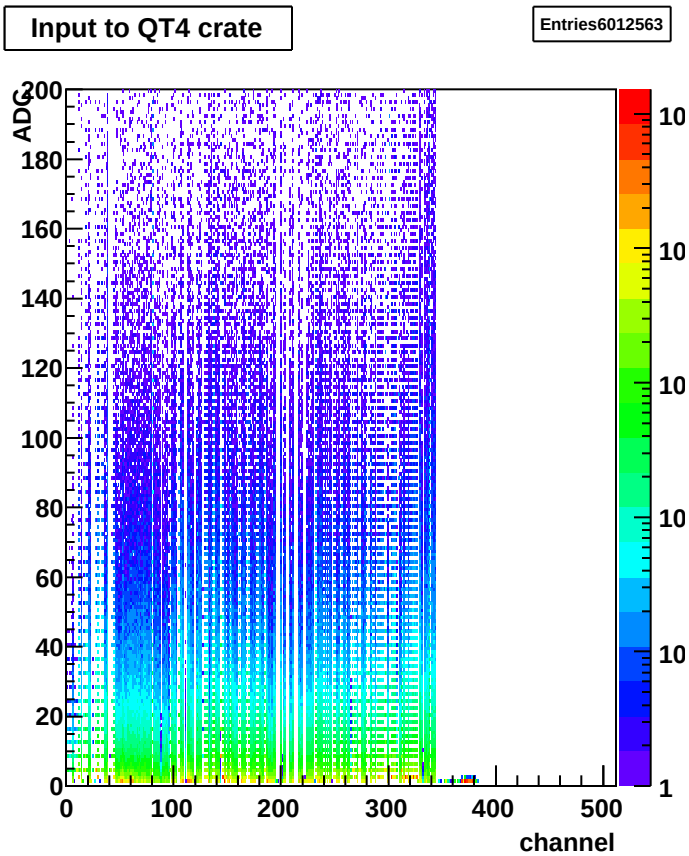

Entries 440209
ZDC East ADC Sum Att
ZDC West ADC Sum Att

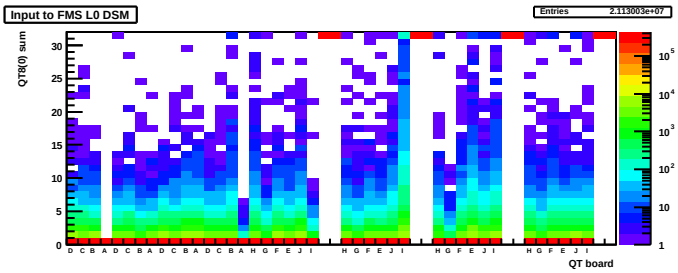

Entries 440209
BBC-L TAC Diff
BCC-S TAC Diff
Entries 440209
BBC-L TAC Diff
VPD TAC Diff
Entries 440209
BBC-L TAC Diff
ZDC TAC Diff
Entries 440209
BBC-S TAC Diff
VPD TAC Diff
Entries 440209
BBC-S TAC Diff
ZDC TAC Diff
Entries 440209
VPD TAC Diff
ZDC TAC Diff

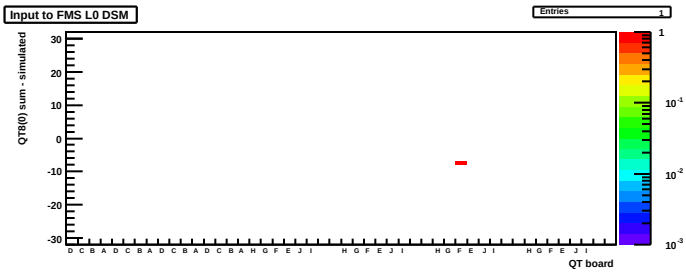

Input to QT1 crate
Entries 8387894
ADC
channel

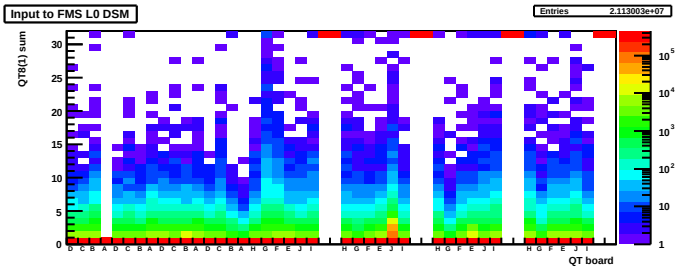

Input to QT2 crate
Entries4894449
ADC
channel

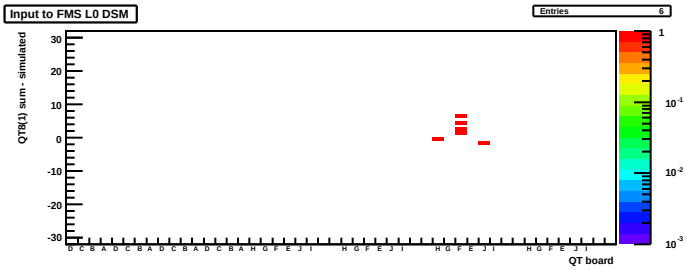

Input to QT3 crate
Entries5857362
ADC
channel

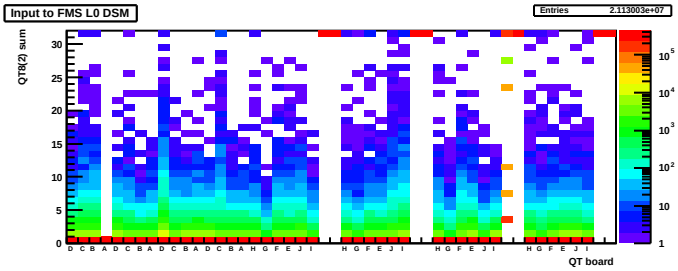

Input to QT4 crate
Entries6012563
ADC
channel

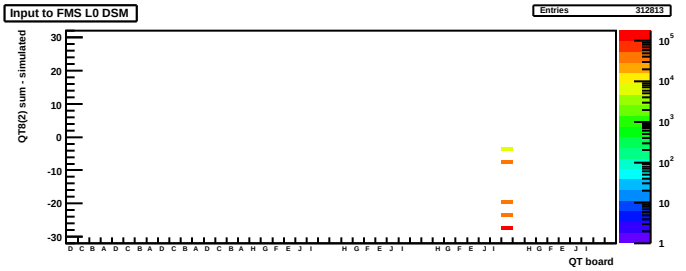

Input to FMS L0 DSM
Entries 2.113003e+07
QT8(0) sum
QT board

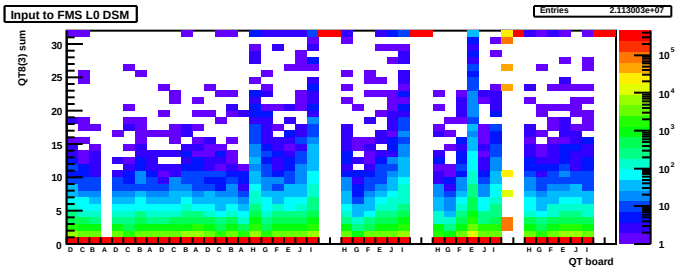

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

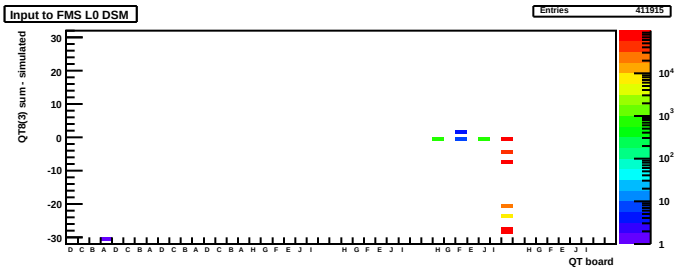

Input to FMS L0 DSM
Entries 2.113003e+07
QT8(1) sum
QT board

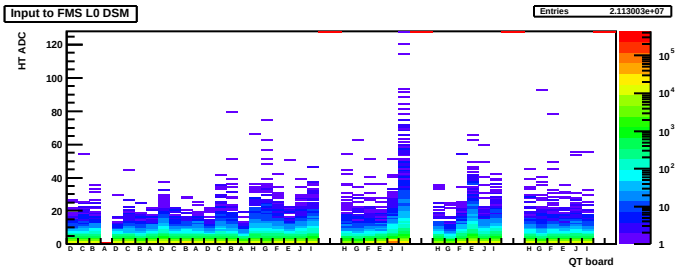

Input to FMS L0 DSM
Entries 6
QT8(1) sum - simulated
QT board

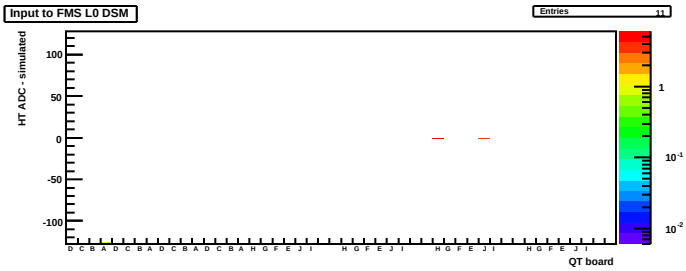

Input to FMS L0 DSM
Entries 2.113003e+07
QT8(2) sum
QT board

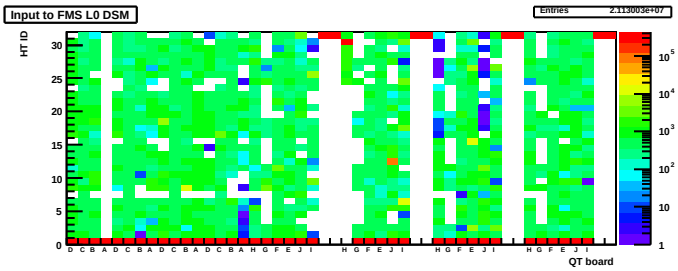

Input to FMS L0 DSM
Entries 312813
QT8(2) sum - simulated
QT board

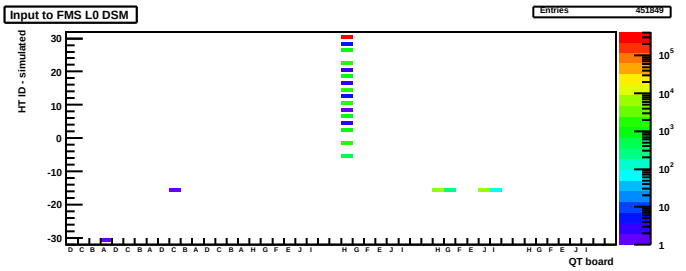

Input to FMS L0 DSM
Entries 2.113003e+07
QT8(3) sum
QT board

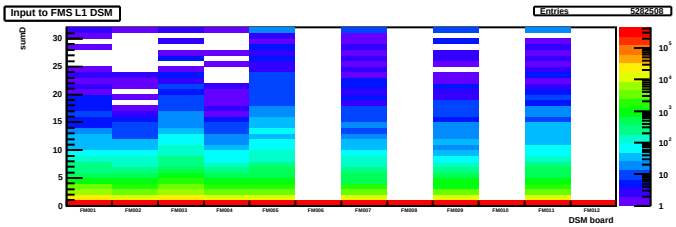

Input to FMS L0 DSM
Entries 411915
QT8(3) sum - simulated
QT board

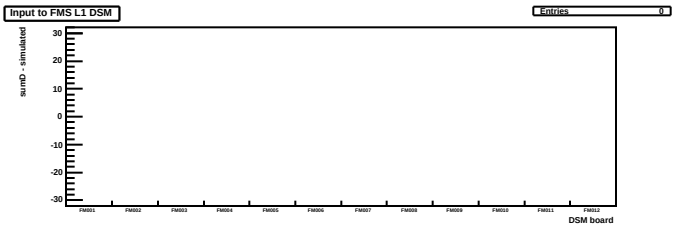

Input to FMS L0 DSM
Entries 2.113003e+07
HT ADC
QT board

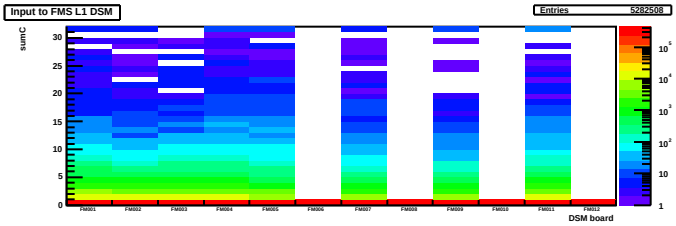

Input to FMS L0 DSM
Entries 11
HT ADC - simulated
QT board

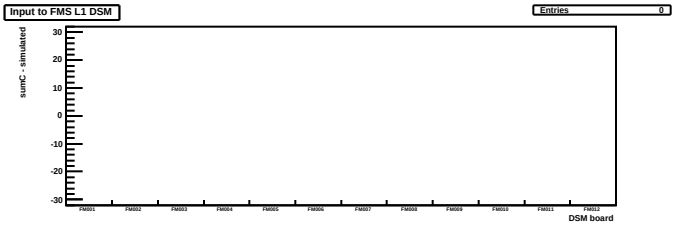

Input to FMS L0 DSM
Entries 2.113003e+07
HT ID
QT board

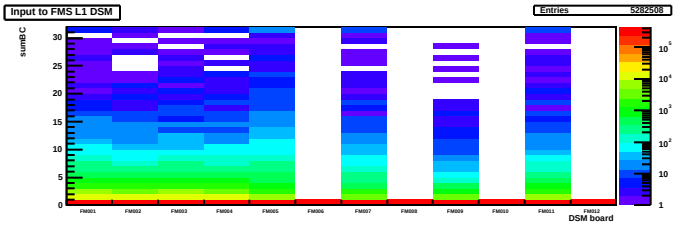

Input to FMS L0 DSM
Entries 451849
HT ID - simulated
QT board

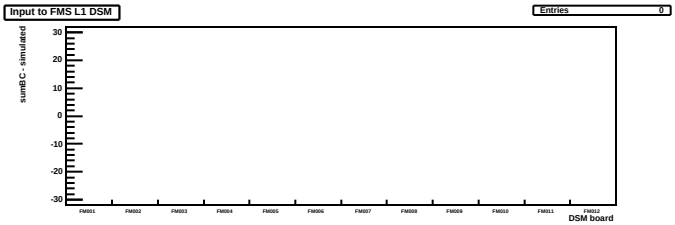

Input to FMS L1 DSM
Entries 5282508
sumD
DSM board

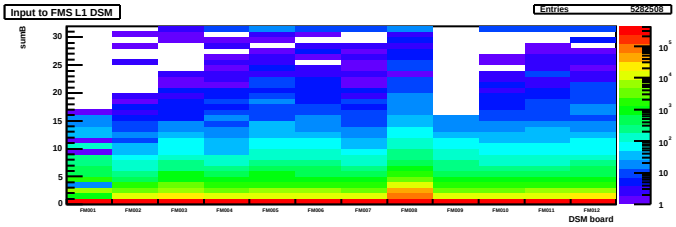

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

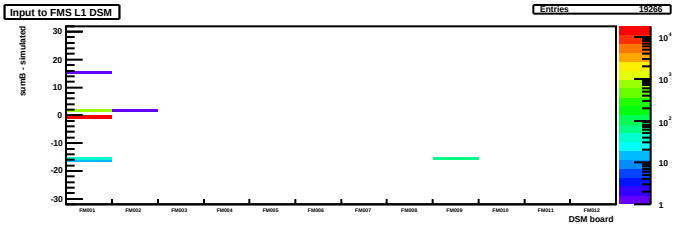

Input to FMS L1 DSM
Entries 5282508
sumC
DSM board

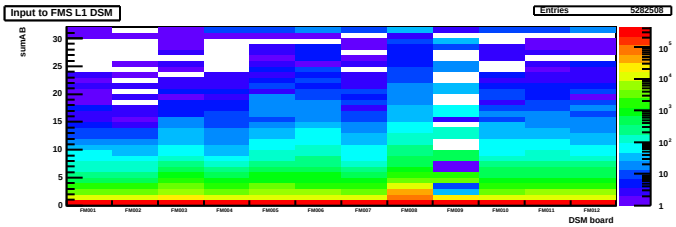

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

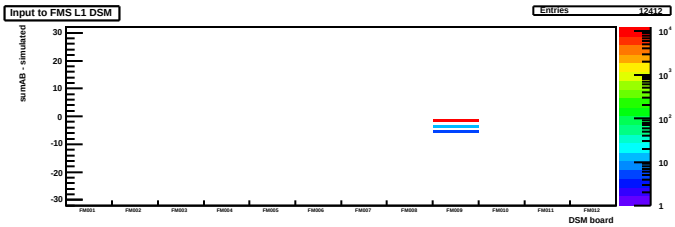

Input to FMS L1 DSM
Entries 5282508
sumBC
DSM board

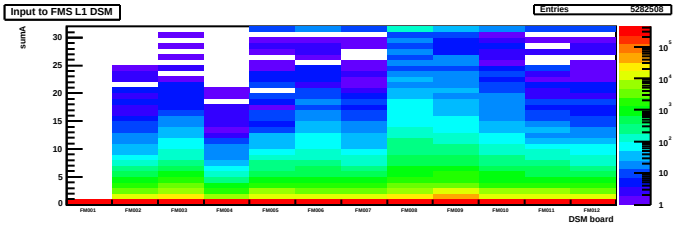

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

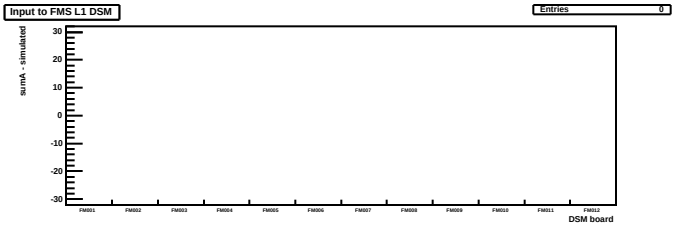

Input to FMS L1 DSM
Entries 5282508
sumB
DSM board

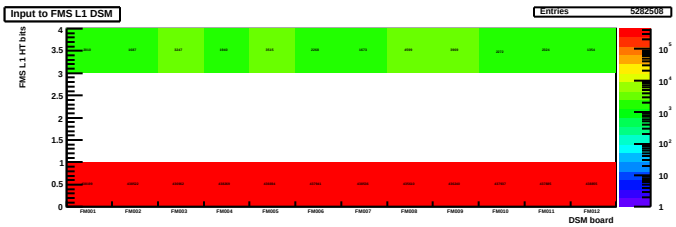

Input to FMS L1 DSM
Entries 19266
sumB - simulated
DSM board

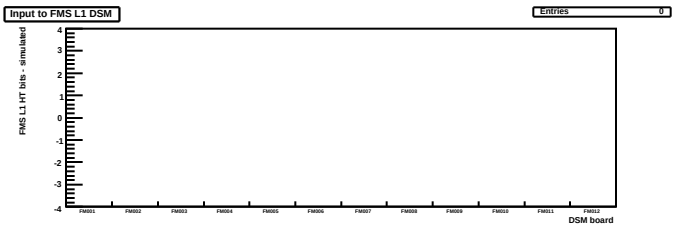

Input to FMS L1 DSM
Entries 5282508
sumAB
DSM board

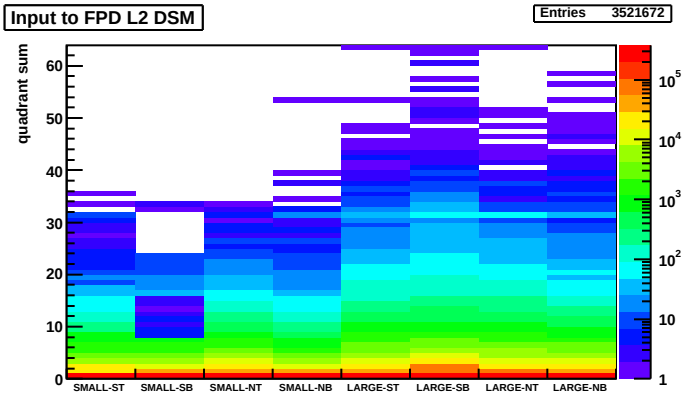

Input to FMS L1 DSM
Entries 12412
sumAB - simulated
DSM board

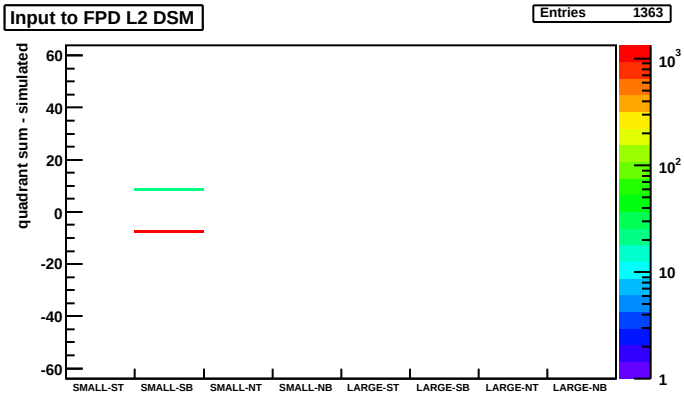

Input to FMS L1 DSM
Entries 5282508
sumA
DSM board

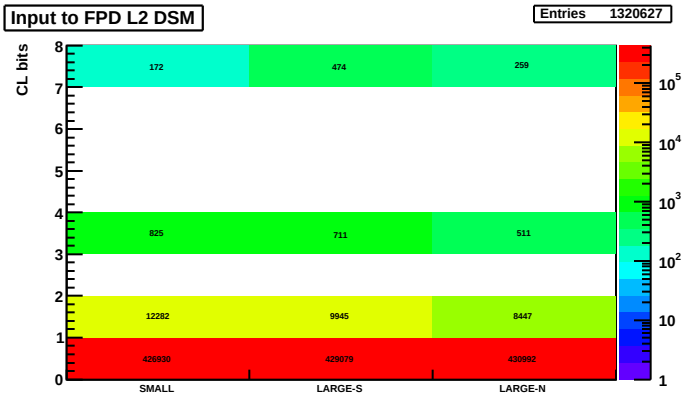

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

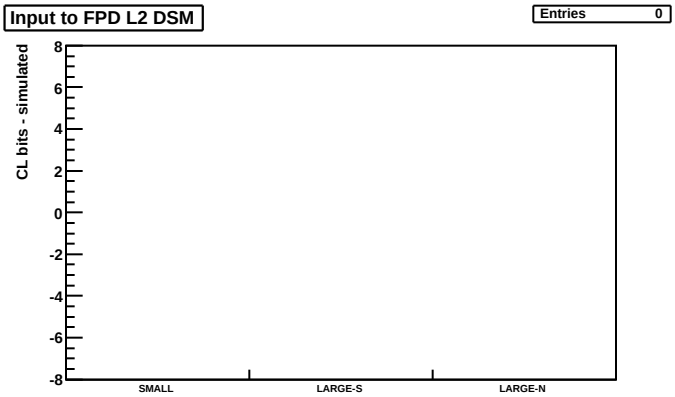

Input to FMS L1 DSM
Entries 5282508
FMS L1 HT bits
DSM board

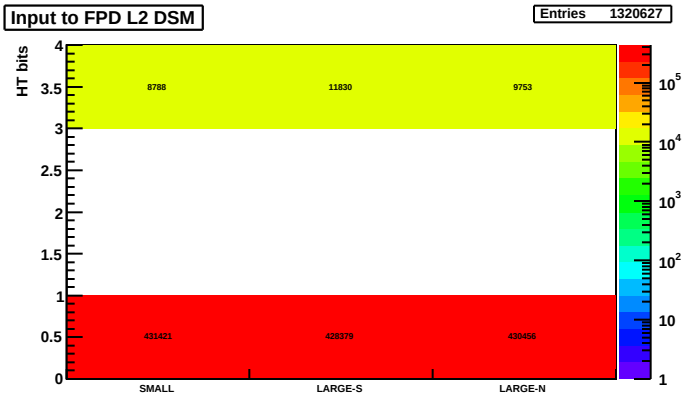

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

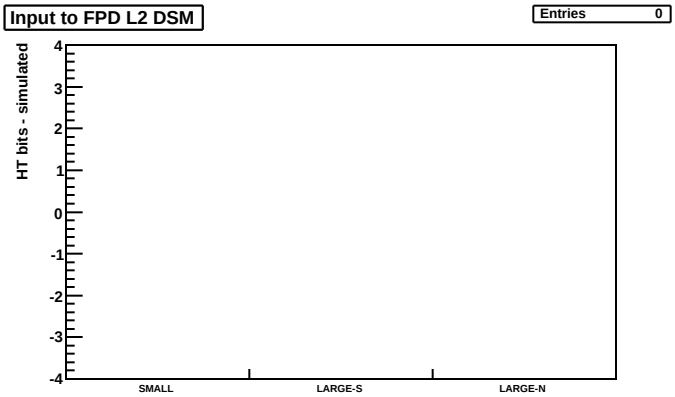

Input to FPD L2 DSM
Entries 3521672
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

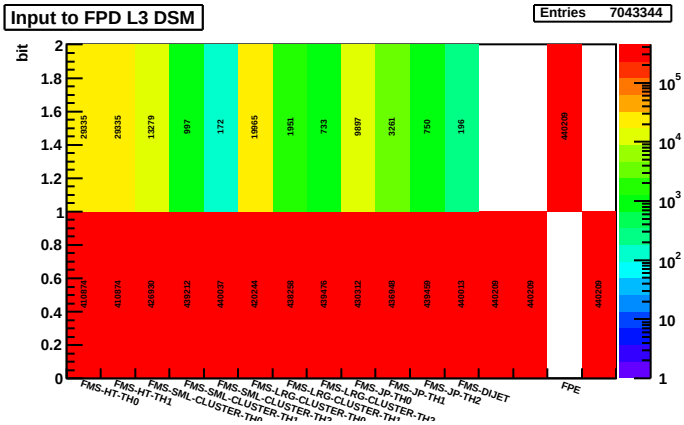

Input to FPD L2 DSM
Entries 1363
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

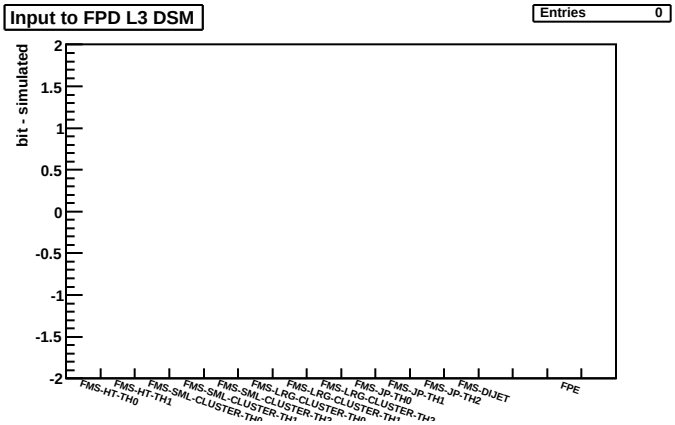

Input to FPD L2 DSM
Entries 1320627
CL bits
172
474
259
825
711
511
12282
9945
8447
426930
429079
430992
SMALL
LARGE-S
LARGE-N

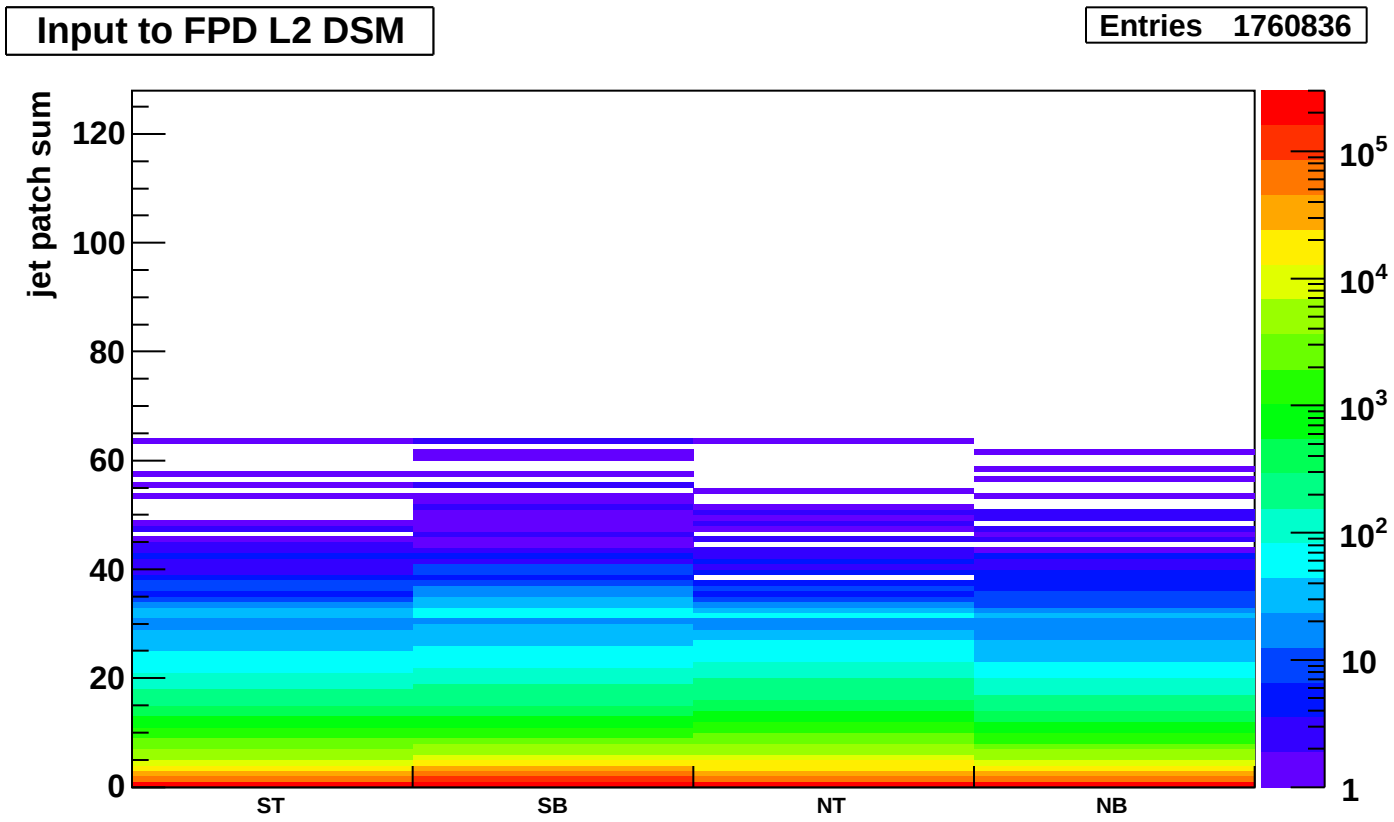

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

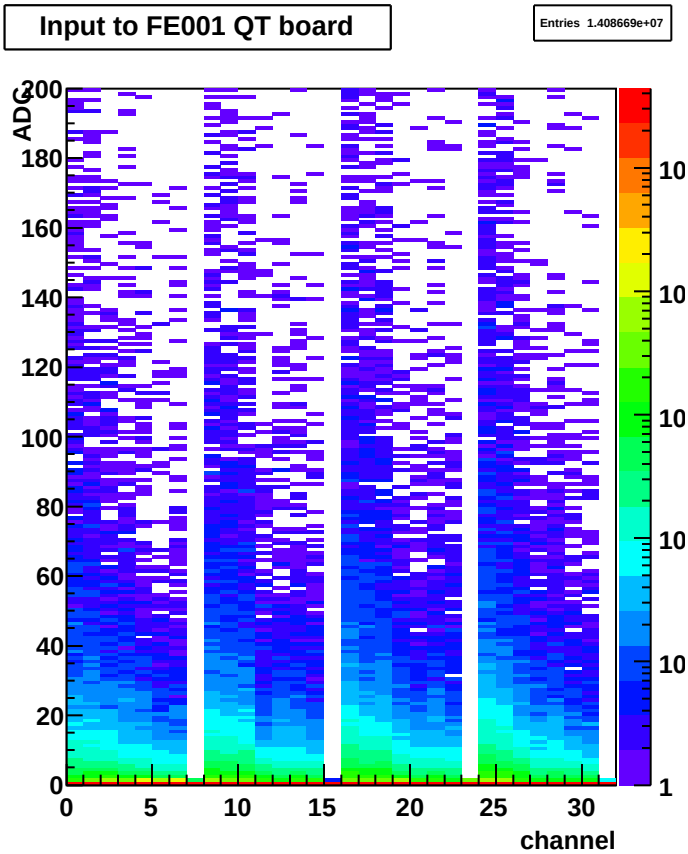

Input to FPD L2 DSM
Entries 1320627
HT bits
8788
11830
9753
431421
428379
430456
SMALL
LARGE-S
LARGE-N

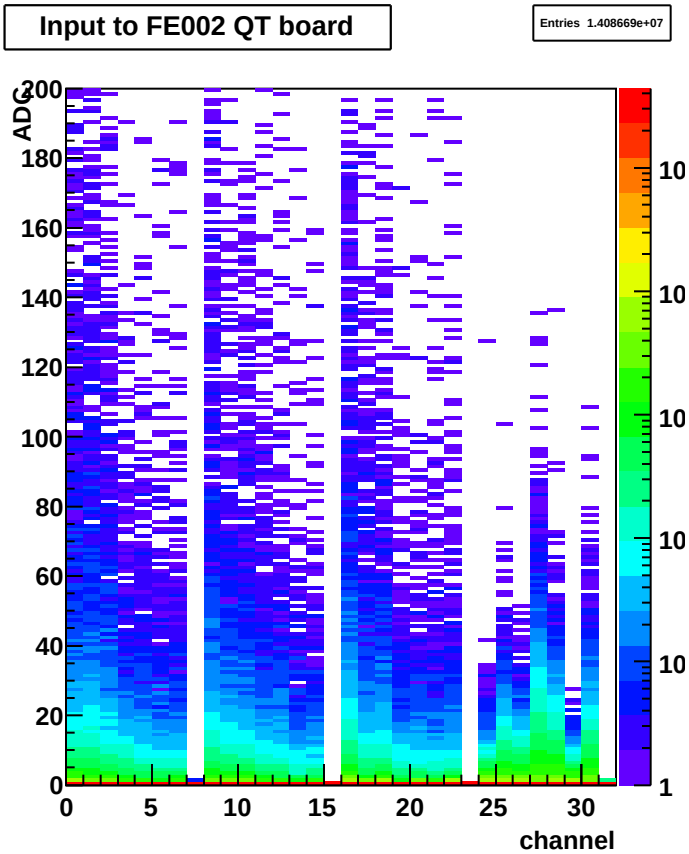

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

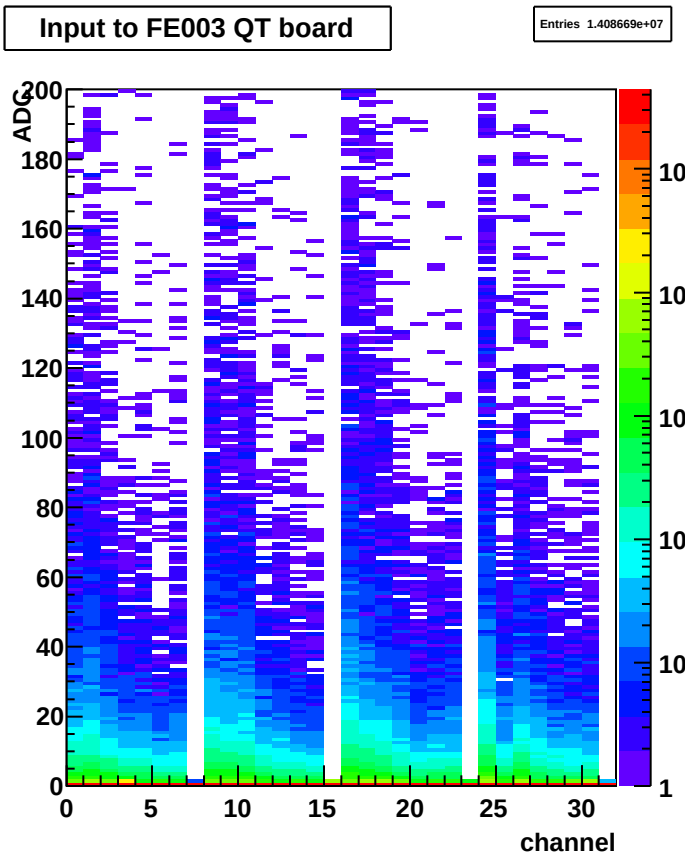

Input to FPD L3 DSM
Entries 7043344
bit
28335
28335
13279
997
172
19965
1951
733
9897
3261
750
196
440209
410874
410874
426930
439212
440037
439244
438258
439475
439312
436948
439469
440013
440209
440209
440209
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

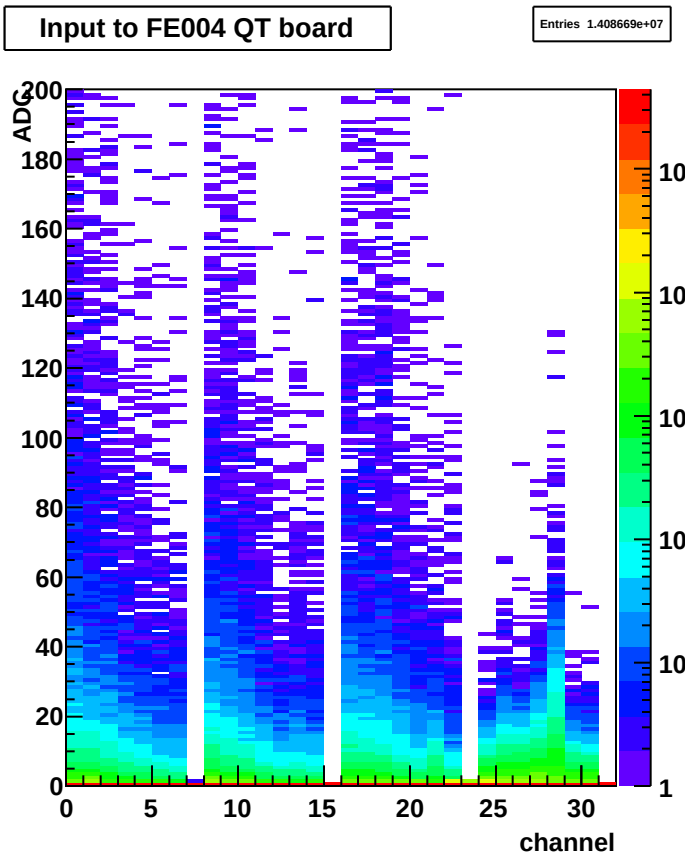

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

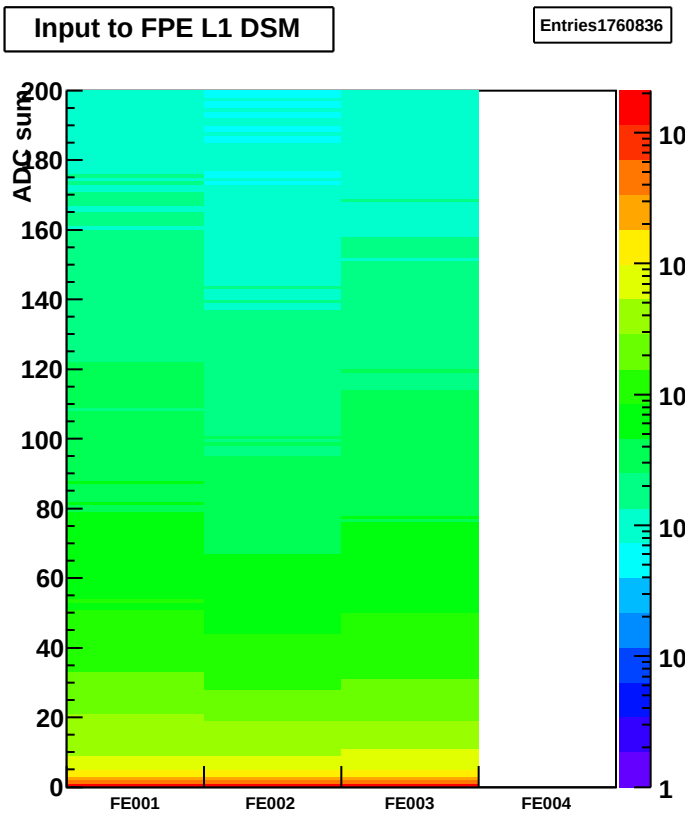

Input to FPD L2 DSM
Entries 1760836
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

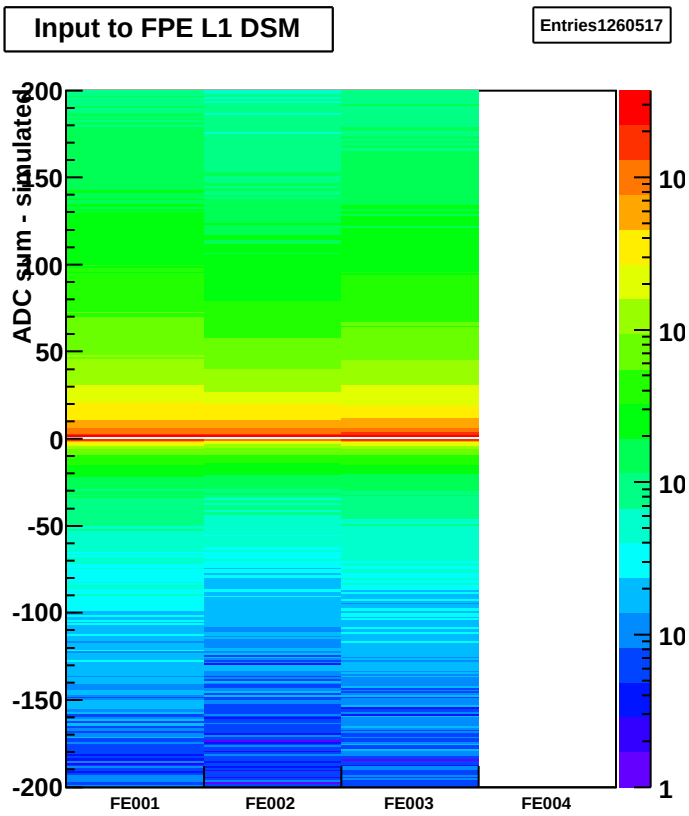

Input to FE001 QT board
Entries 1.408669e+07
ADC
channel

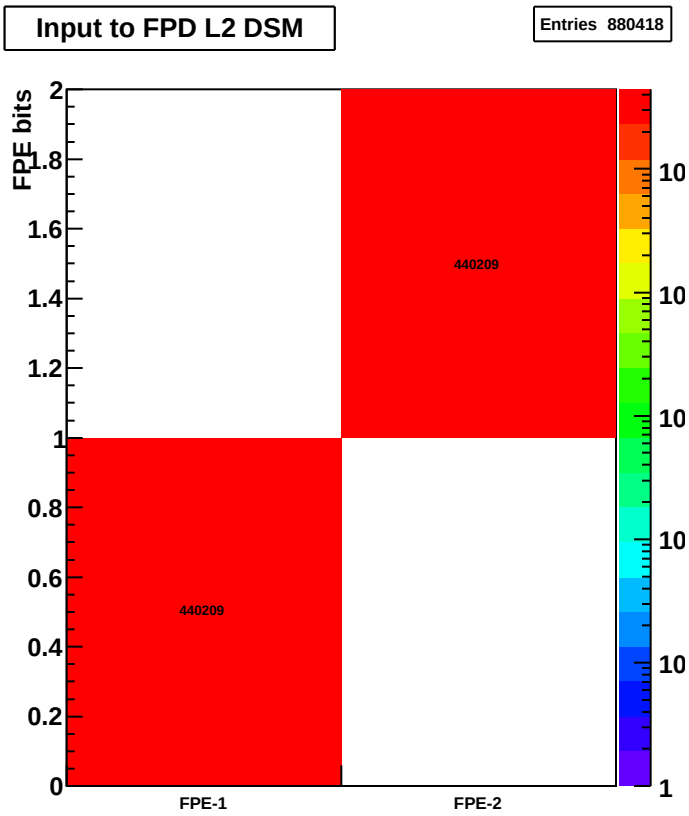

Input to FE002 QT board
Entries 1.408669e+07
ADC
channel

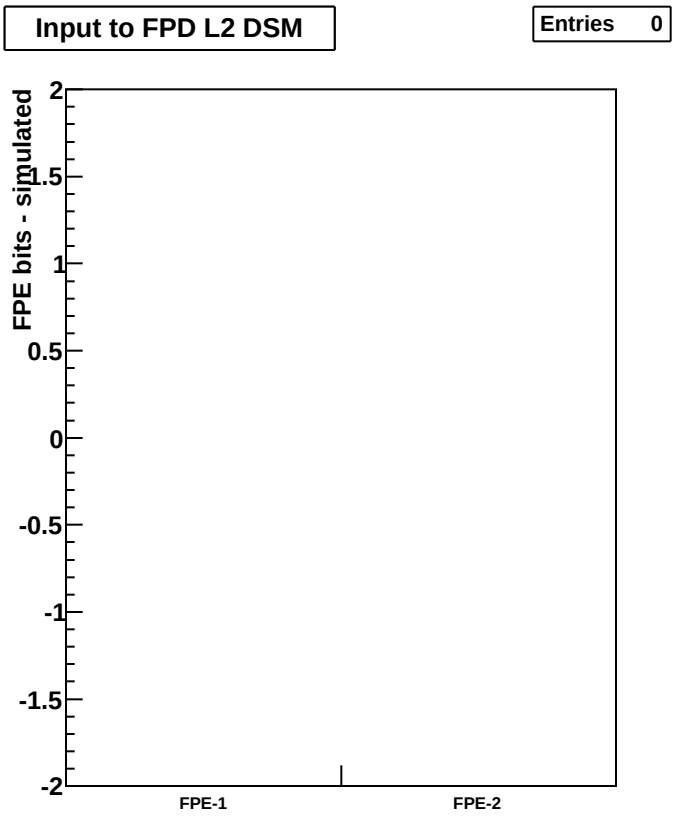

Input to FE003 QT board
Entries 1.408669e+07
ADC
channel


Input to FE004 QT board
Entries 1.408669e+07
ADC
channel


Input to FPE L1 DSM
Entries1760836
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1260517
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 880418
FPE bits
440209
440209
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2