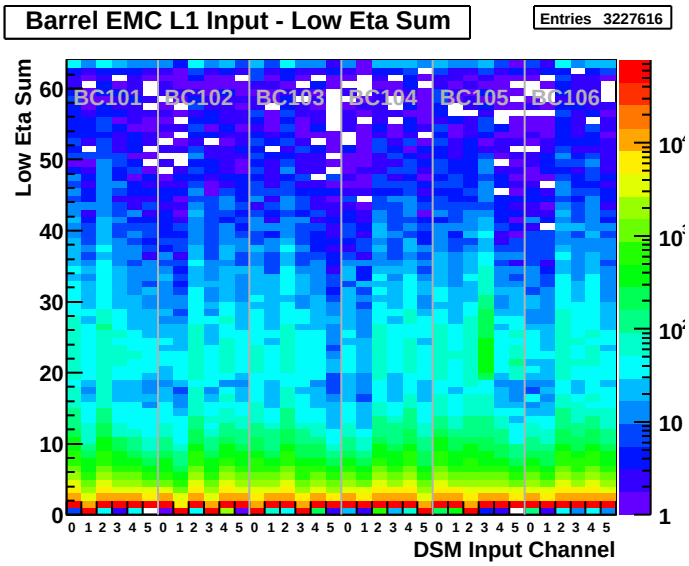
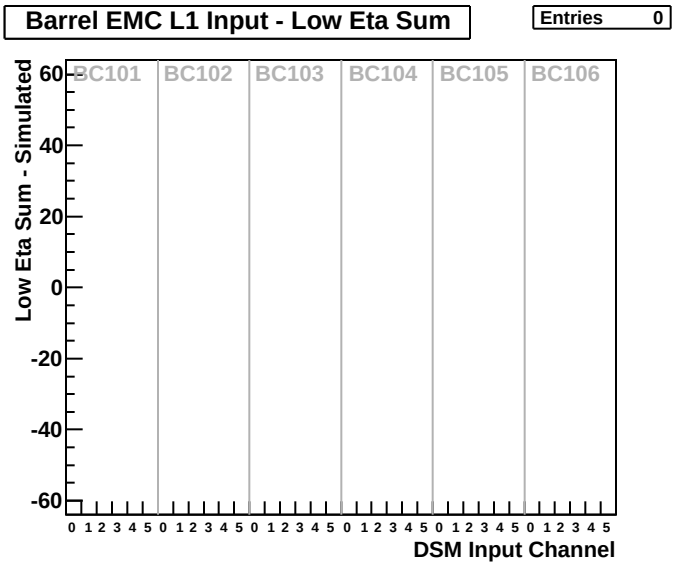
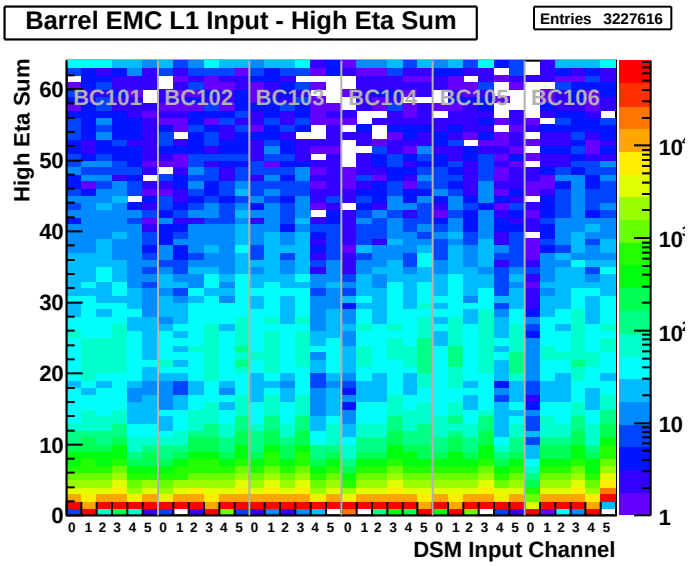
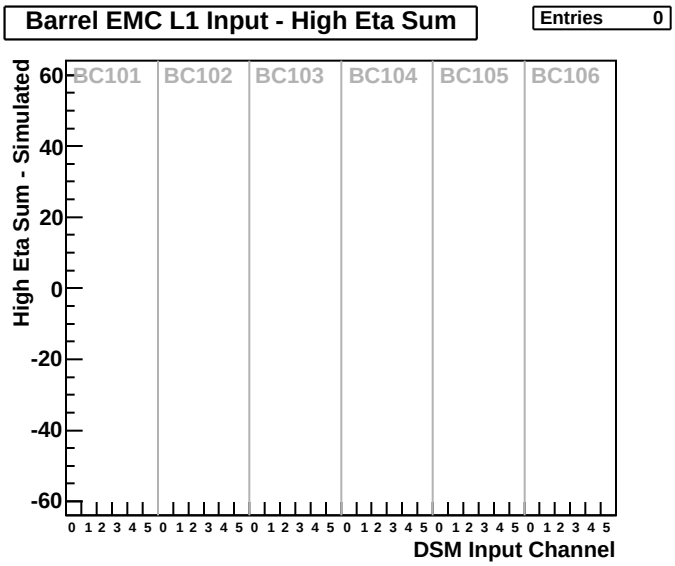
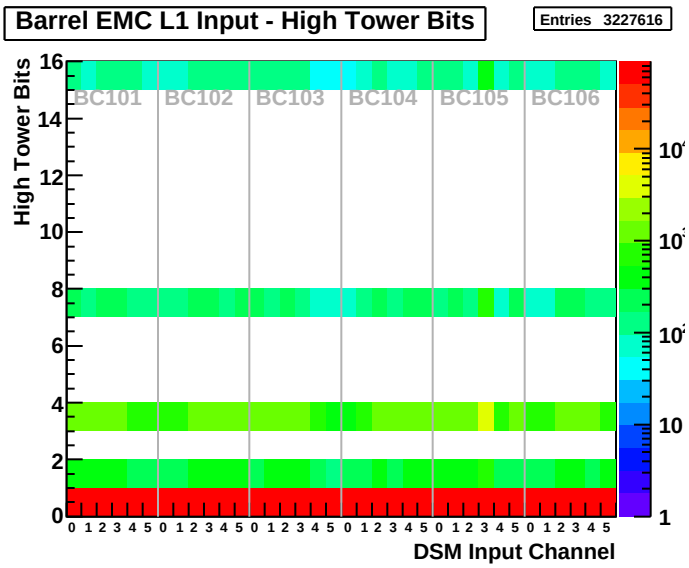
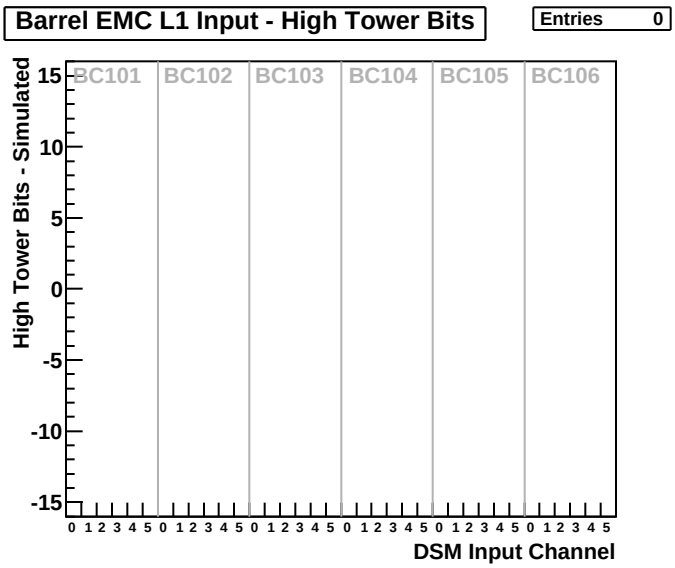

Barrel EMC L1 Input - Low Eta Sum
Entries 3227616
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

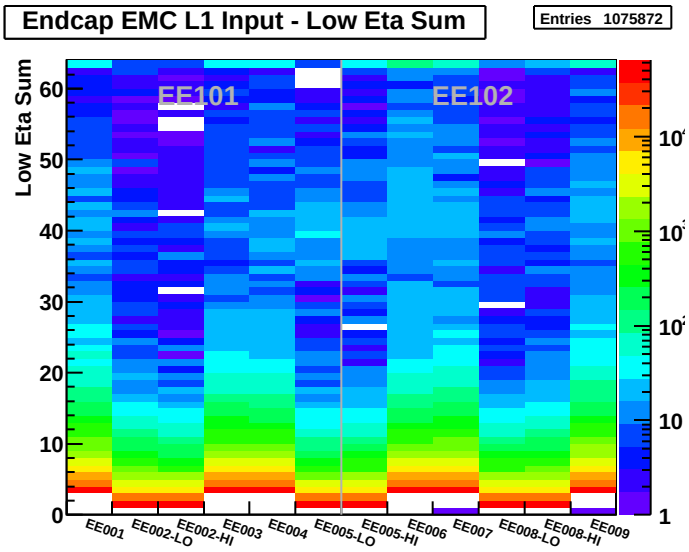

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

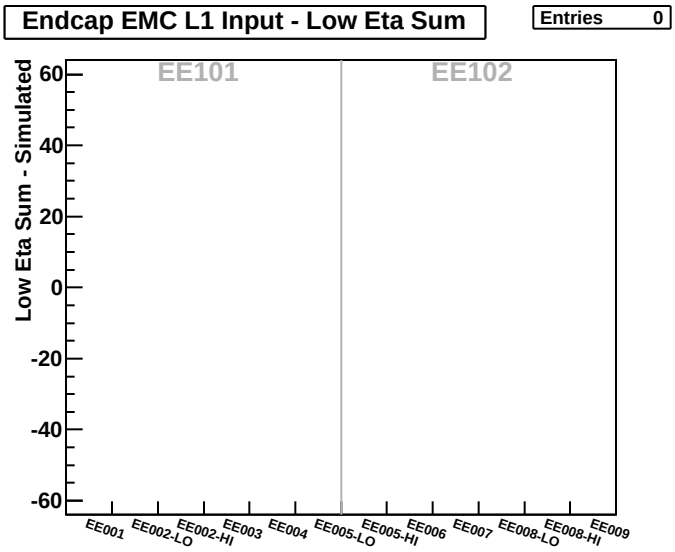

Barrel EMC L1 Input - High Eta Sum
Entries 3227616
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

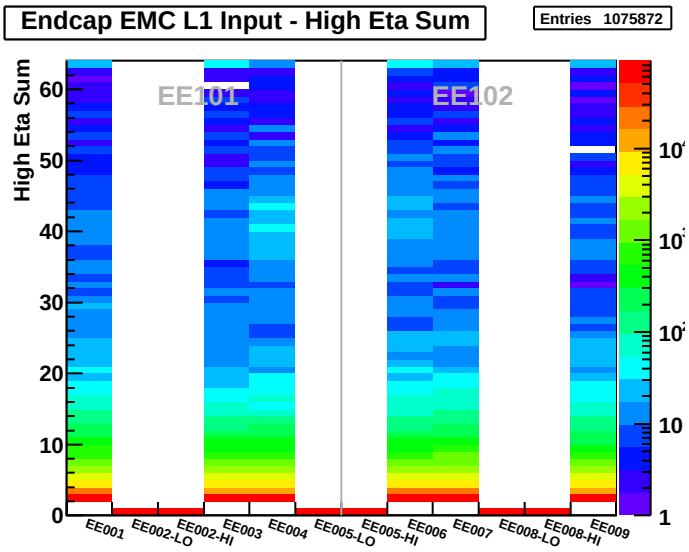

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

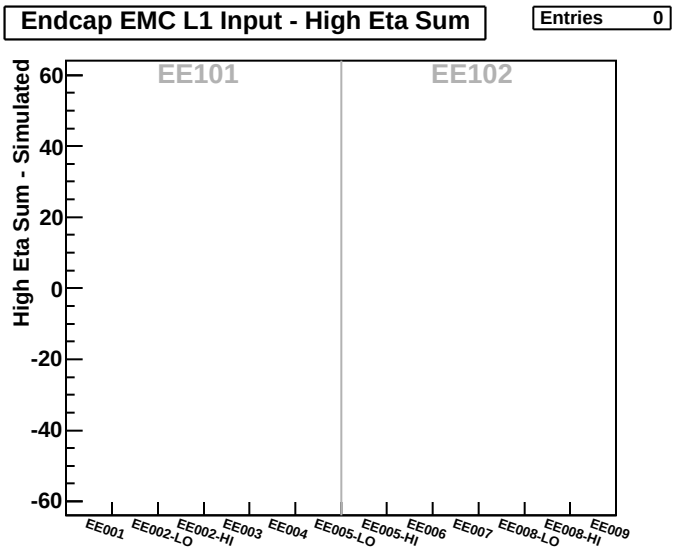

Barrel EMC L1 Input - High Tower Bits
Entries 3227616
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

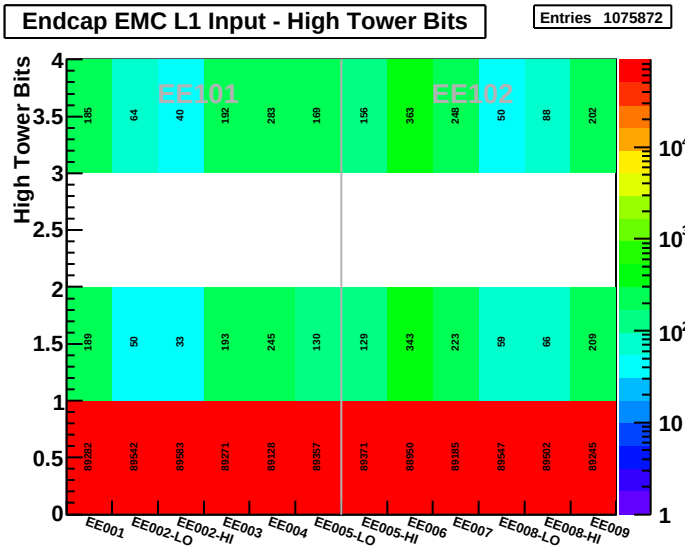

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

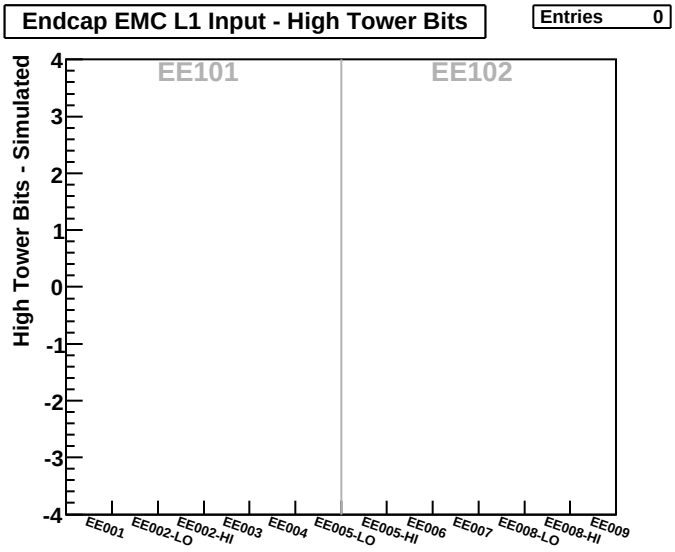

Endcap EMC L1 Input - Low Eta Sum
Entries 1075872
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

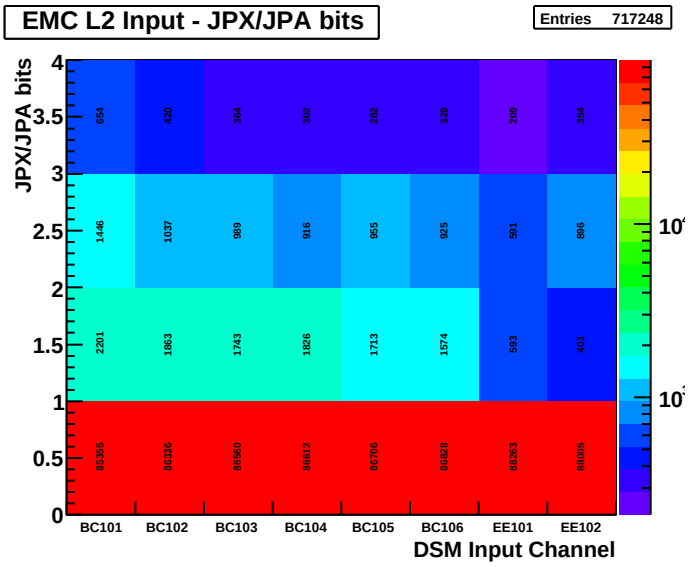

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

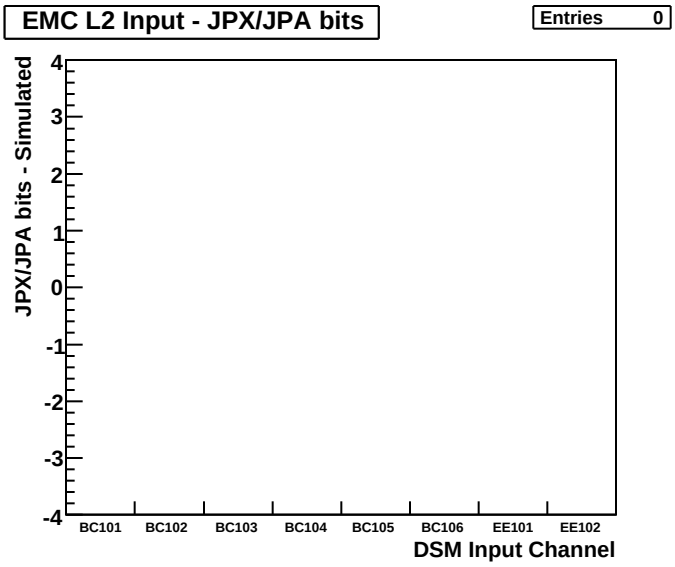

Endcap EMC L1 Input - High Eta Sum
Entries 1075872
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

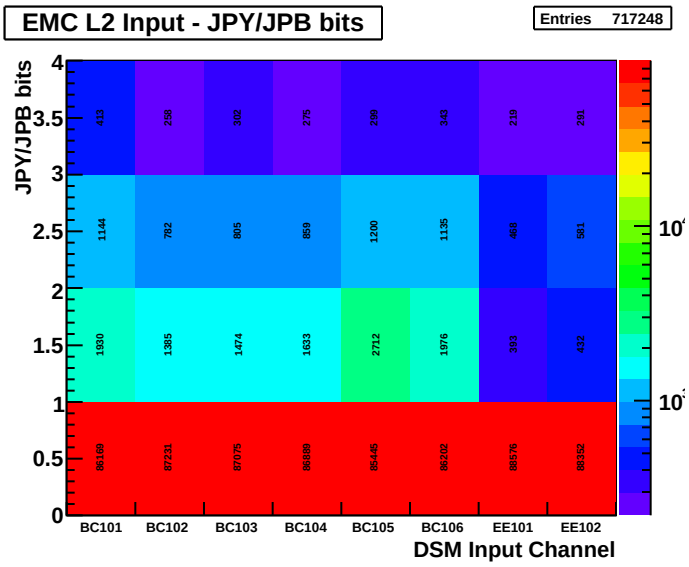

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

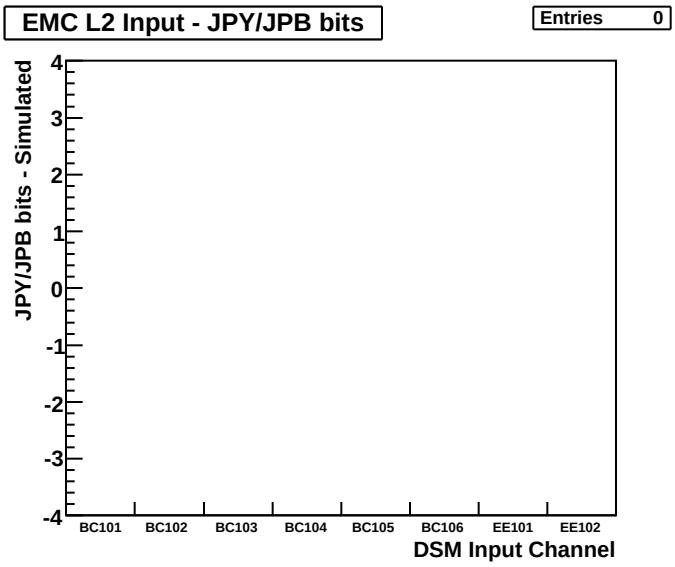

Endcap EMC L1 Input - High Tower Bits
Entries 1075872
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

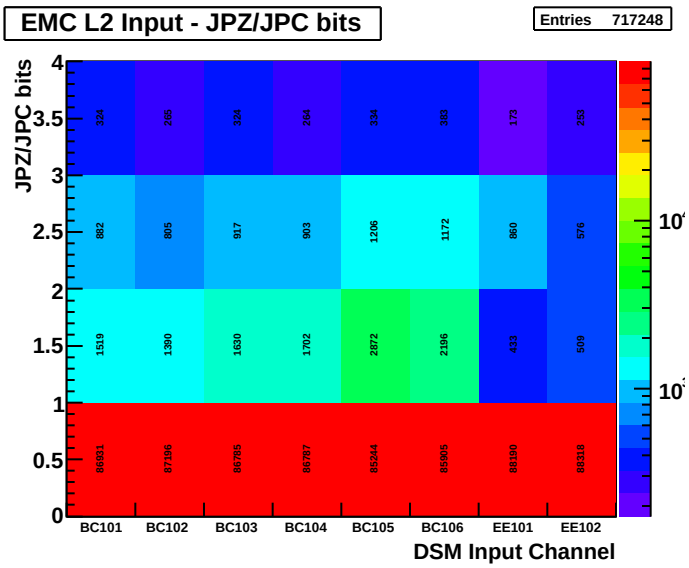

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

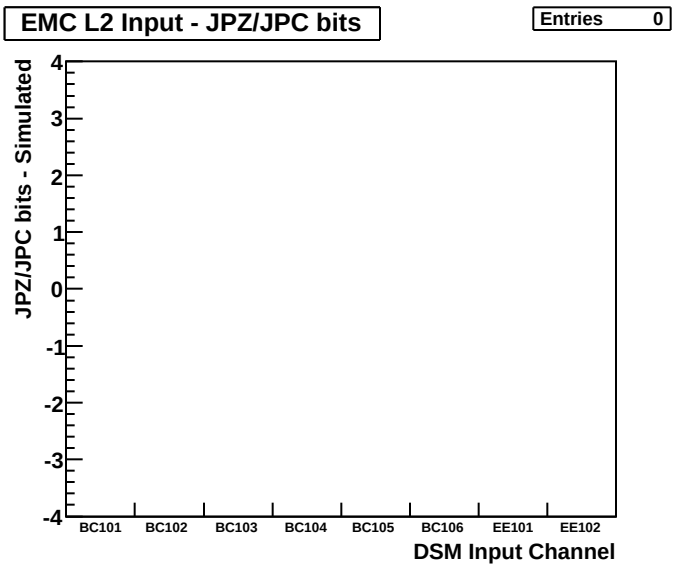

EMC L2 Input - JPX/JPA bits
Entries 717248
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
654 420 364 302 282 329 209 354
1446 1037 989 916 955 925 591 896
2201 1863 1743 1826 1713 1574 593 401
86355 86336 86560 86612 86706 86828 88263 88005

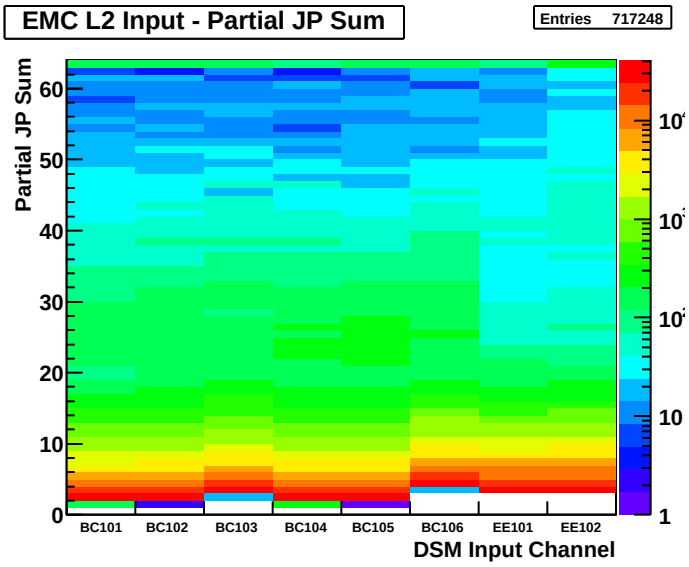

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

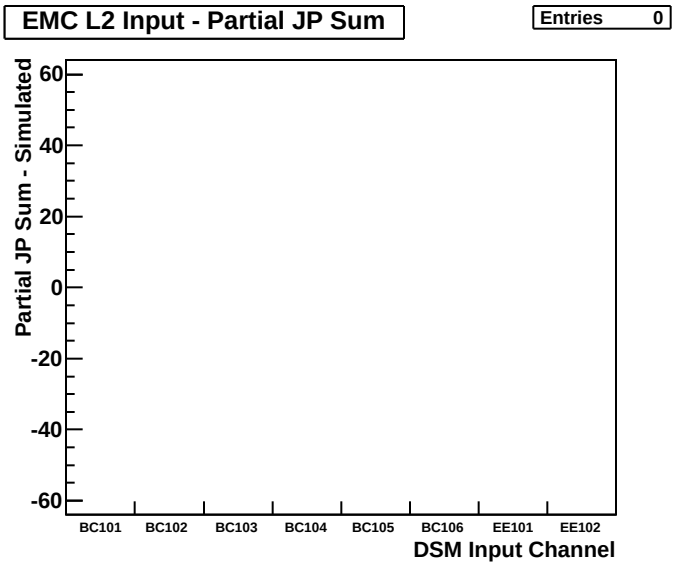

EMC L2 Input - JPY/JPB bits
Entries 717248
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
415 258 302 275 299 343 219 291
1144 782 805 859 1200 1135 468 581
1930 1395 1474 1631 2712 1976 393 432
86169 87231 87075 86889 85445 86202 88576 88352

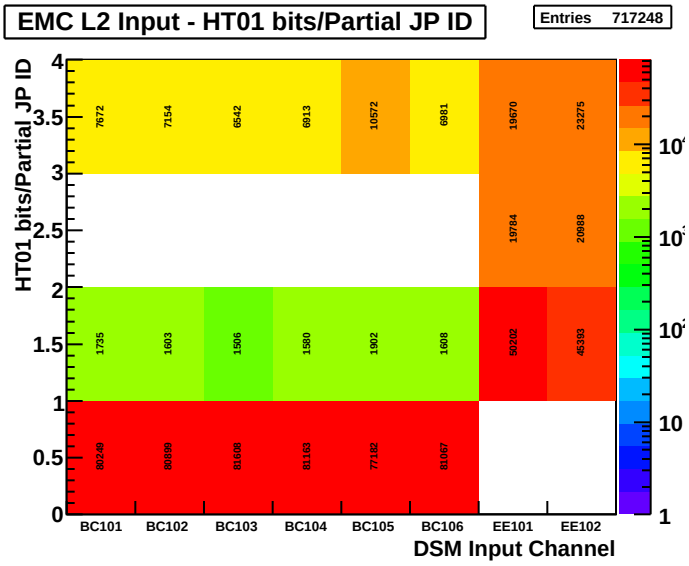

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

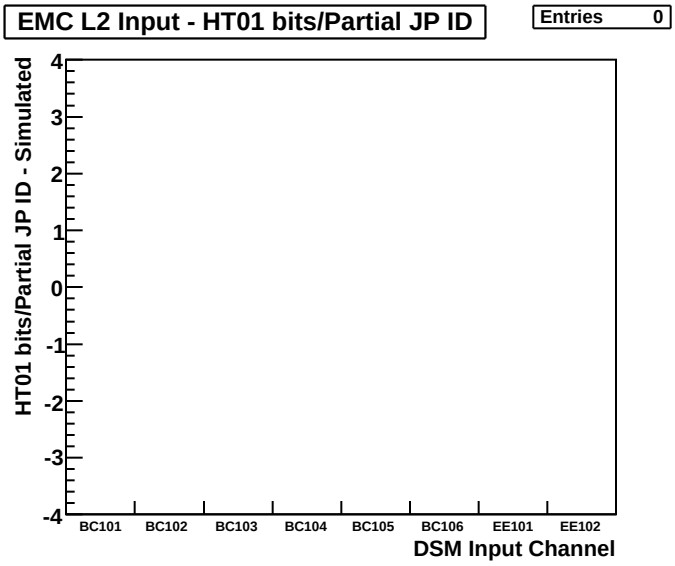

EMC L2 Input - JPZ/JPC bits
Entries 717248
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
324 265 324 264 334 383 173 253
882 805 917 903 1206 1172 860 576
1519 1390 1630 1702 2872 2196 433 509
86931 87196 86785 86787 85244 85905 88190 88318

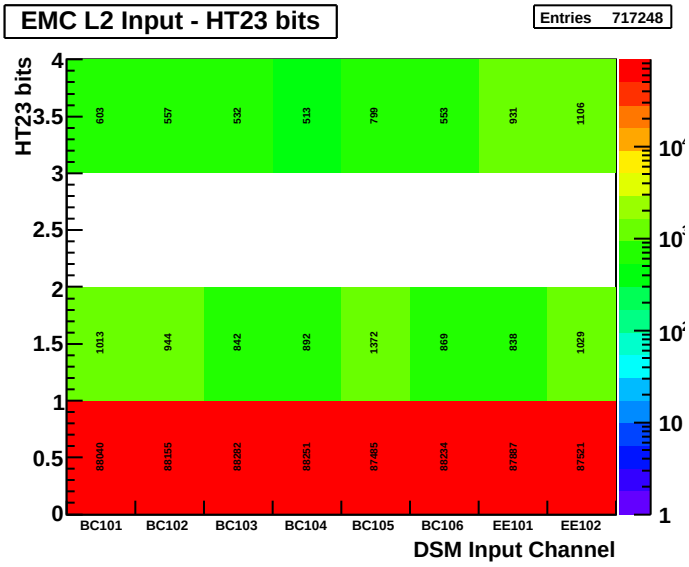

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

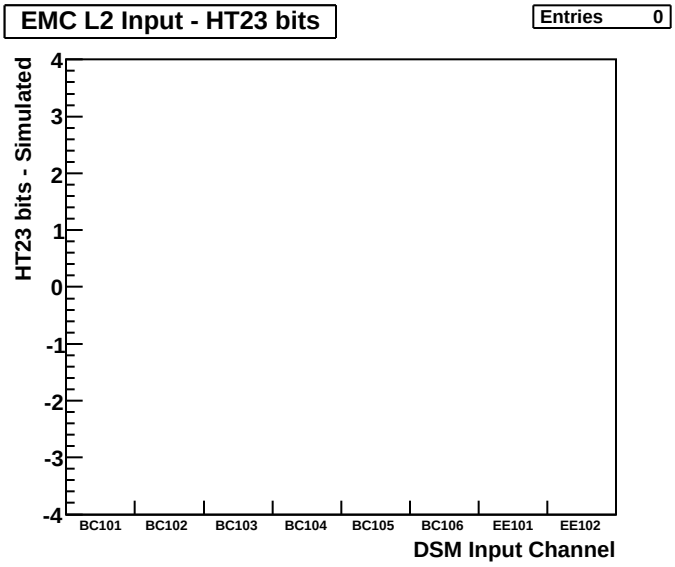

EMC L2 Input - Partial JP Sum
Entries 717248
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

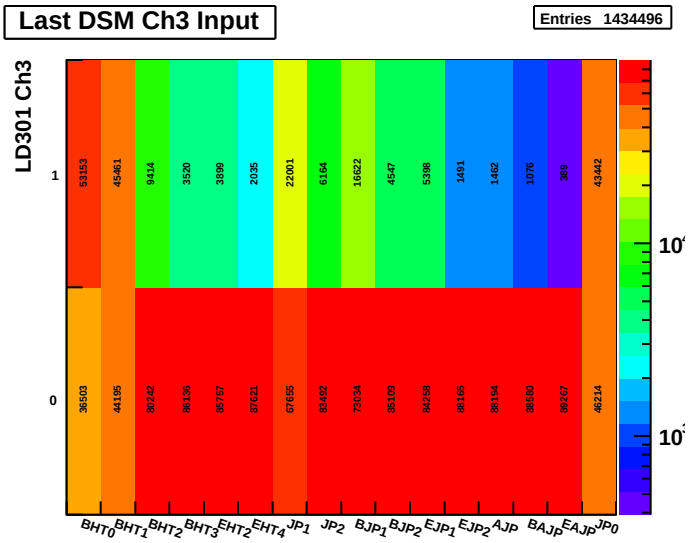

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

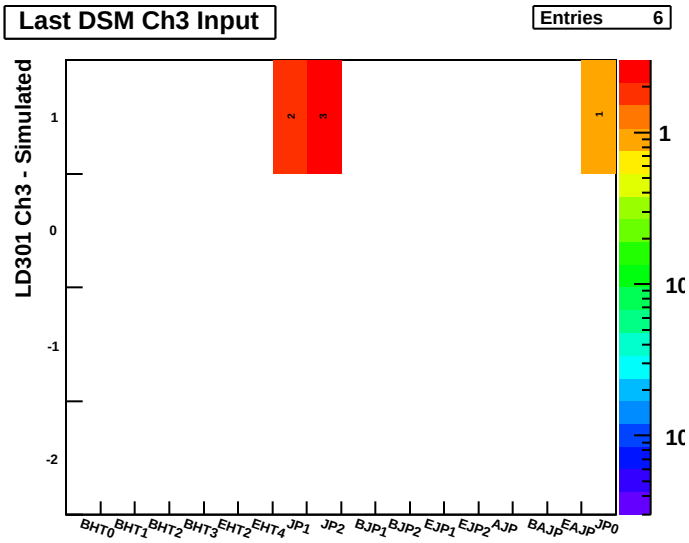

EMC L2 Input - HT01 bits/Partial JP ID
Entries 717248
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

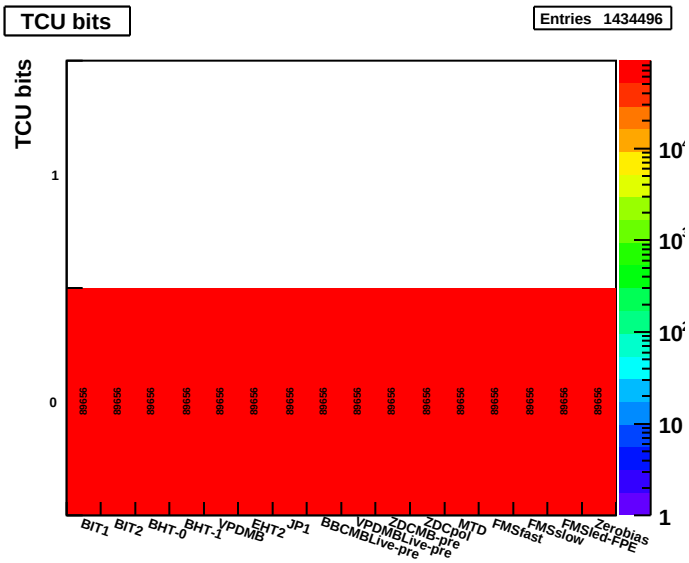

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

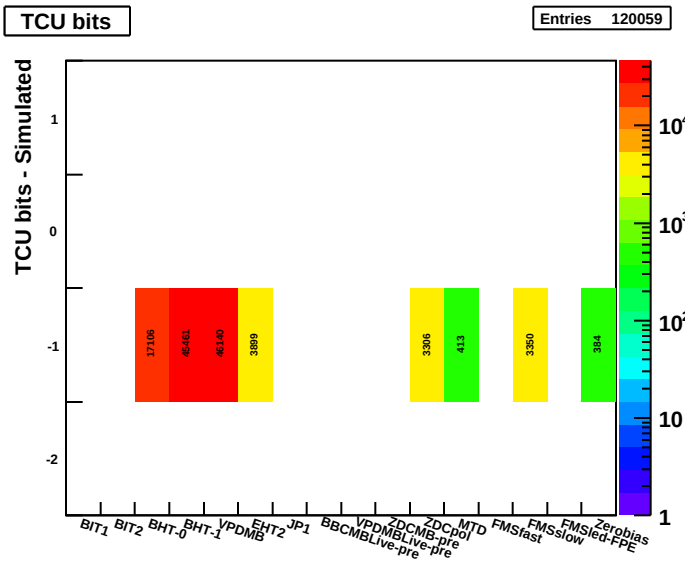

EMC L2 Input - HT23 bits
Entries 717248
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

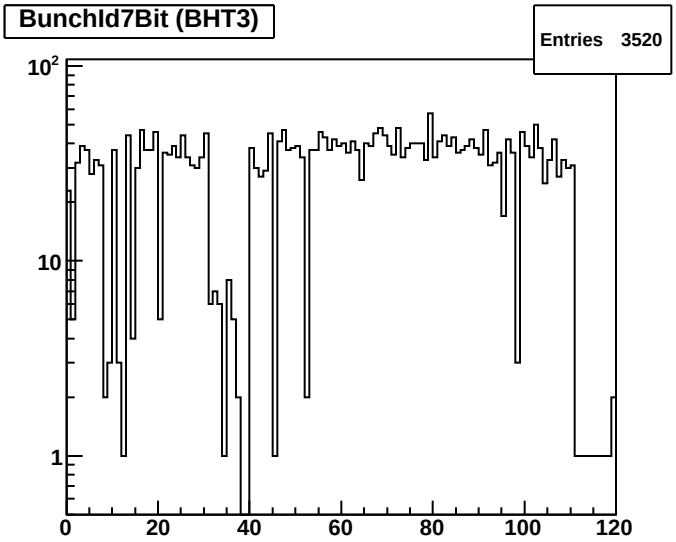

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

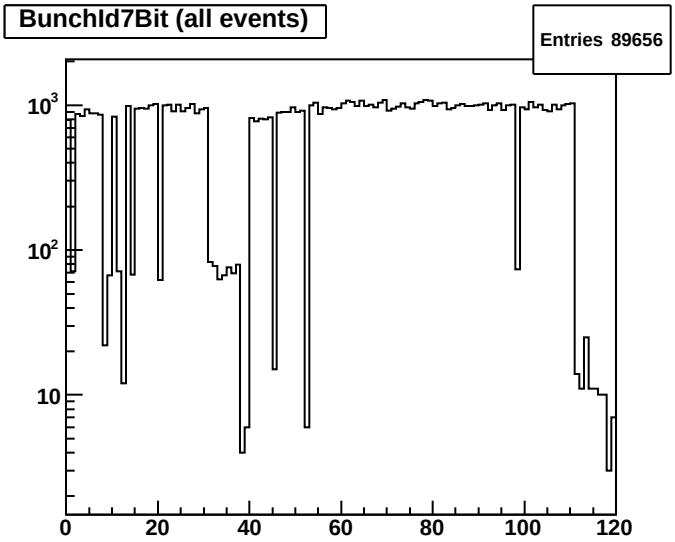

Last DSM Ch3 Input
Entries 1434496
LD301 Ch3
53153 45461 9414 3520 3899 2035 22001 6164 16622 4547 5398 1491 1462 1076 389 43442
36503 44195 80242 86136 85757 87621 67655 83492 73034 85109 84258 88165 88194 88580 89267 46214
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

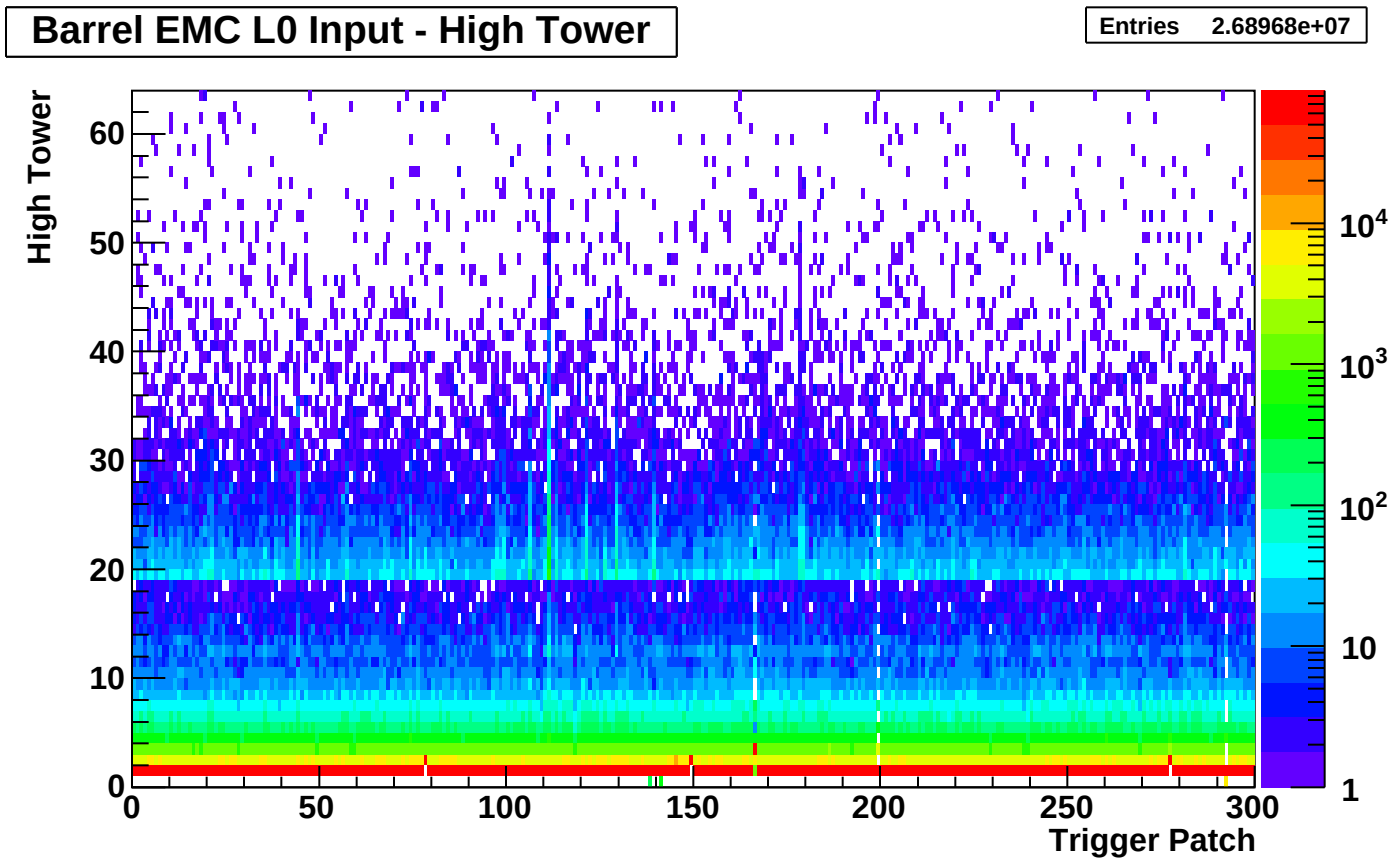

Last DSM Ch3 Input
Entries 6
LD301 Ch3 - Simulated
2 3 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

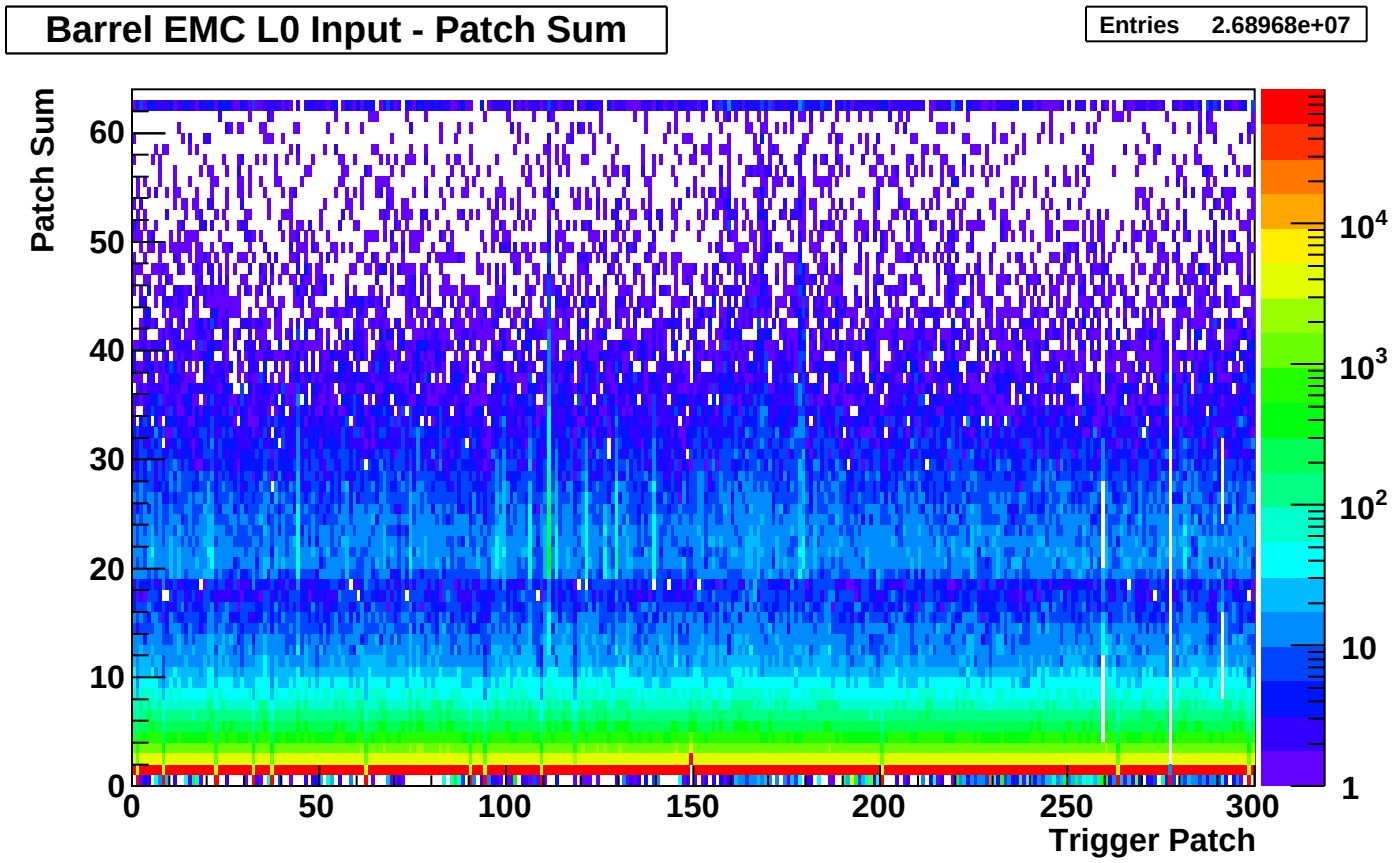

TCU bits
Entries 1434496
TCU bits
89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656 89656
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

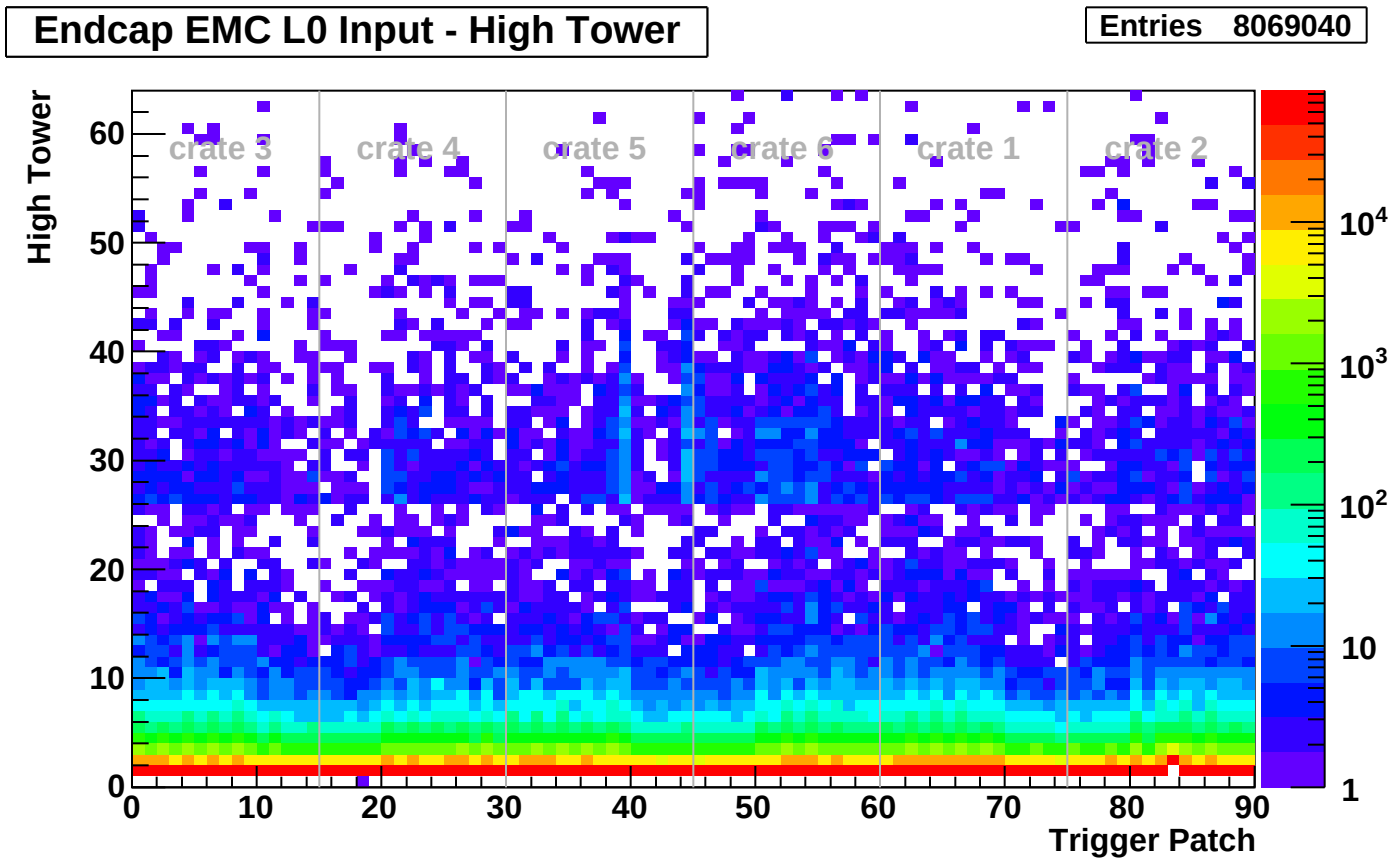

TCU bits
Entries 120059
TCU bits - Simulated
17106 45461 46140 3899 3306 413 3350 384
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

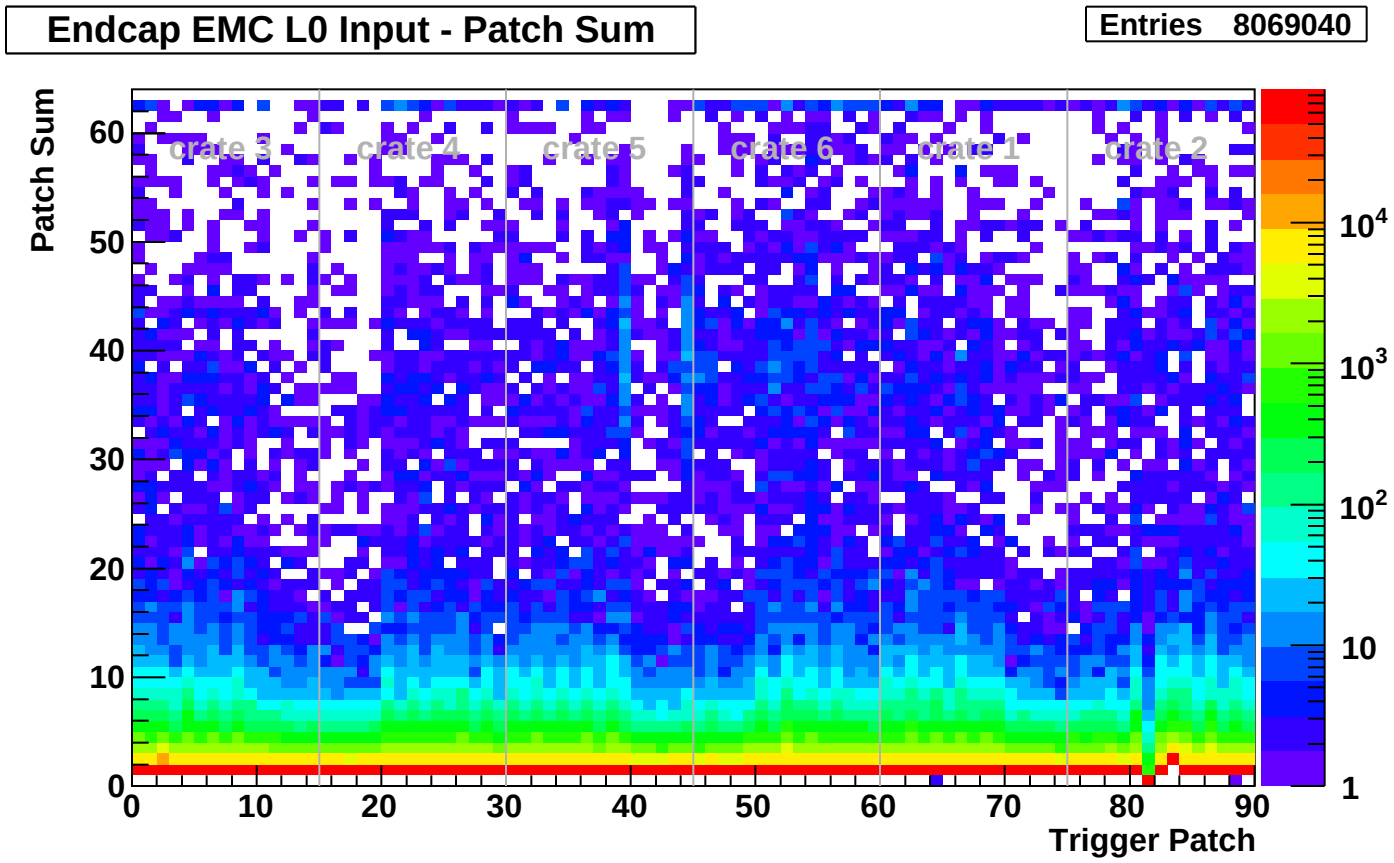

BunchId7Bit (BHT3)
Entries 3520

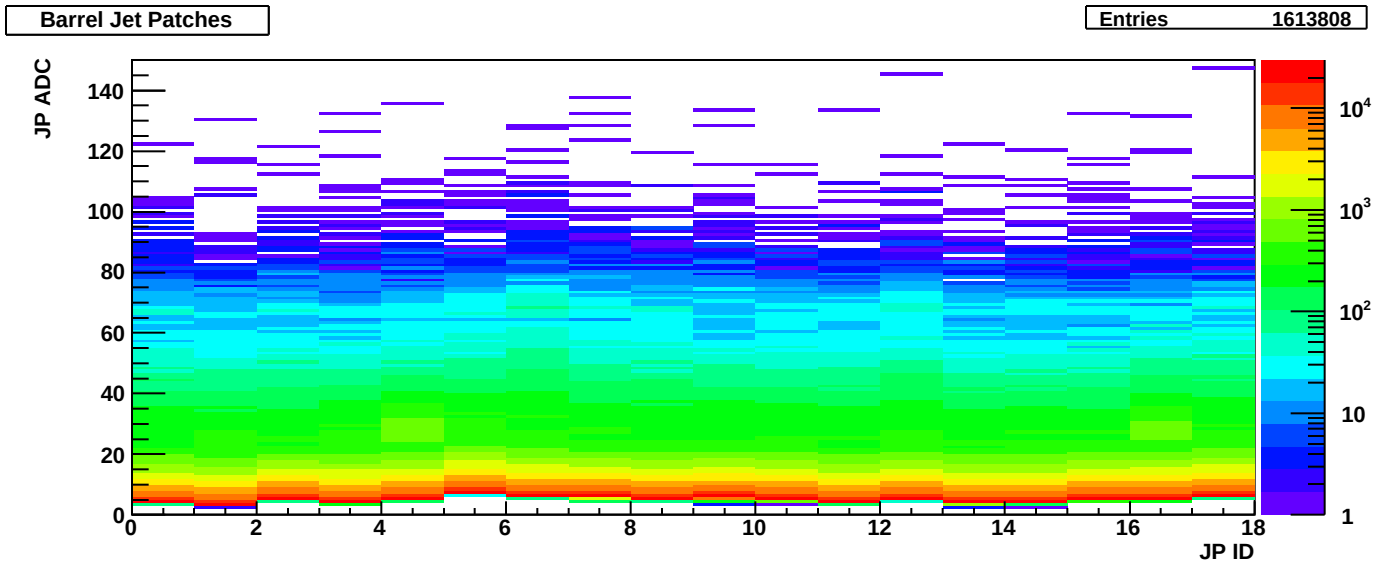

BunchId7Bit (all events)
Entries 89656

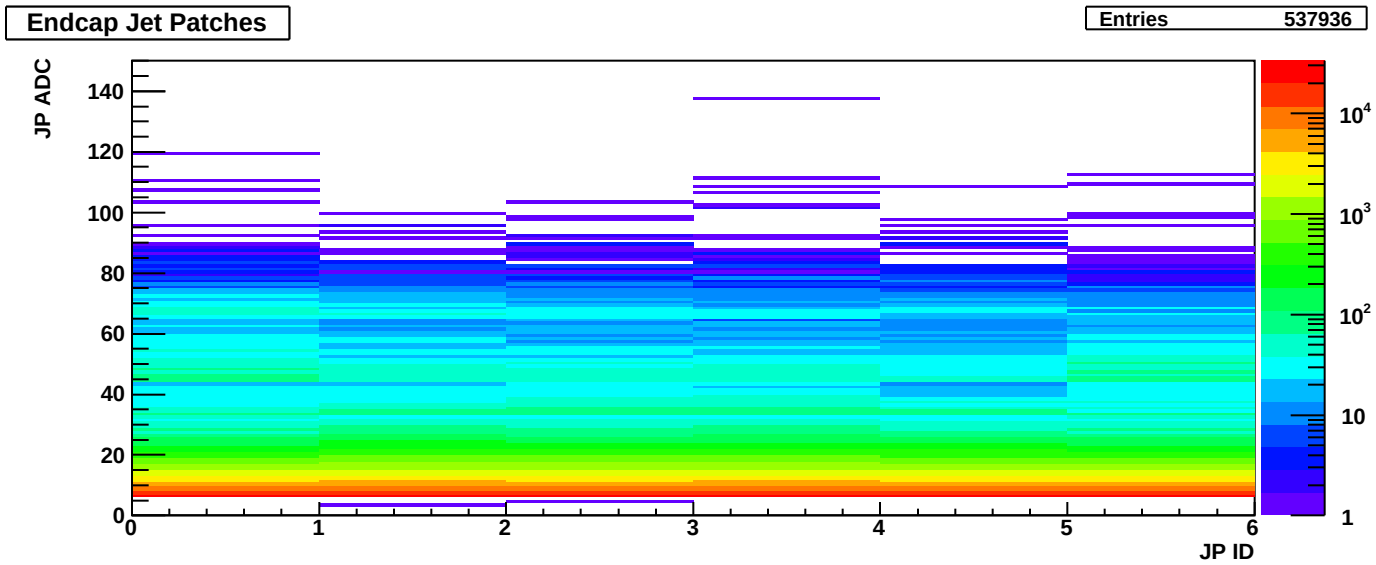

Barrel EMC L0 Input - High Tower
Entries 2.68968e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

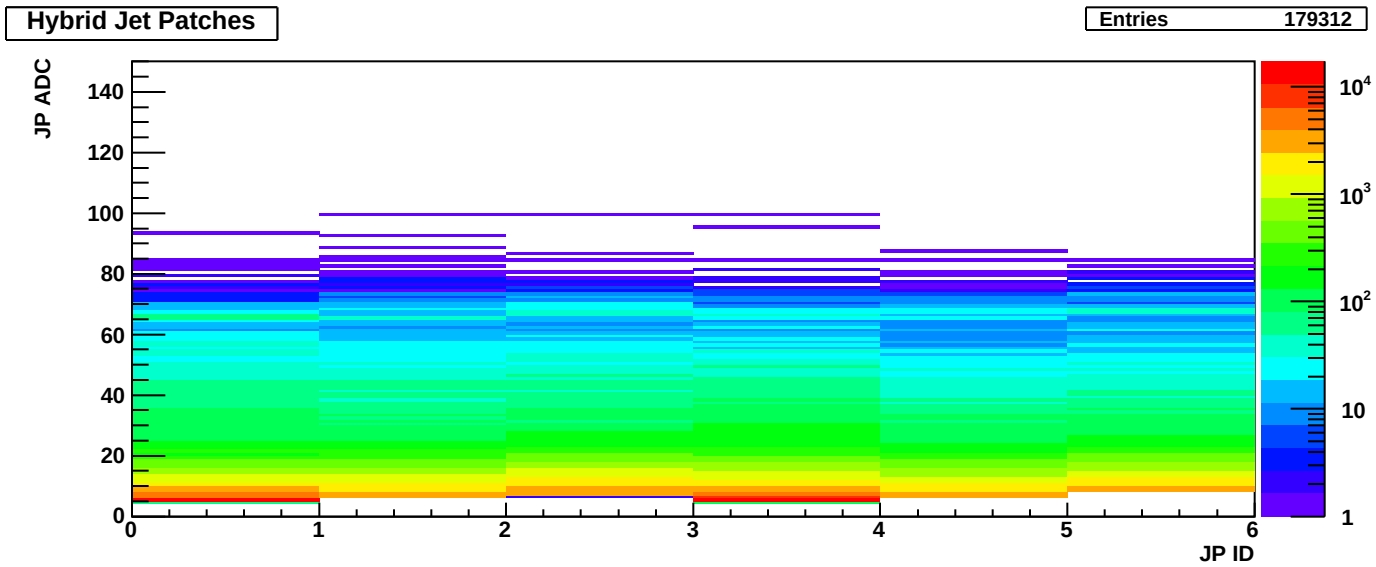

Barrel EMC L0 Input - Patch Sum
Entries 2.68968e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

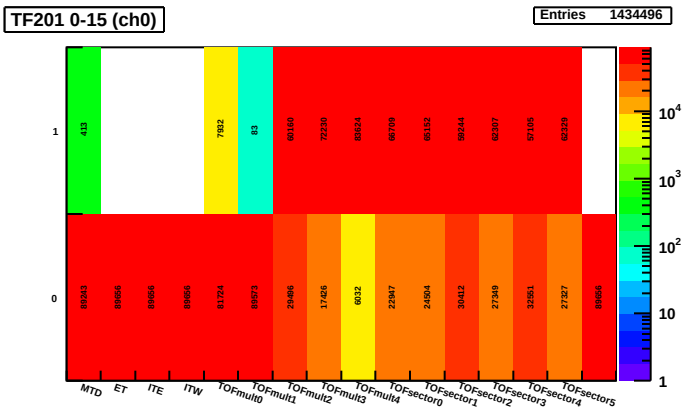

Endcap EMC L0 Input - High Tower
Entries 8069040
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

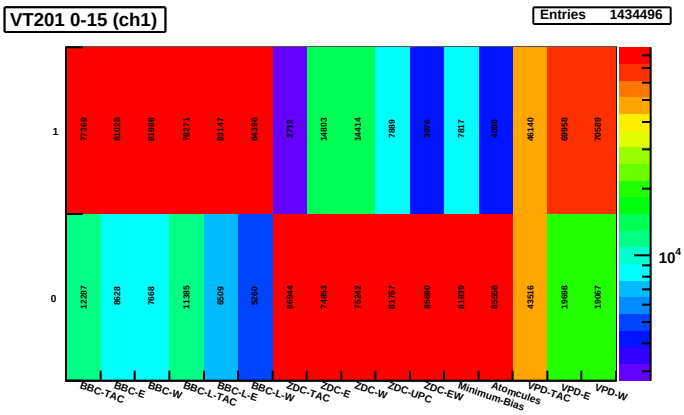

Endcap EMC L0 Input - Patch Sum
Entries 8069040
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

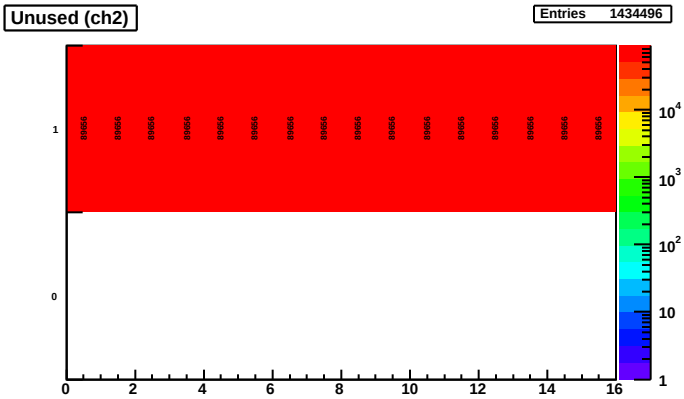

Barrel Jet Patches
Entries 1613808
JP ADC
JP ID

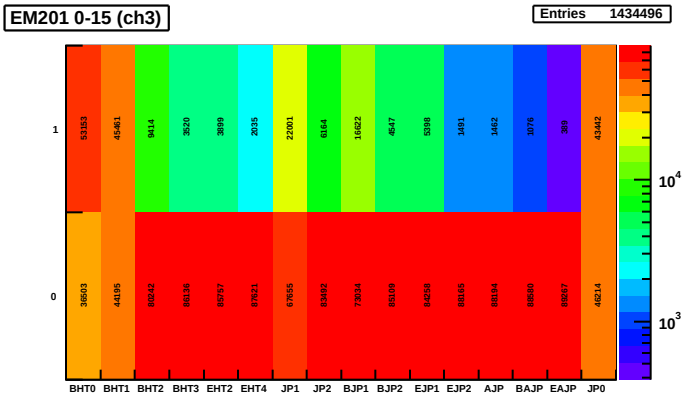

Endcap Jet Patches
Entries 537936
JP ADC
JP ID

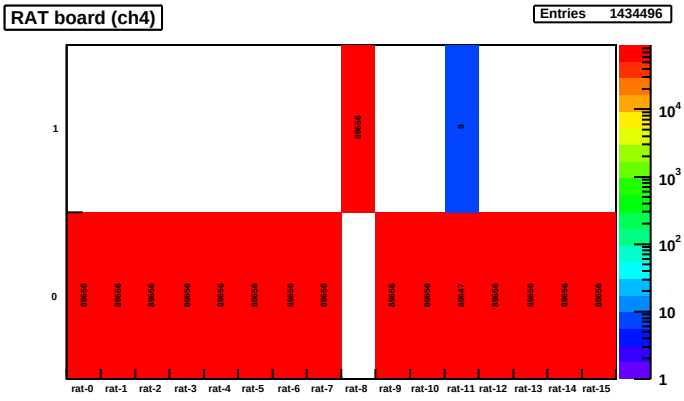

Hybrid Jet Patches
Entries 179312
JP ADC
JP ID

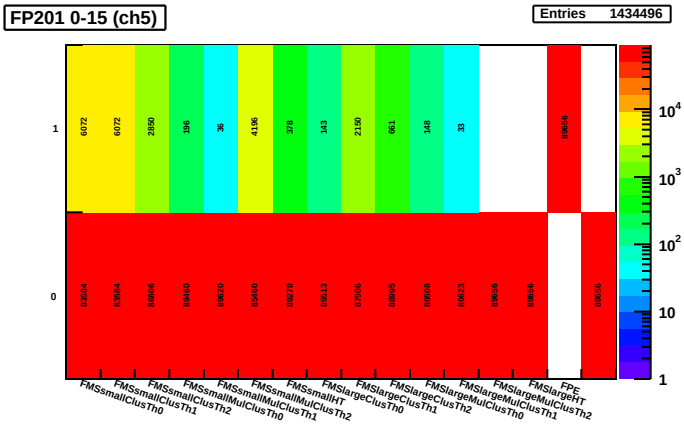

TF201 0-15 (ch0)
Entries 1434496
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

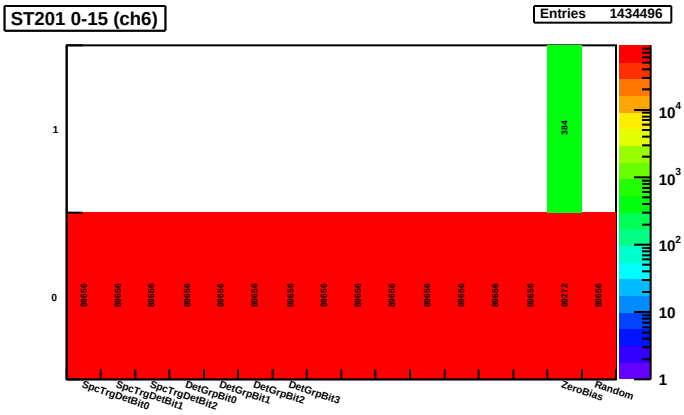

VT201 0-15 (ch1)
Entries 1434496
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

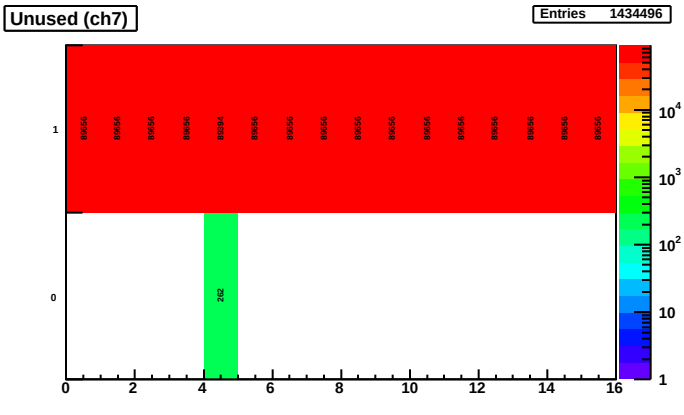

Unused (ch2)
Entries 1434496

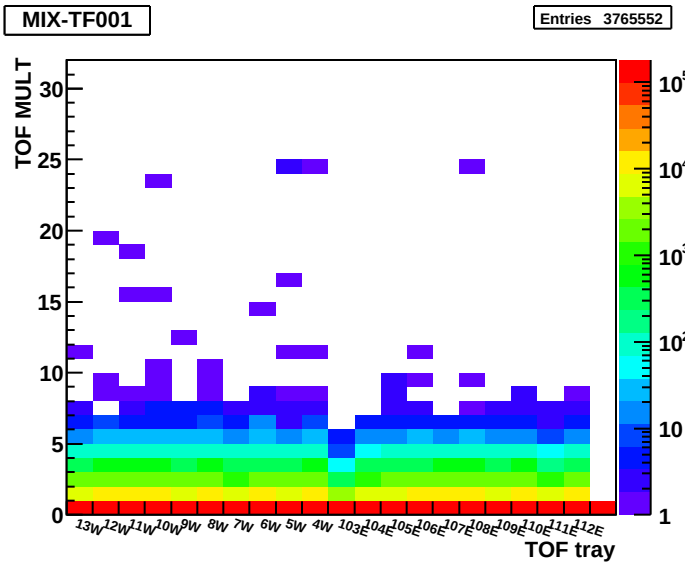

EM201 0-15 (ch3)
Entries 1434496
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

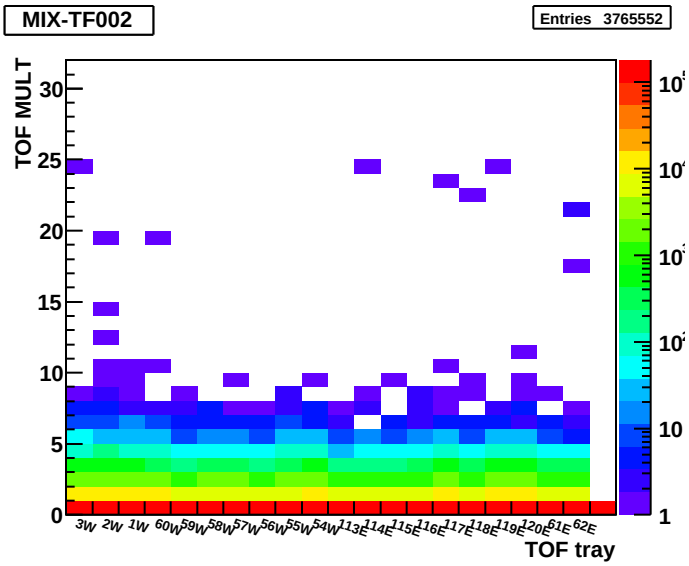

RAT board (ch4)
Entries 1434496
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

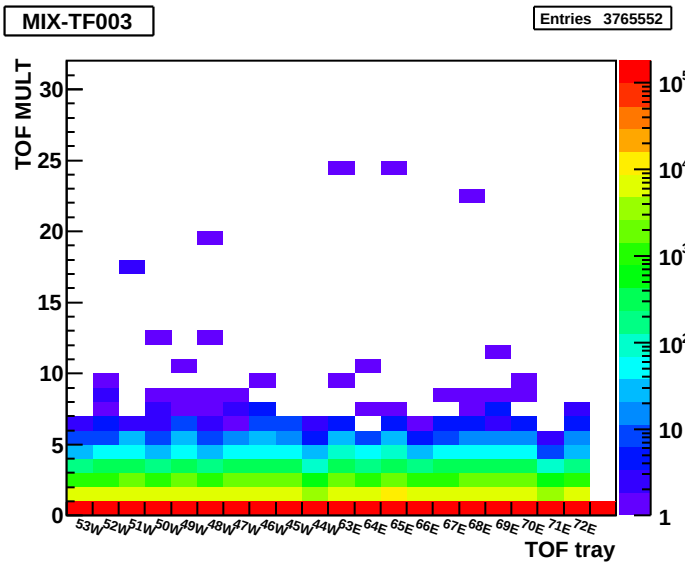

FP201 0-15 (ch5)
Entries 1434496
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

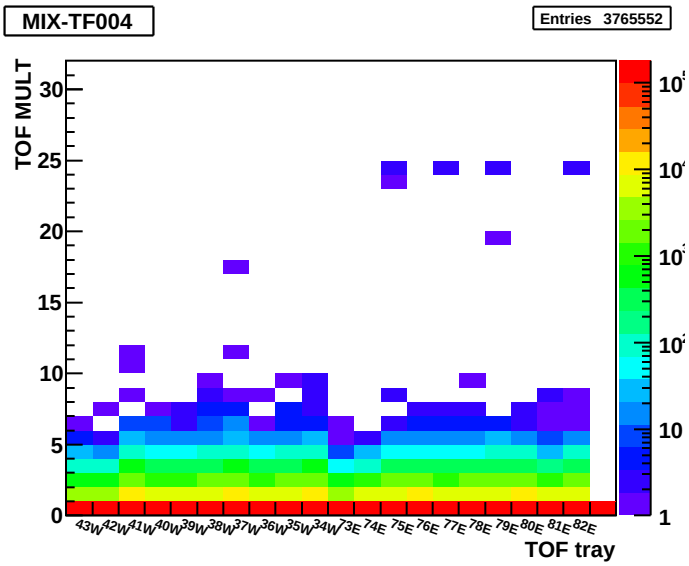

ST201 0-15 (ch6)
Entries 1434496
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

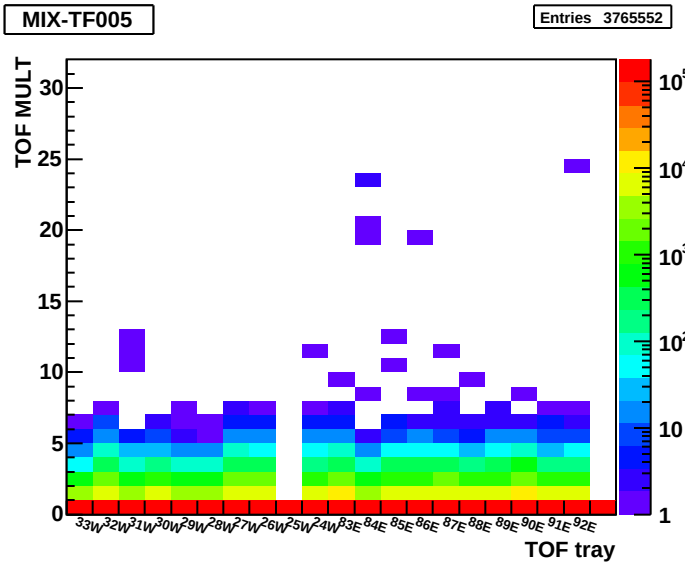

Unused (ch7)
Entries 1434496

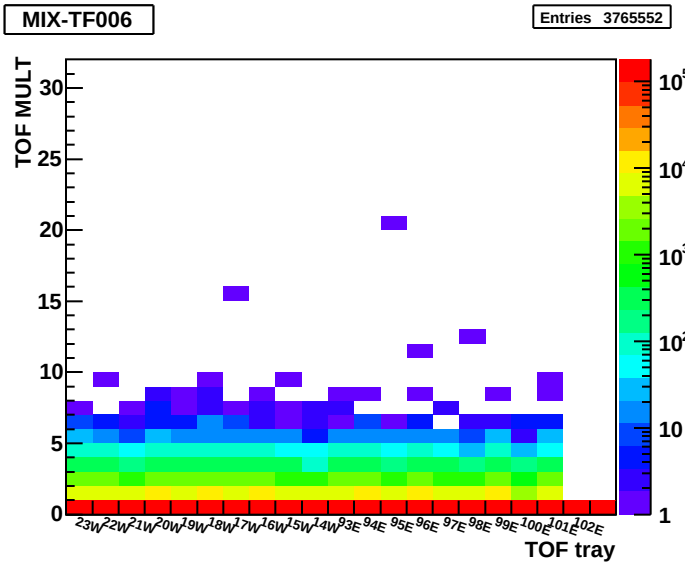

MIX-TF001
Entries 3765552
TOF MULT
TOF tray

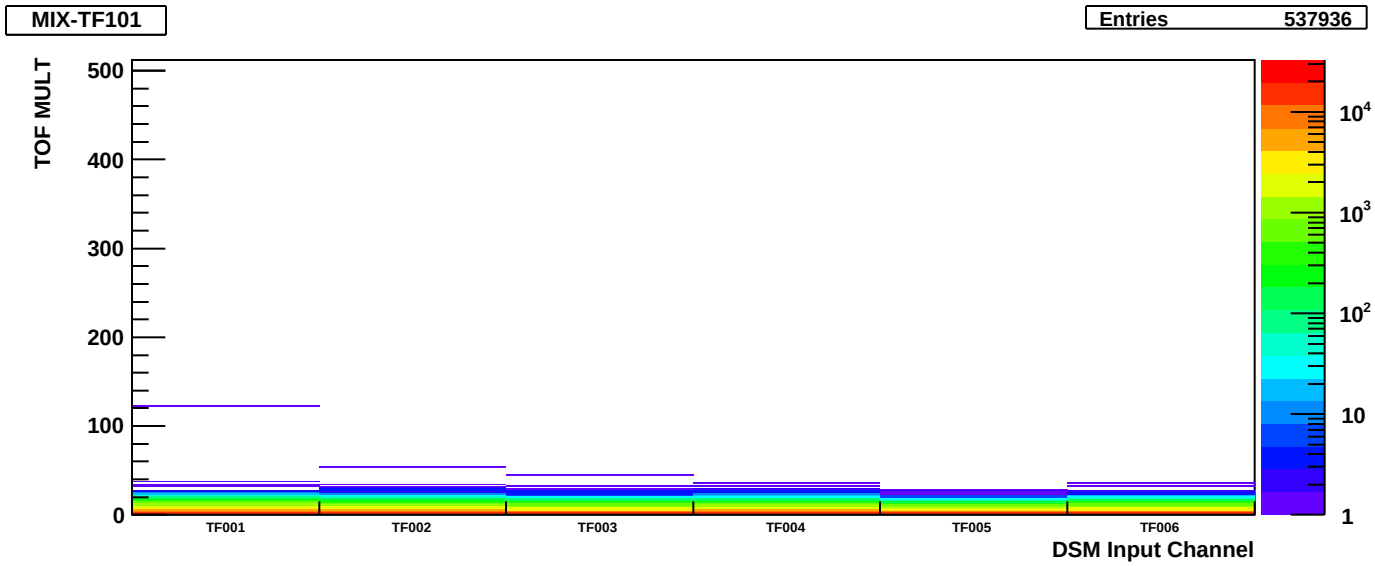

MIX-TF002
Entries 3765552
TOF MULT
TOF tray

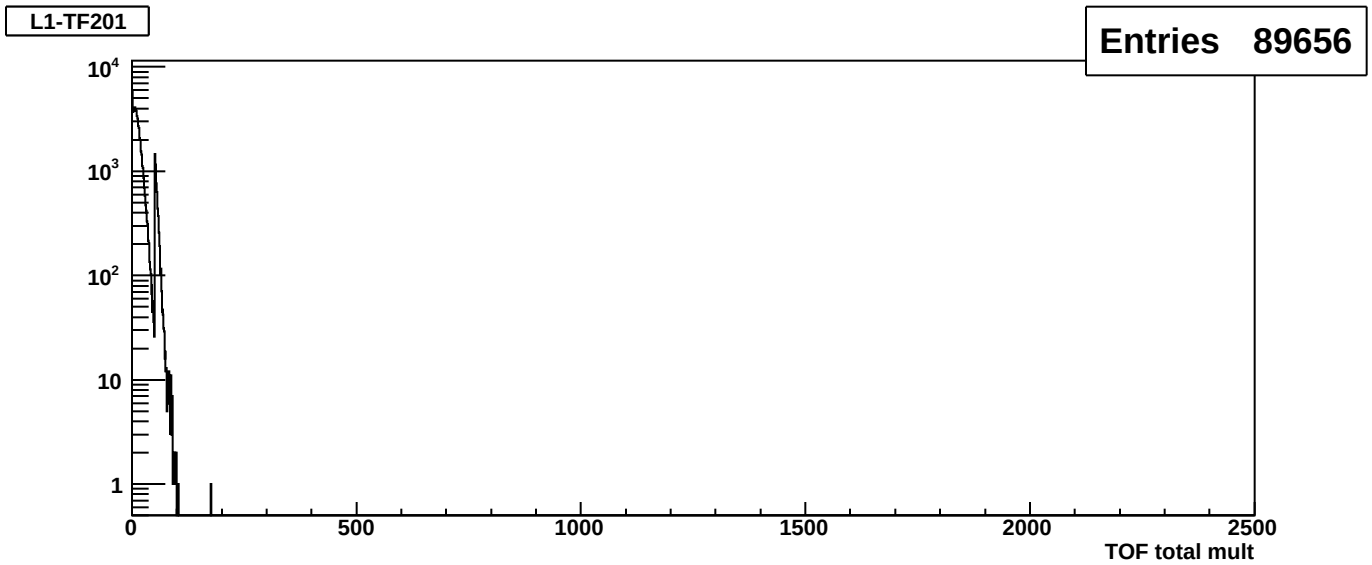

MIX-TF003
Entries 3765552
TOF MULT
TOF tray

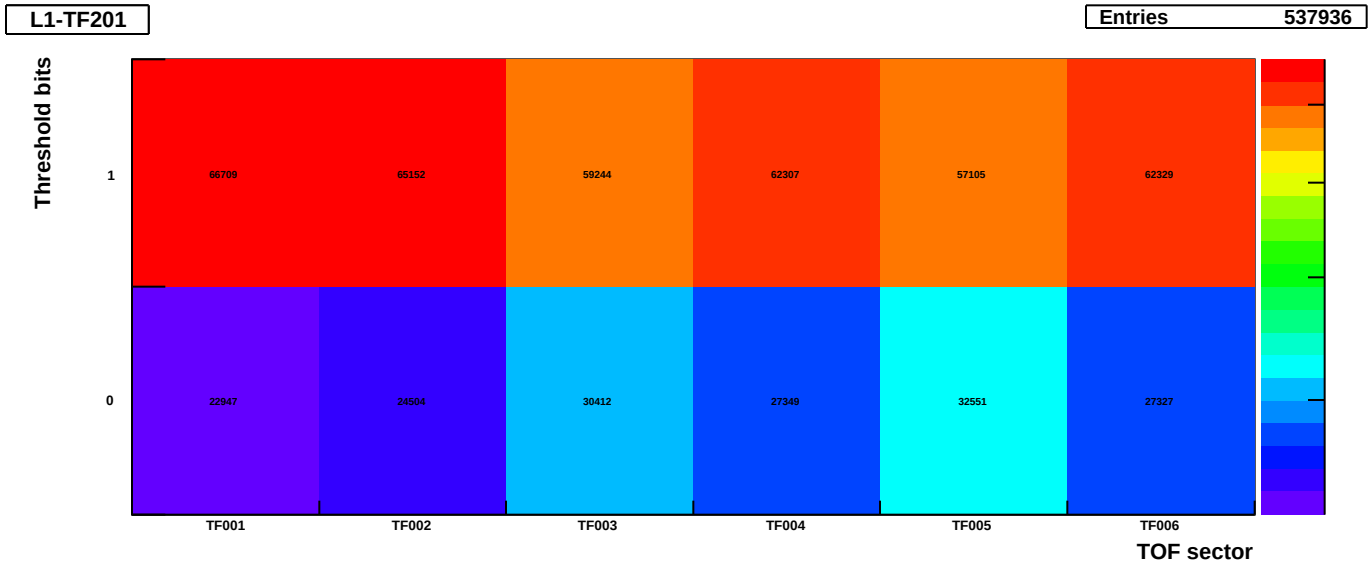

MIX-TF004
Entries 3765552
TOF MULT
TOF tray

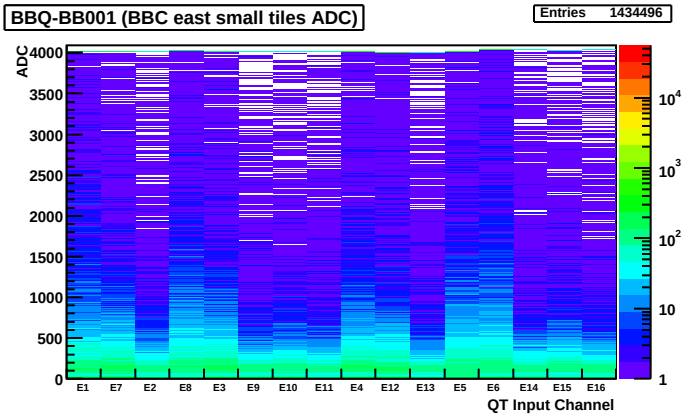

MIX-TF005
Entries 3765552
TOF MULT
TOF tray

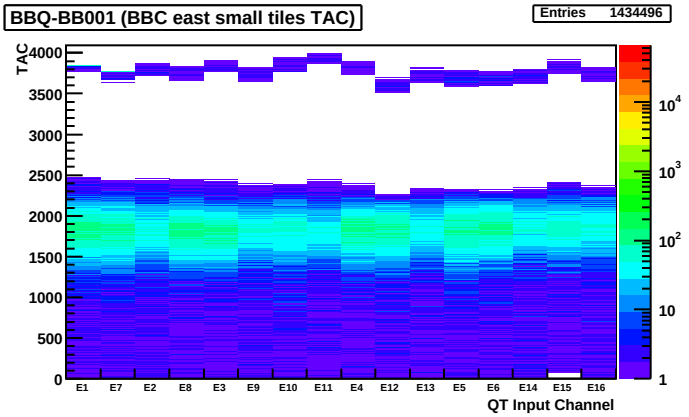

MIX-TF006
Entries 3765552
TOF MULT
TOF tray

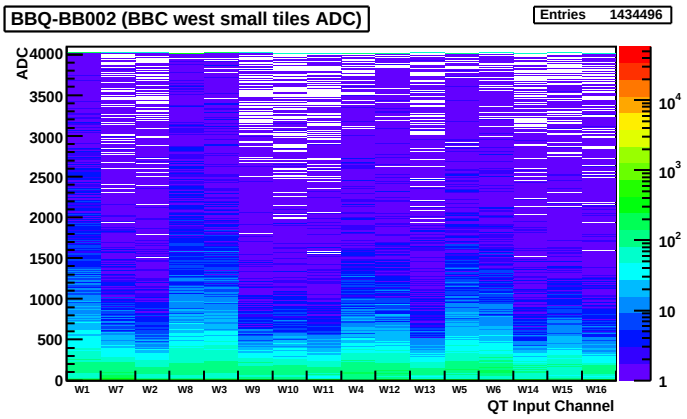

MIX-TF101
Entries 537936
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

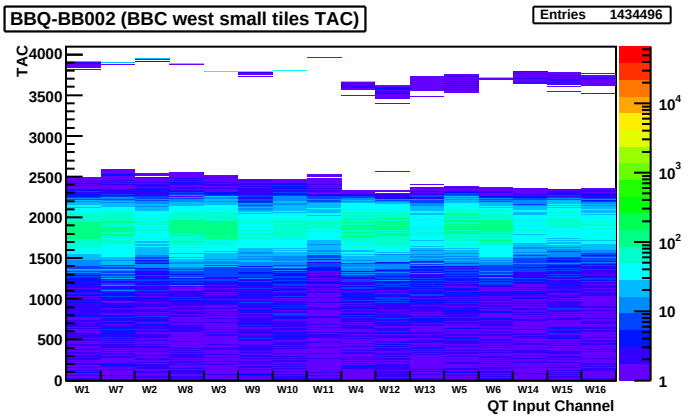

L1-TF201
Entries 89656
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

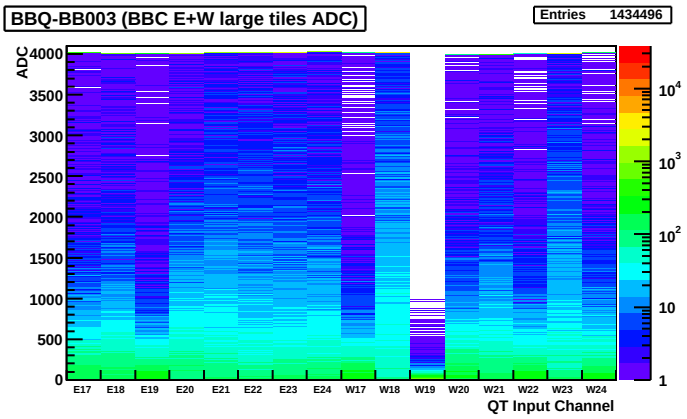

L1-TF201
Entries 537936
Threshold bits
1
0
66709
65152
59244
62307
57105
62329
22947
24504
30412
27349
32551
27327
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

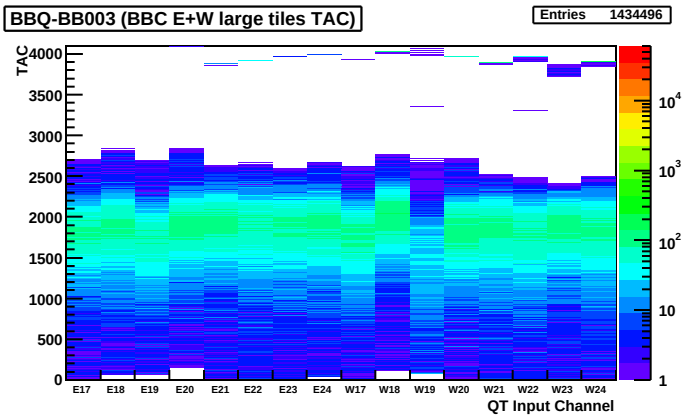

BBQ-BB001 (BBC east small tiles ADC)
Entries 1434496
ADC
QT Input Channel

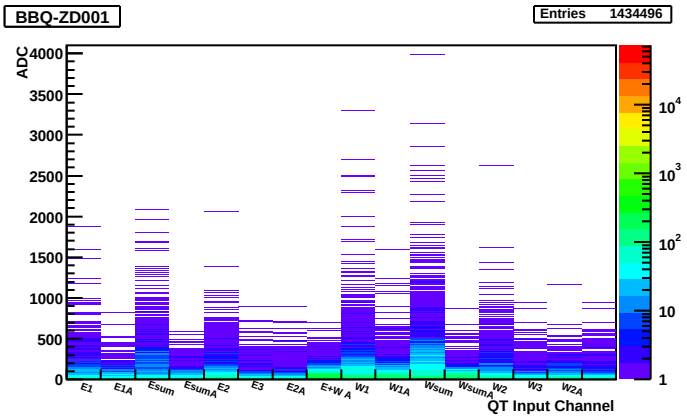

BBQ-BB001 (BBC east small tiles TAC)
Entries 1434496
TAC
QT Input Channel

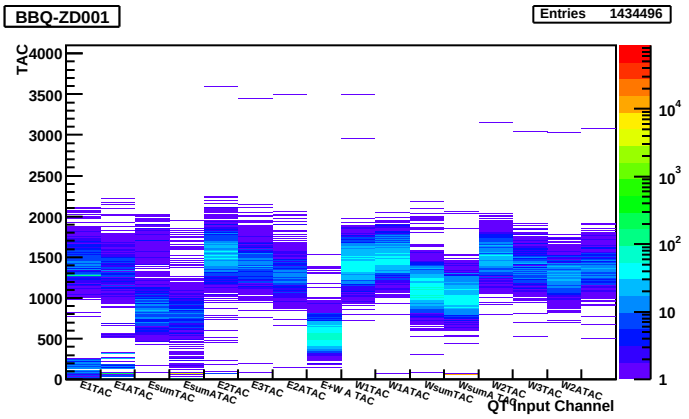

BBQ-BB002 (BBC west small tiles ADC)
Entries 1434496
ADC
QT Input Channel

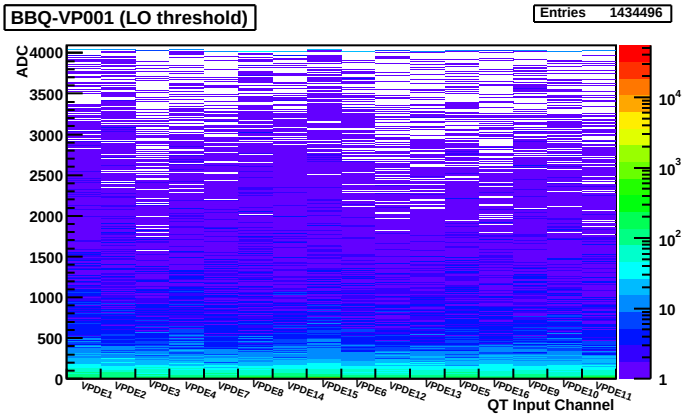

BBQ-BB002 (BBC west small tiles TAC)
Entries 1434496
TAC
QT Input Channel

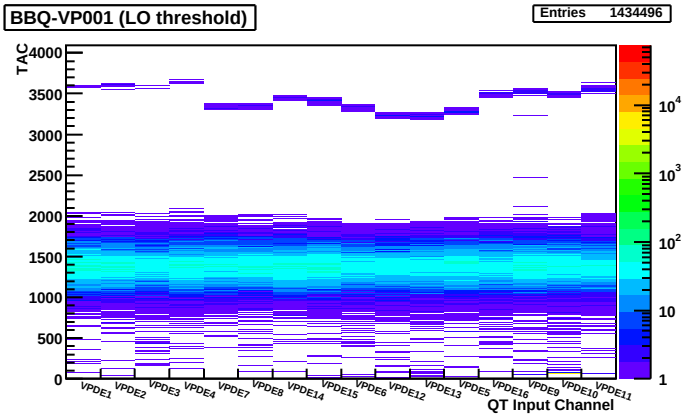

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1434496
ADC
QT Input Channel

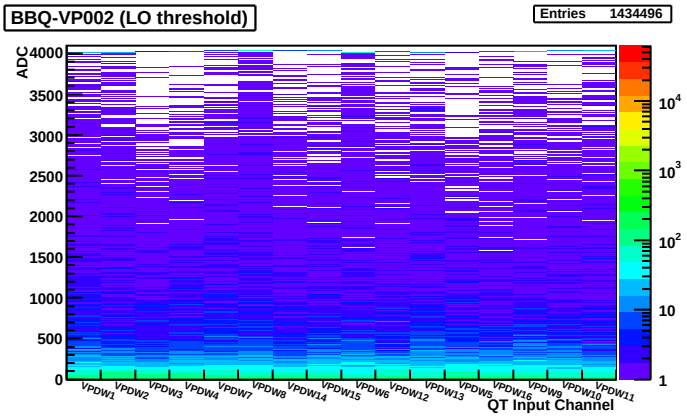

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1434496
TAC
QT Input Channel

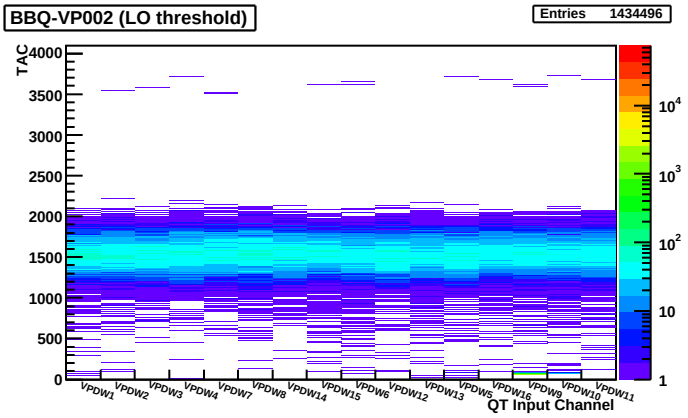

BBQ-ZD001
Entries 1434496
ADC
QT Input Channel

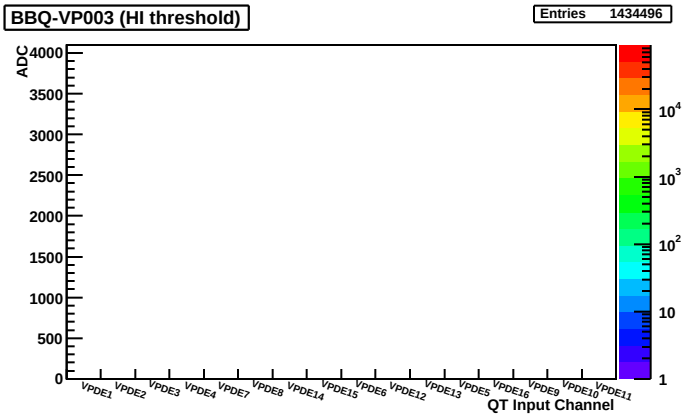

BBQ-ZD001
Entries 1434496
TAC
QT Input Channel

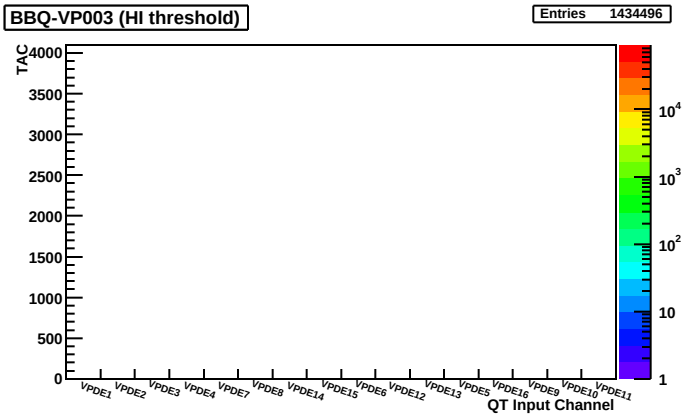

BBQ-VP001 (LO threshold)
Entries 1434496
ADC
QT Input Channel

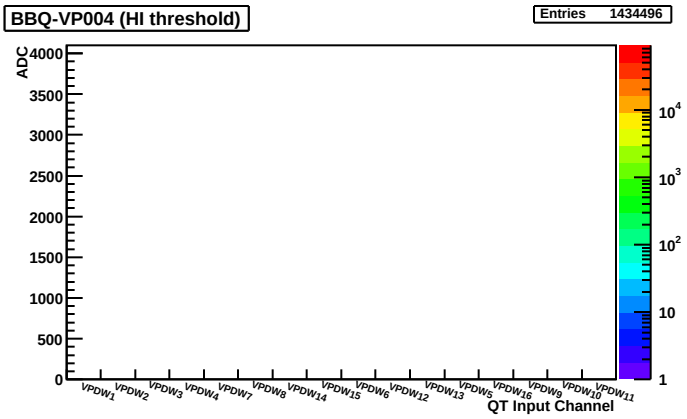

BBQ-VP001 (LO threshold)
Entries 1434496
TAC
QT Input Channel

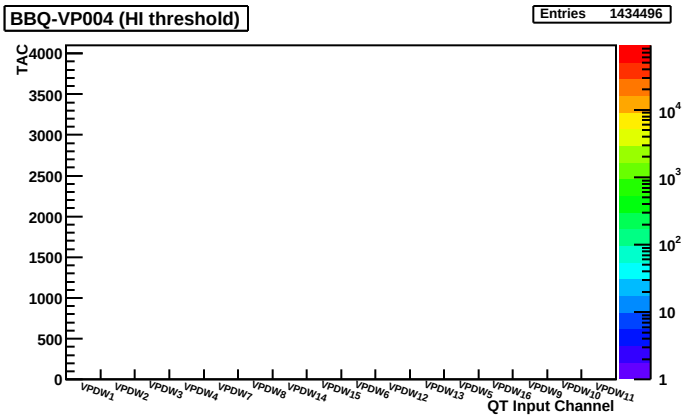

BBQ-VP002 (LO threshold)
Entries 1434496
ADC
QT Input Channel

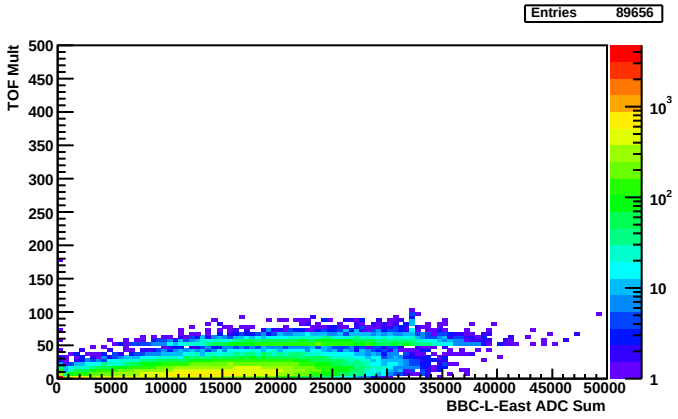

BBQ-VP002 (LO threshold)
Entries 1434496
TAC
QT Input Channel

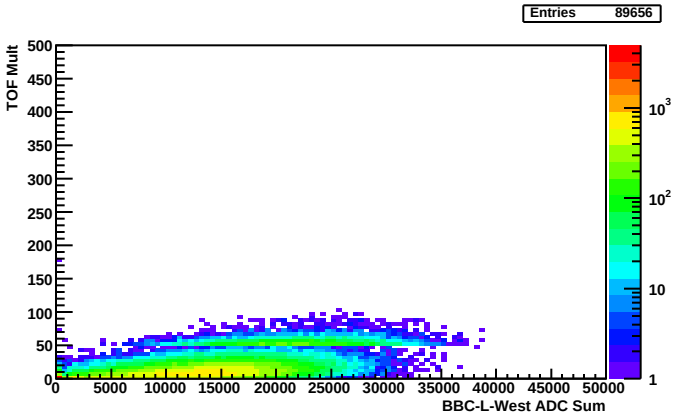

BBQ-VP003 (HI threshold)
Entries 1434496
ADC
QT Input Channel

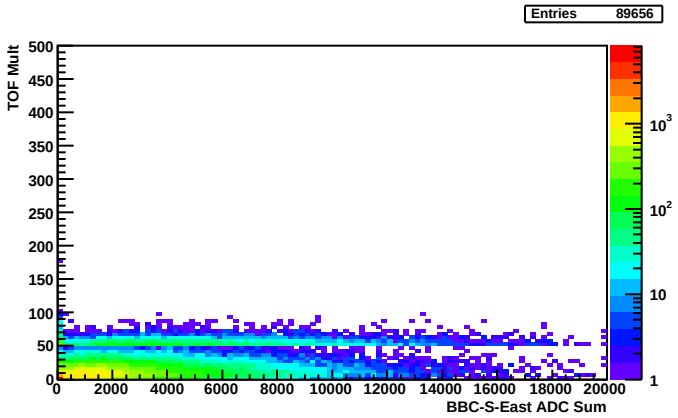

BBQ-VP003 (HI threshold)
Entries 1434496
TAC
QT Input Channel

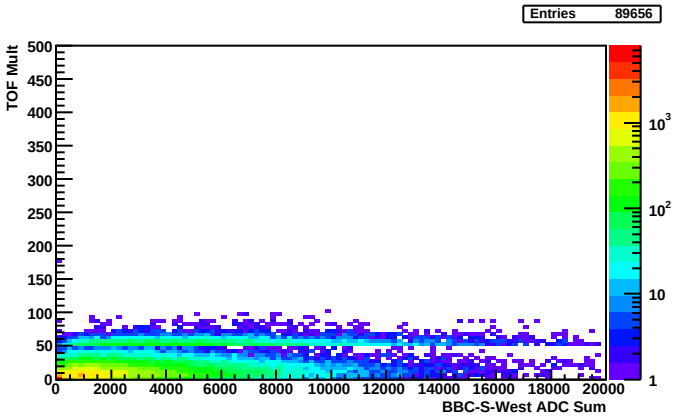

BBQ-VP004 (HI threshold)
Entries 1434496
ADC
QT Input Channel

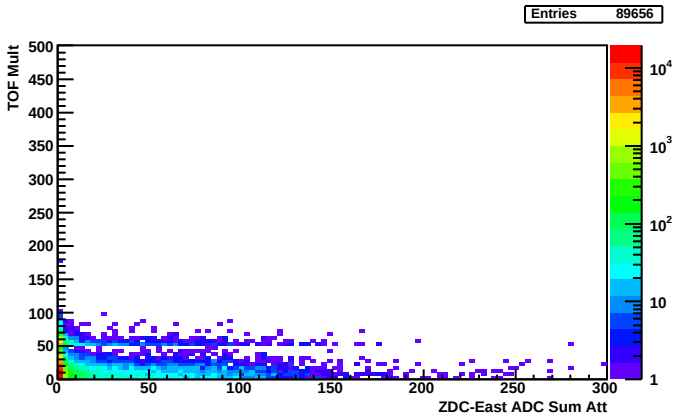

BBQ-VP004 (HI threshold)
Entries 1434496
TAC
QT Input Channel

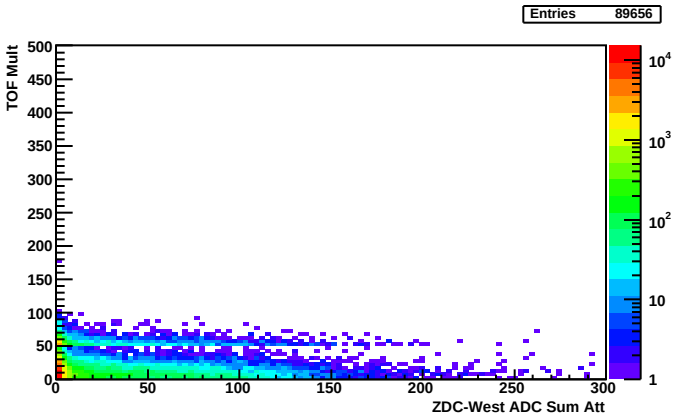

Entries 89656
TOF Mult
BBC-L-East ADC Sum

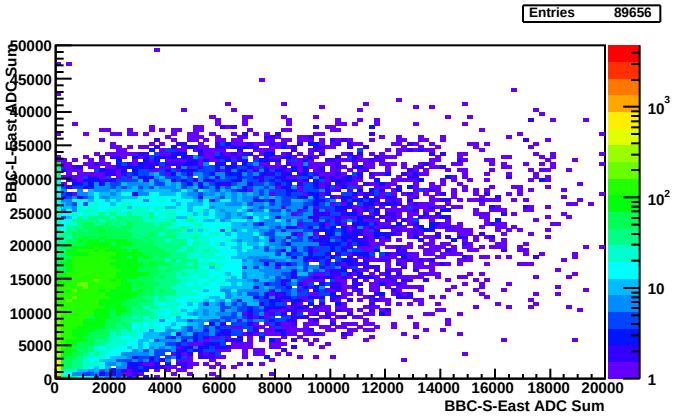

Entries 89656
TOF Mult
BBC-L-West ADC Sum

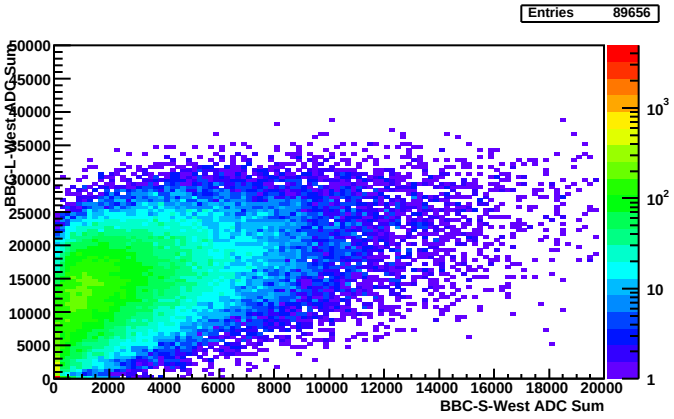

Entries 89656
TOF Mult
BBC-S-East ADC Sum

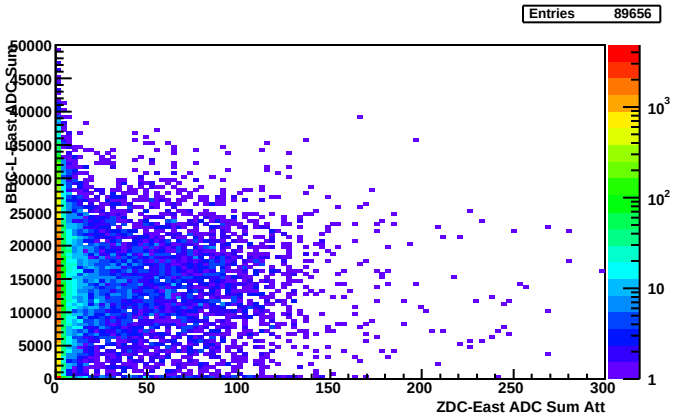

Entries 89656
TOF Mult
BBC-S-West ADC Sum

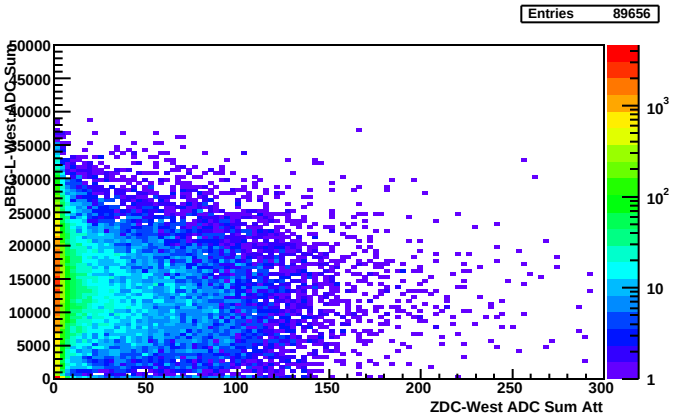

Entries 89656
TOF Mult
ZDC-East ADC Sum Att

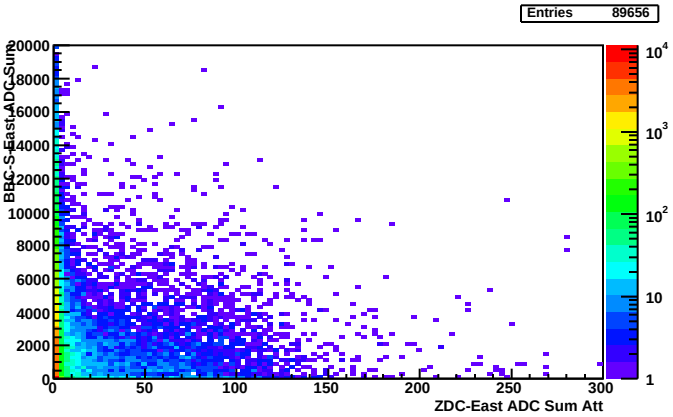

Entries 89656
TOF Mult
ZDC-West ADC Sum Att

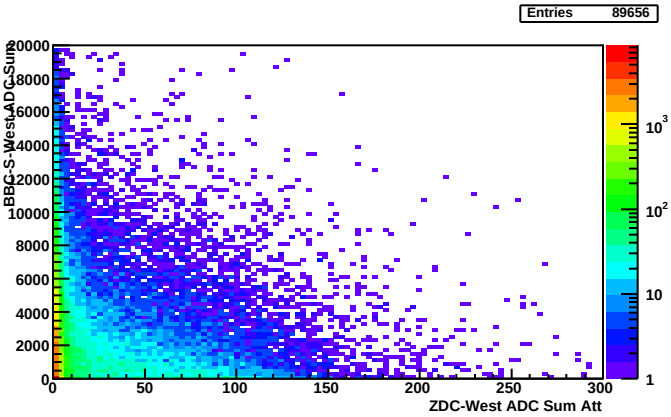

Entries 89656
BBC-L-East ADC Sum
BBC-S-East ADC Sum

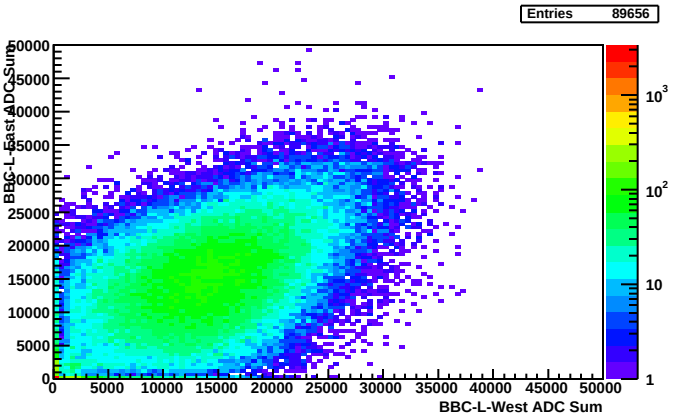

Entries 89656
BBC-L-West ADC Sum
BBC-S-West ADC Sum

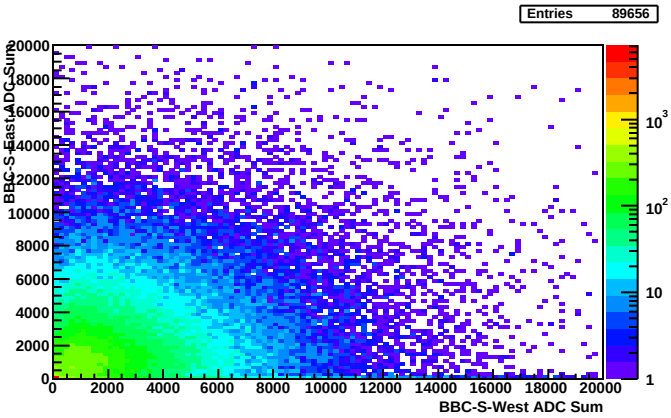

Entries 89656
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

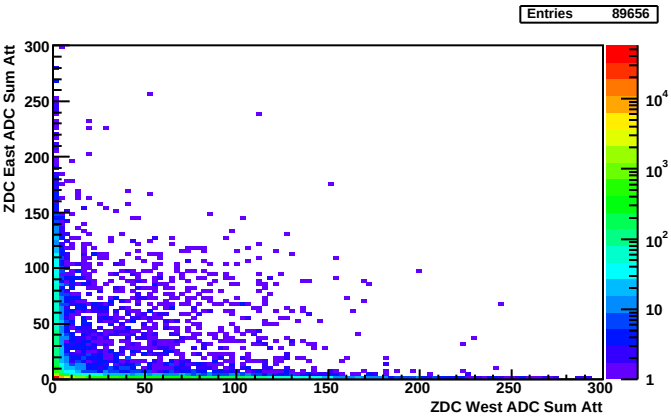

Entries 89656
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

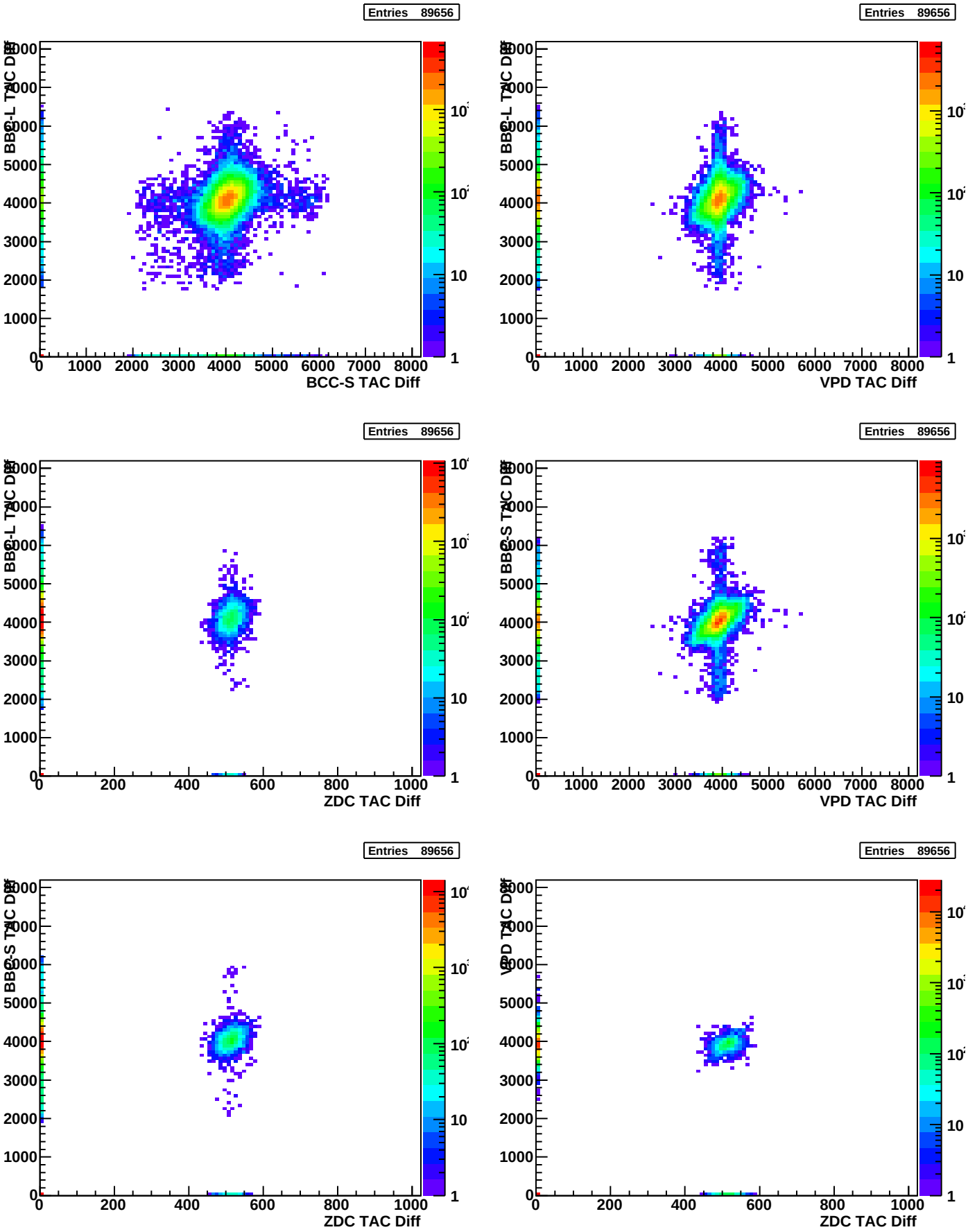

Entries 89656
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

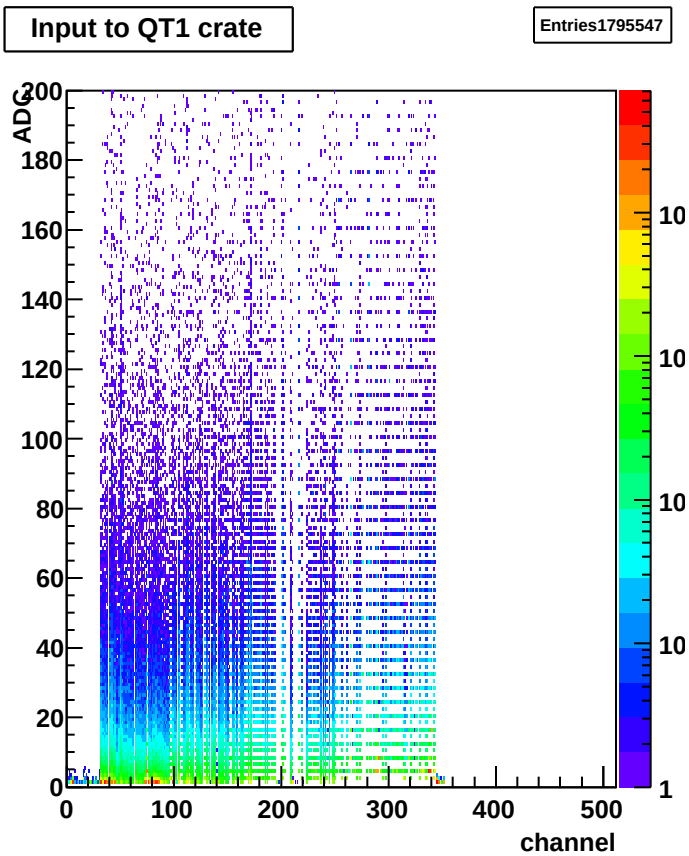

Entries 89656
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

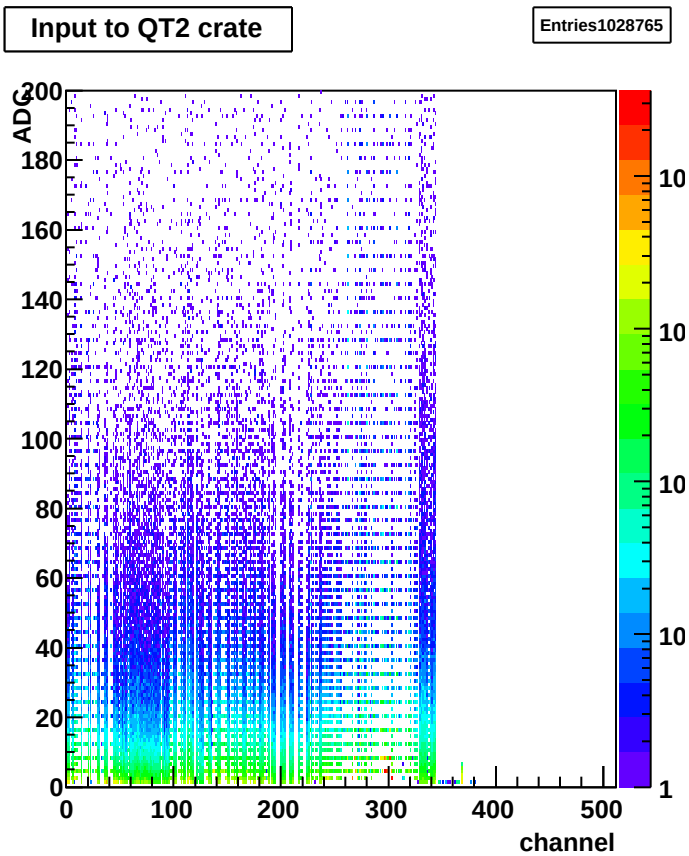

Entries 89656
BBC-L-East ADC Sum
BBC-L-West ADC Sum

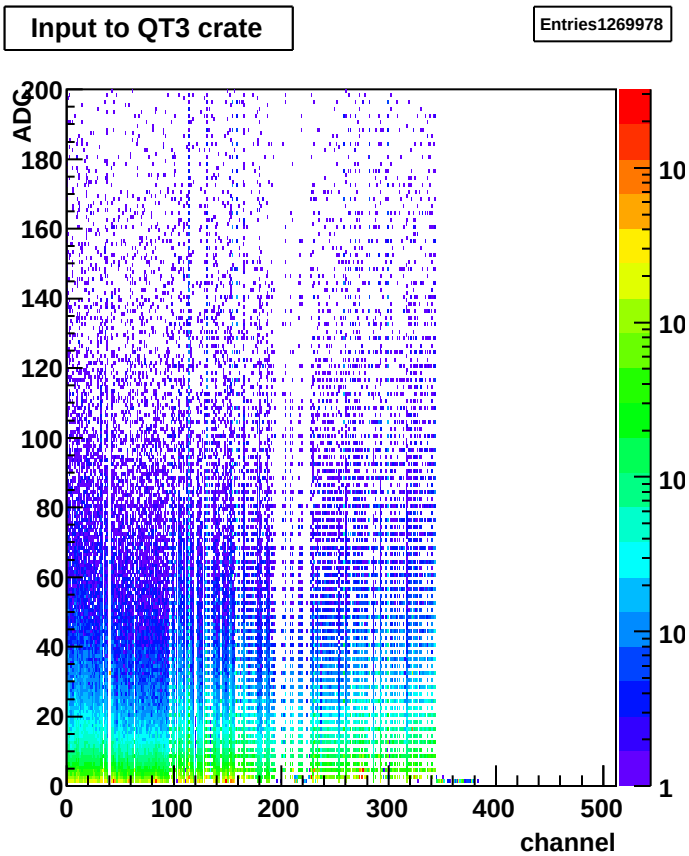

Entries 89656
BBC-S-East ADC Sum
BBC-S-West ADC Sum

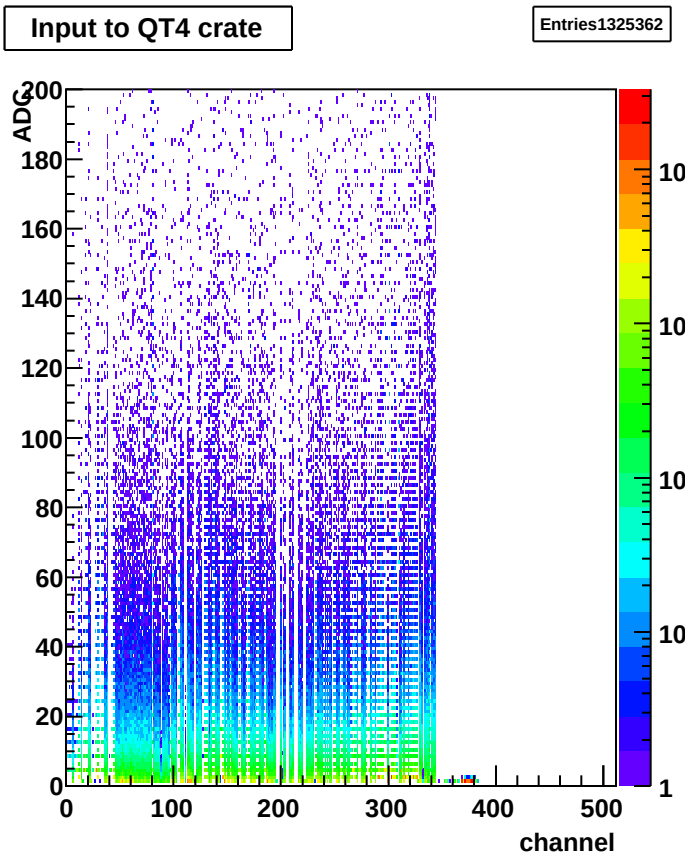

Entries 89656
ZDC East ADC Sum Att
ZDC West ADC Sum Att

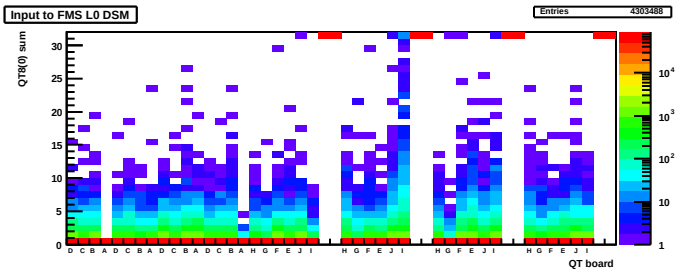

Entries 89656
BBC-L TAC Diff
BCC-S TAC Diff
Entries 89656
BBC-L TAC Diff
VPD TAC Diff
Entries 89656
BBC-L TAC Diff
ZDC TAC Diff
Entries 89656
BBC-S TAC Diff
VPD TAC Diff
Entries 89656
BBC-S TAC Diff
ZDC TAC Diff
Entries 89656
VPD TAC Diff
ZDC TAC Diff

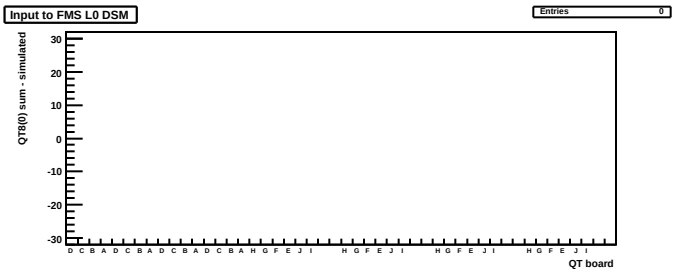

Input to QT1 crate
Entries1795547
ADC
channel

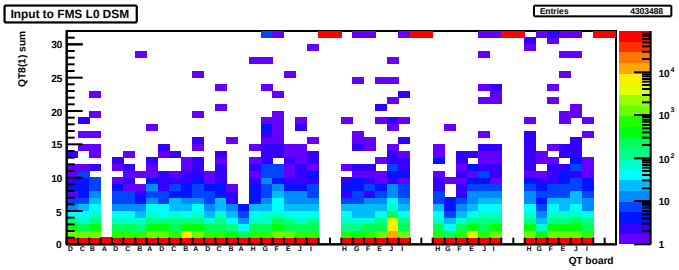

Input to QT2 crate
Entries1028765
ADC
channel

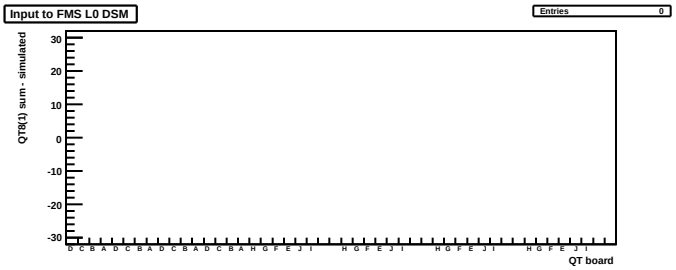

Input to QT3 crate
Entries1269978
ADC
channel

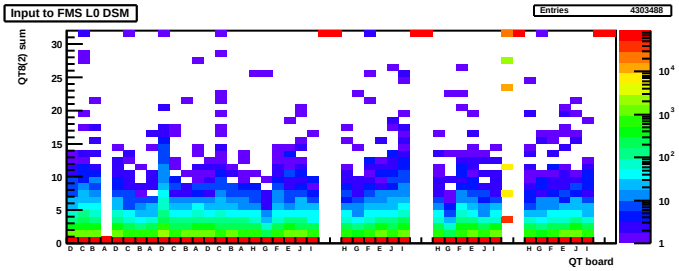

Input to QT4 crate
Entries1325362
ADC
channel

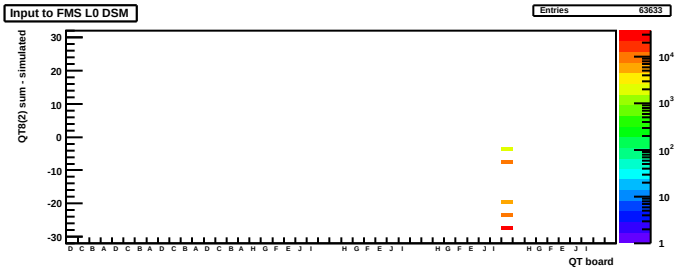

Input to FMS L0 DSM
Entries 4303488
QT8(0) sum
QT board

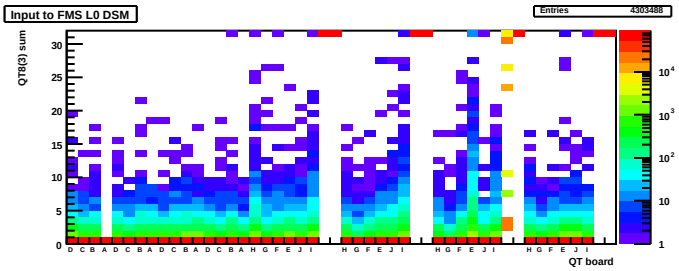

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

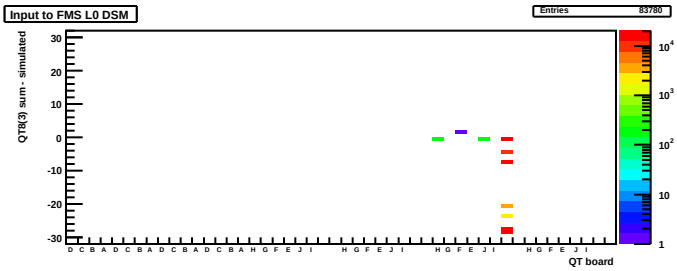

Input to FMS L0 DSM
Entries 4303488
QT8(1) sum
QT board

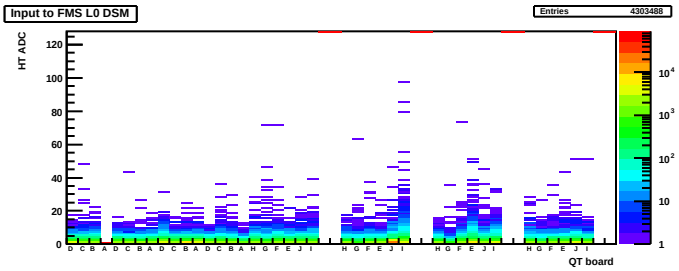

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

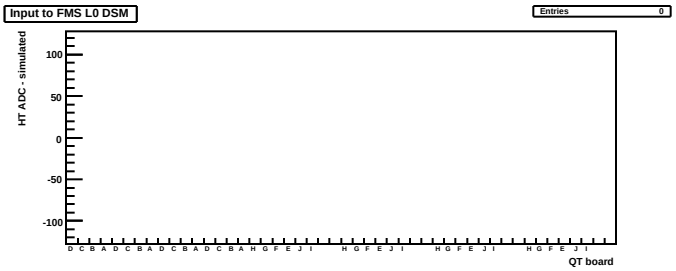

Input to FMS L0 DSM
Entries 4303488
QT8(2) sum
QT board

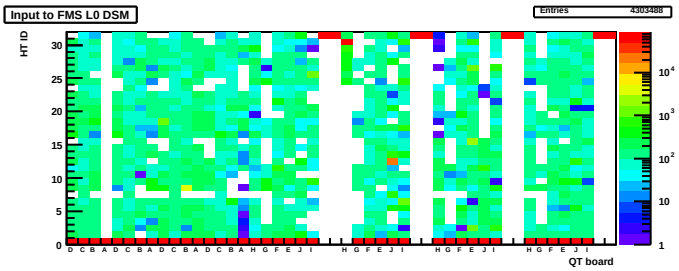

Input to FMS L0 DSM
Entries 63633
QT8(2) sum - simulated
QT board

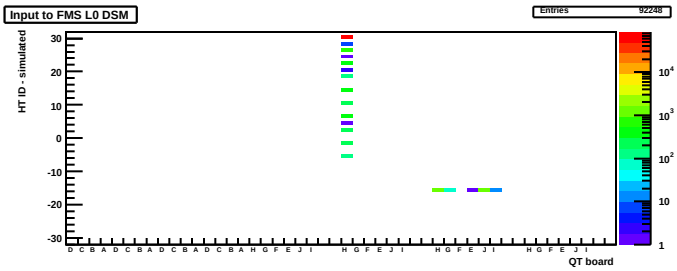

Input to FMS L0 DSM
Entries 4303488
QT8(3) sum
QT board

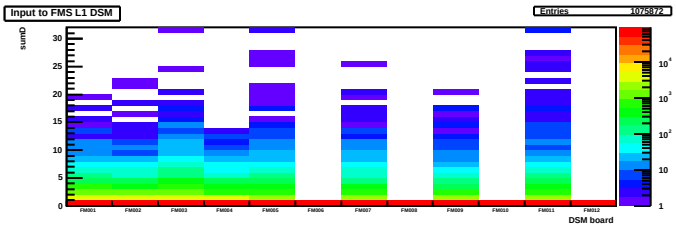

Input to FMS L0 DSM
Entries 83780
QT8(3) sum - simulated
QT board

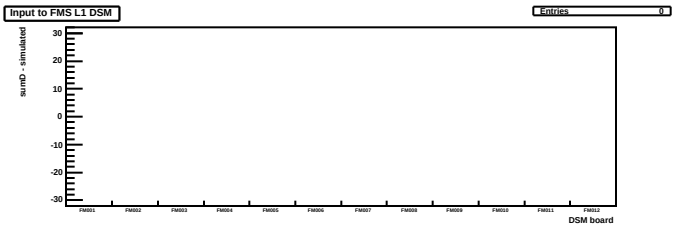

Input to FMS L0 DSM
Entries 4303488
HT ADC
QT board

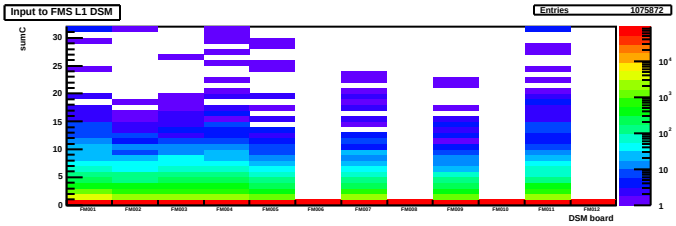

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

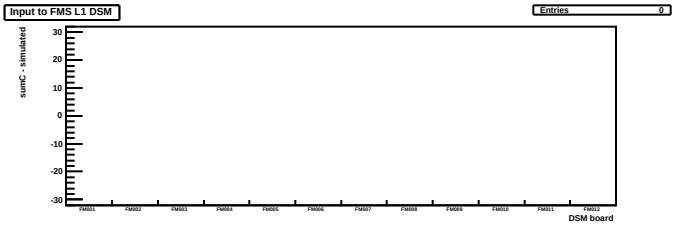

Input to FMS L0 DSM
Entries 4303488
HT ID
QT board

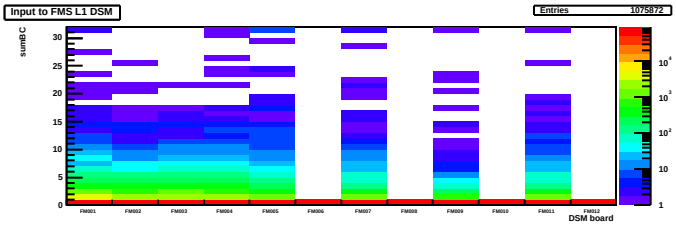

Input to FMS L0 DSM
Entries 92248
HT ID - simulated
QT board

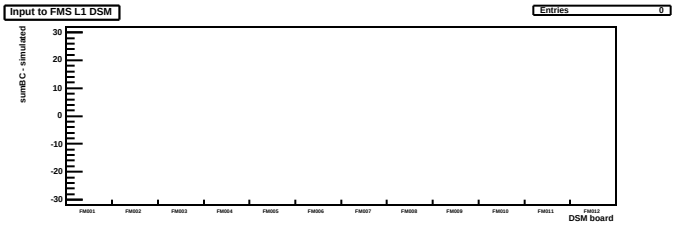

Input to FMS L1 DSM
Entries 1075872
sumD
DSM board

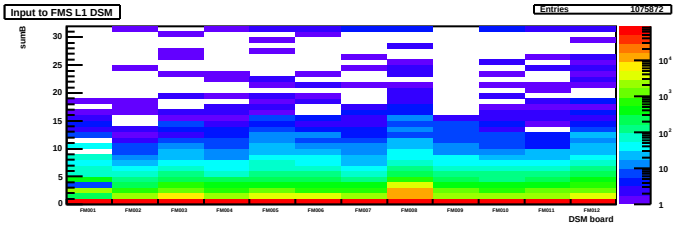

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

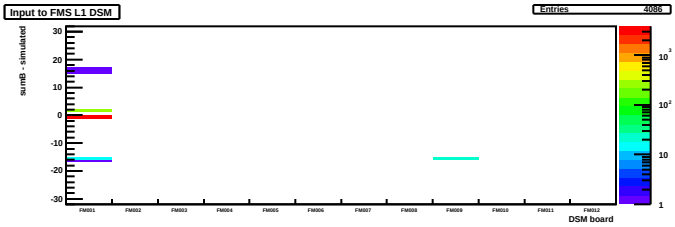

Input to FMS L1 DSM
Entries 1075872
sumC
DSM board

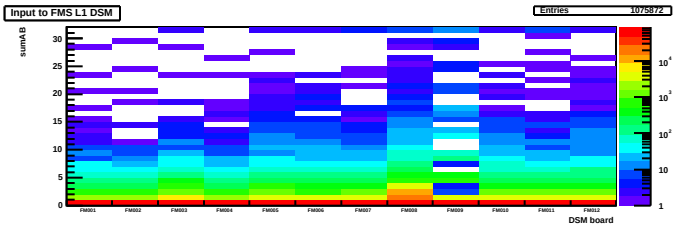

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

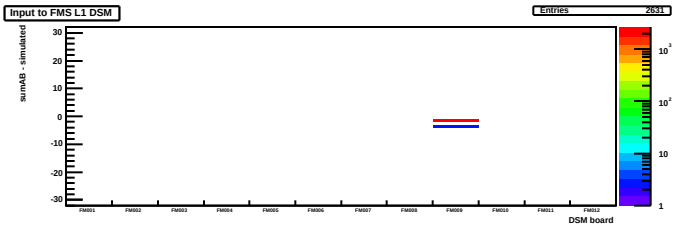

Input to FMS L1 DSM
Entries 1075872
sumBC
DSM board

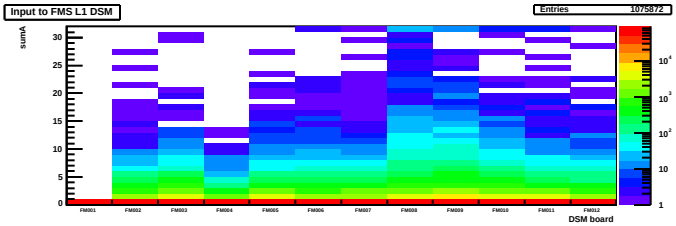

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

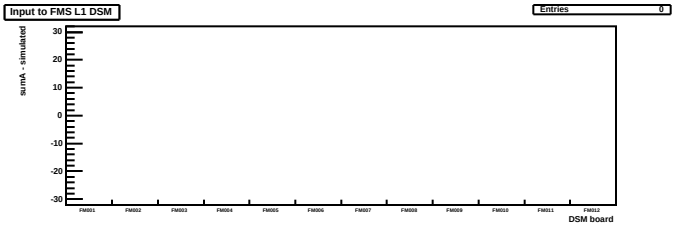

Input to FMS L1 DSM
Entries 1075872
sumB
DSM board

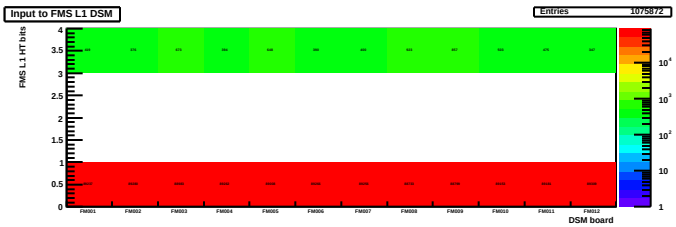

Input to FMS L1 DSM
Entries 4086
sumB - simulated
DSM board

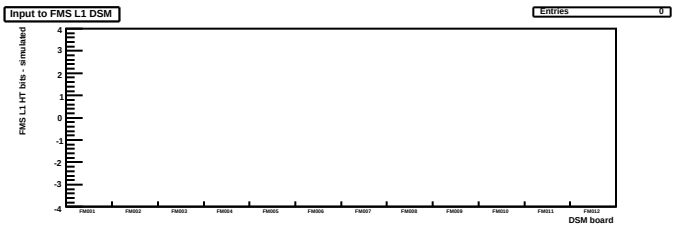

Input to FMS L1 DSM
Entries 1075872
sumAB
DSM board

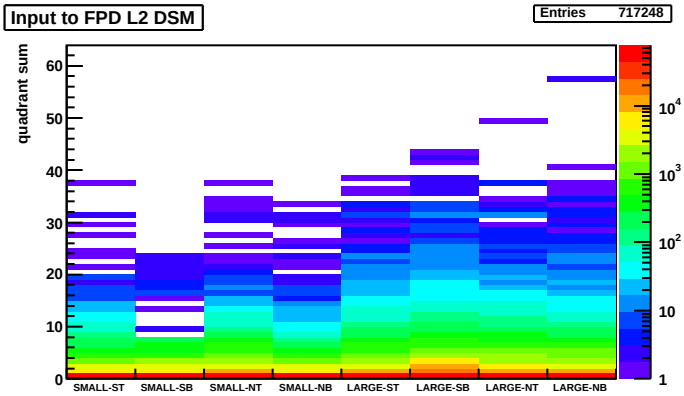

Input to FMS L1 DSM
Entries 2631
sumAB - simulated
DSM board

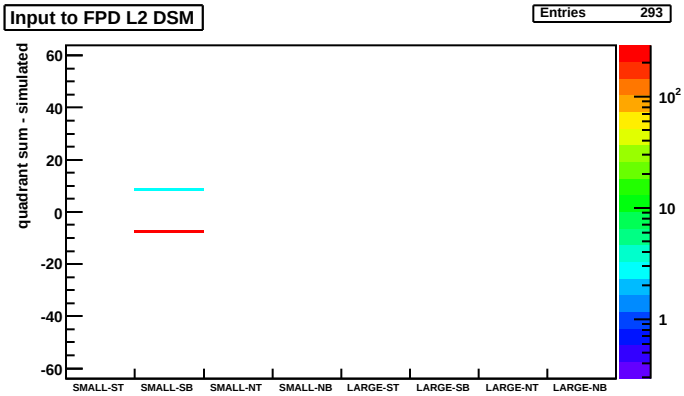

Input to FMS L1 DSM
Entries 1075872
sumA
DSM board

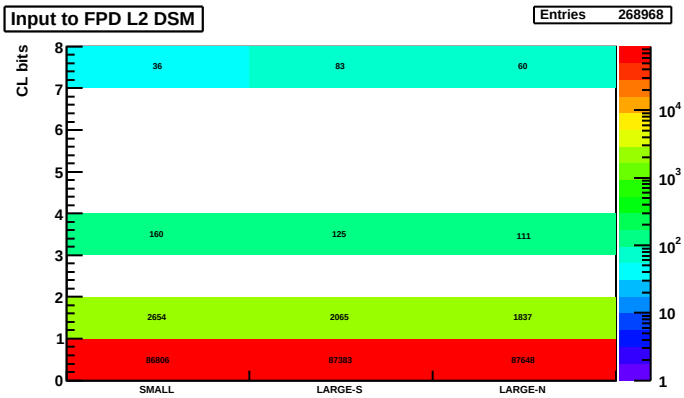

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

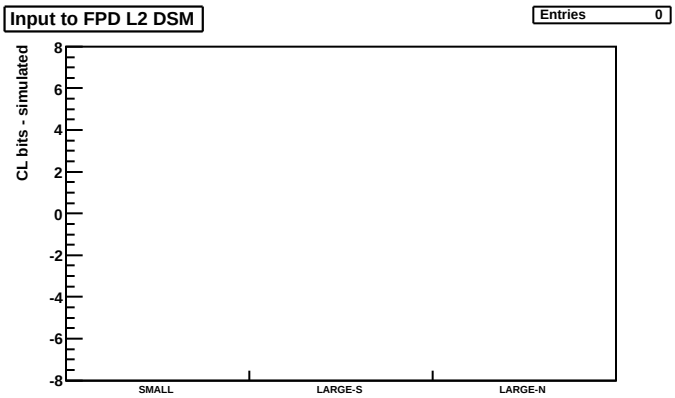

Input to FMS L1 DSM
Entries 1075872
FMS L1 HT bits
DSM board

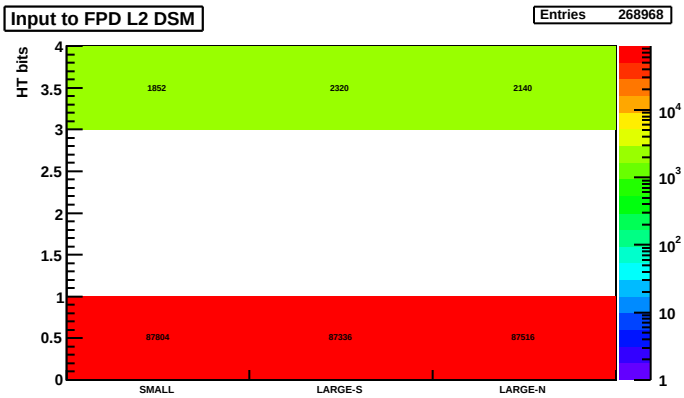

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

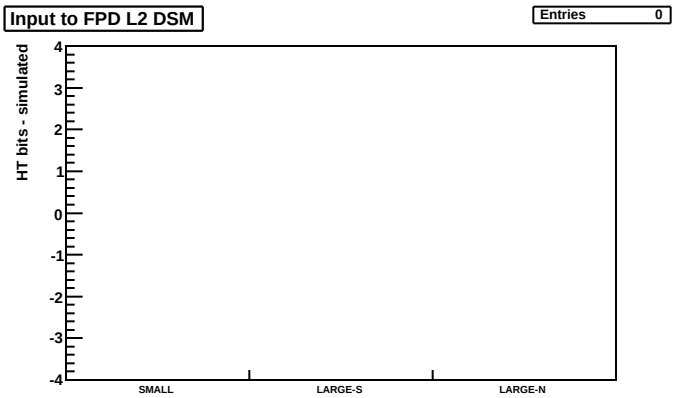

Input to FPD L2 DSM
Entries 717248
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

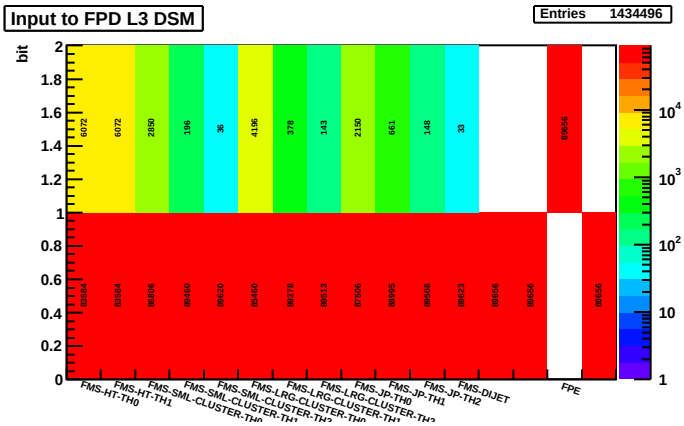

Input to FPD L2 DSM
Entries 293
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

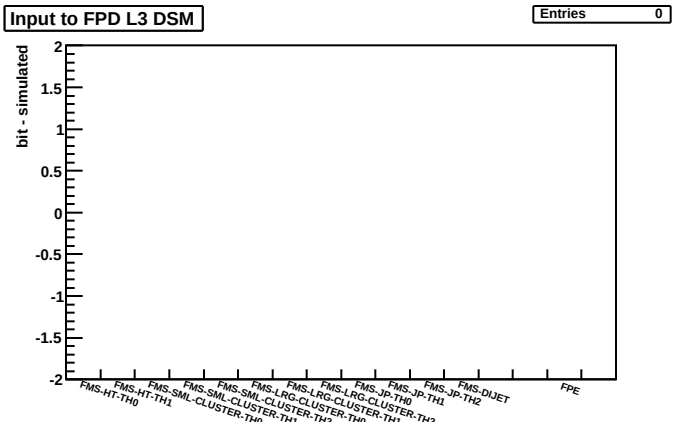

Input to FPD L2 DSM
Entries 268968
CL bits
36
83
60
160
125
111
2654
2065
1837
86806
87383
87648
SMALL
LARGE-S
LARGE-N

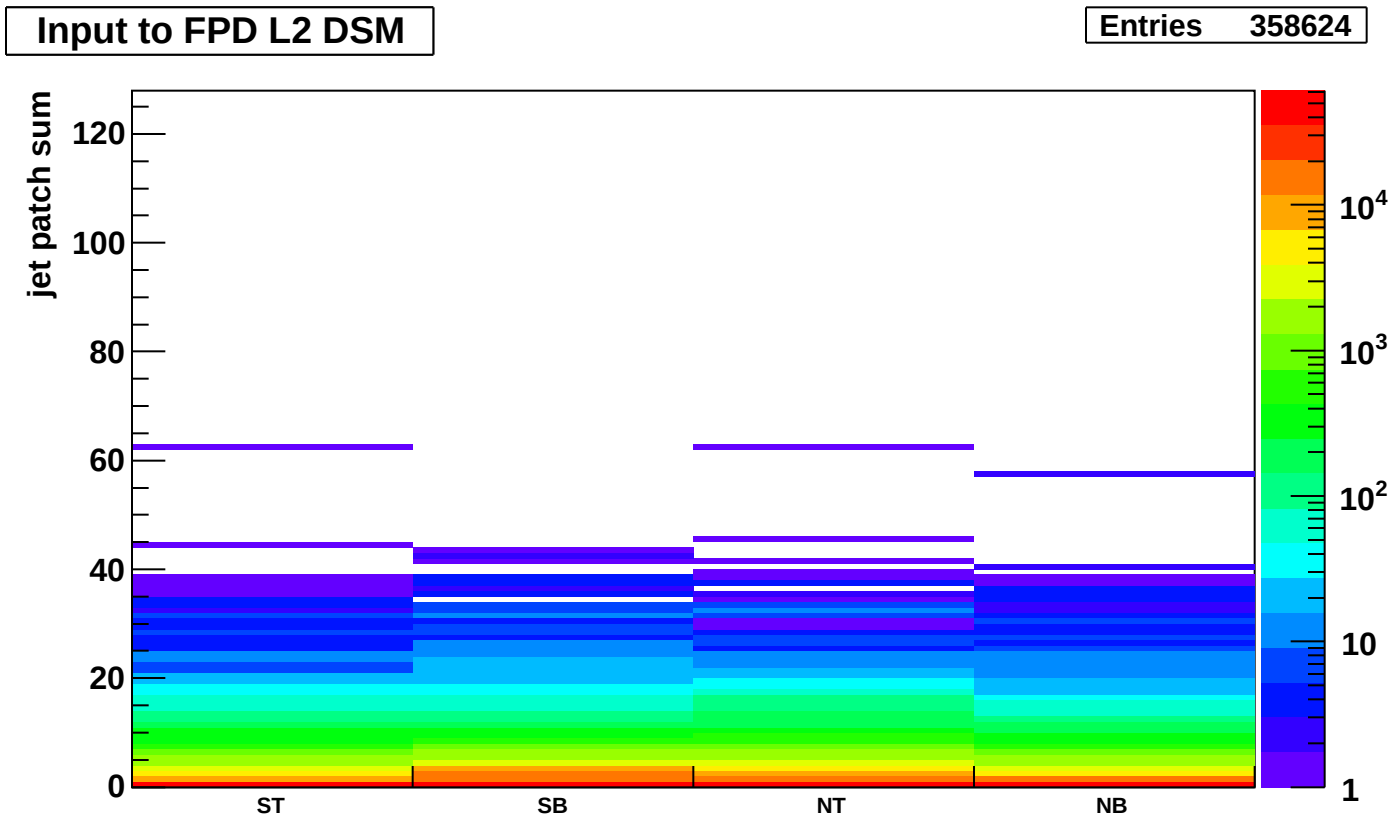

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

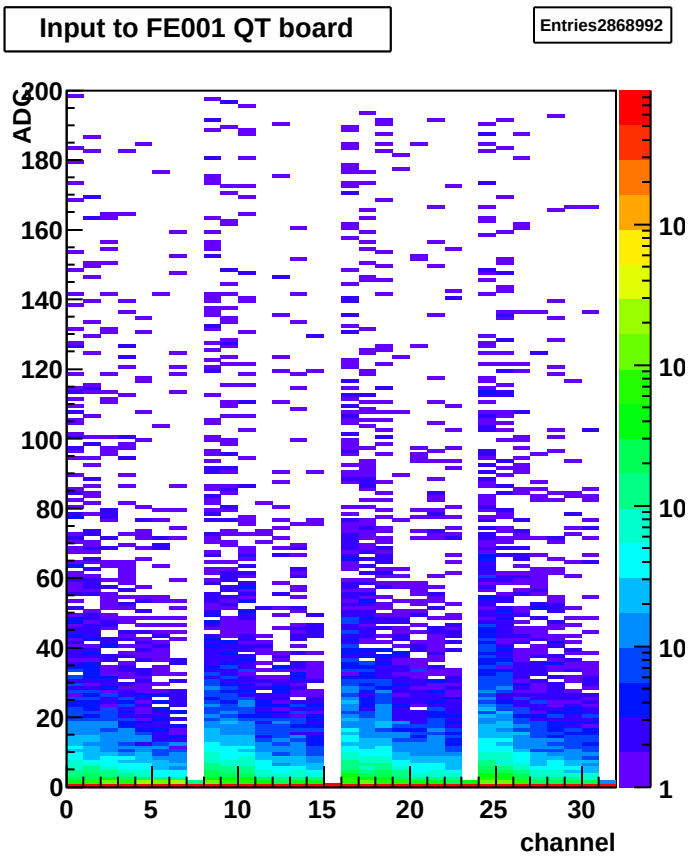

Input to FPD L2 DSM
Entries 268968
HT bits
1852
2320
2140
87804
87336
87516
SMALL
LARGE-S
LARGE-N

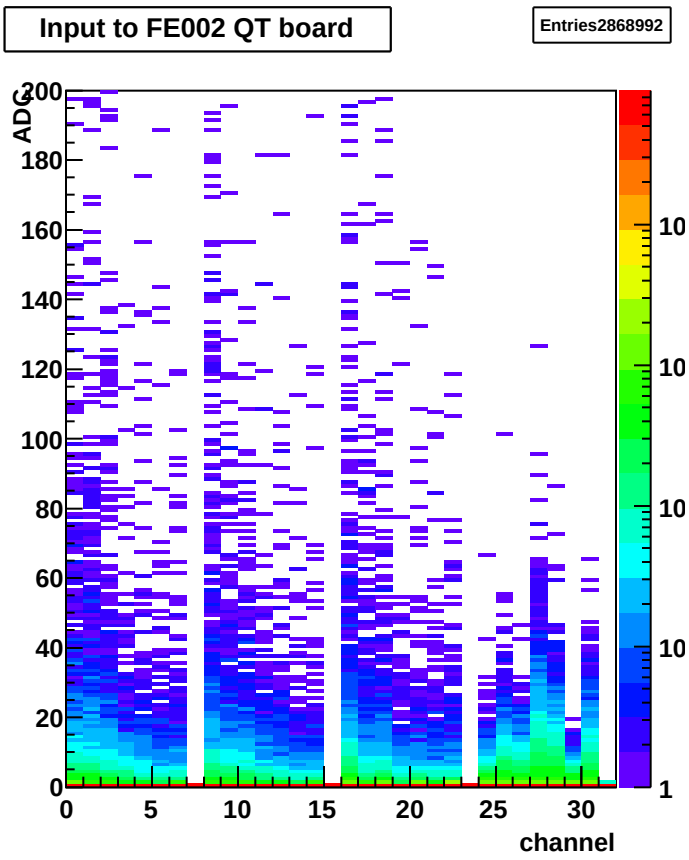

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

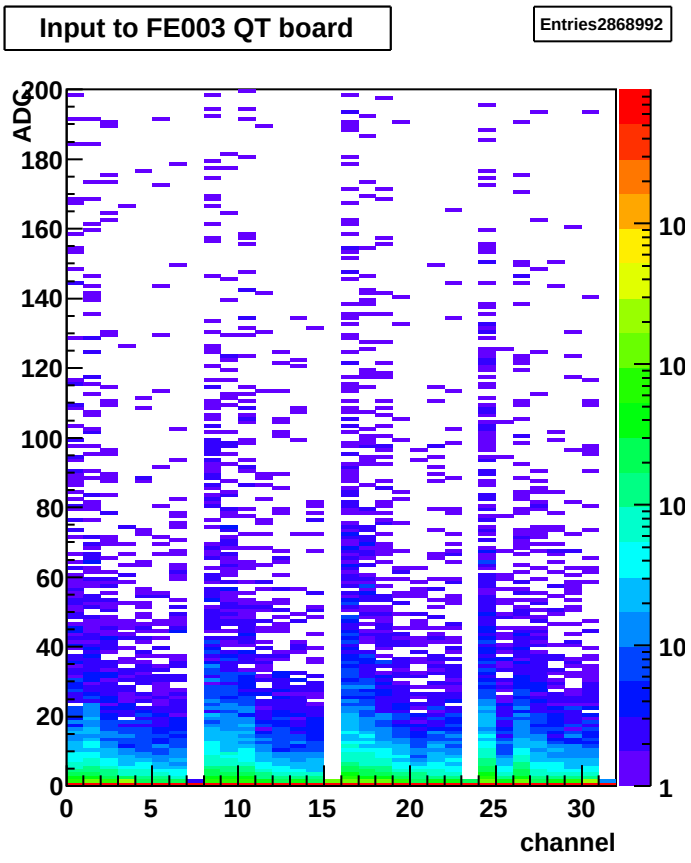

Input to FPD L3 DSM
Entries 1434496
bit
6072
6072
2850
196
36
4196
378
143
2150
661
148
33
89656
89656
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

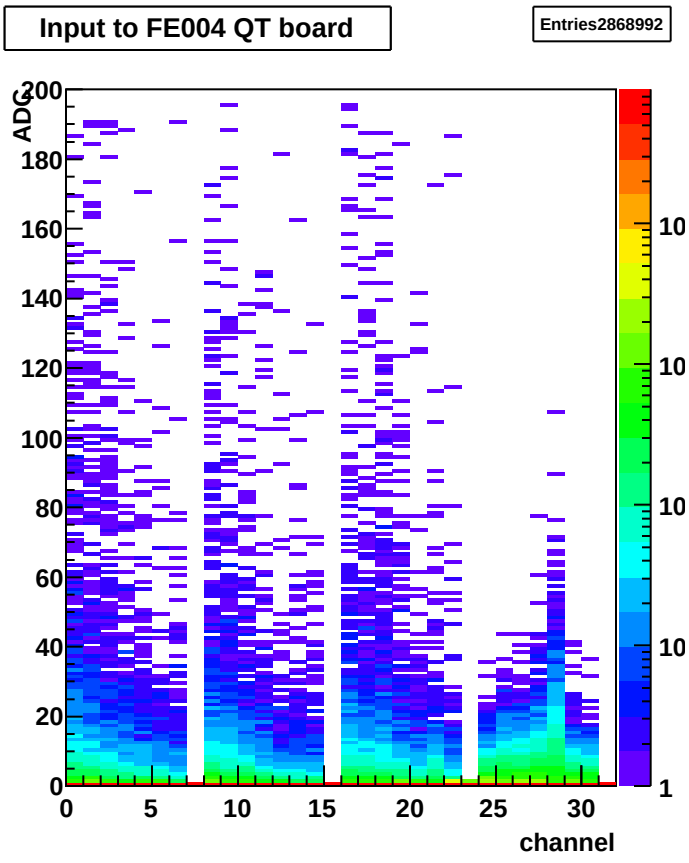

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

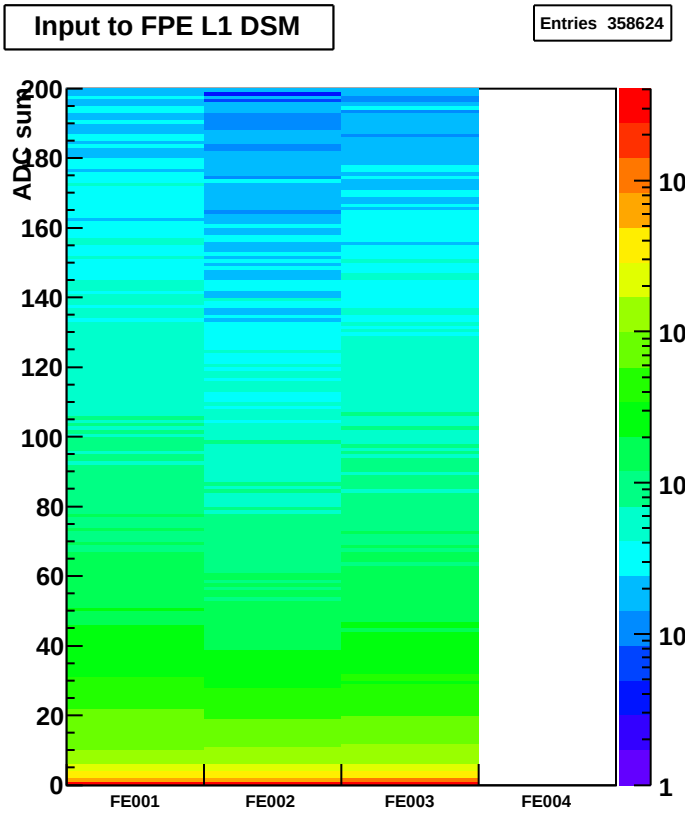

Input to FPD L2 DSM
Entries 358624
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

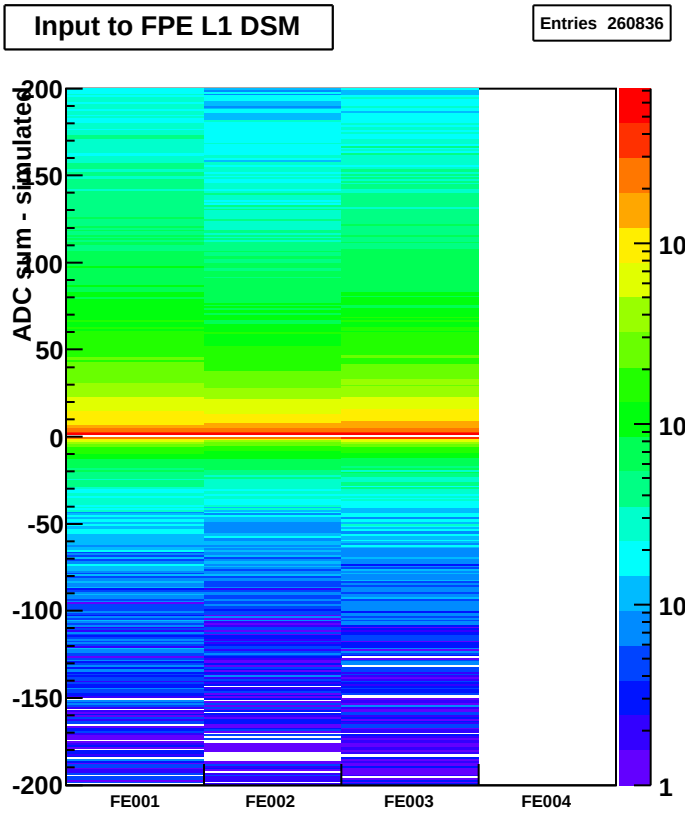

Input to FE001 QT board
Entries2868992
ADC
channel

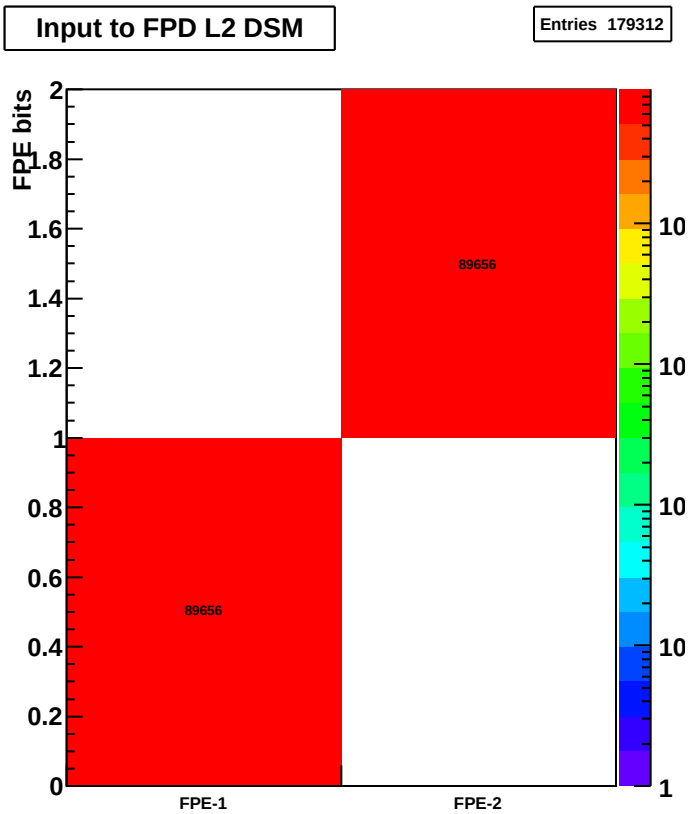

Input to FE002 QT board
Entries2868992
ADC
channel

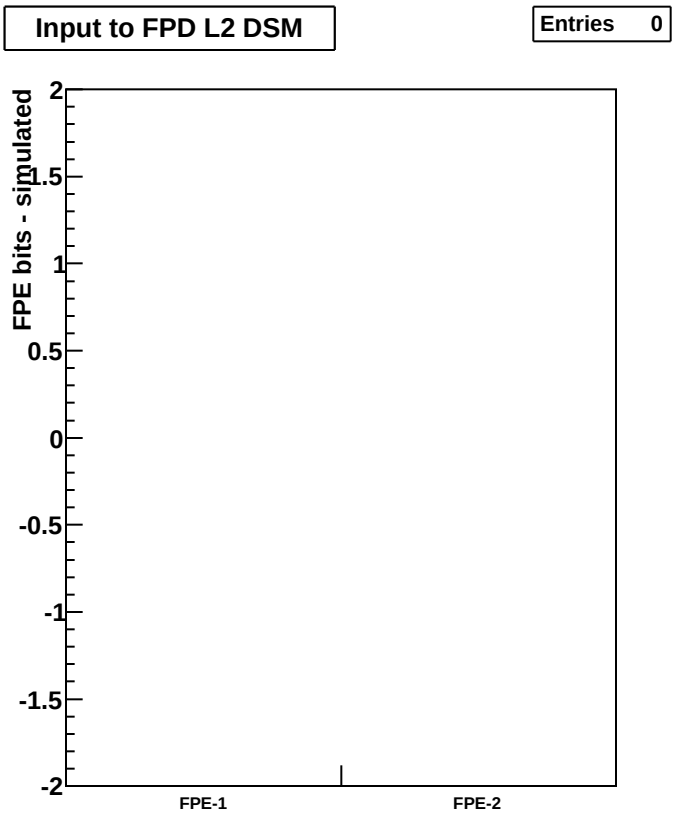

Input to FE003 QT board
Entries2868992
ADC
channel


Input to FE004 QT board
Entries2868992
ADC
channel


Input to FPE L1 DSM
Entries 358624
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 260836
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 179312
FPE bits
89656
89656
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2