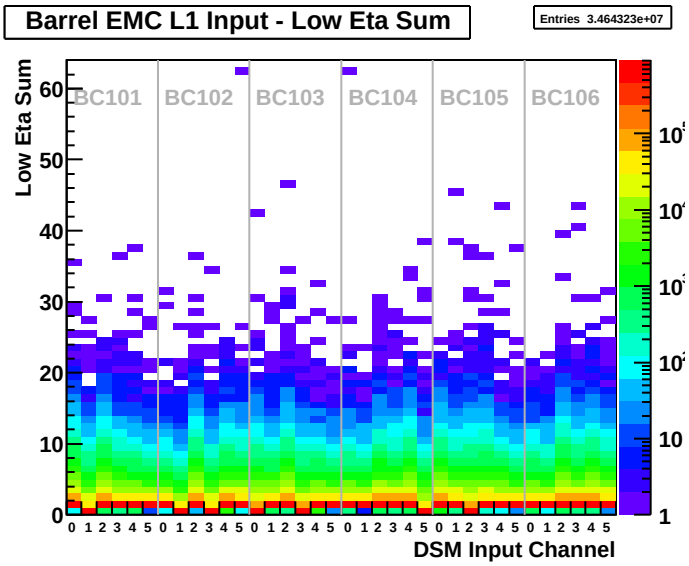
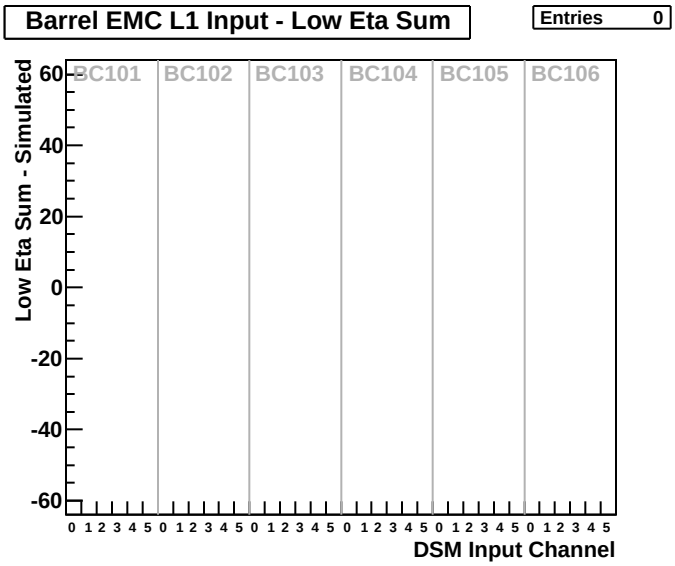
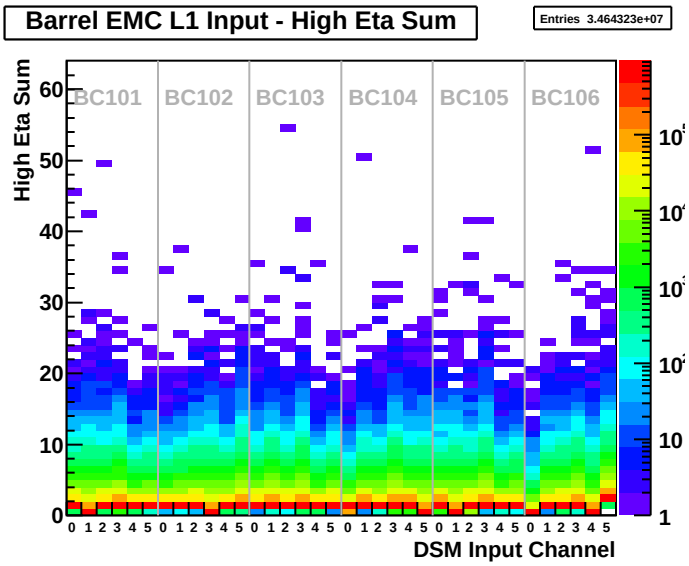
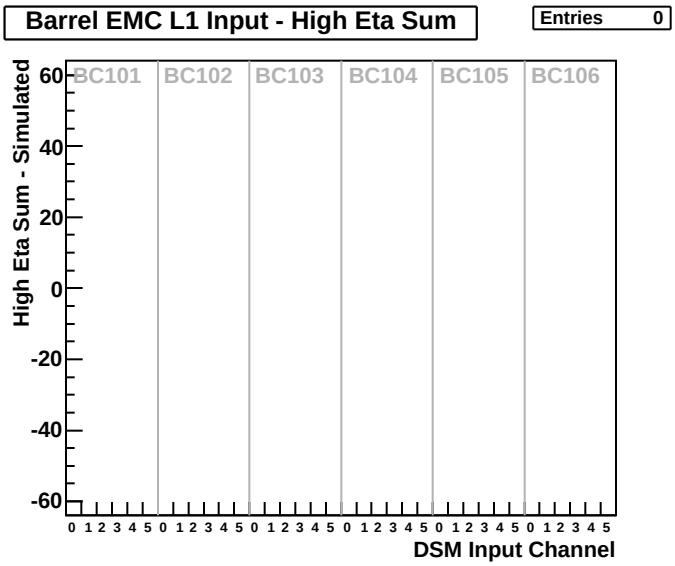
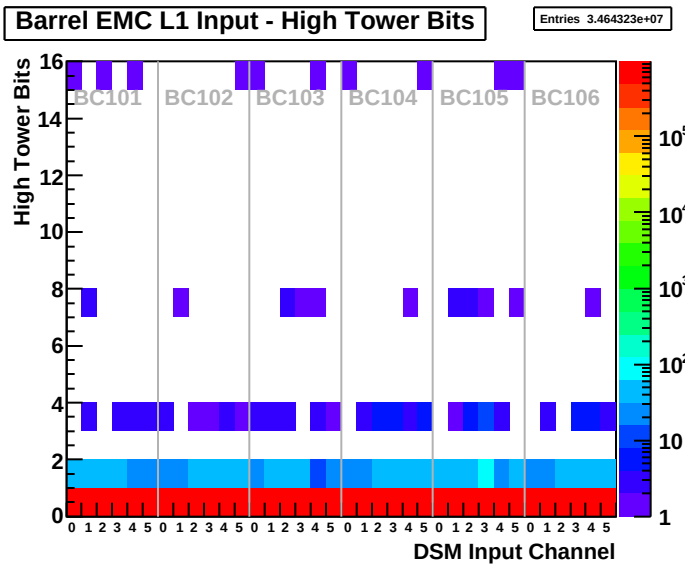
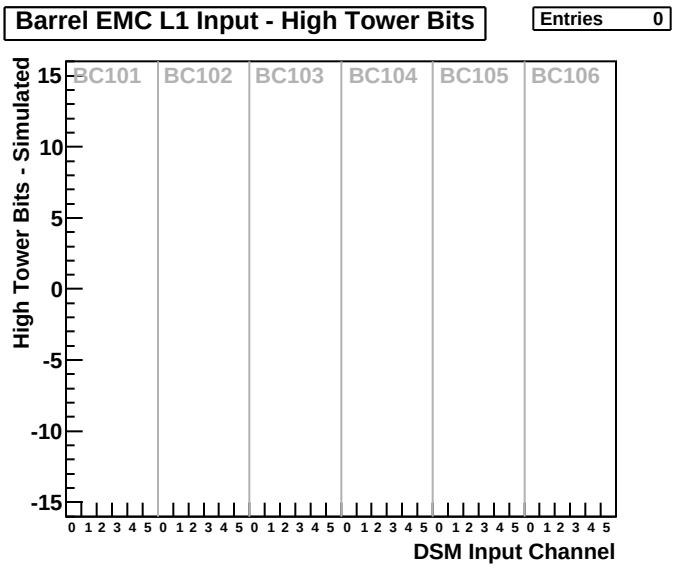

Barrel EMC L1 Input - Low Eta Sum
Entries 3.464323e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

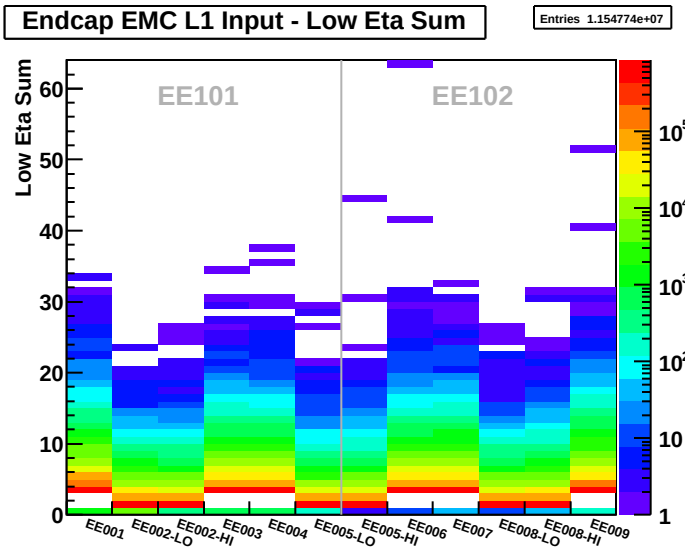

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

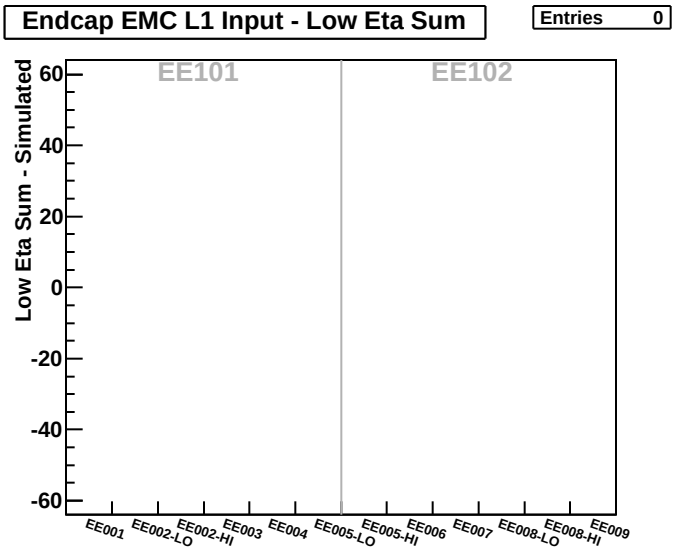

Barrel EMC L1 Input - High Eta Sum
Entries 3.464323e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

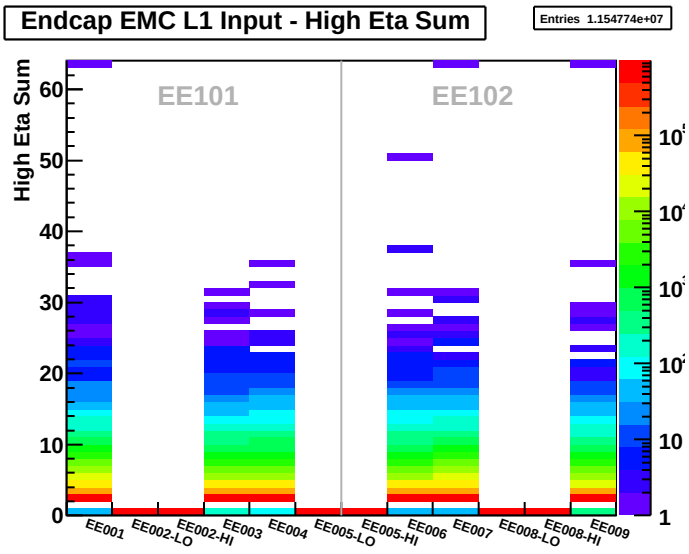

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

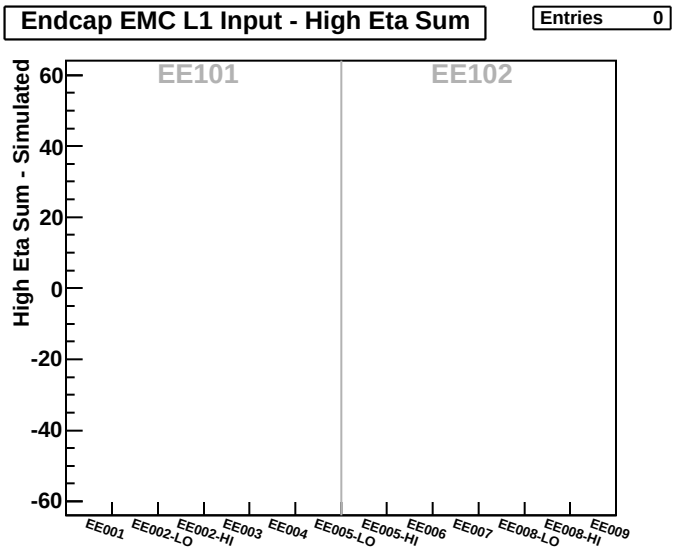

Barrel EMC L1 Input - High Tower Bits
Entries 3.464323e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

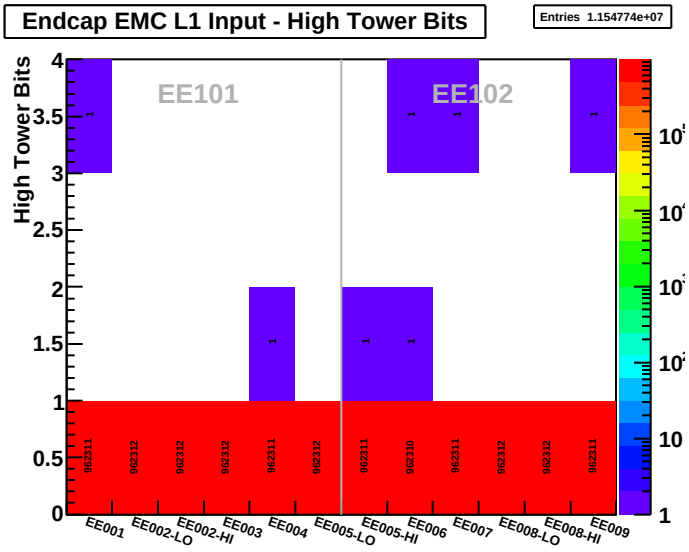

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

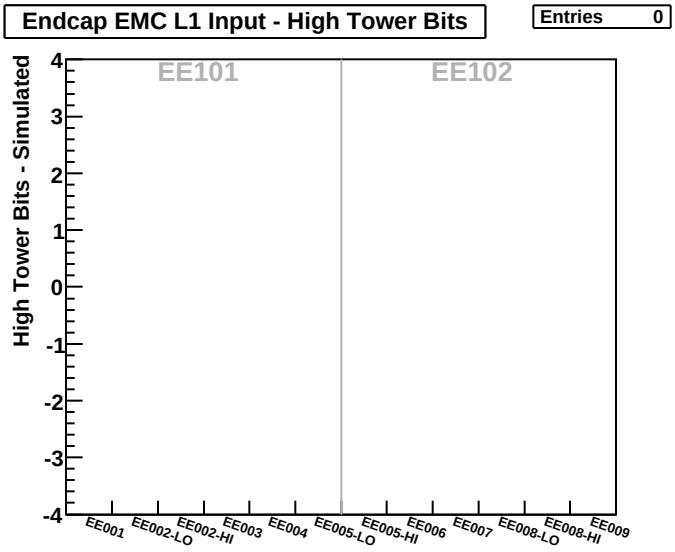

Endcap EMC L1 Input - Low Eta Sum
Entries 1.154774e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

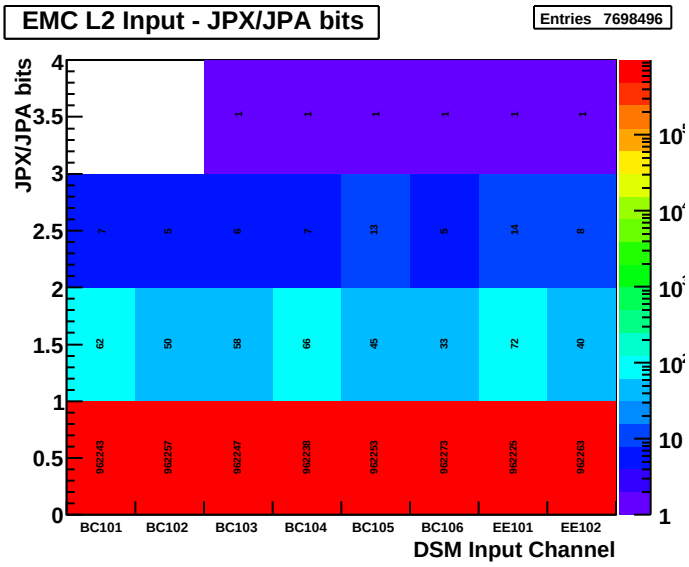

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

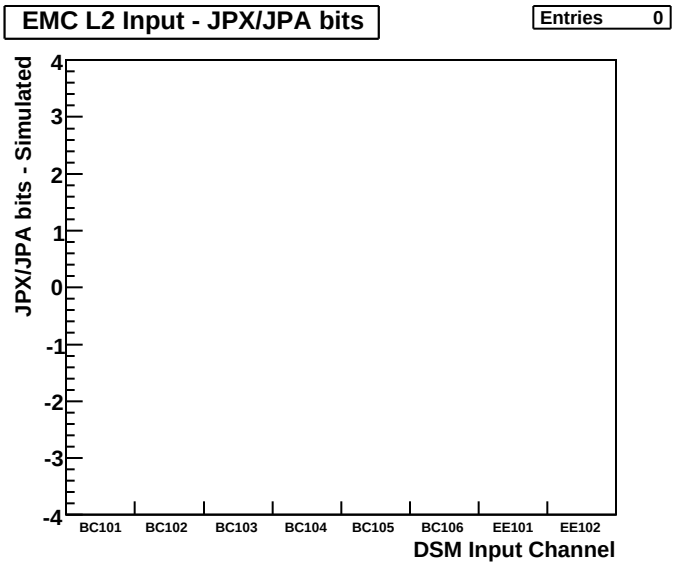

Endcap EMC L1 Input - High Eta Sum
Entries 1.154774e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

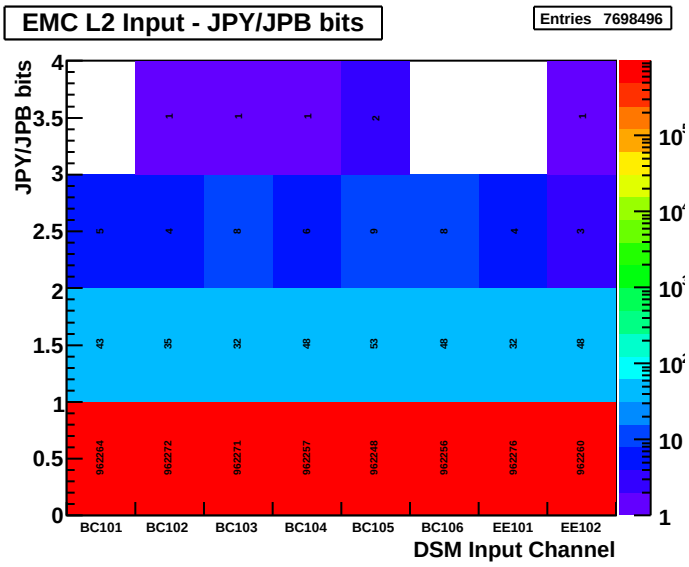

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

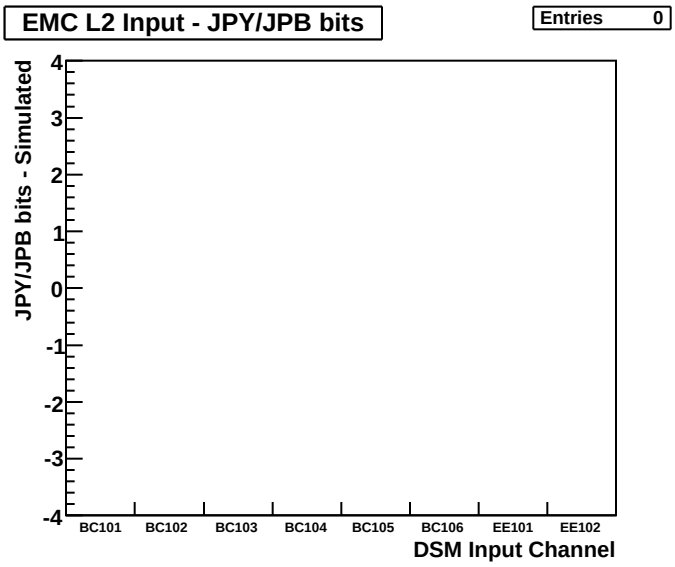

Endcap EMC L1 Input - High Tower Bits
Entries 1.154774e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

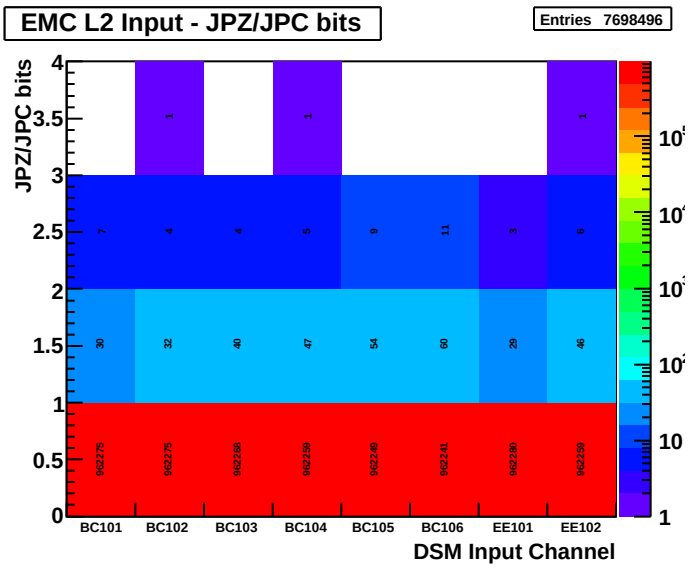

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

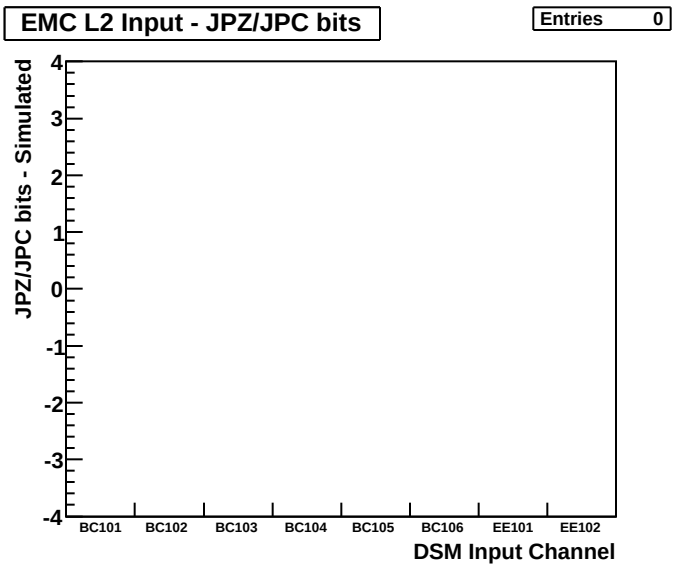

EMC L2 Input - JPX/JPA bits
Entries 7698496
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

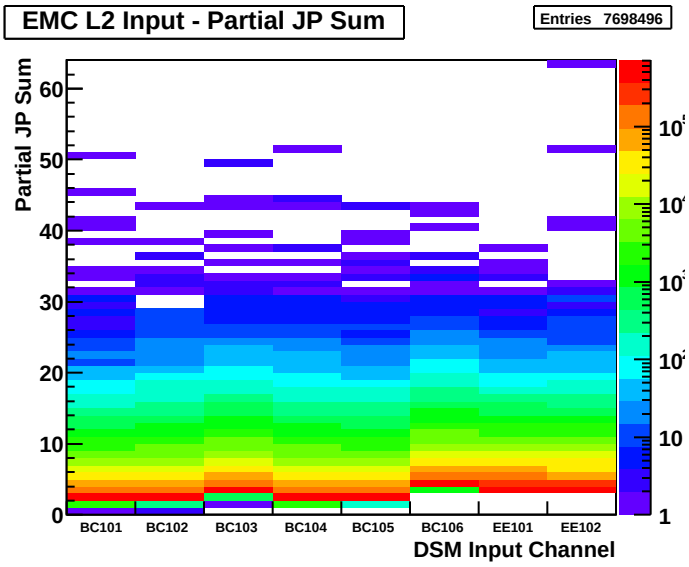

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

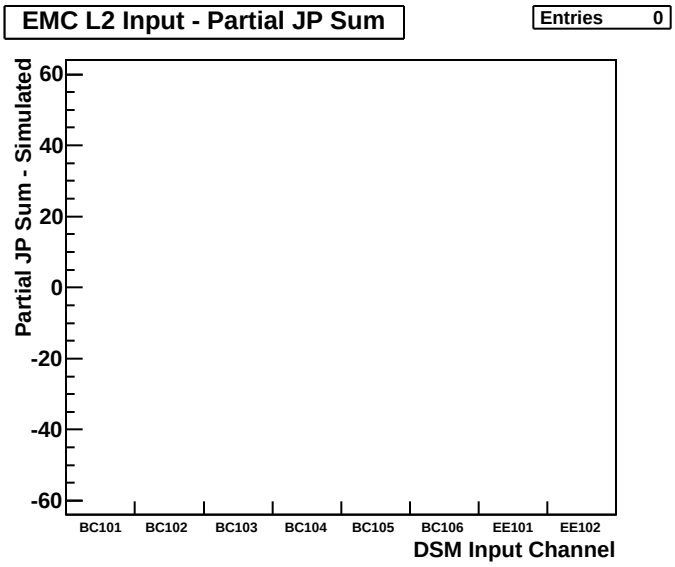

EMC L2 Input - JPY/JPB bits
Entries 7698496
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

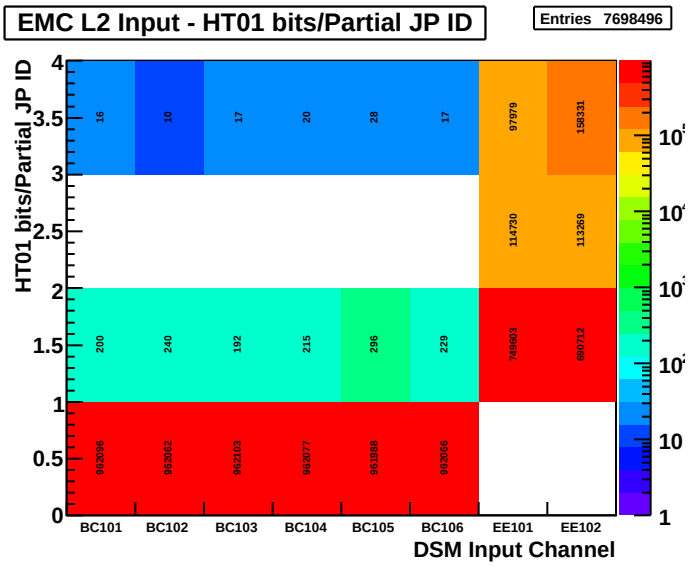

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

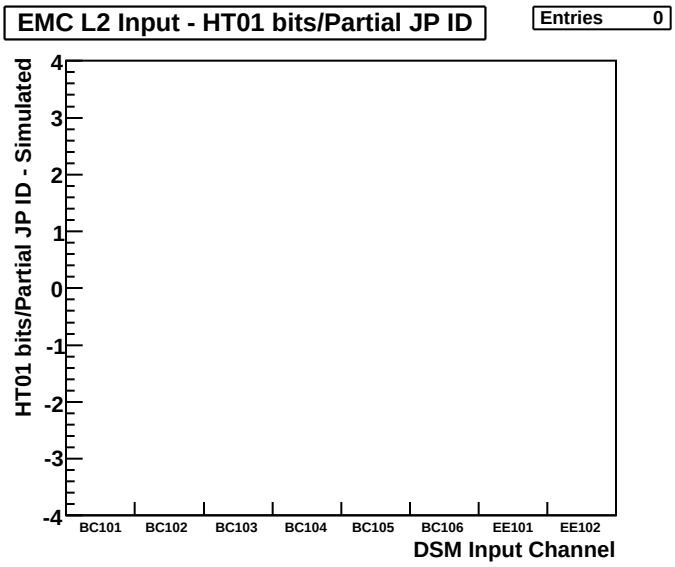

EMC L2 Input - JPZ/JPC bits
Entries 7698496
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

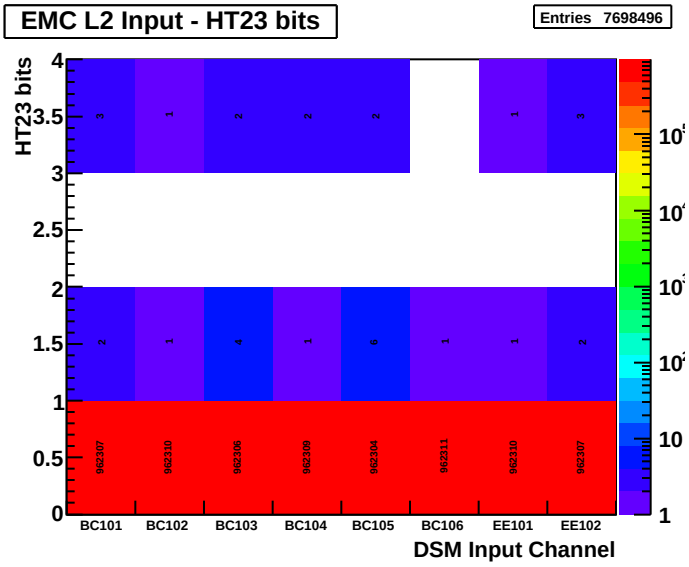

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

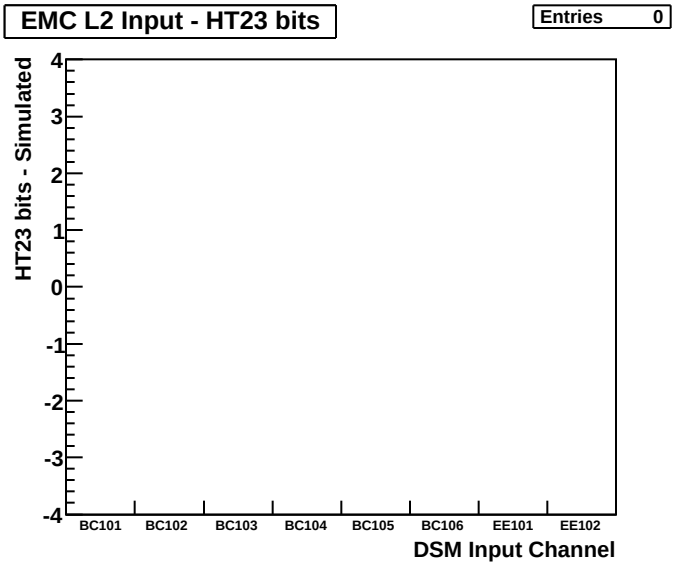

EMC L2 Input - Partial JP Sum
Entries 7698496
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

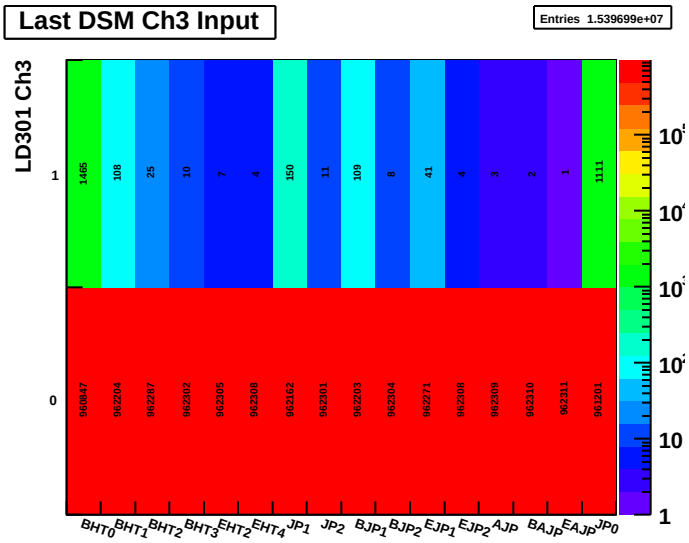

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

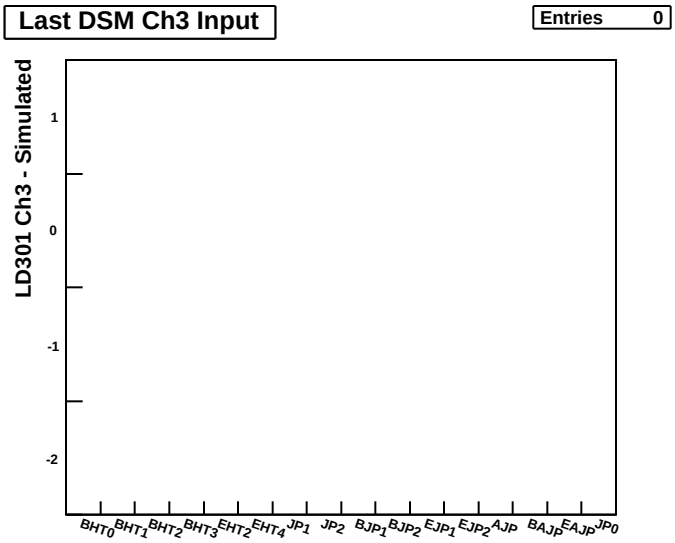

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7698496
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

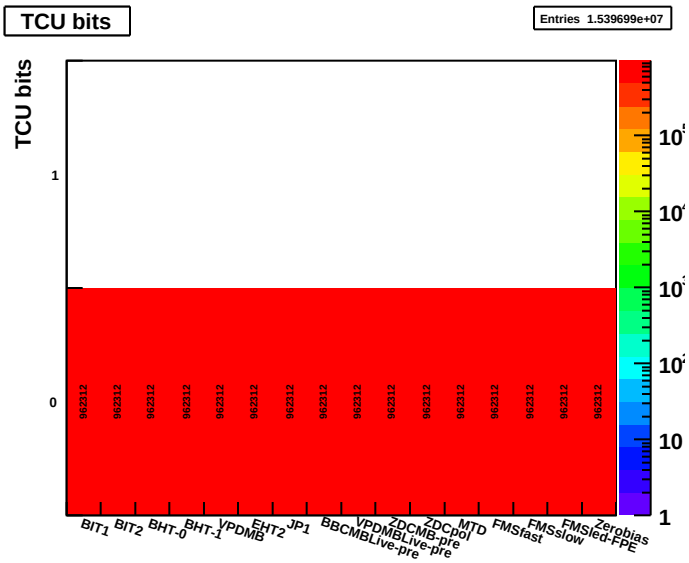

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

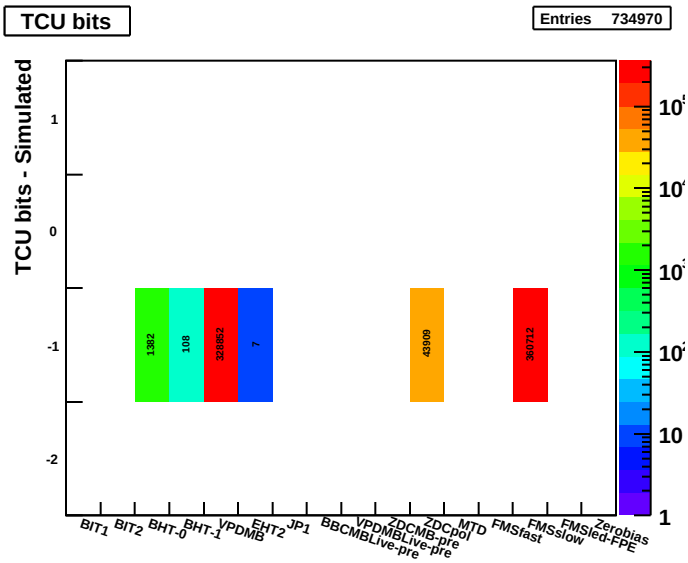

EMC L2 Input - HT23 bits
Entries 7698496
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

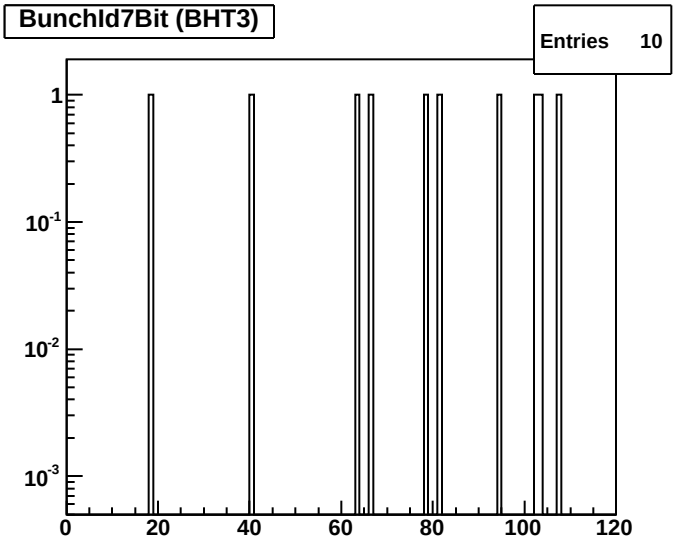

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

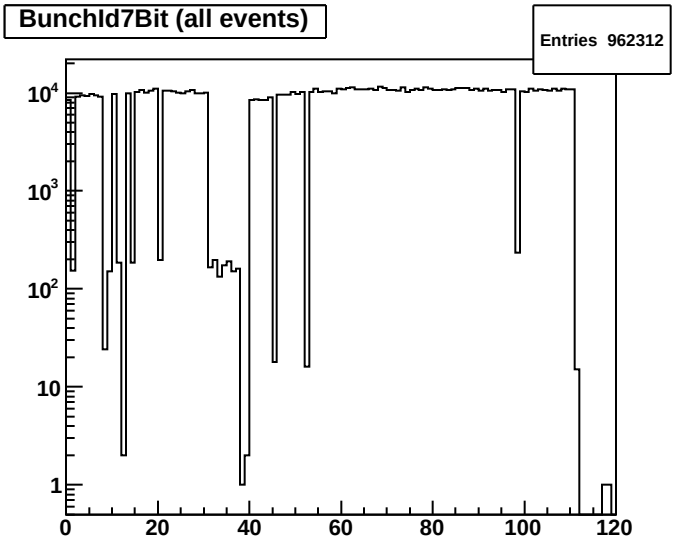

Last DSM Ch3 Input
Entries 1.539699e+07
LD301 Ch3
1465 108 25 10 7 4 150 11 109 8 41 4 3 2 1 1111
960847 962204 962287 962302 962305 962308 962162 962301 962203 962304 962271 962308 962309 962310 962311 961201
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

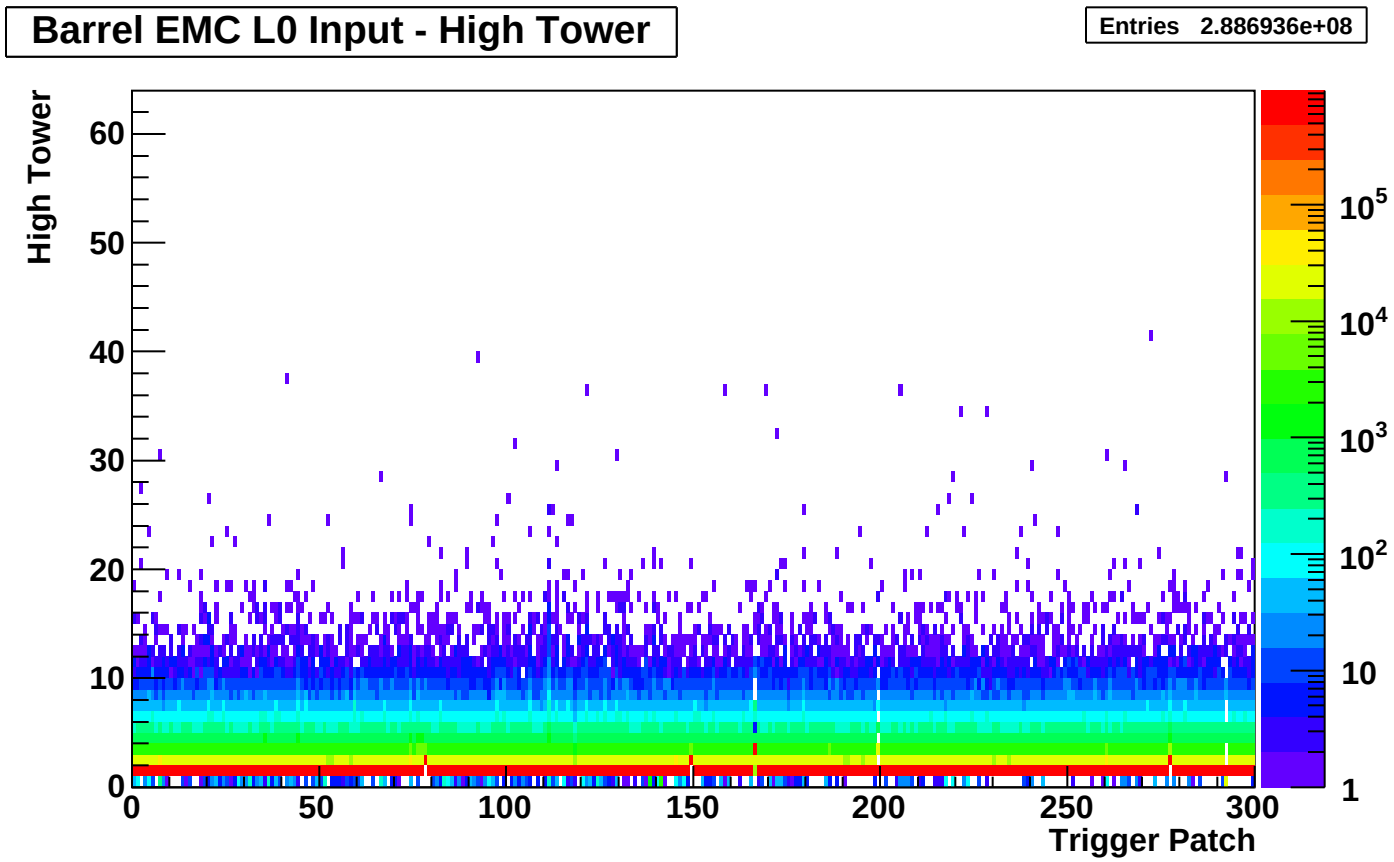

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

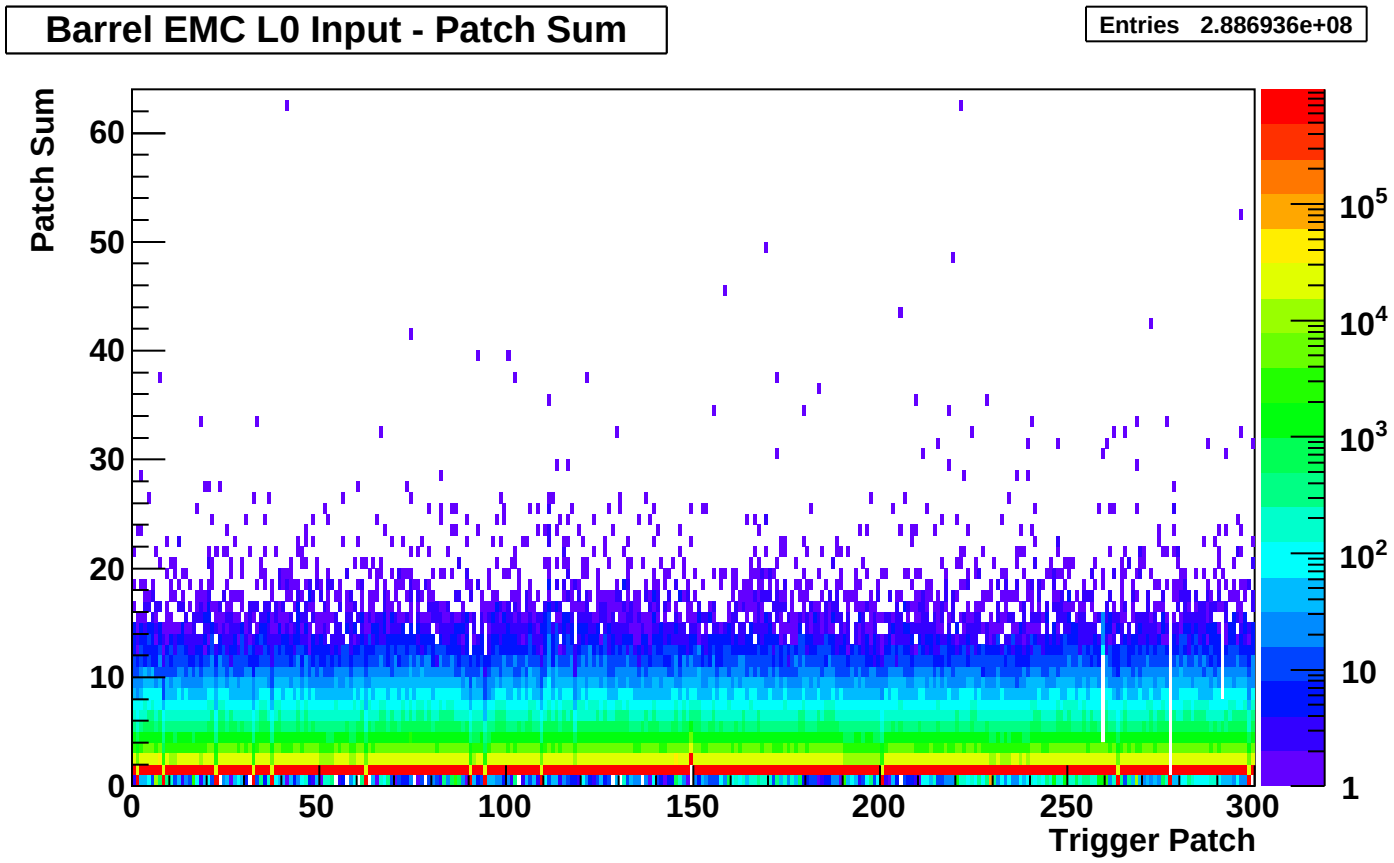

TCU bits
Entries 1.539699e+07
TCU bits
962312
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

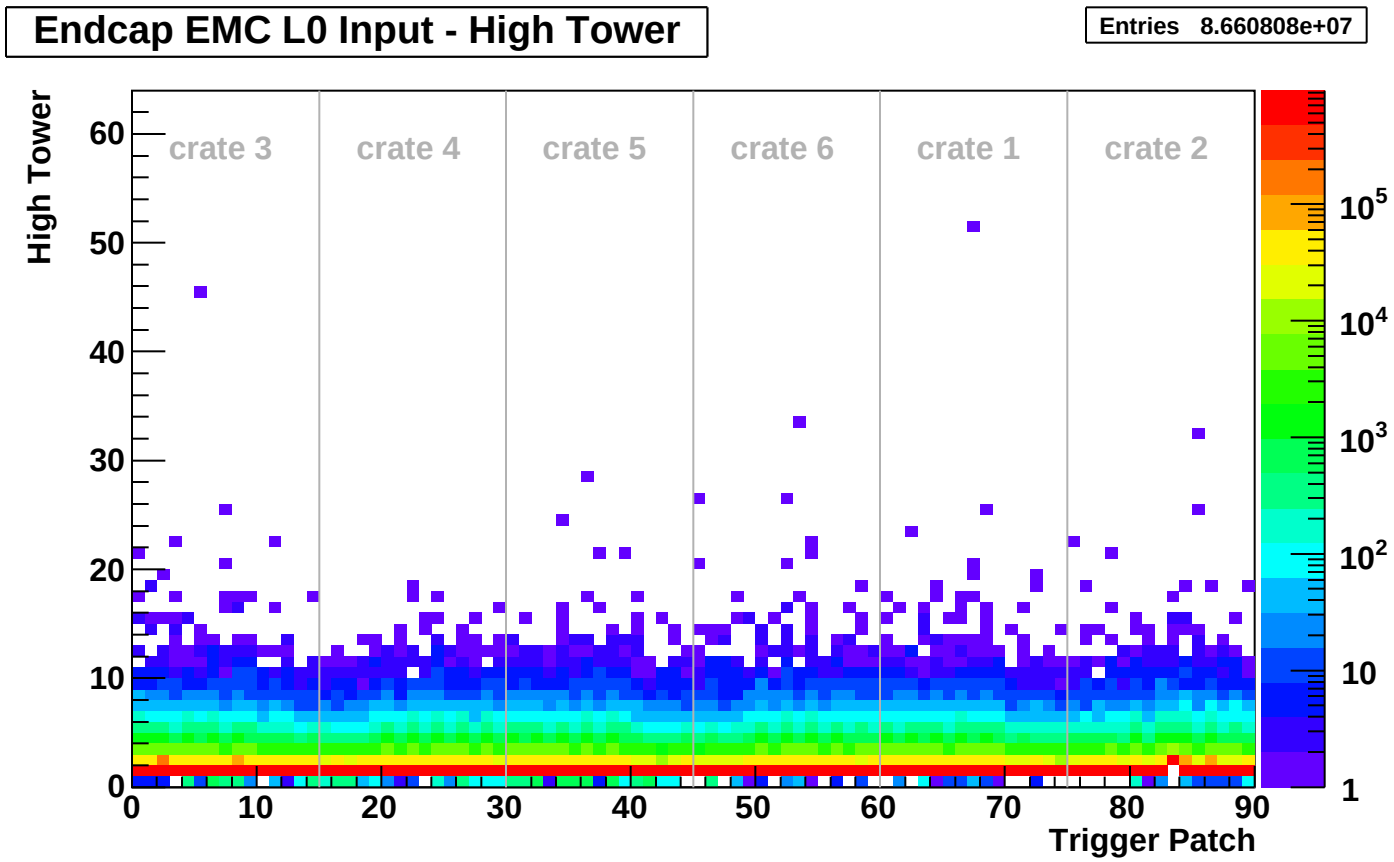

TCU bits
Entries 734970
TCU bits - Simulated
1382 108 328852 7 43909 360712
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

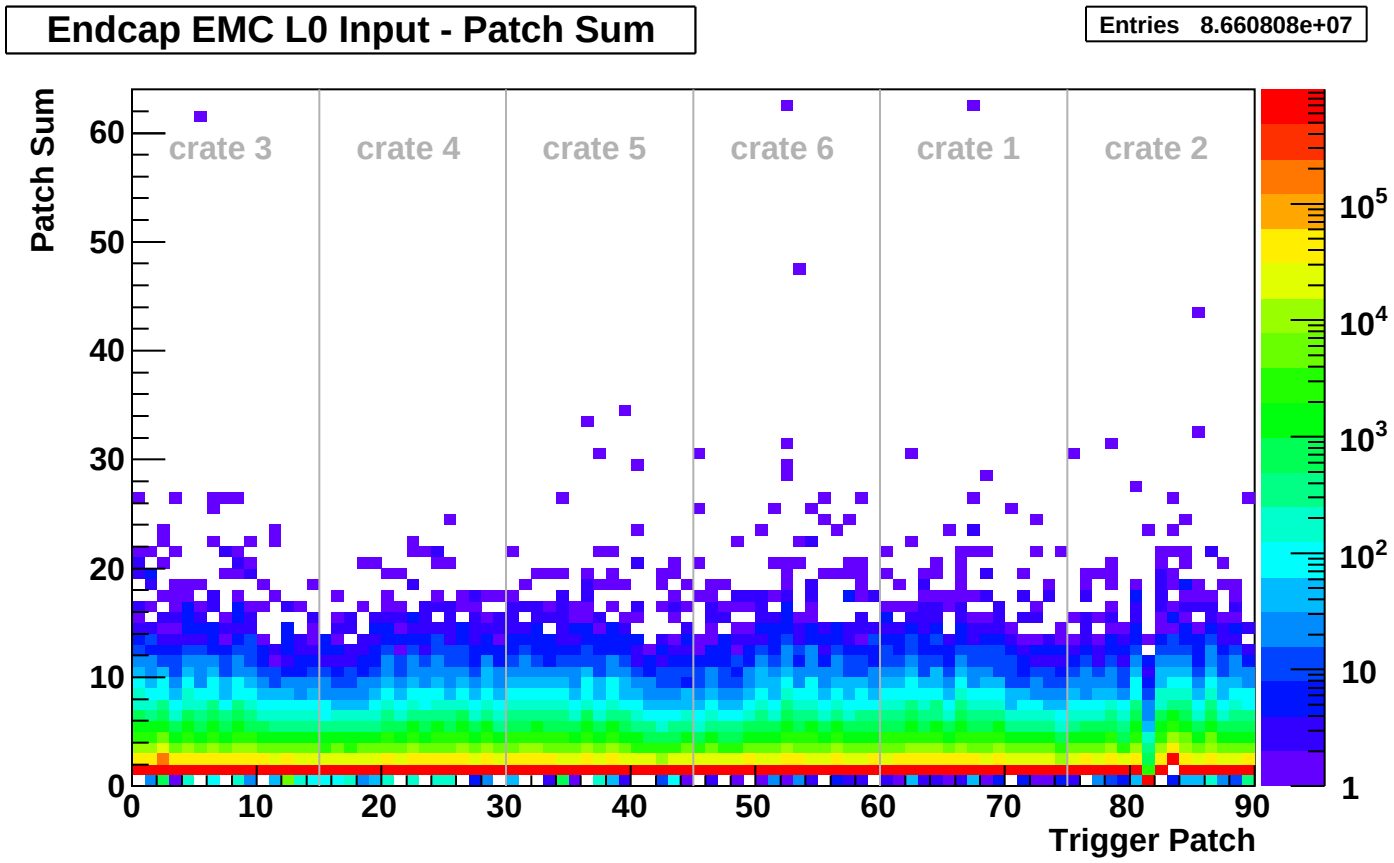

BunchId7Bit (BHT3)
Entries 10

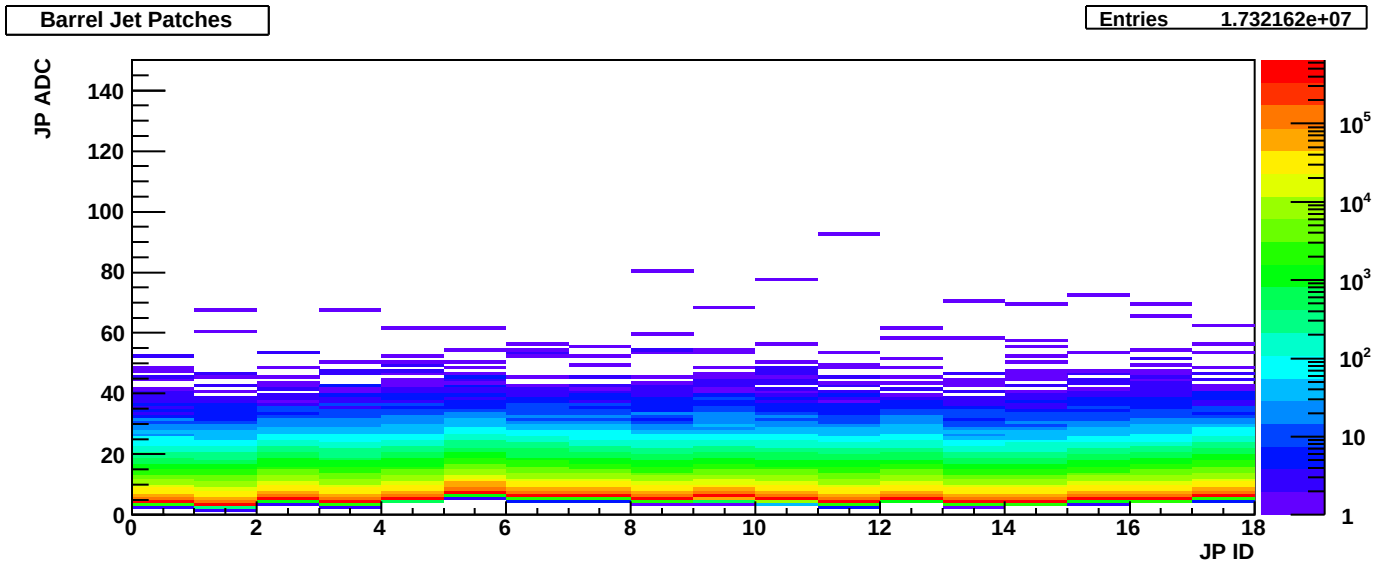

BunchId7Bit (all events)
Entries 962312

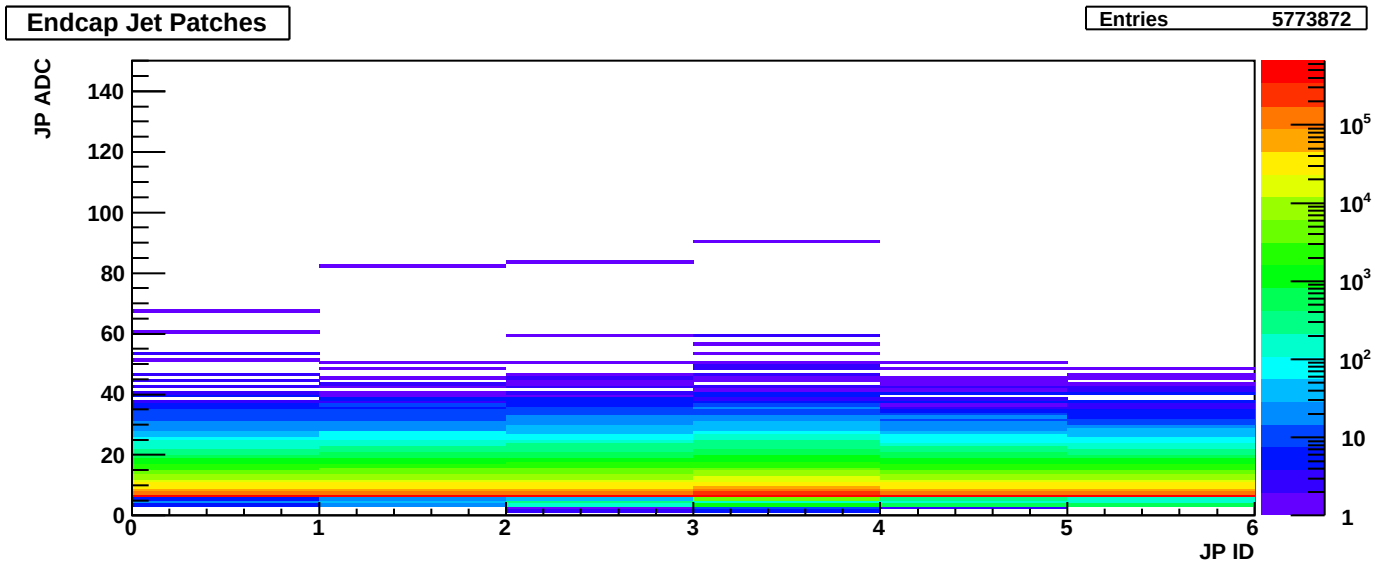

Barrel EMC L0 Input - High Tower
Entries 2.886936e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

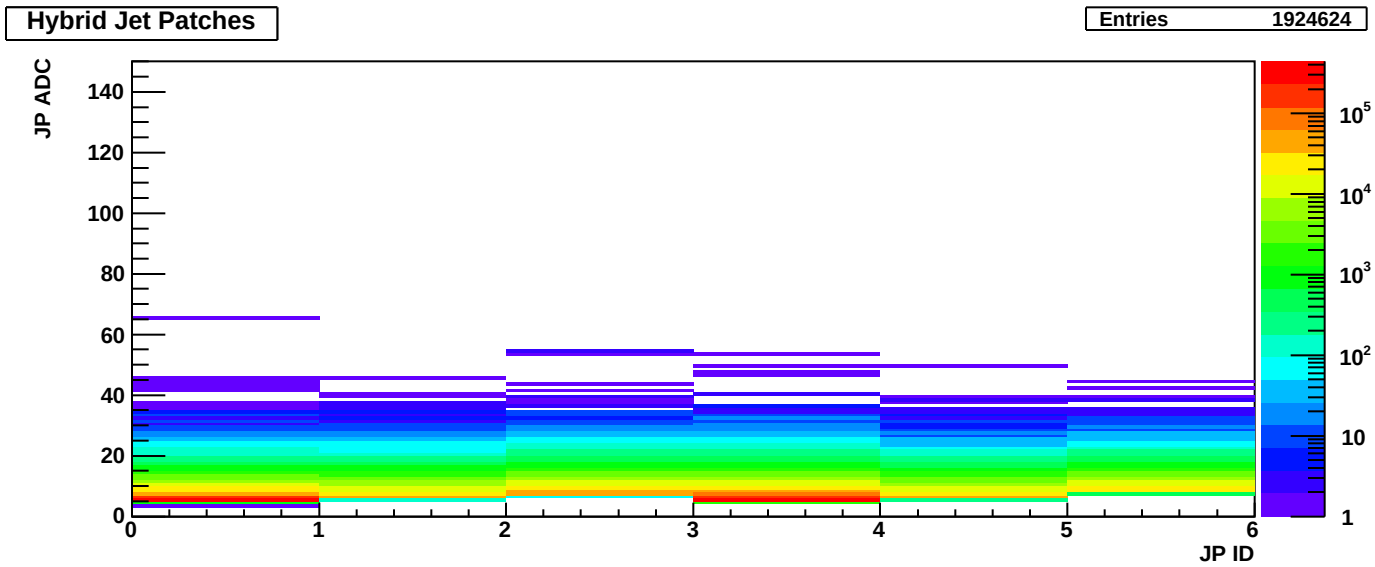

Barrel EMC L0 Input - Patch Sum
Entries 2.886936e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 8.660808e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Endcap EMC L0 Input - Patch Sum
Entries 8.660808e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

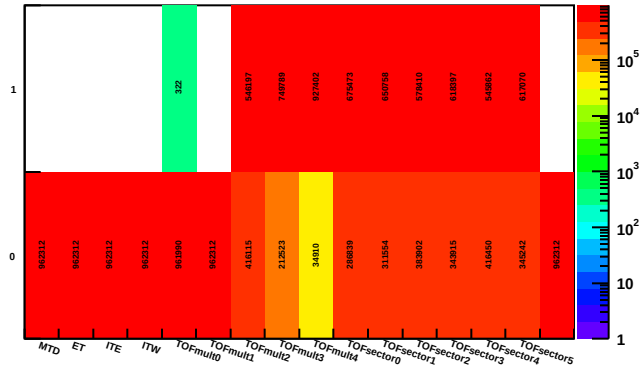

Barrel Jet Patches
Entries 1.732162e+07
JP ADC
JP ID


Endcap Jet Patches
Entries 5773872
JP ADC
JP ID


Hybrid Jet Patches
Entries 1924624
JP ADC
JP ID

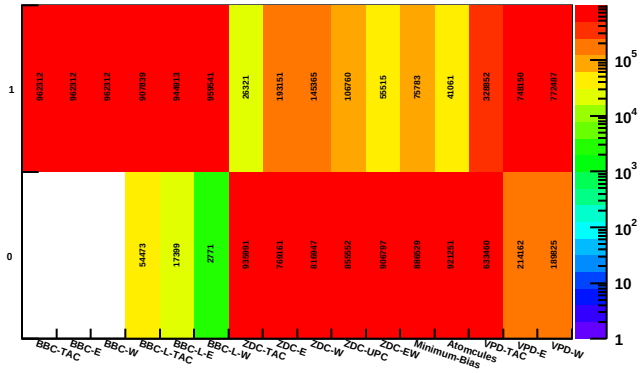
TF201 0-15 (ch0)

Entries 1.539699e+07



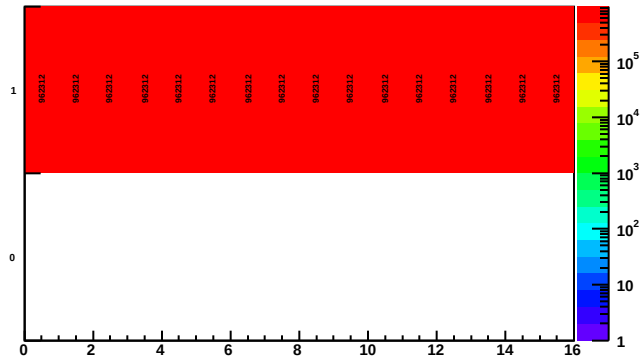
VT201 0-15 (ch1)

Entries 1.539699e+07



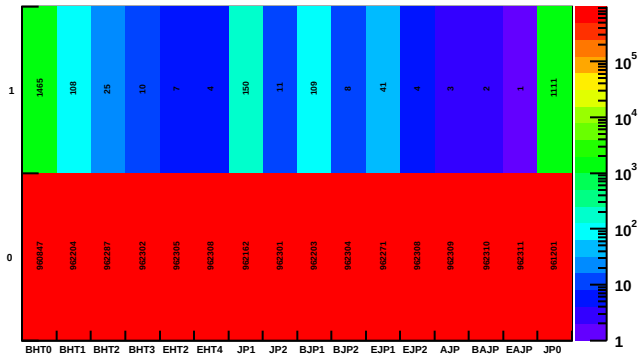
Unused (ch2)

Entries 1.539699e+07



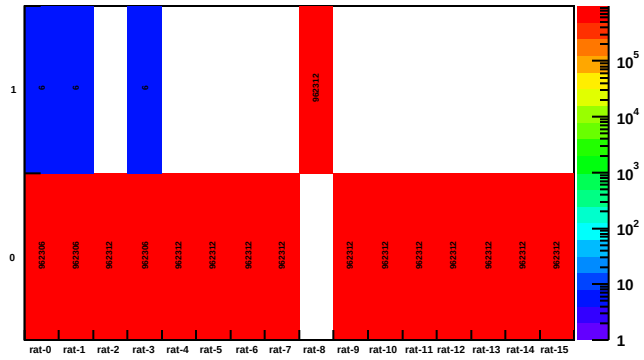
EM201 0-15 (ch3)

Entries 1.539699e+07



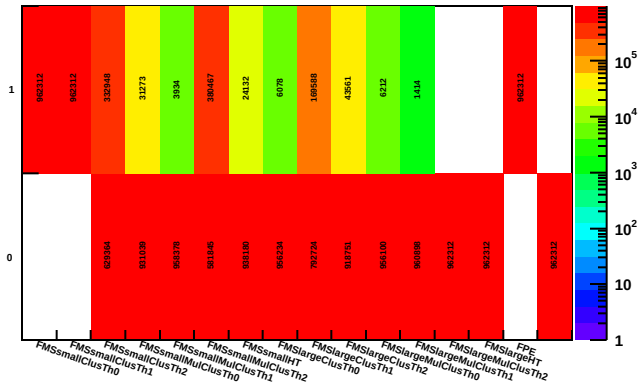
RAT board (ch4)

Entries 1.539699e+07



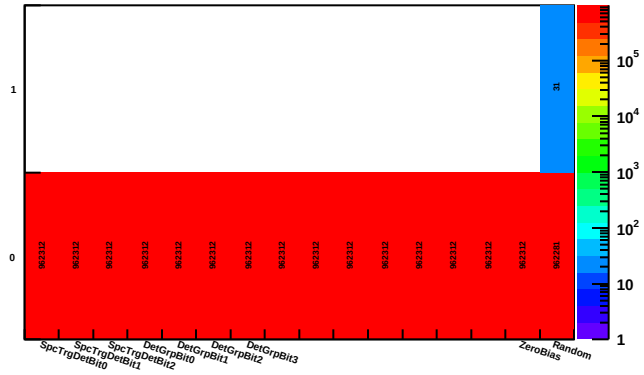
FP201 0-15 (ch5)

Entries 1.539699e+07



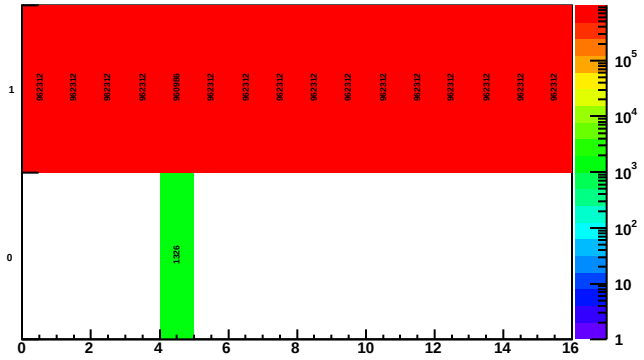
ST201 0-15 (ch6)

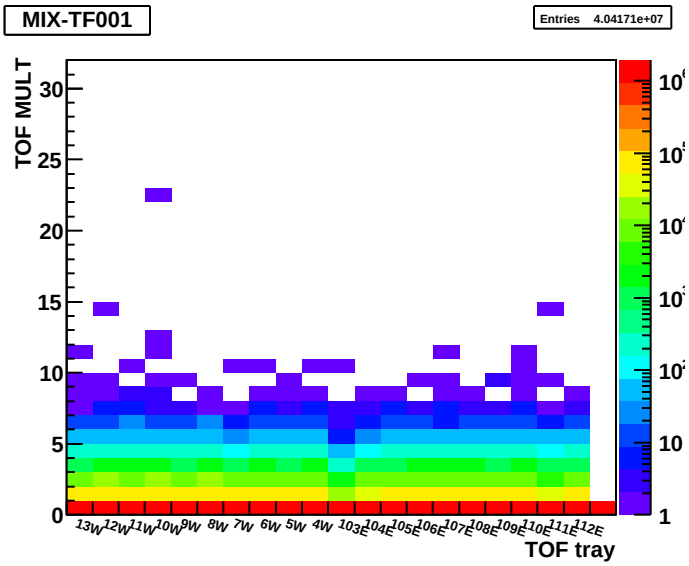
Entries 1.539699e+07

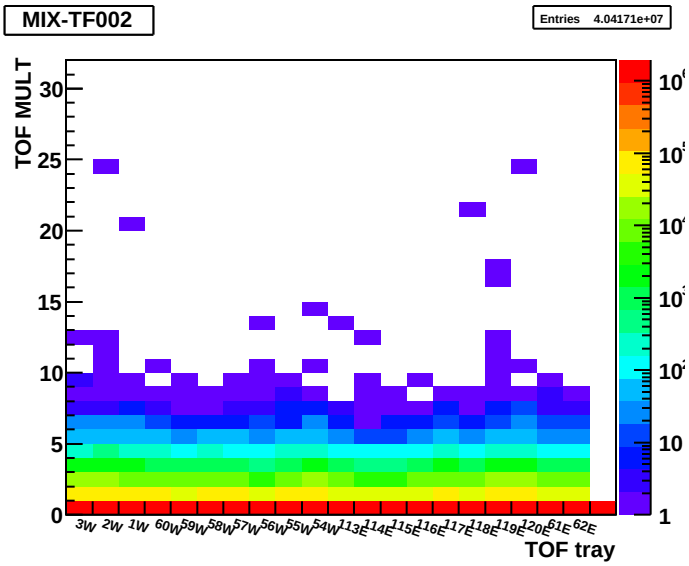


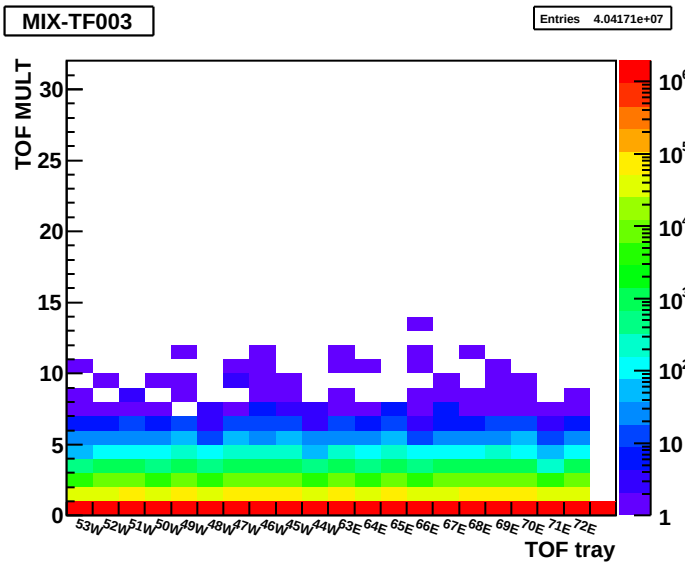
Unused (ch7)

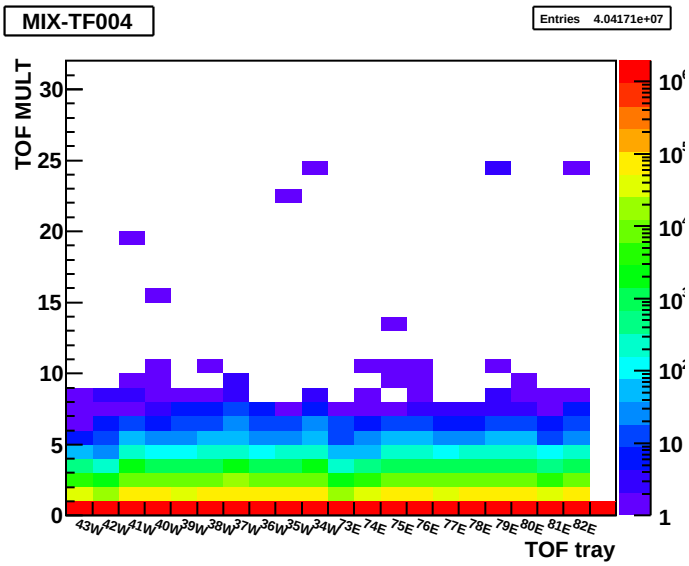
Entries 1.539699e+07

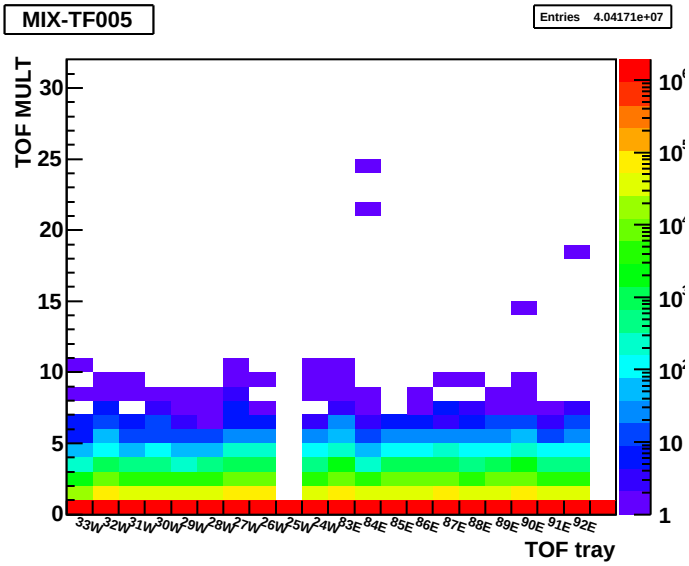


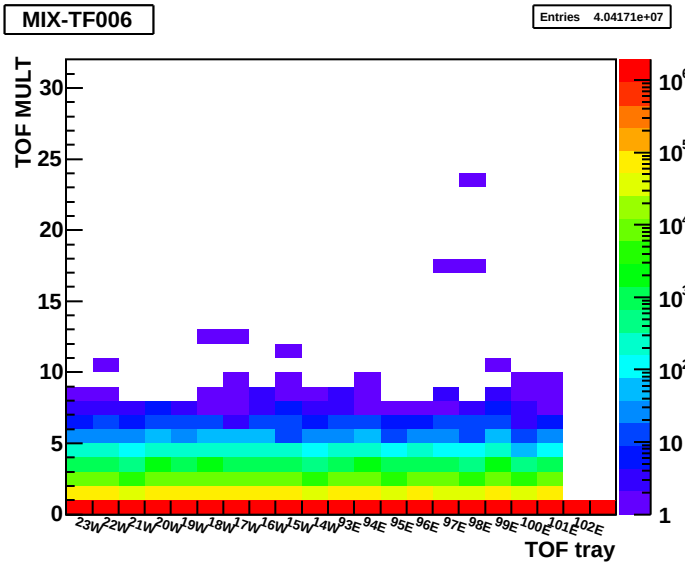

MIX-TF001
Entries 4.04171e+07
TOF MULT
TOF tray

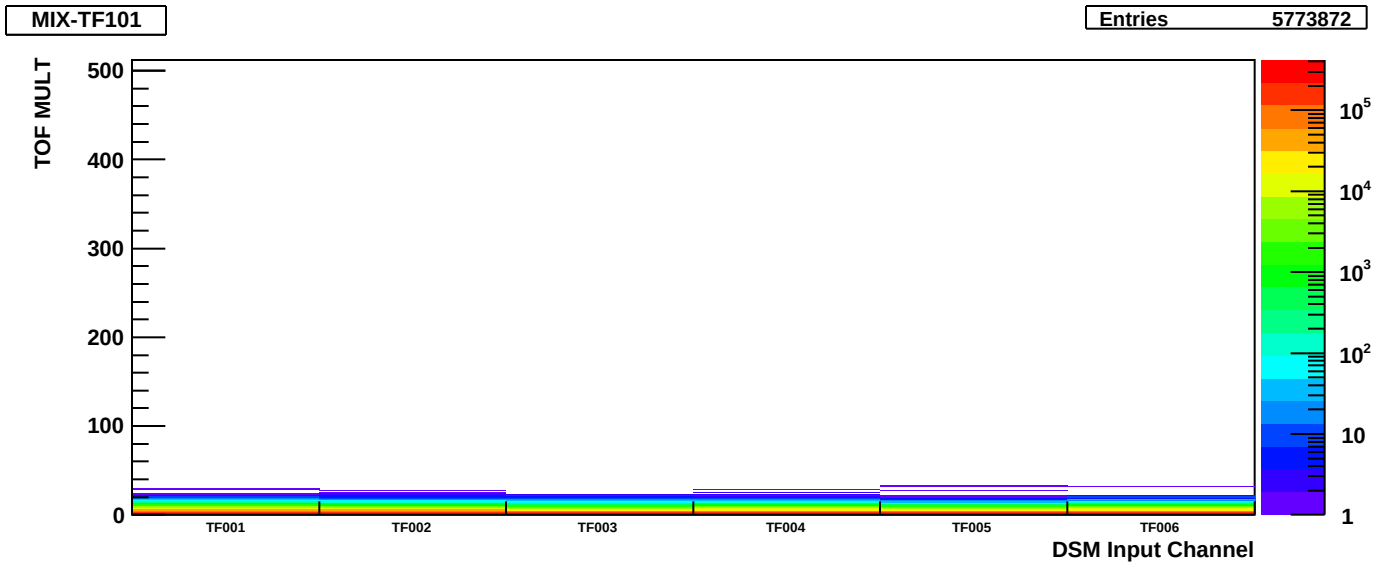

MIX-TF002
Entries 4.04171e+07
TOF MULT
TOF tray

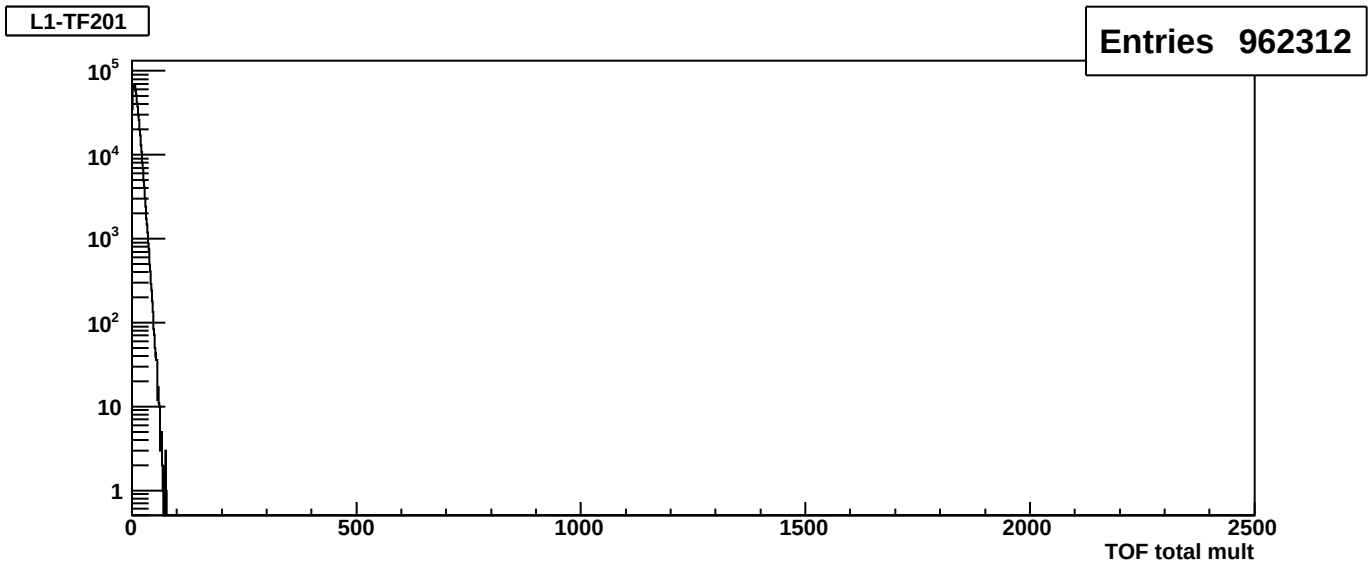

MIX-TF003
Entries 4.04171e+07
TOF MULT
TOF tray

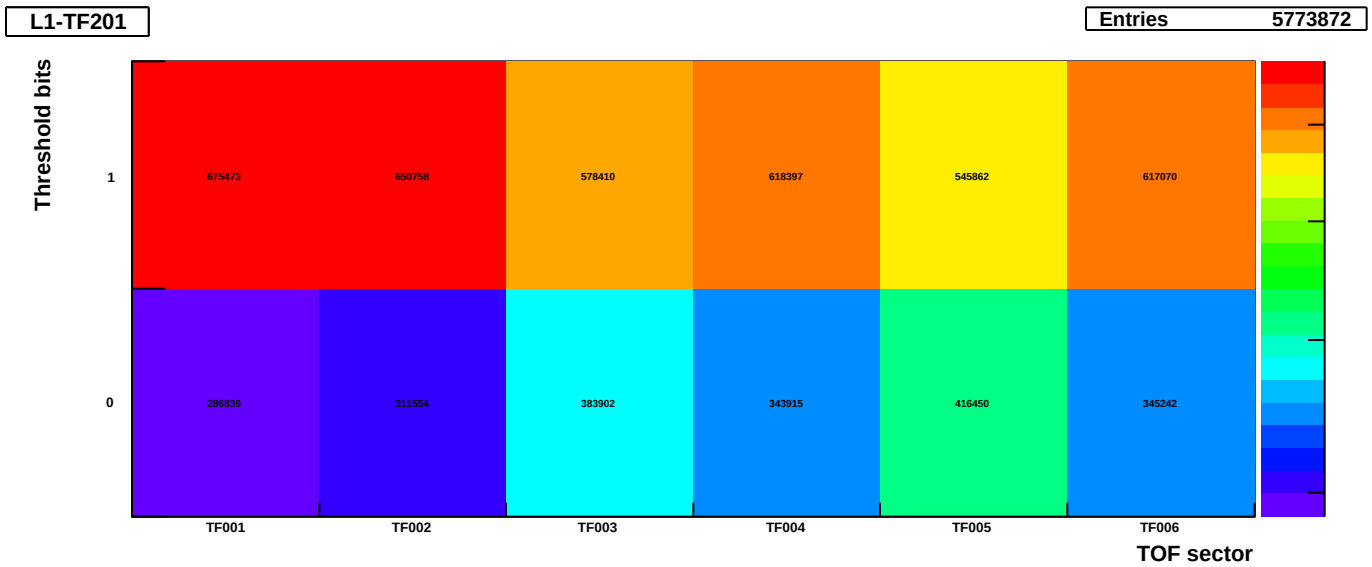

MIX-TF004
Entries 4.04171e+07
TOF MULT
TOF tray

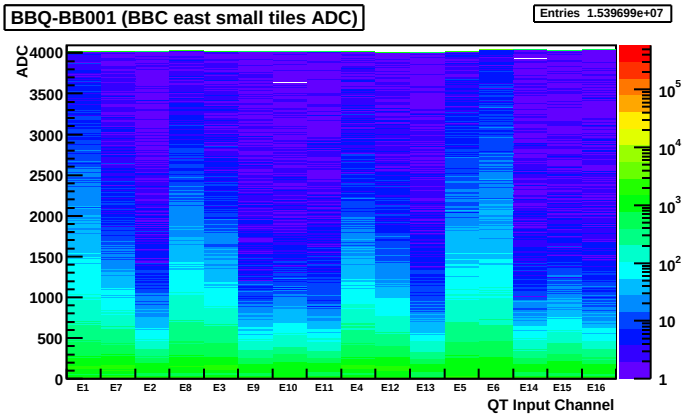

MIX-TF005
Entries 4.04171e+07
TOF MULT
TOF tray

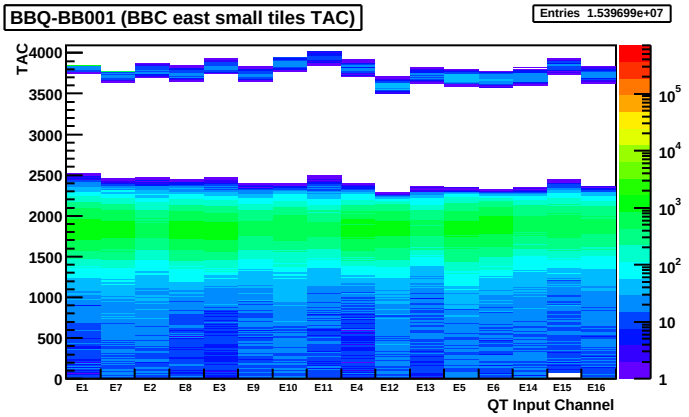

MIX-TF006
Entries 4.04171e+07
TOF MULT
TOF tray

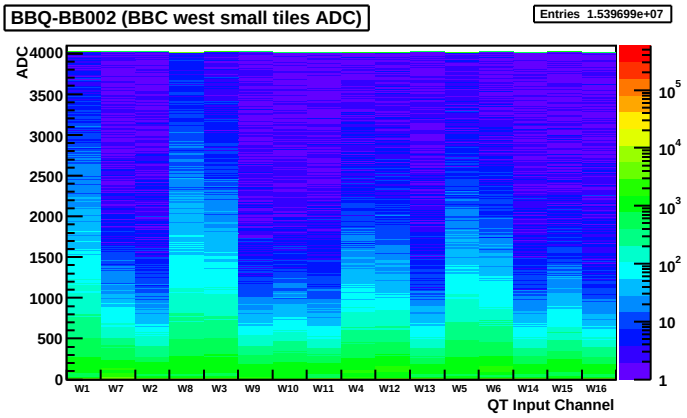

MIX-TF101
Entries 5773872
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

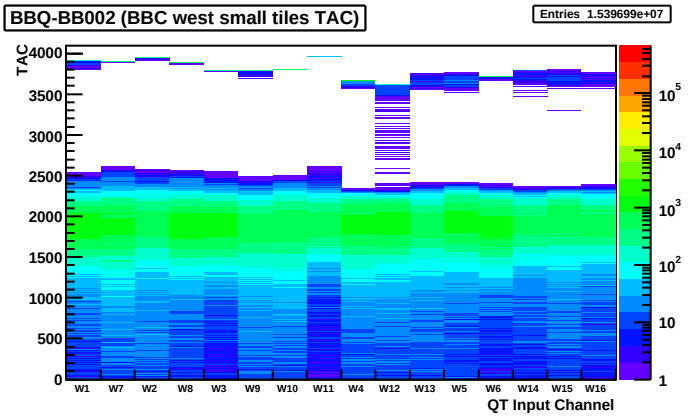

L1-TF201
Entries 962312
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

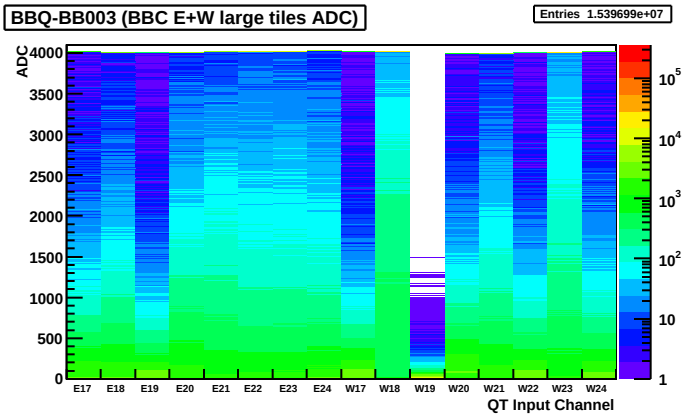

L1-TF201
Entries 5773872
Threshold bits
1
0
675473
650758
578410
618397
545862
617070
286839
311554
383902
343915
416450
345242
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

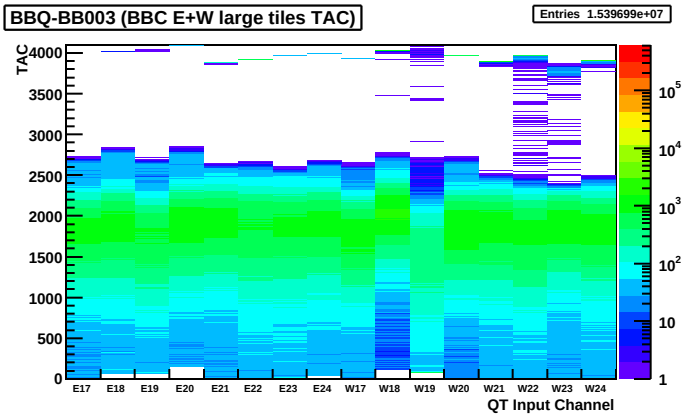

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.539699e+07
ADC
QT Input Channel

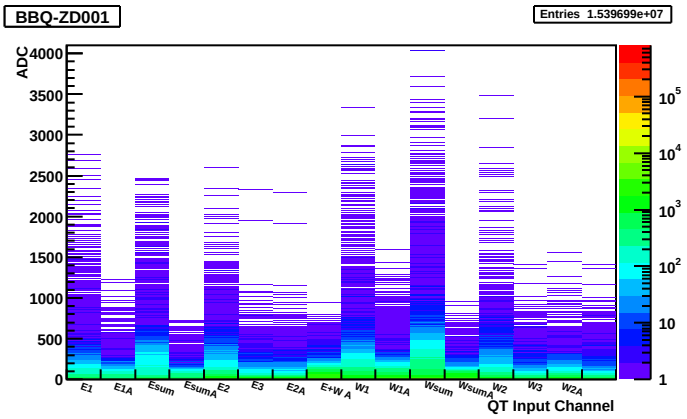

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.539699e+07
TAC
QT Input Channel

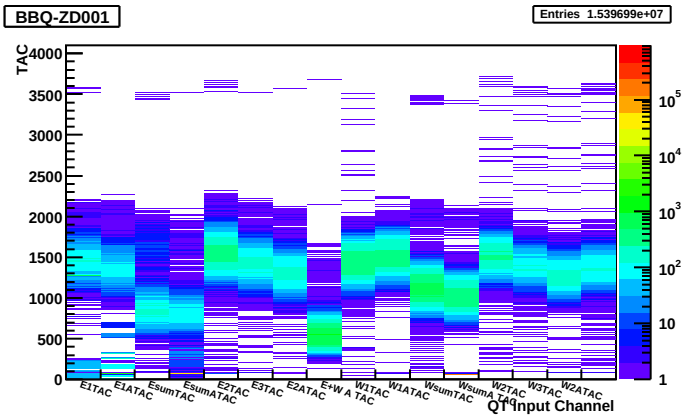

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.539699e+07
ADC
QT Input Channel

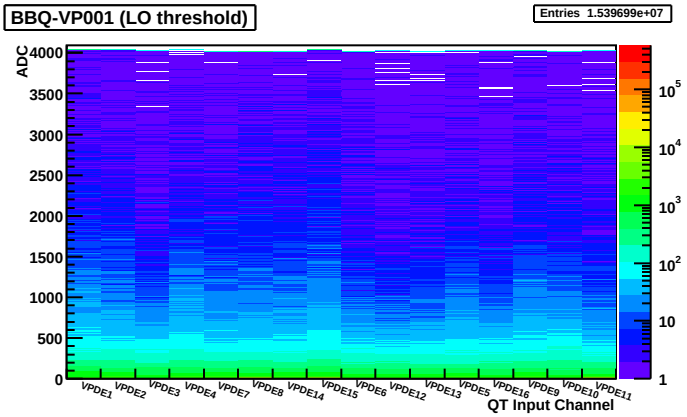

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.539699e+07
TAC
QT Input Channel

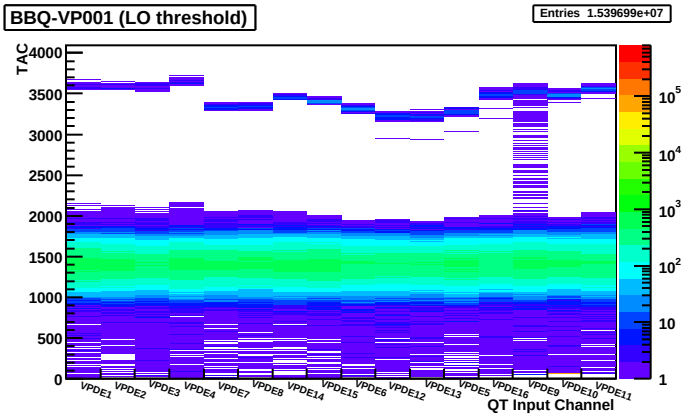

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.539699e+07
ADC
QT Input Channel

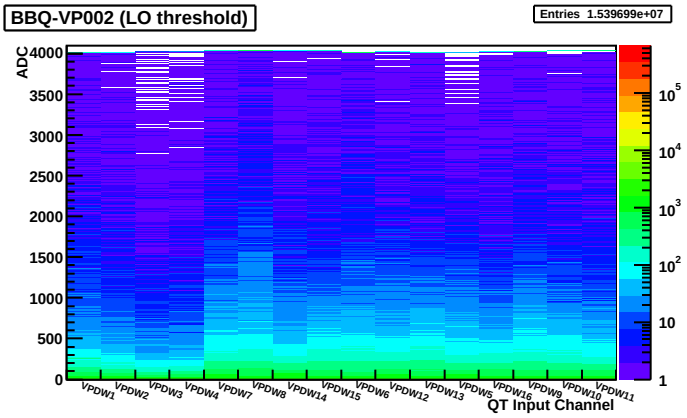

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.539699e+07
TAC
QT Input Channel

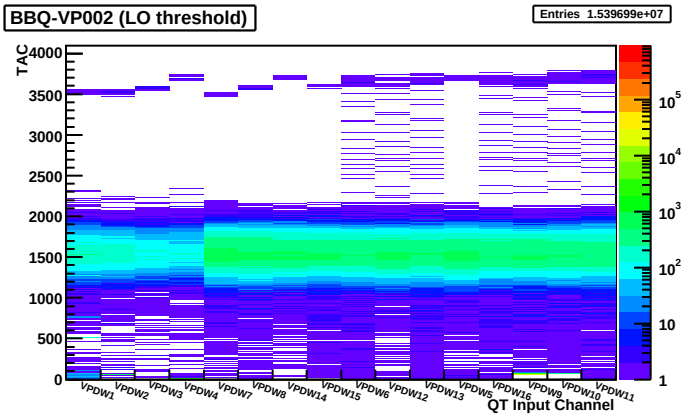

BBQ-ZD001
Entries 1.539699e+07
ADC
QT Input Channel

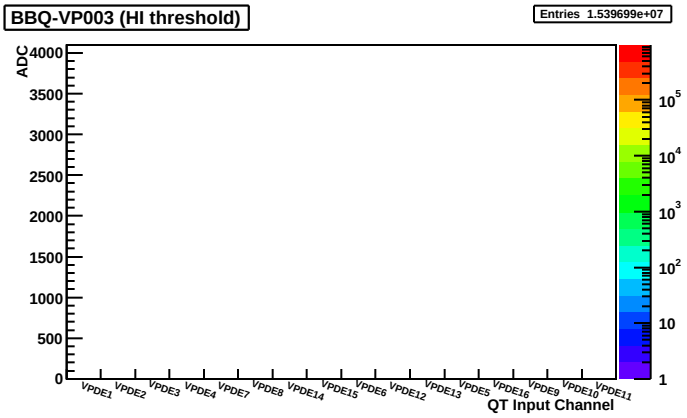

BBQ-ZD001
Entries 1.539699e+07
TAC
QT Input Channel

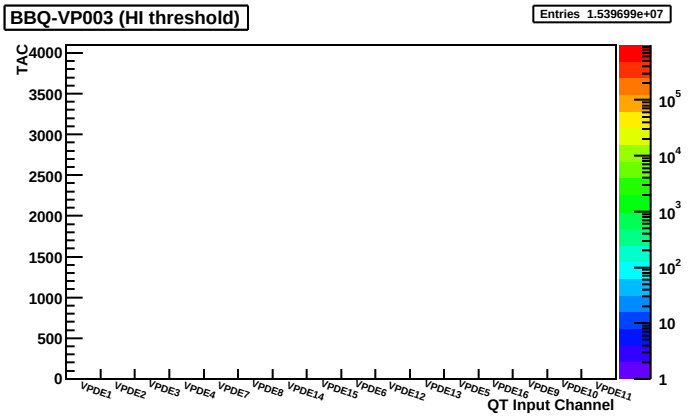

BBQ-VP001 (LO threshold)
Entries 1.539699e+07
ADC
QT Input Channel

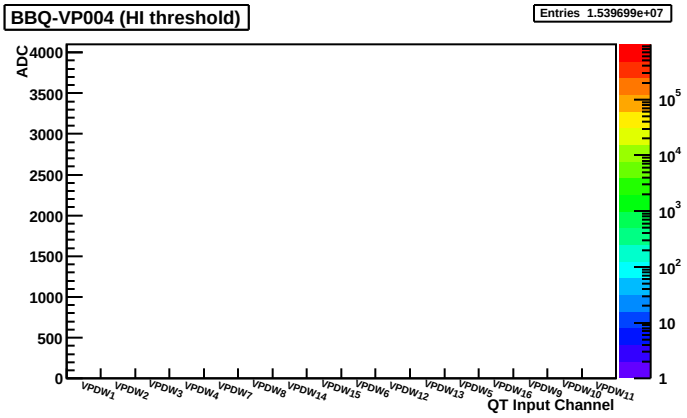

BBQ-VP001 (LO threshold)
Entries 1.539699e+07
TAC
QT Input Channel

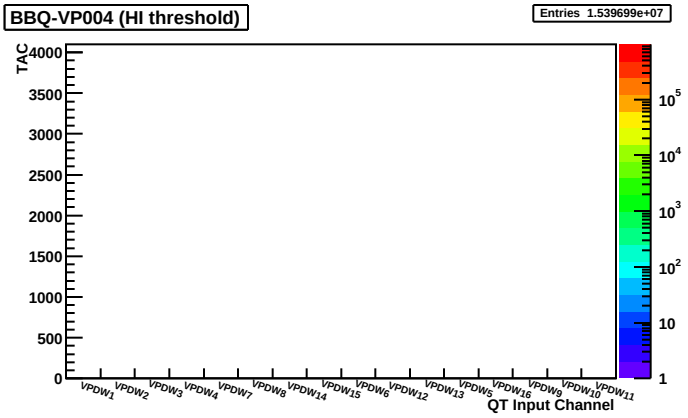

BBQ-VP002 (LO threshold)
Entries 1.539699e+07
ADC
QT Input Channel

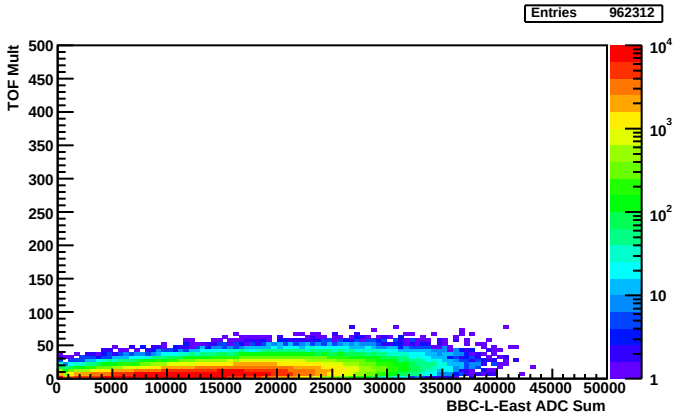

BBQ-VP002 (LO threshold)
Entries 1.539699e+07
TAC
QT Input Channel

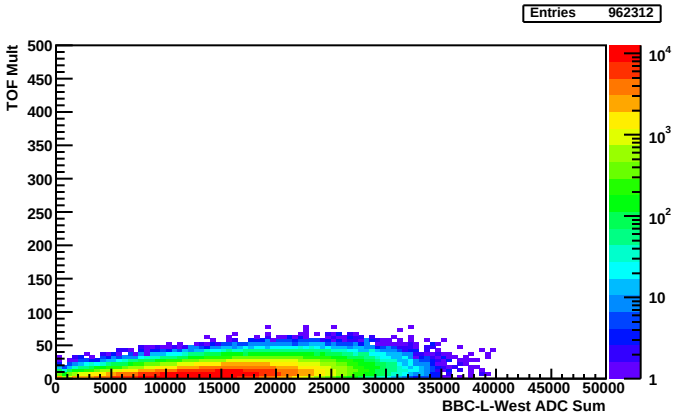

BBQ-VP003 (HI threshold)
Entries 1.539699e+07
ADC
QT Input Channel

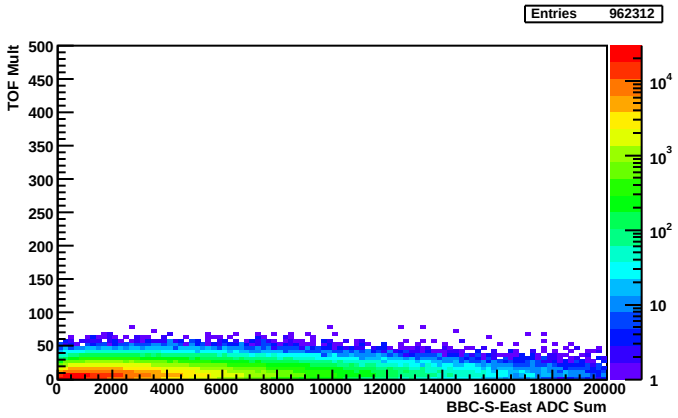

BBQ-VP003 (HI threshold)
Entries 1.539699e+07
TAC
QT Input Channel

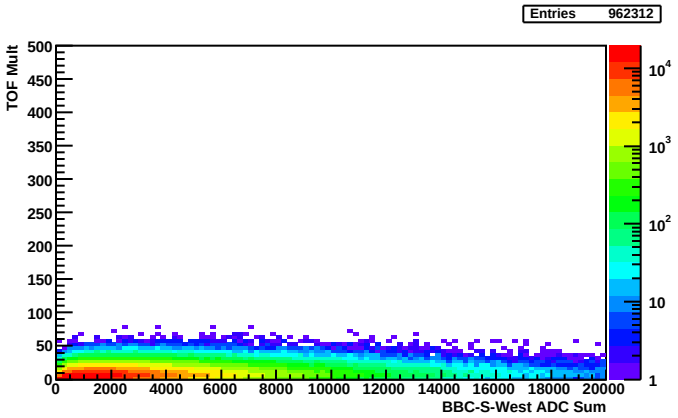

BBQ-VP004 (HI threshold)
Entries 1.539699e+07
ADC
QT Input Channel

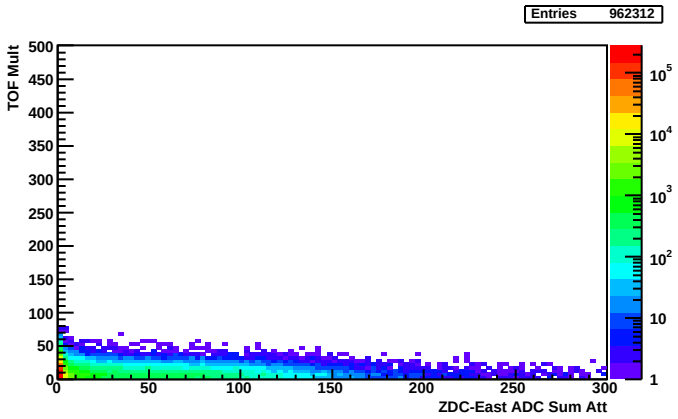

BBQ-VP004 (HI threshold)
Entries 1.539699e+07
TAC
QT Input Channel

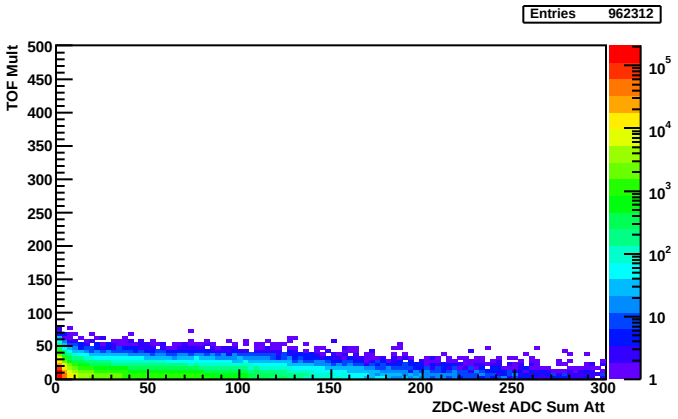

Entries 962312
TOF Mult
BBC-L-East ADC Sum

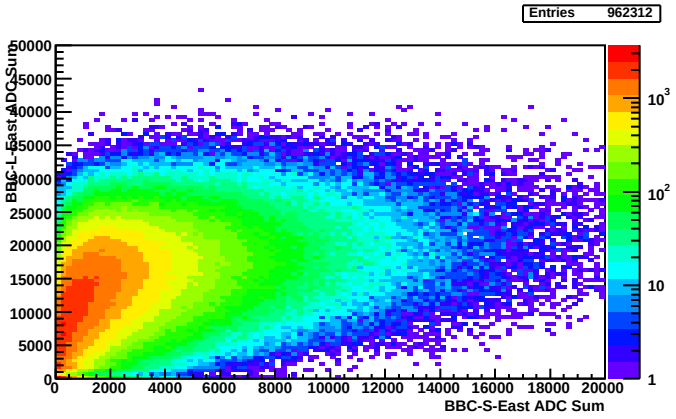

Entries 962312
TOF Mult
BBC-L-West ADC Sum

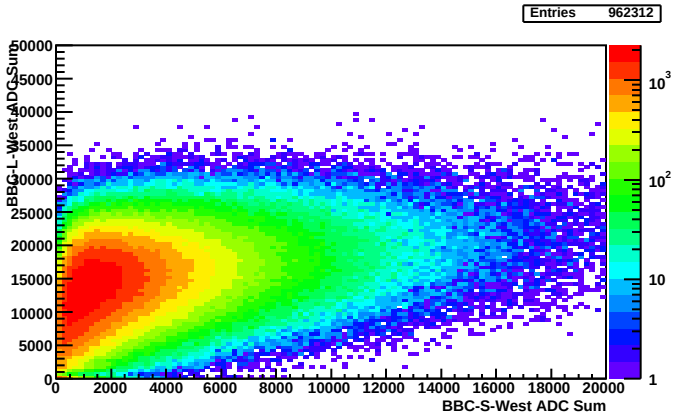

Entries 962312
TOF Mult
BBC-S-East ADC Sum

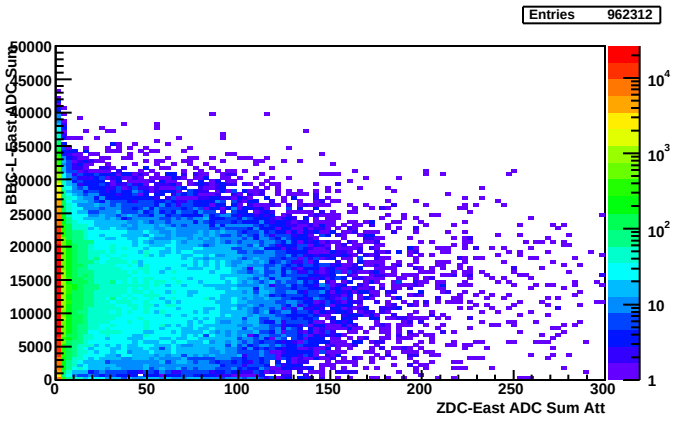

Entries 962312
TOF Mult
BBC-S-West ADC Sum

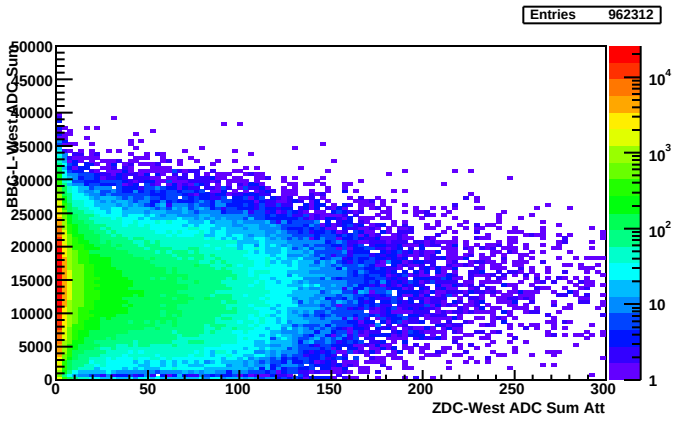

Entries 962312
TOF Mult
ZDC-East ADC Sum Att

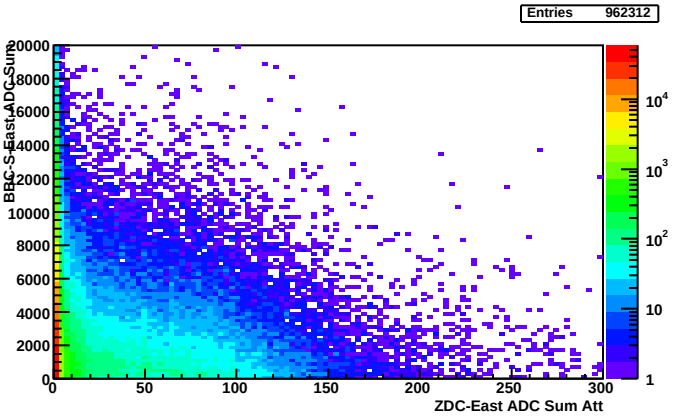

Entries 962312
TOF Mult
ZDC-West ADC Sum Att

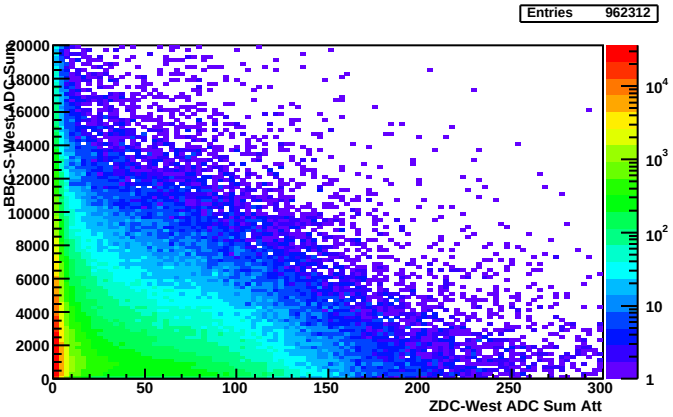

Entries 962312
BBC-L-East ADC Sum
BBC-S-East ADC Sum

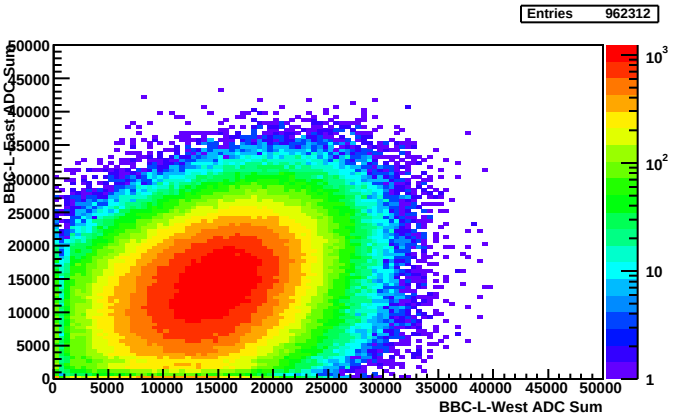

Entries 962312
BBC-L-West ADC Sum
BBC-S-West ADC Sum

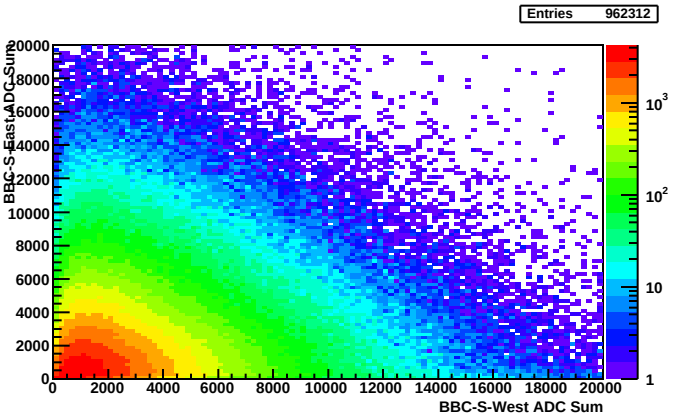

Entries 962312
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

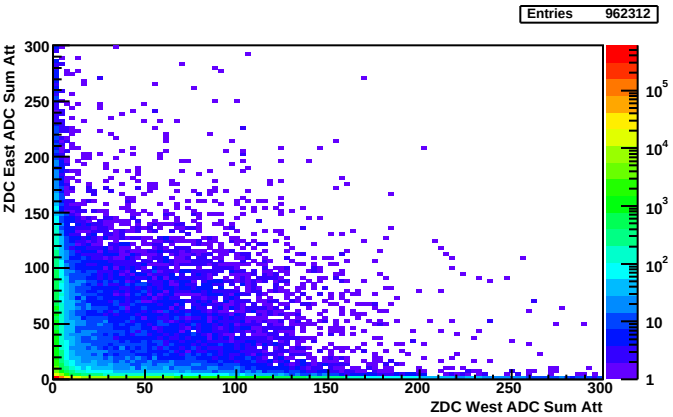

Entries 962312
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

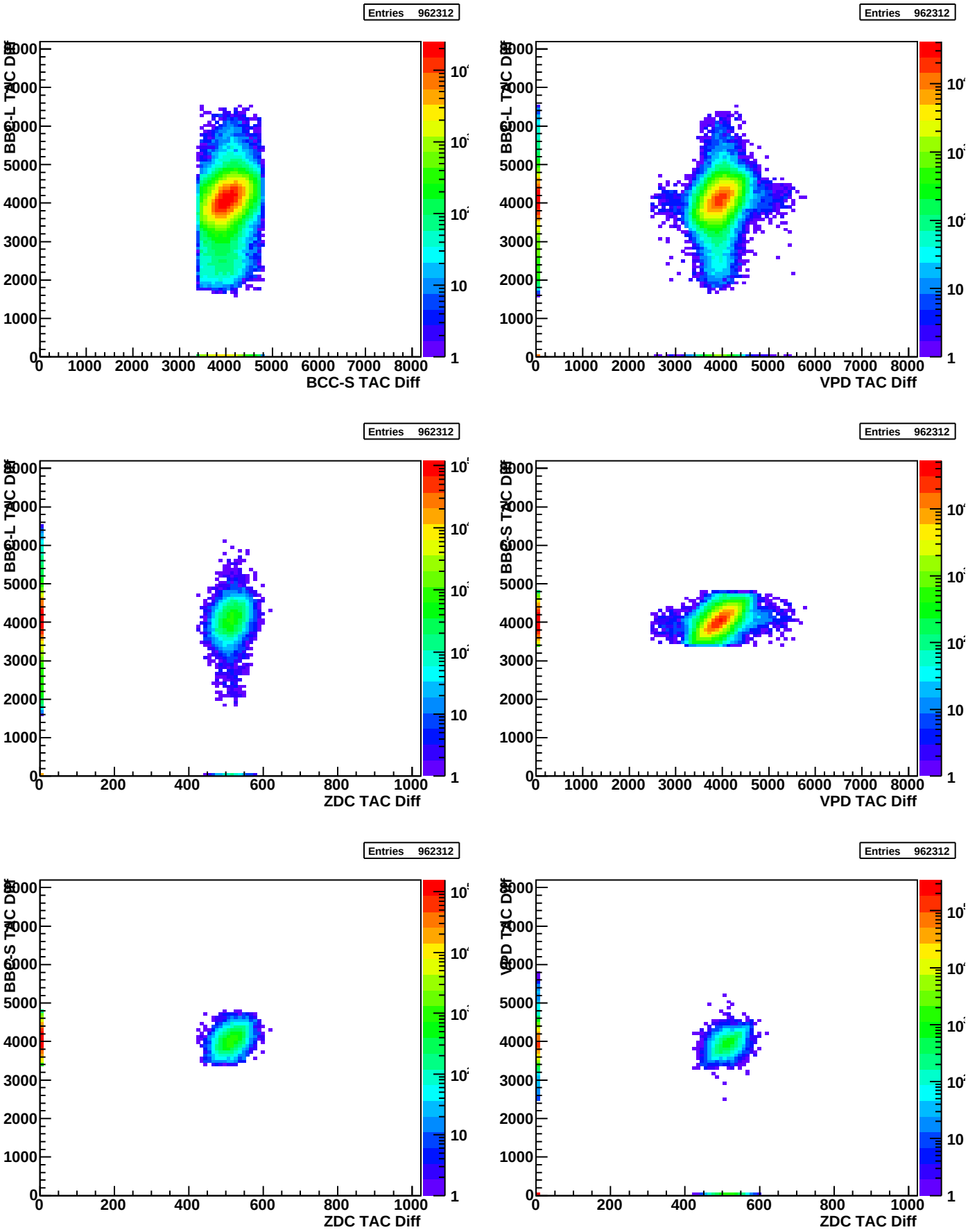

Entries 962312
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

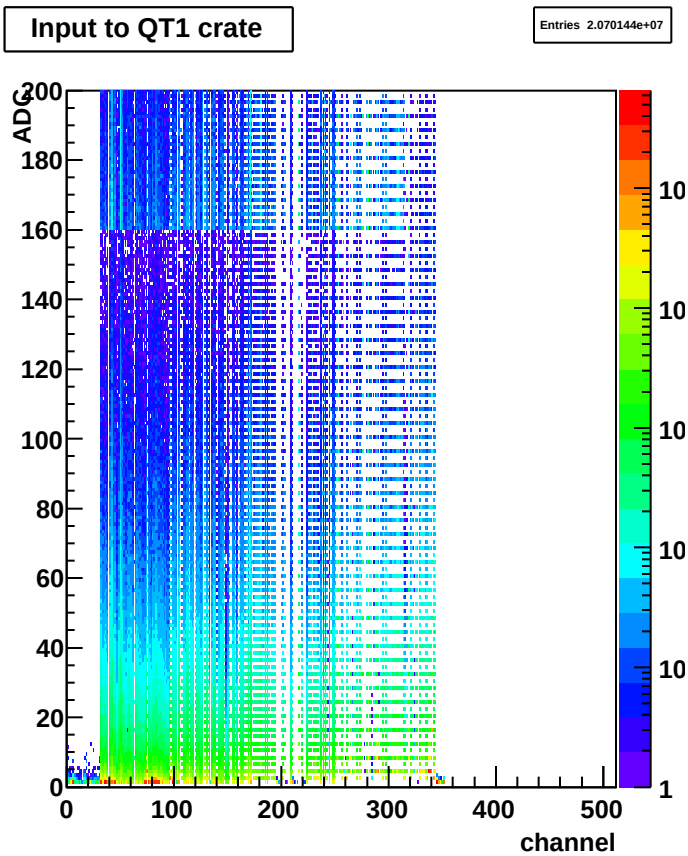

Entries 962312
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

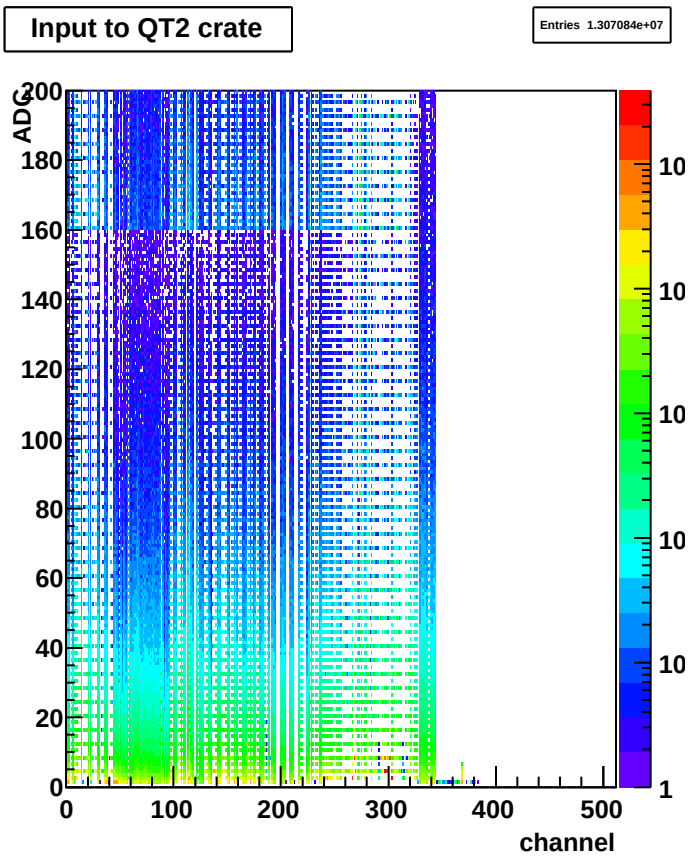

Entries 962312
BBC-L-East ADC Sum
BBC-L-West ADC Sum

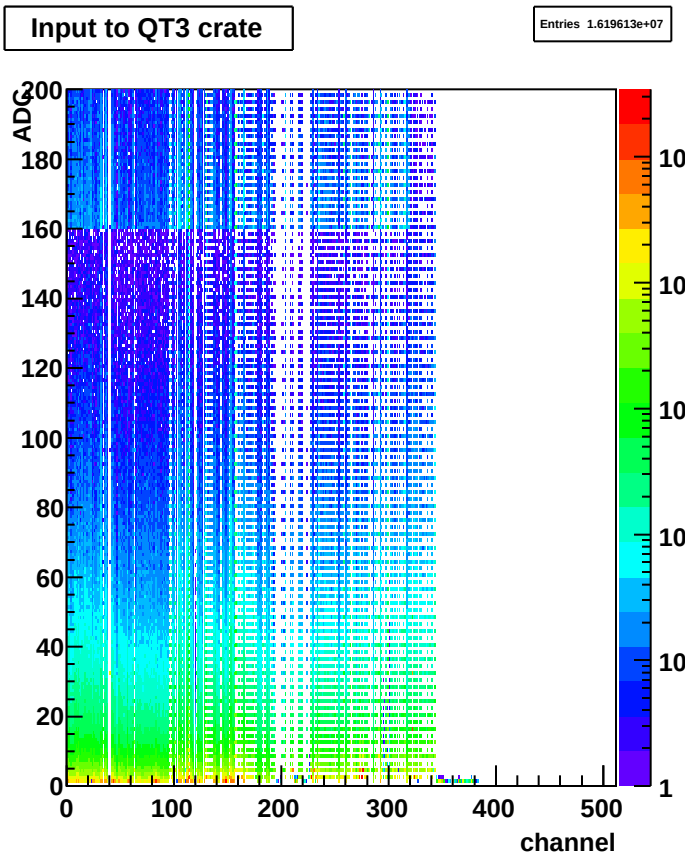

Entries 962312
BBC-S-East ADC Sum
BBC-S-West ADC Sum

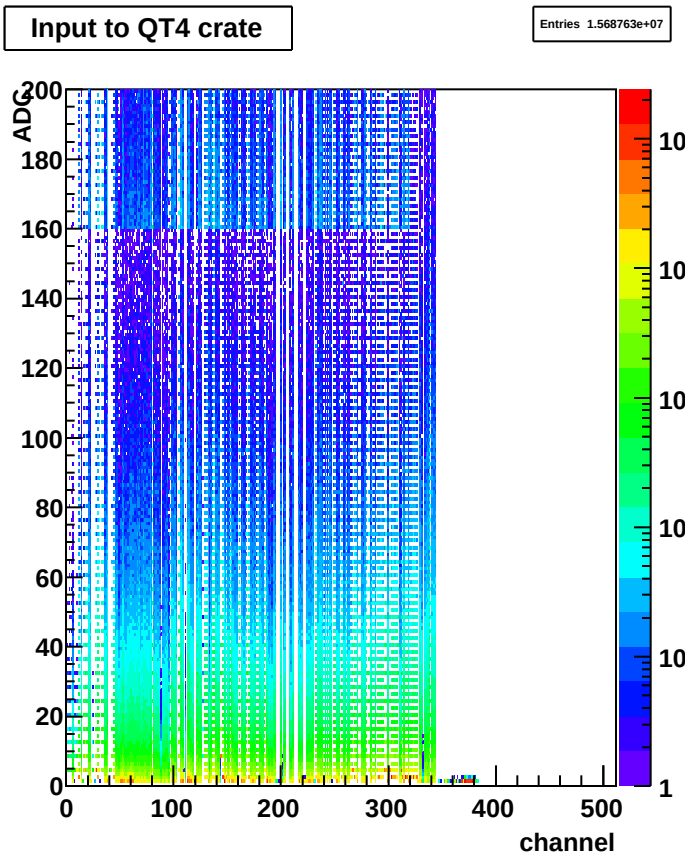

Entries 962312
ZDC East ADC Sum Att
ZDC West ADC Sum Att

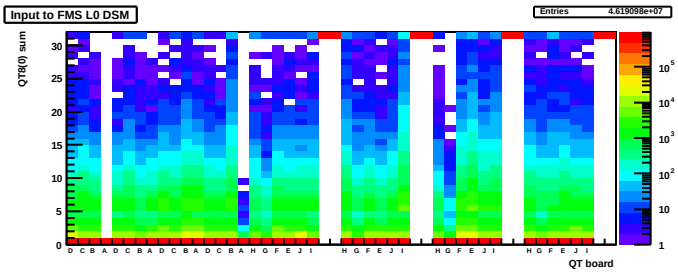

Entries 962312
BBC-L TAC Diff
BCC-S TAC Diff
Entries 962312
BBC-L TAC Diff
VPD TAC Diff
Entries 962312
BBC-L TAC Diff
ZDC TAC Diff
Entries 962312
BBC-S TAC Diff
VPD TAC Diff
Entries 962312
BBC-S TAC Diff
ZDC TAC Diff
Entries 962312
VPD TAC Diff
ZDC TAC Diff

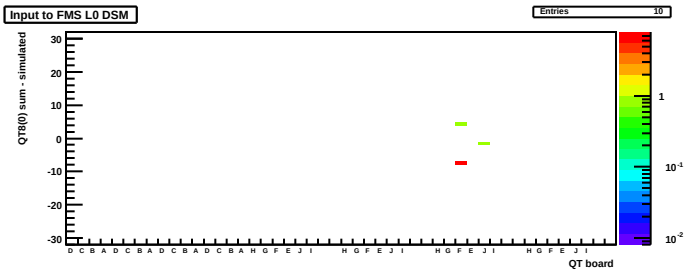

Input to QT1 crate
Entries 2.070144e+07
ADC
channel

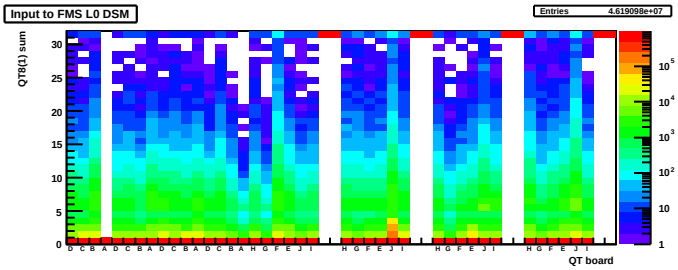

Input to QT2 crate
Entries 1.307084e+07
ADC
channel

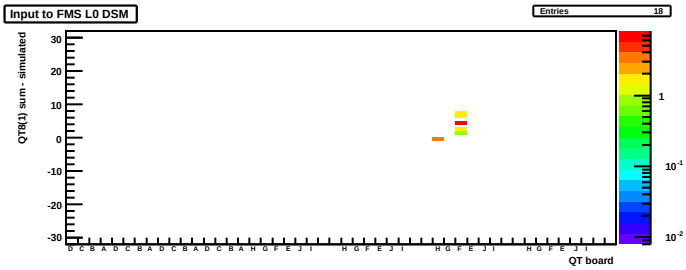

Input to QT3 crate
Entries 1.619613e+07
ADC
channel

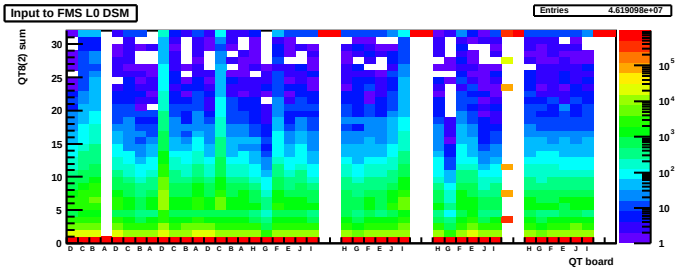

Input to QT4 crate
Entries 1.568763e+07
ADC
channel

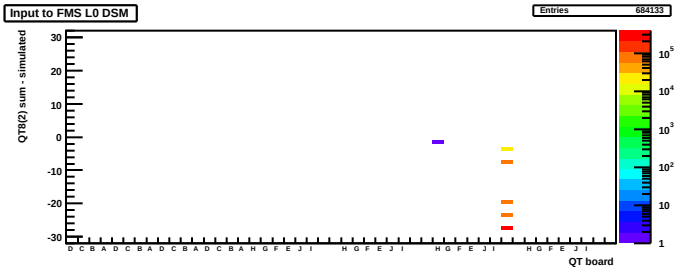

Input to FMS L0 DSM
Entries 4.619098e+07
QT8(0) sum
QT board

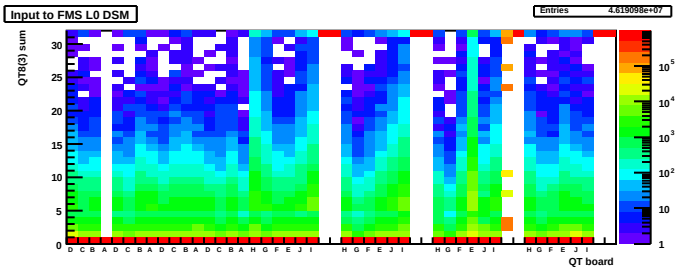

Input to FMS L0 DSM
Entries 10
QT8(0) sum - simulated
QT board

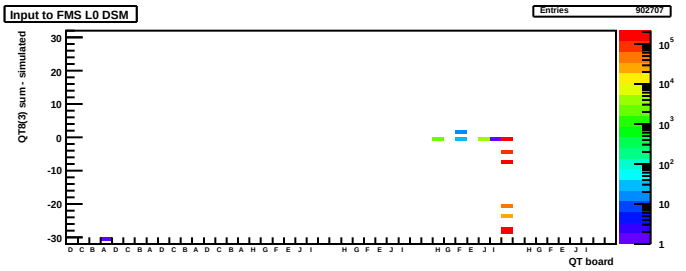

Input to FMS L0 DSM
Entries 4.619098e+07
QT8(1) sum
QT board

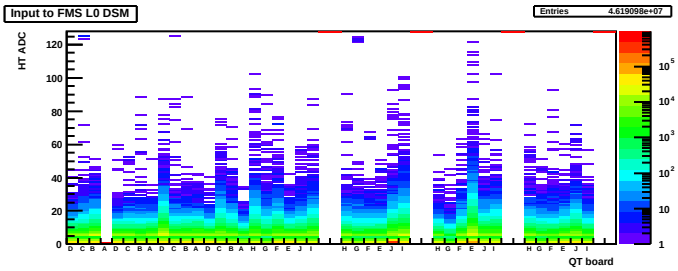

Input to FMS L0 DSM
Entries 18
QT8(1) sum - simulated
QT board

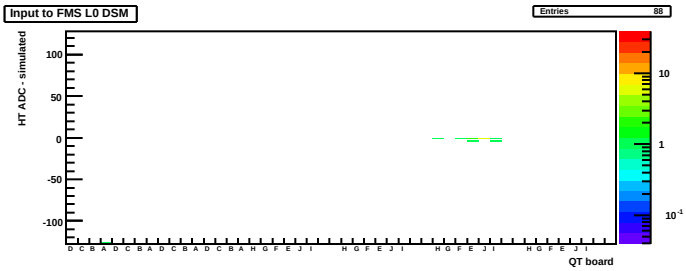

Input to FMS L0 DSM
Entries 4.619098e+07
QT8(2) sum
QT board

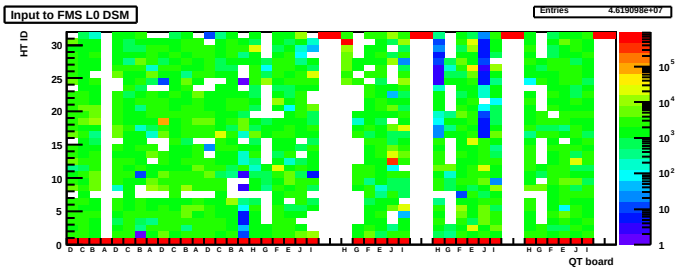

Input to FMS L0 DSM
Entries 684133
QT8(2) sum - simulated
QT board

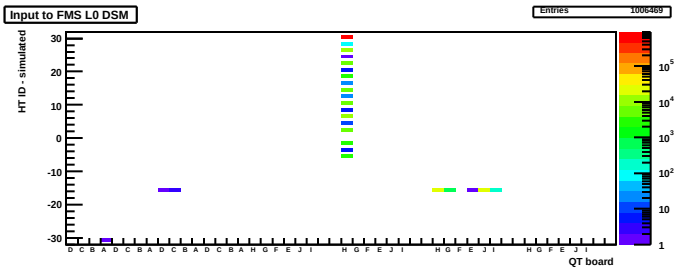

Input to FMS L0 DSM
Entries 4.619098e+07
QT8(3) sum
QT board

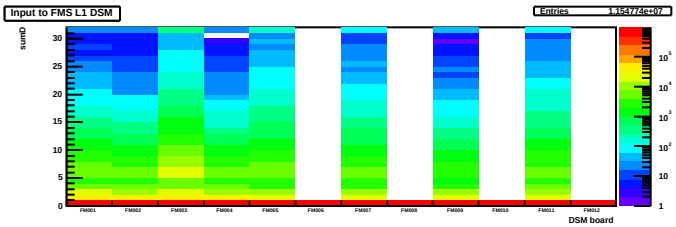

Input to FMS L0 DSM
Entries 902707
QT8(3) sum - simulated
QT board

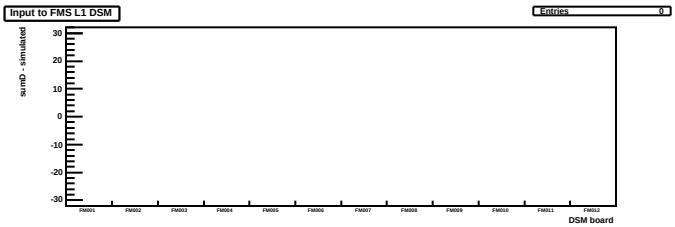

Input to FMS L0 DSM
Entries 4.619098e+07
HT ADC
QT board

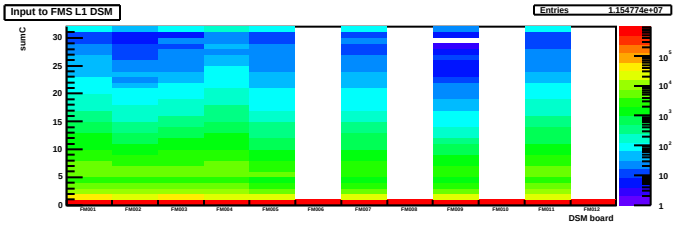

Input to FMS L0 DSM
Entries 88
HT ADC - simulated
QT board

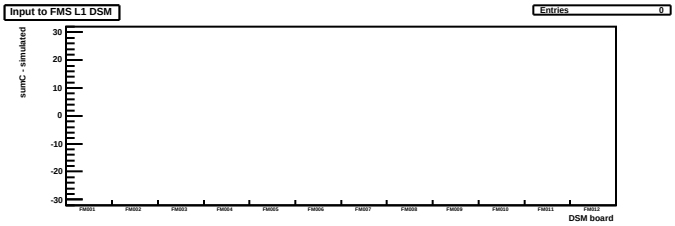

Input to FMS L0 DSM
Entries 4.619098e+07
HT ID
QT board

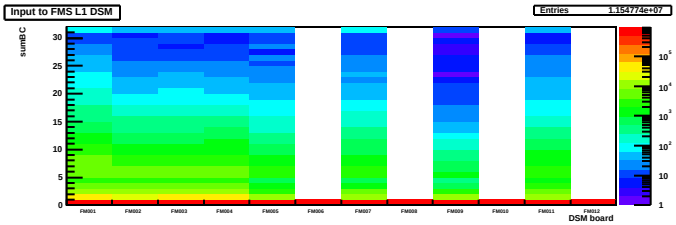

Input to FMS L0 DSM
Entries 1006469
HT ID - simulated
QT board

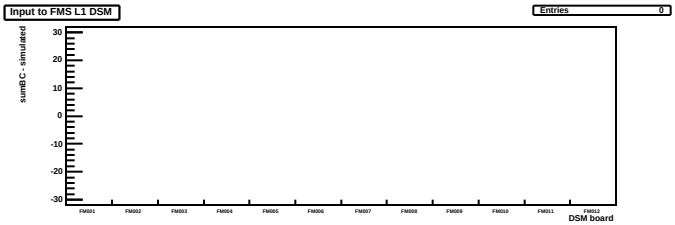

Input to FMS L1 DSM
Entries 1.154774e+07
sumD
DSM board

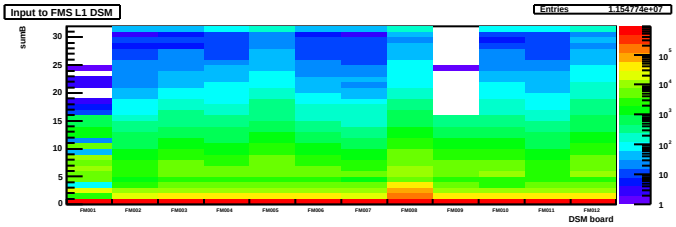

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

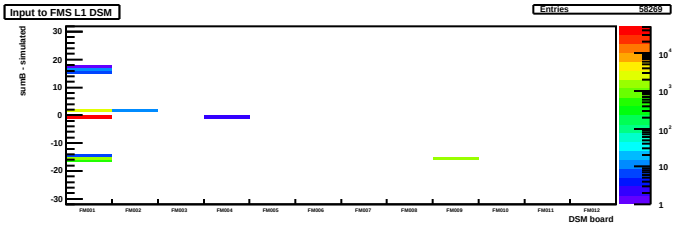

Input to FMS L1 DSM
Entries 1.154774e+07
sumC
DSM board

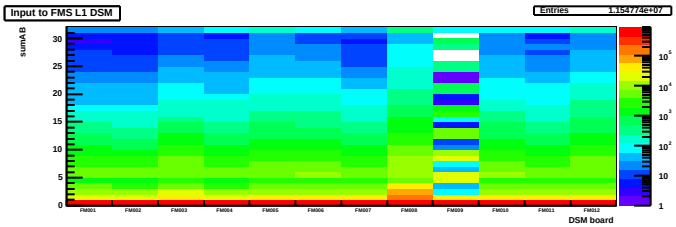

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

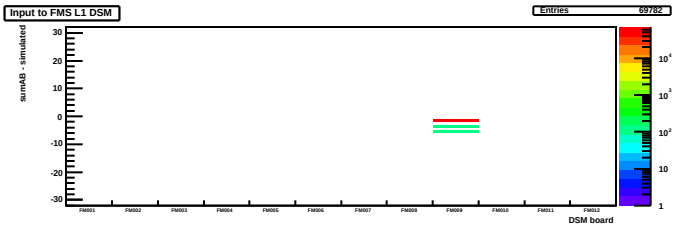

Input to FMS L1 DSM
Entries 1.154774e+07
sumBC
DSM board

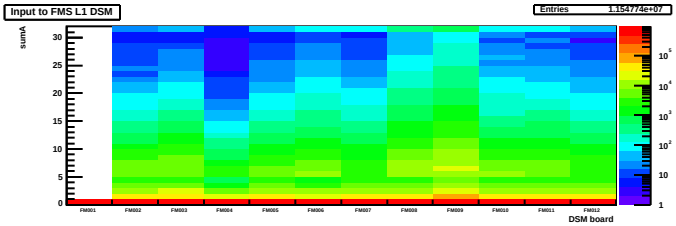

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

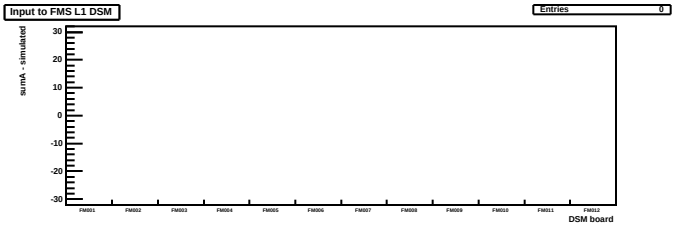

Input to FMS L1 DSM
Entries 1.154774e+07
sumB
DSM board

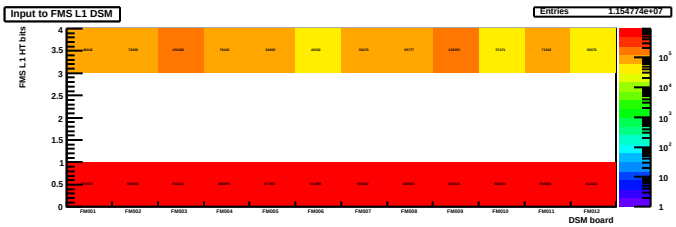

Input to FMS L1 DSM
Entries 58269
sumB - simulated
DSM board

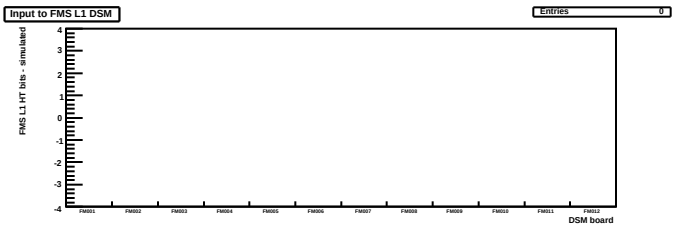

Input to FMS L1 DSM
Entries 1.154774e+07
sumAB
DSM board

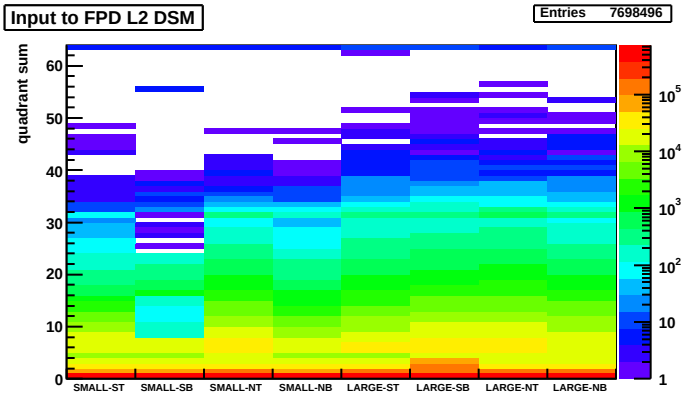

Input to FMS L1 DSM
Entries 69782
sumAB - simulated
DSM board

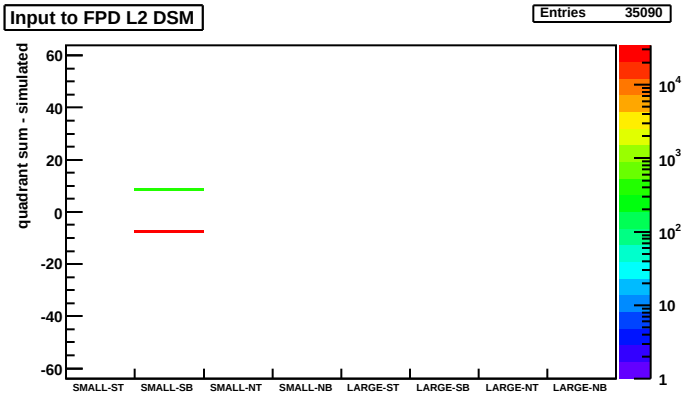

Input to FMS L1 DSM
Entries 1.154774e+07
sumA
DSM board

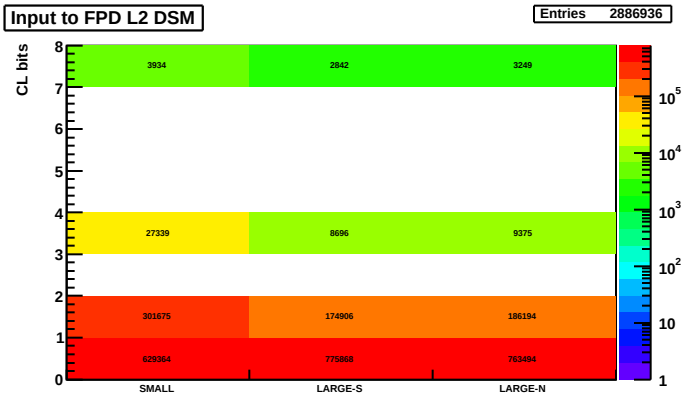

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

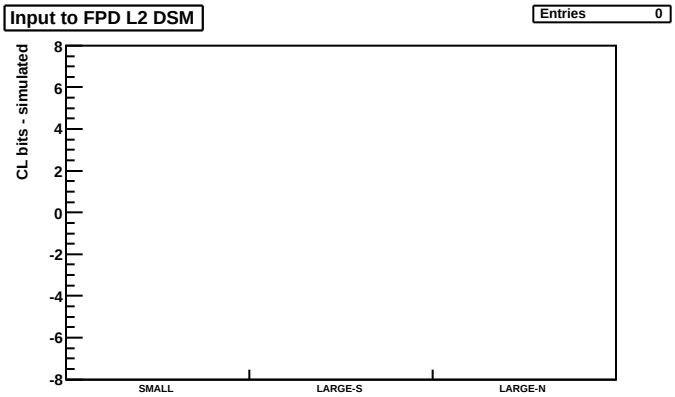

Input to FMS L1 DSM
Entries 1.154774e+07
FMS L1 HT bits
DSM board

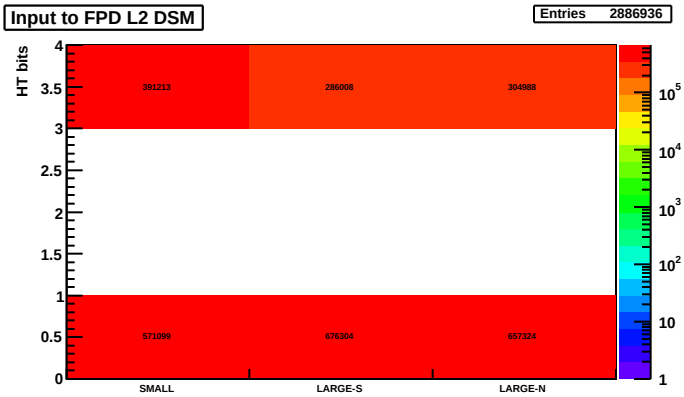

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

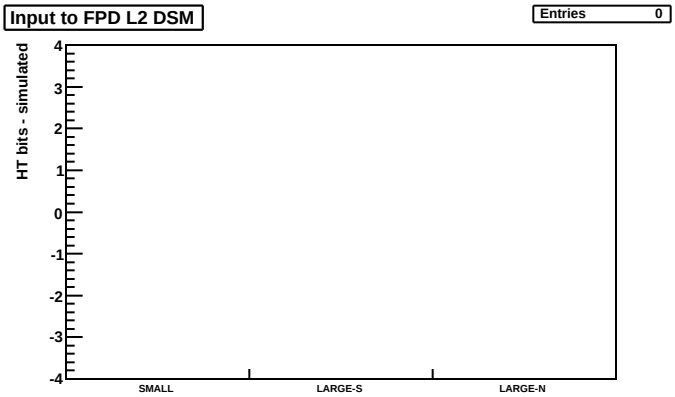

Input to FPD L2 DSM
Entries 7698496
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

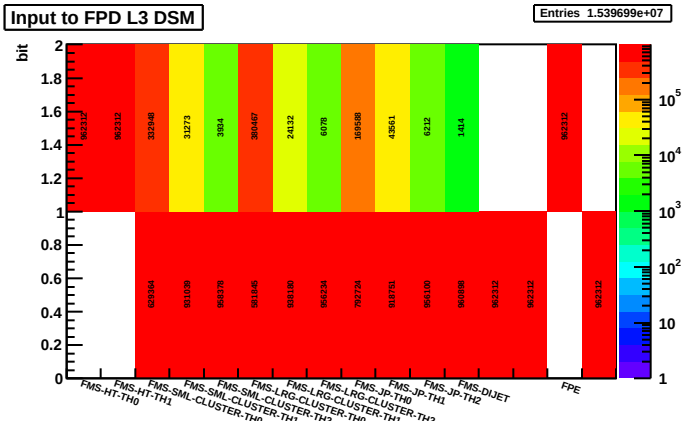

Input to FPD L2 DSM
Entries 35090
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

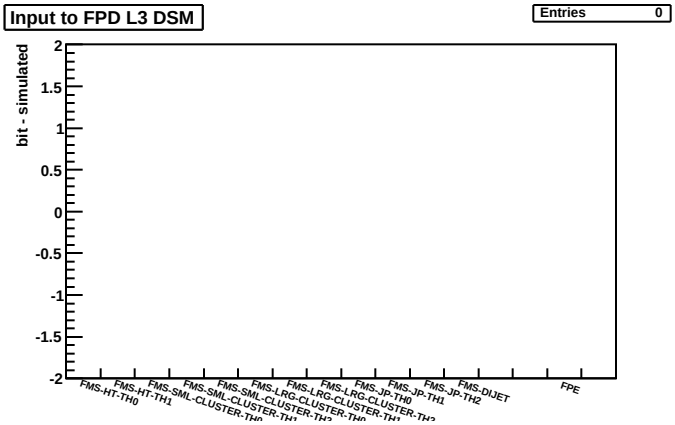

Input to FPD L2 DSM
Entries 2886936
CL bits
3934
2842
3249
27339
8696
9375
301675
174906
186194
629364
775868
763494
SMALL
LARGE-S
LARGE-N

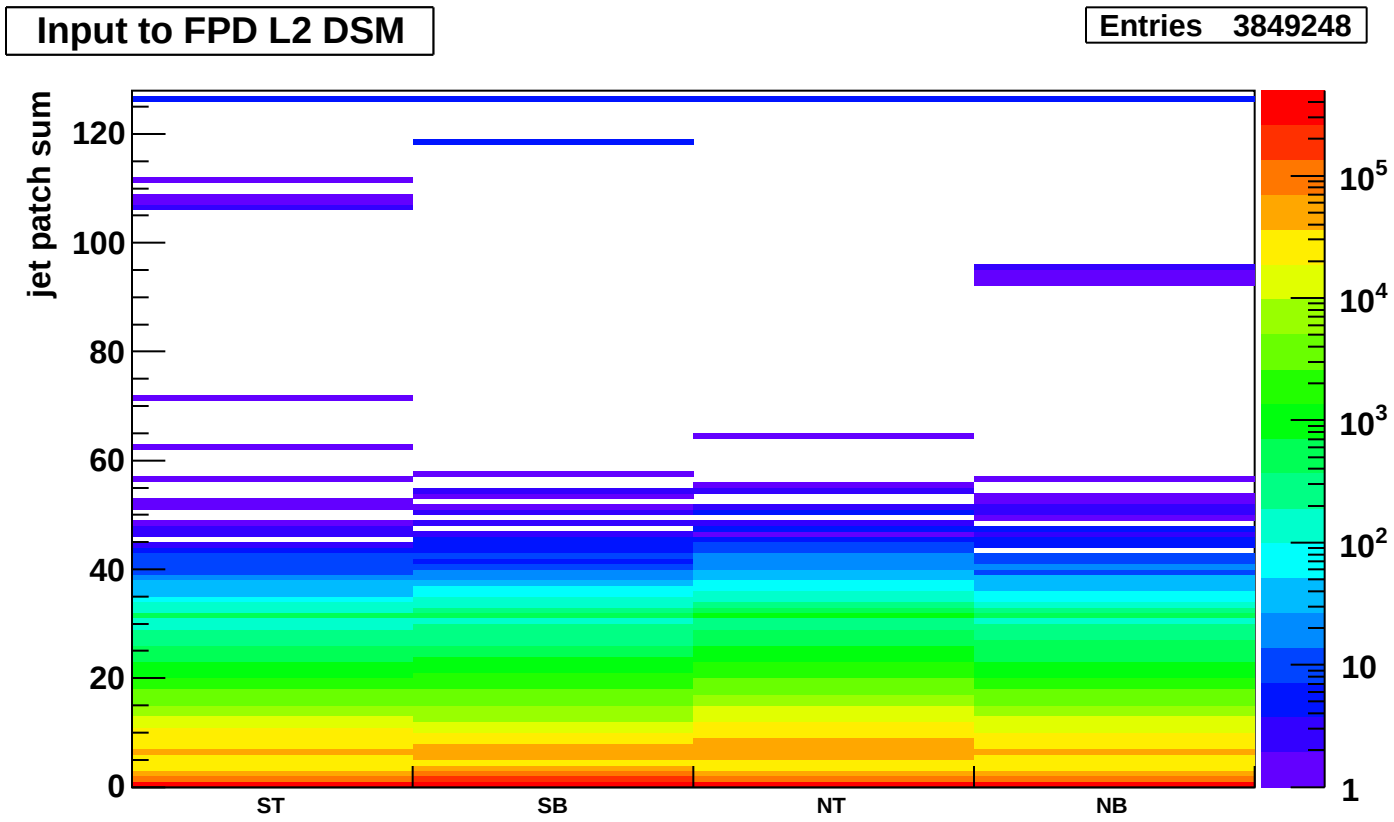

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

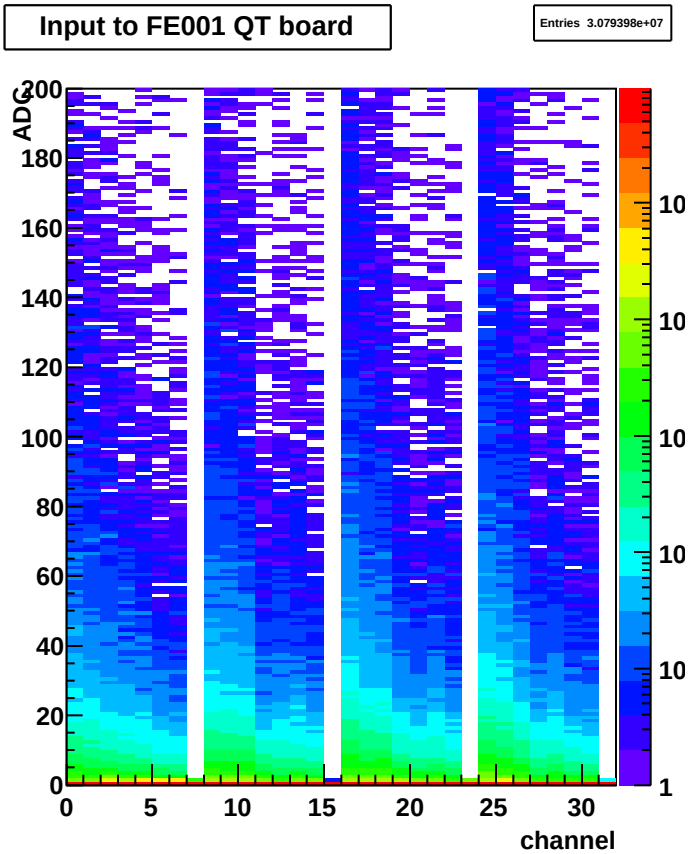

Input to FPD L2 DSM
Entries 2886936
HT bits
391213
286008
304988
571099
676304
657324
SMALL
LARGE-S
LARGE-N

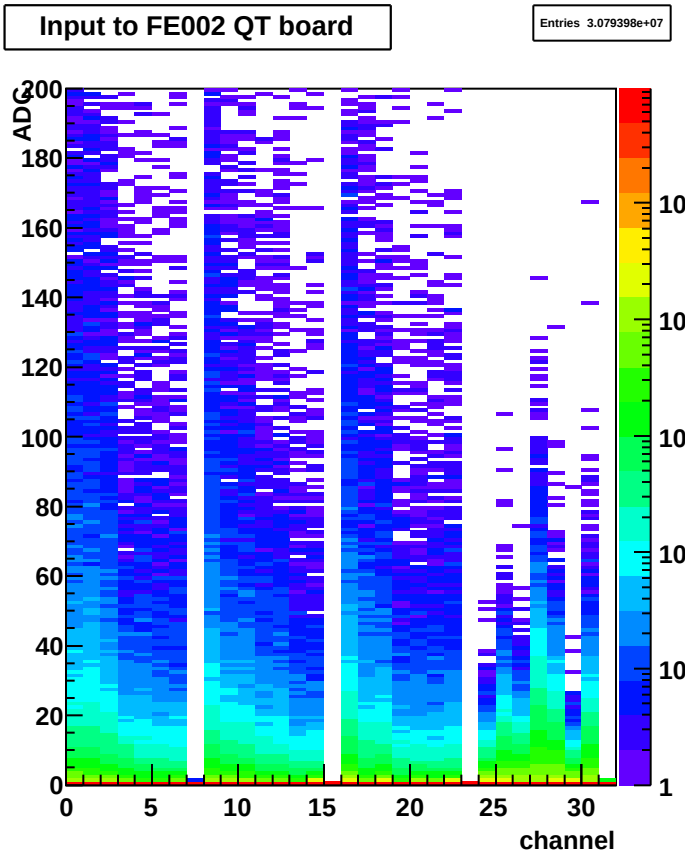

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

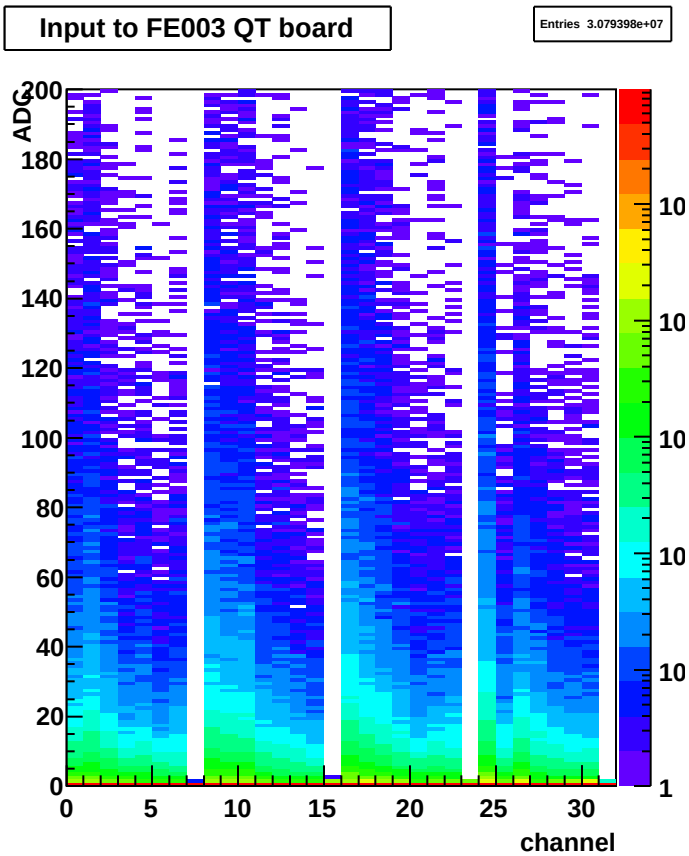

Input to FPD L3 DSM
Entries 1.539699e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

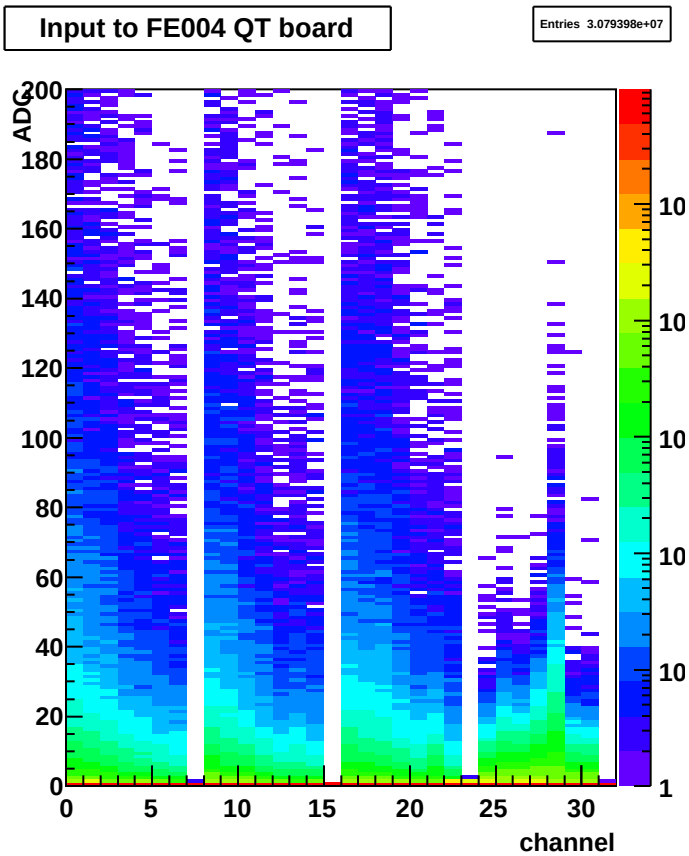

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

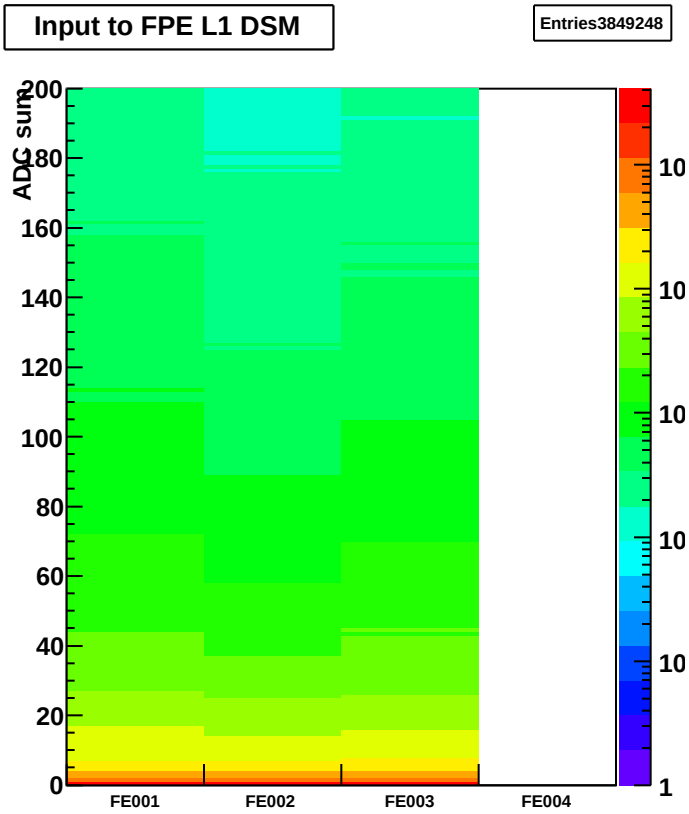

Input to FPD L2 DSM
Entries 3849248
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

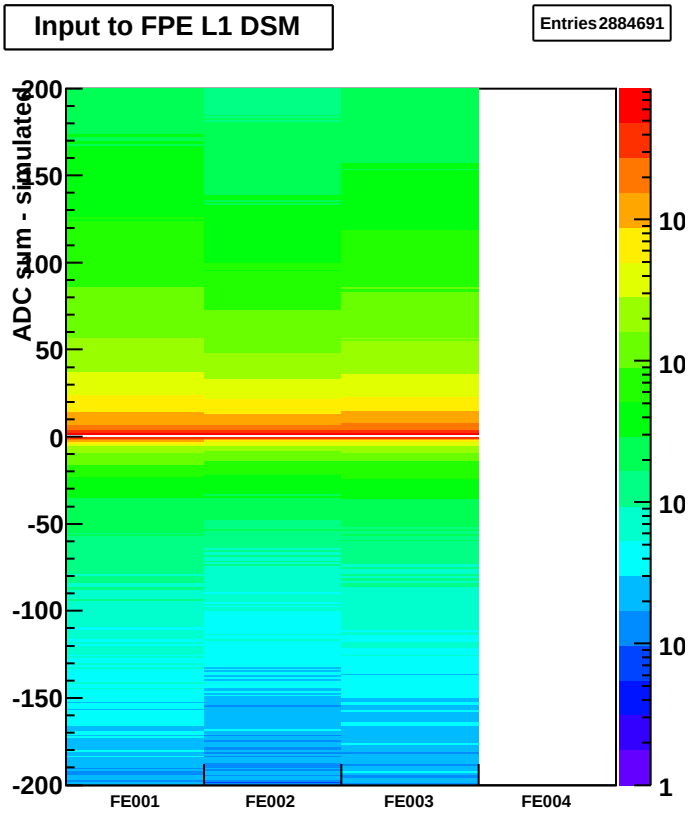

Input to FE001 QT board
Entries 3.079398e+07
ADC
channel

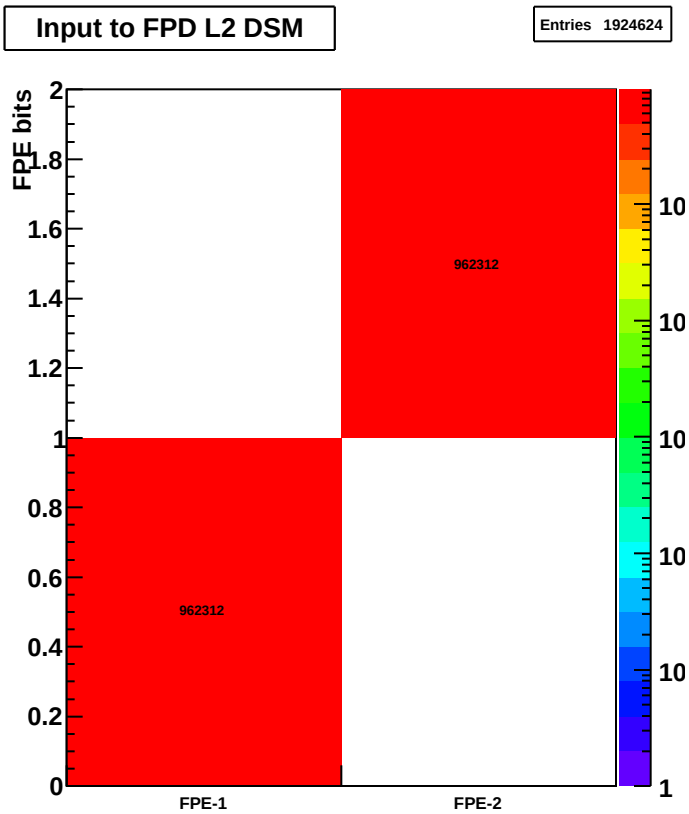

Input to FE002 QT board
Entries 3.079398e+07
ADC
channel

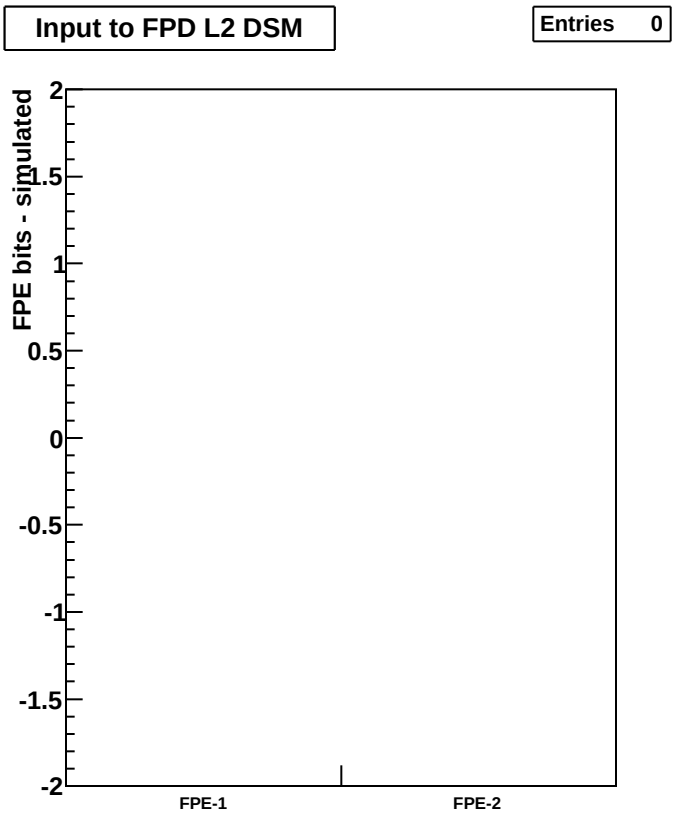

Input to FE003 QT board
Entries 3.079398e+07
ADC
channel


Input to FE004 QT board
Entries 3.079398e+07
ADC
channel


Input to FPE L1 DSM
Entries3849248
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries2884691
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1924624
FPE bits
962312
962312
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2