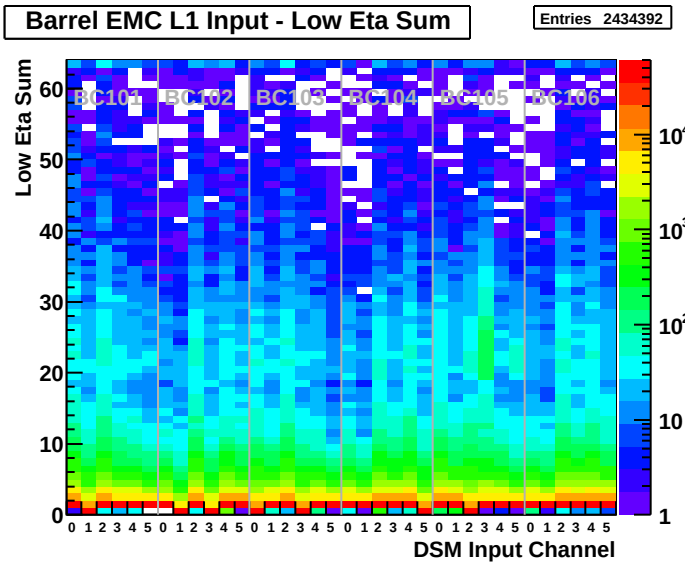
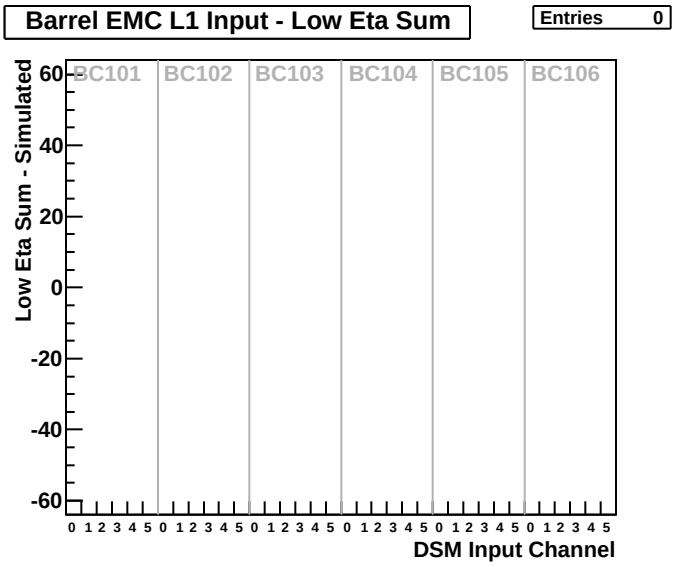
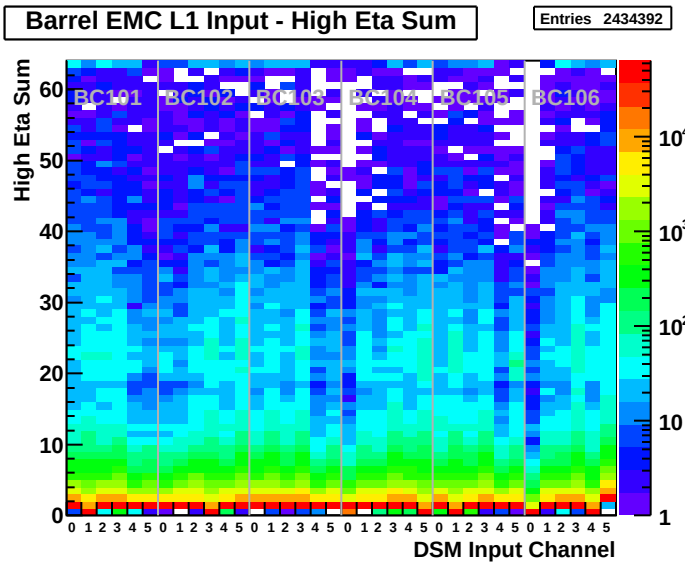
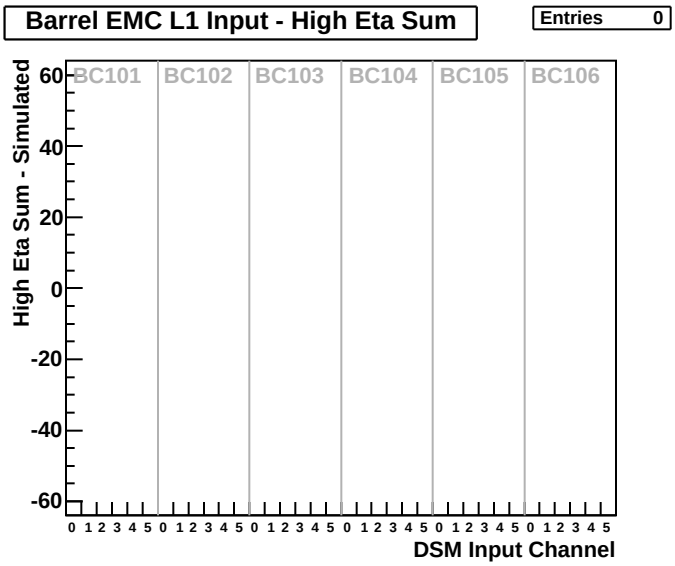
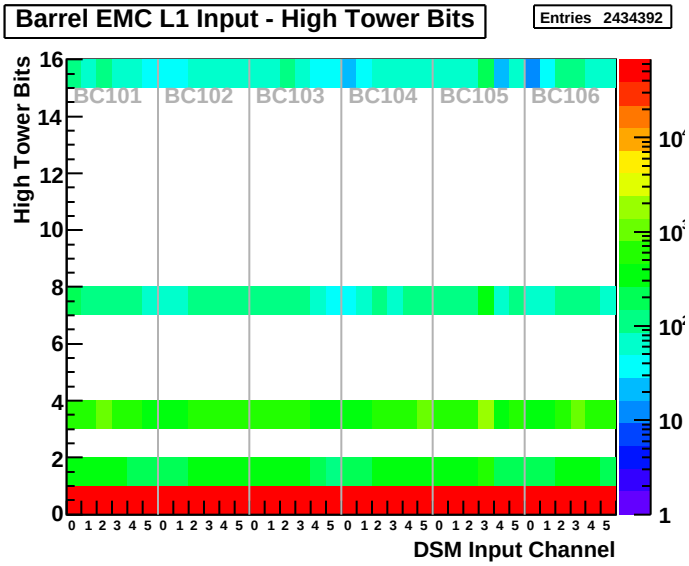
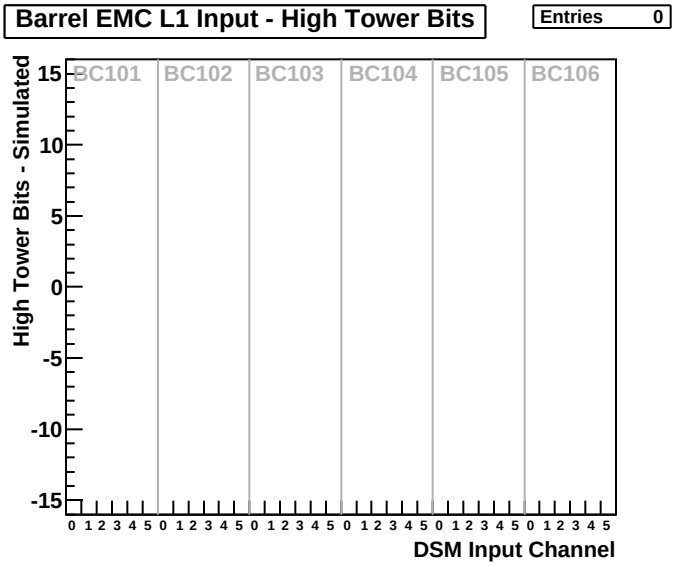

Barrel EMC L1 Input - Low Eta Sum
Entries 2434392
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

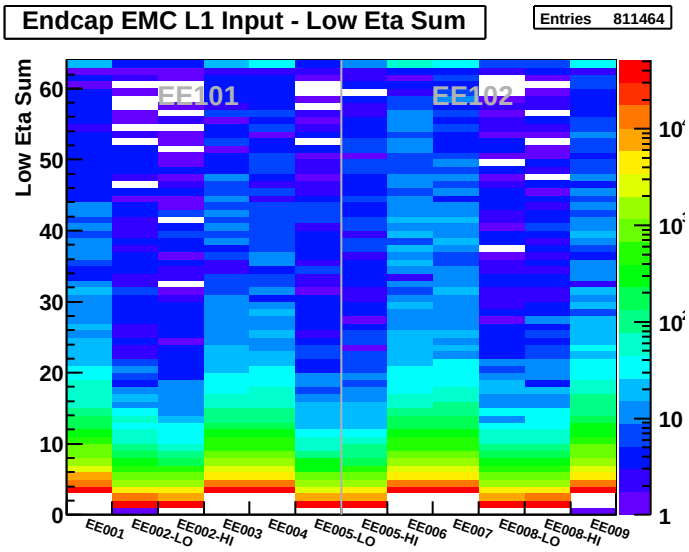

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

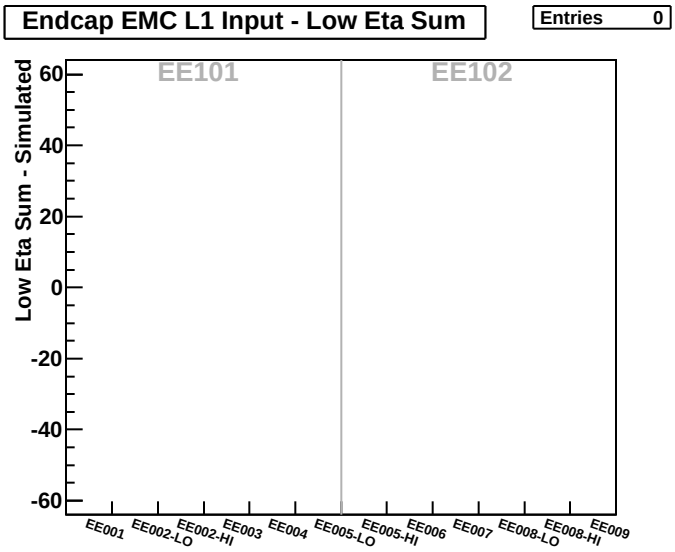

Barrel EMC L1 Input - High Eta Sum
Entries 2434392
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

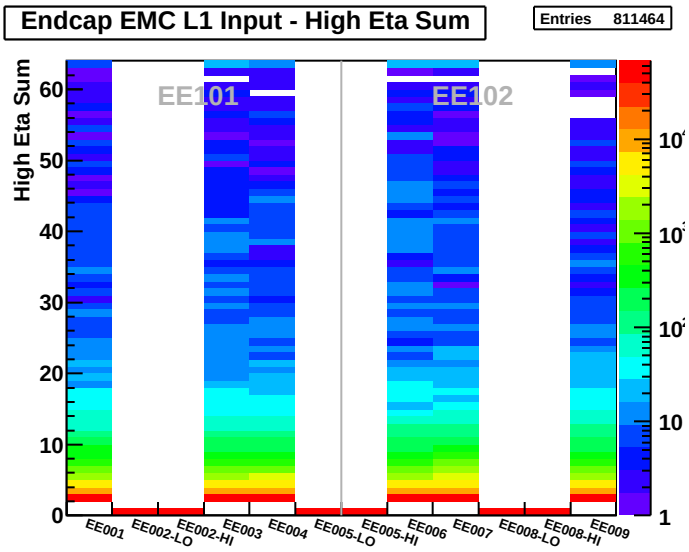

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

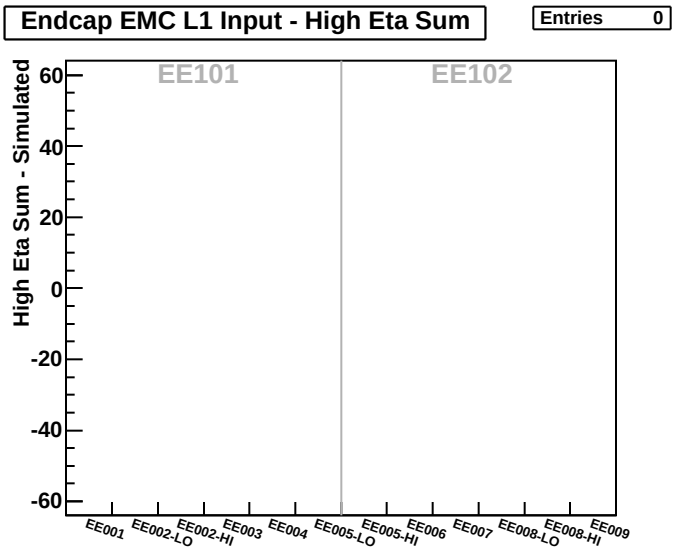

Barrel EMC L1 Input - High Tower Bits
Entries 2434392
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

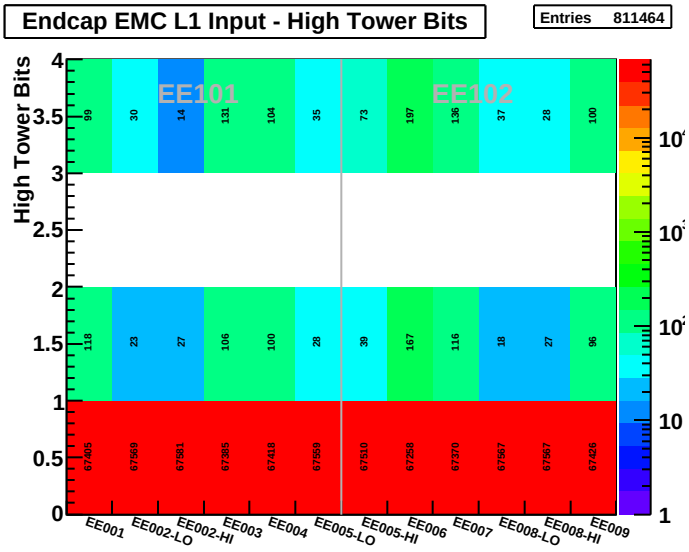

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

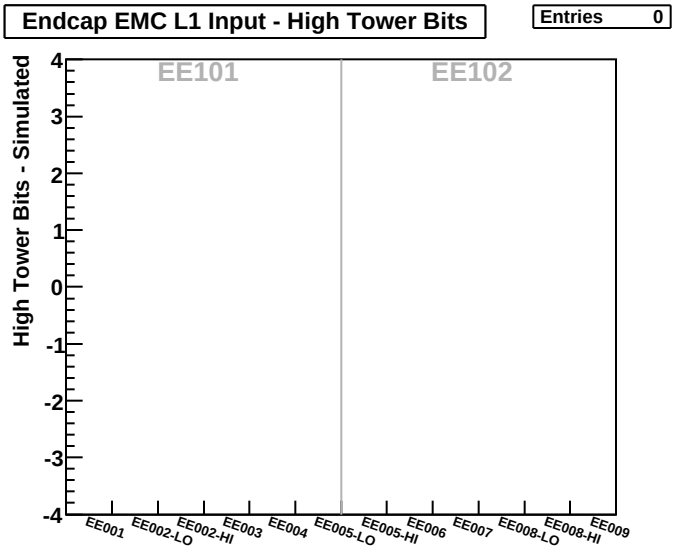

Endcap EMC L1 Input - Low Eta Sum
Entries 811464
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

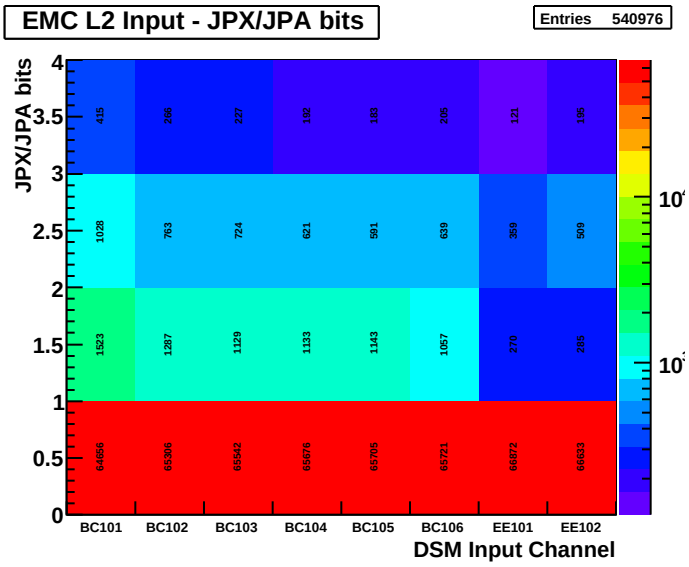

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

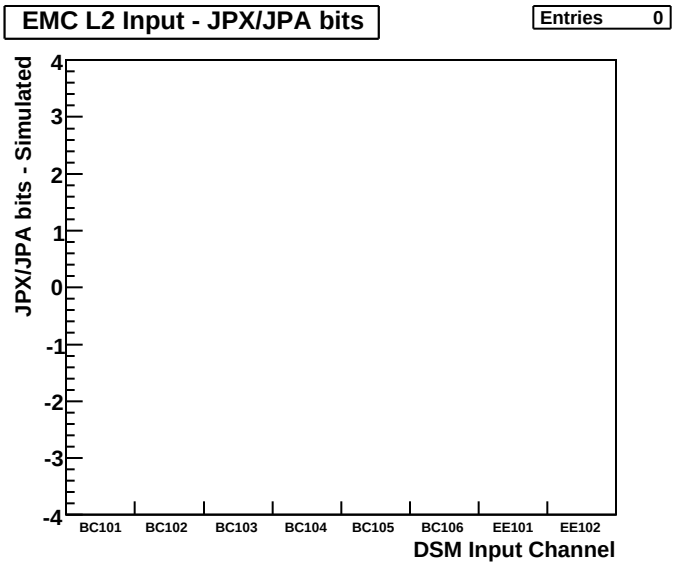

Endcap EMC L1 Input - High Eta Sum
Entries 811464
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

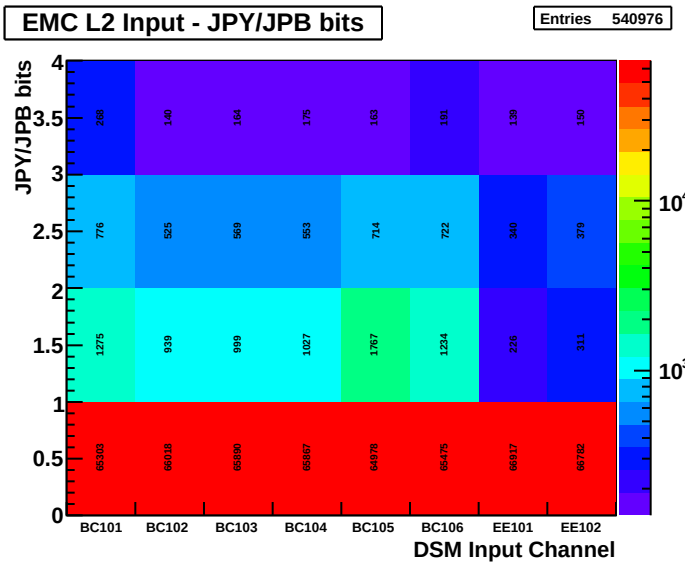

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

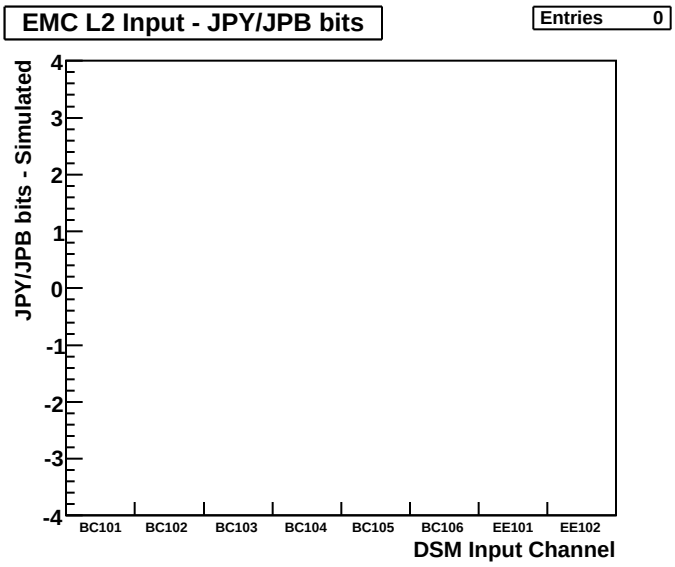

Endcap EMC L1 Input - High Tower Bits
Entries 811464
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

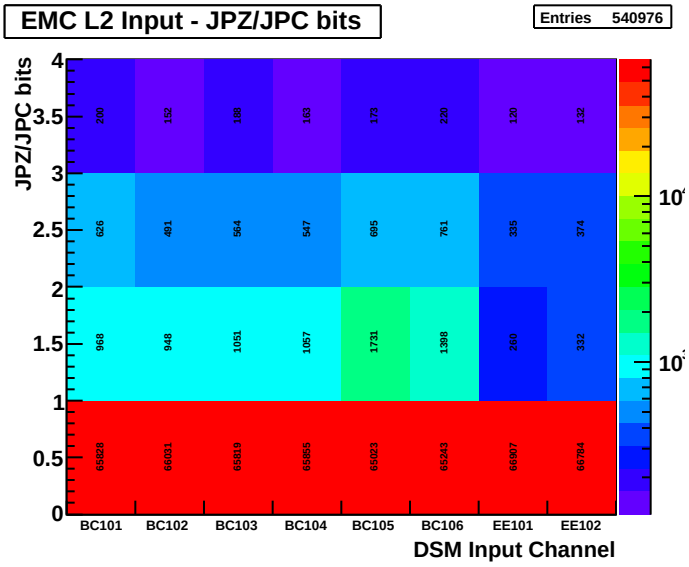

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

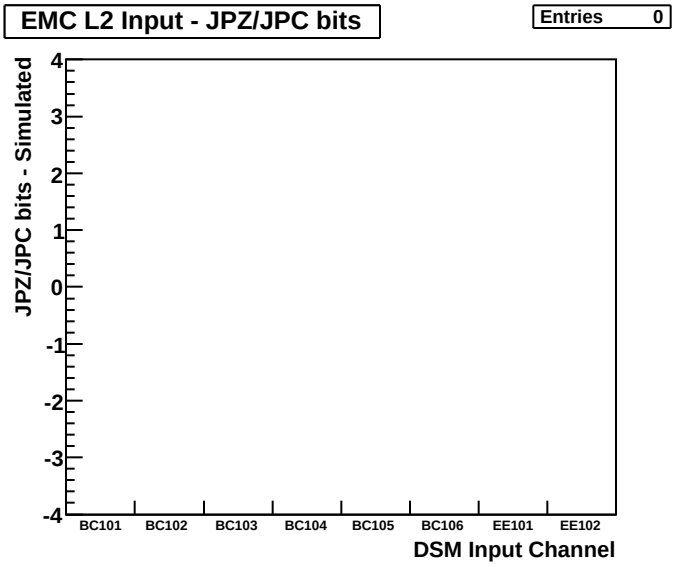

EMC L2 Input - JPX/JPA bits
Entries 540976
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
415 266 227 192 183 205 121 195
1028 763 724 621 591 639 359 509
1523 1287 1129 1133 1143 1057 270 235
64656 65306 65542 65676 65705 65721 66872 66633

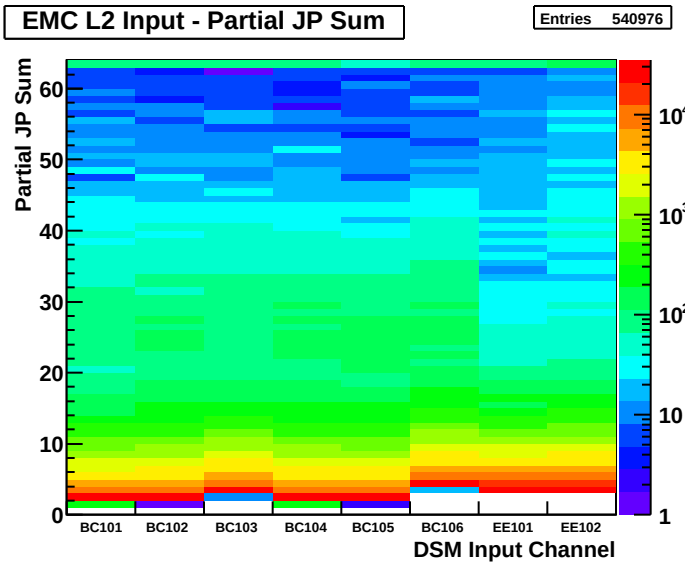

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

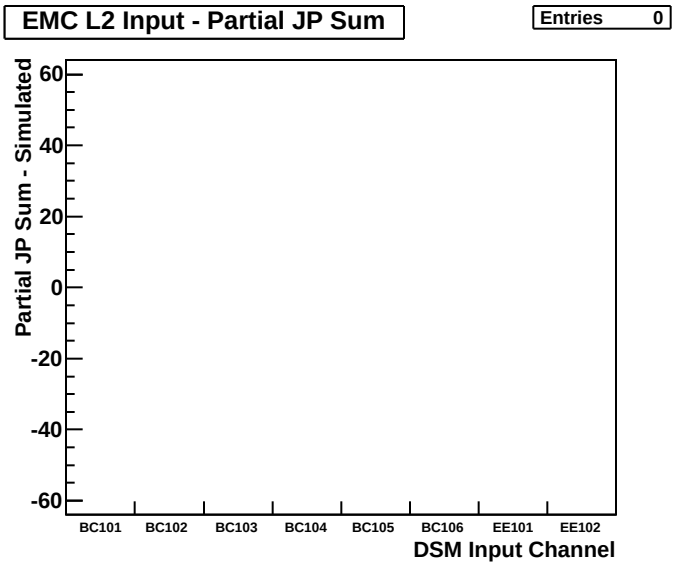

EMC L2 Input - JPY/JPB bits
Entries 540976
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
288 140 164 175 163 191 139 150
776 525 569 553 714 722 340 379
1275 939 999 1027 1767 1234 226 311
65303 66018 65890 65867 64978 65475 66917 66782

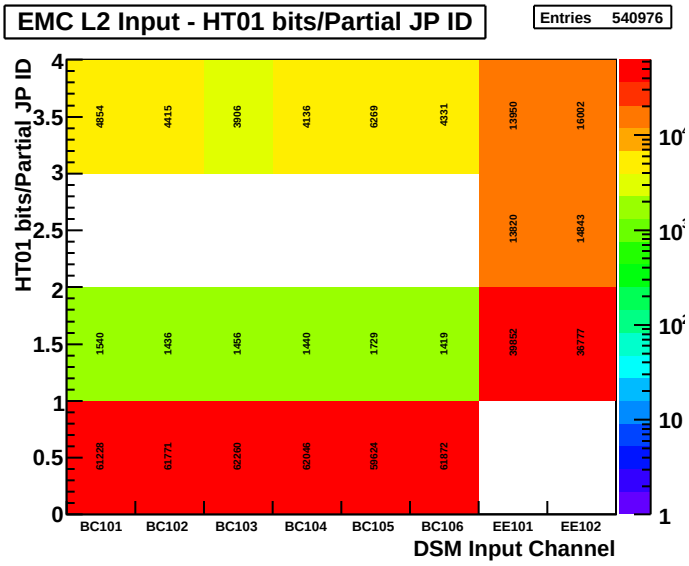

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

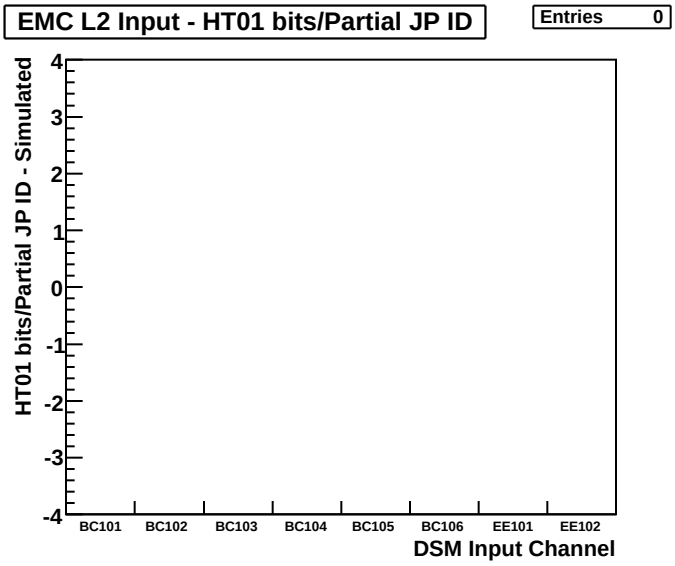

EMC L2 Input - JPZ/JPC bits
Entries 540976
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
200 152 188 163 173 220 120 132
626 491 564 547 695 761 335 374
968 948 1051 1067 1731 1398 260 332
65828 66031 65819 65865 65023 65243 66907 66784

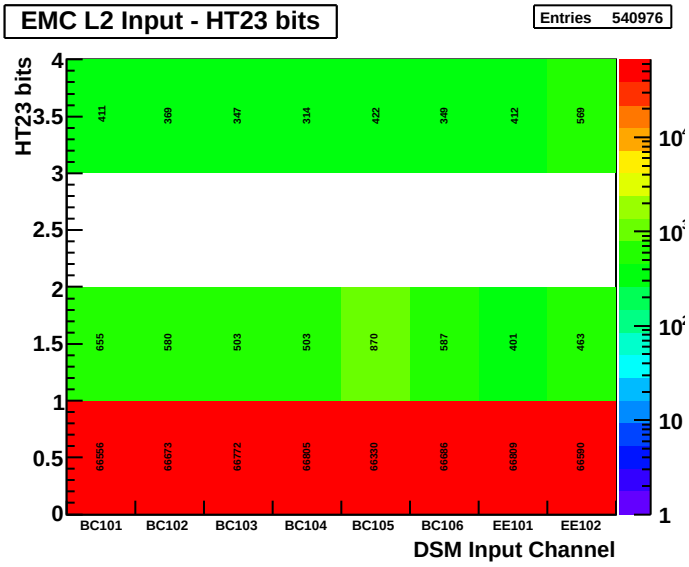

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

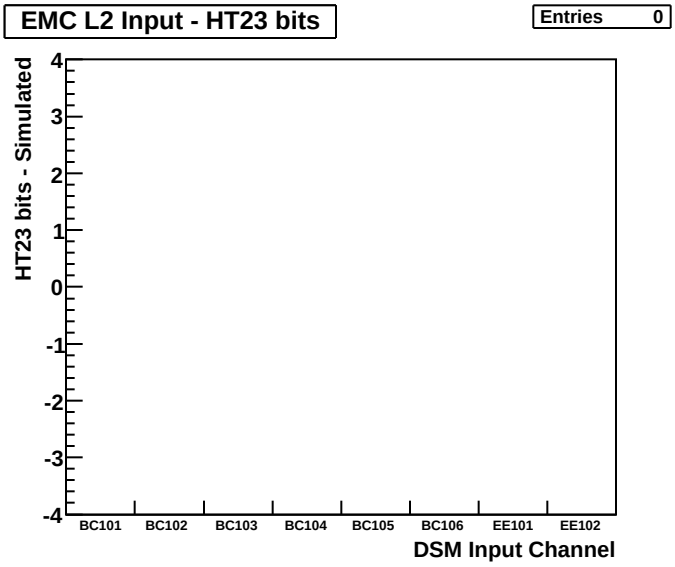

EMC L2 Input - Partial JP Sum
Entries 540976
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

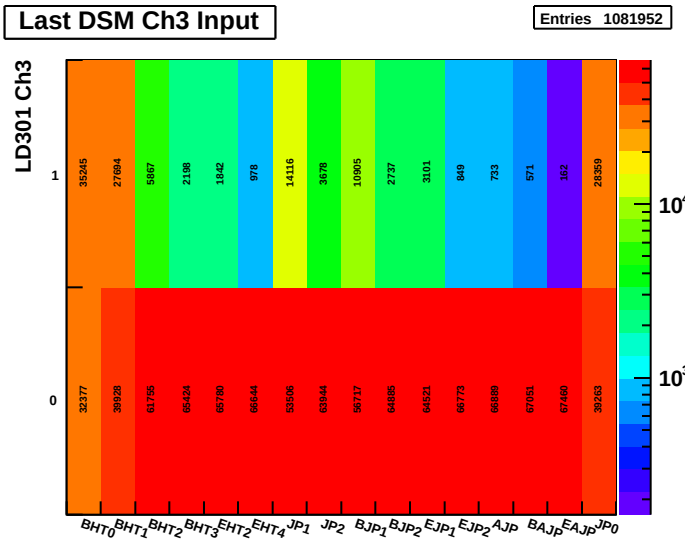

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

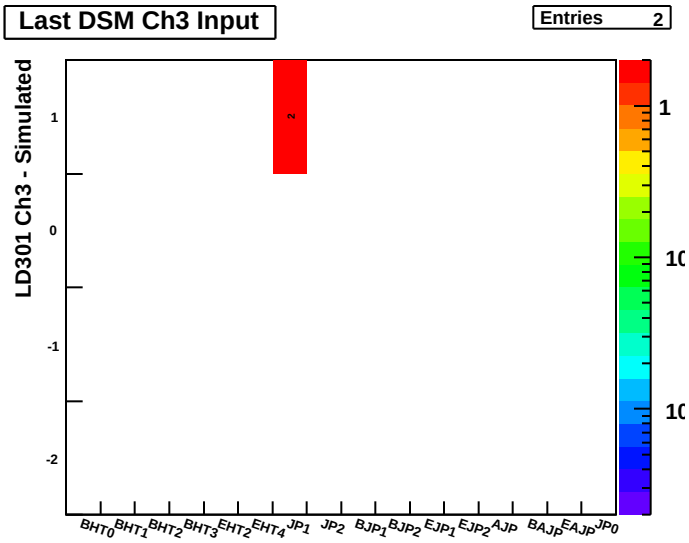

EMC L2 Input - HT01 bits/Partial JP ID
Entries 540976
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

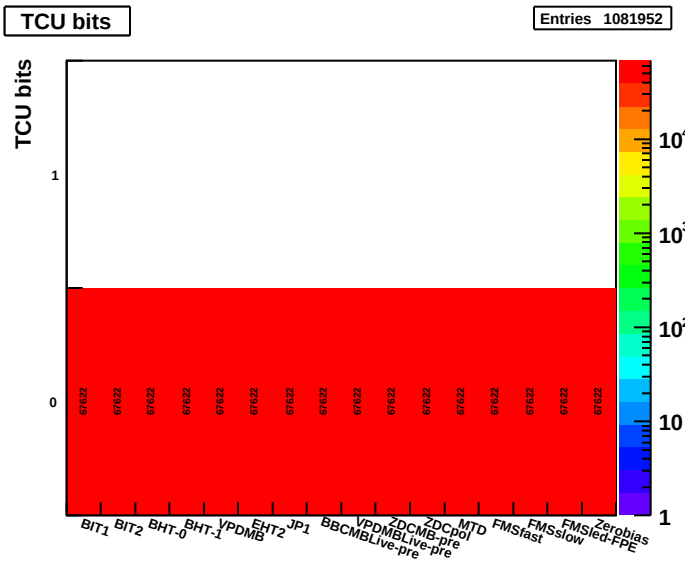

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

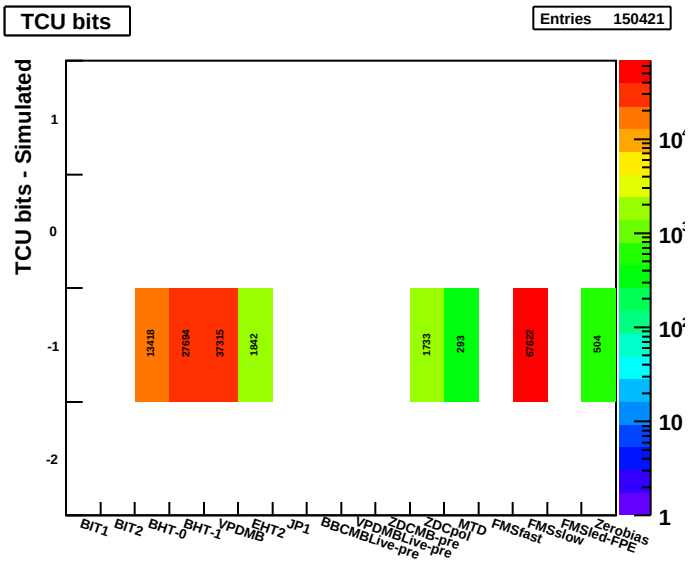

EMC L2 Input - HT23 bits
Entries 540976
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

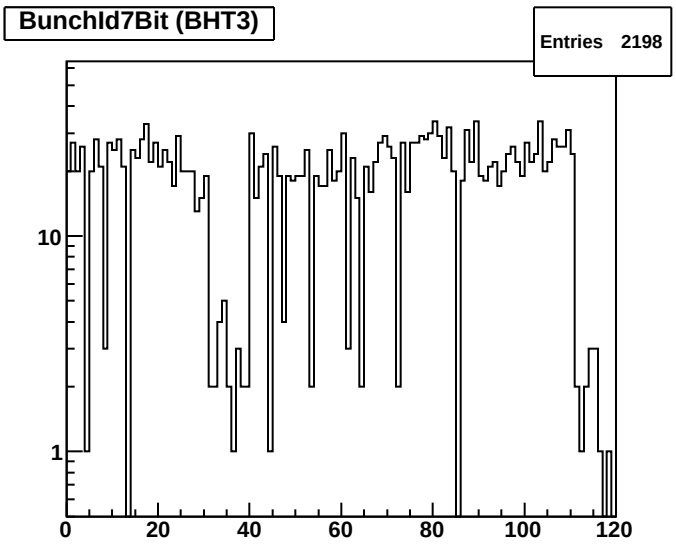

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

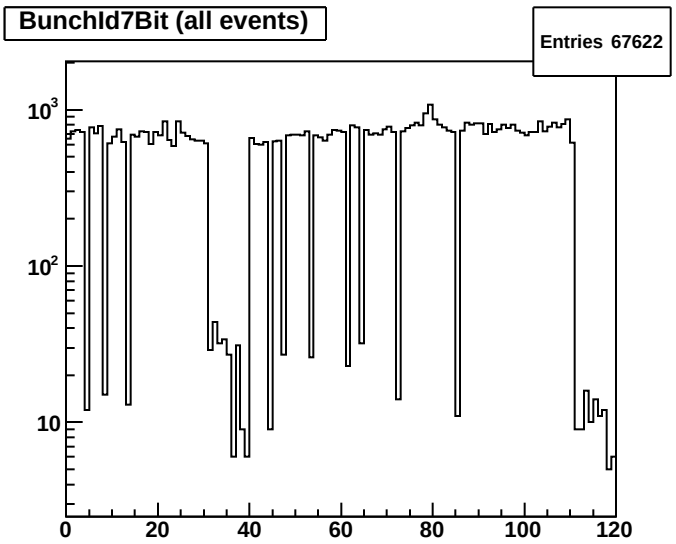

Last DSM Ch3 Input
Entries 1081952
LD301 Ch3
35245 27694 5867 2198 1842 978 14116 3678 10905 2737 3101 849 733 571 162 28359
32377 39928 61755 65424 65780 66644 53506 63944 56717 64885 64521 66773 66889 67051 67460 39263
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

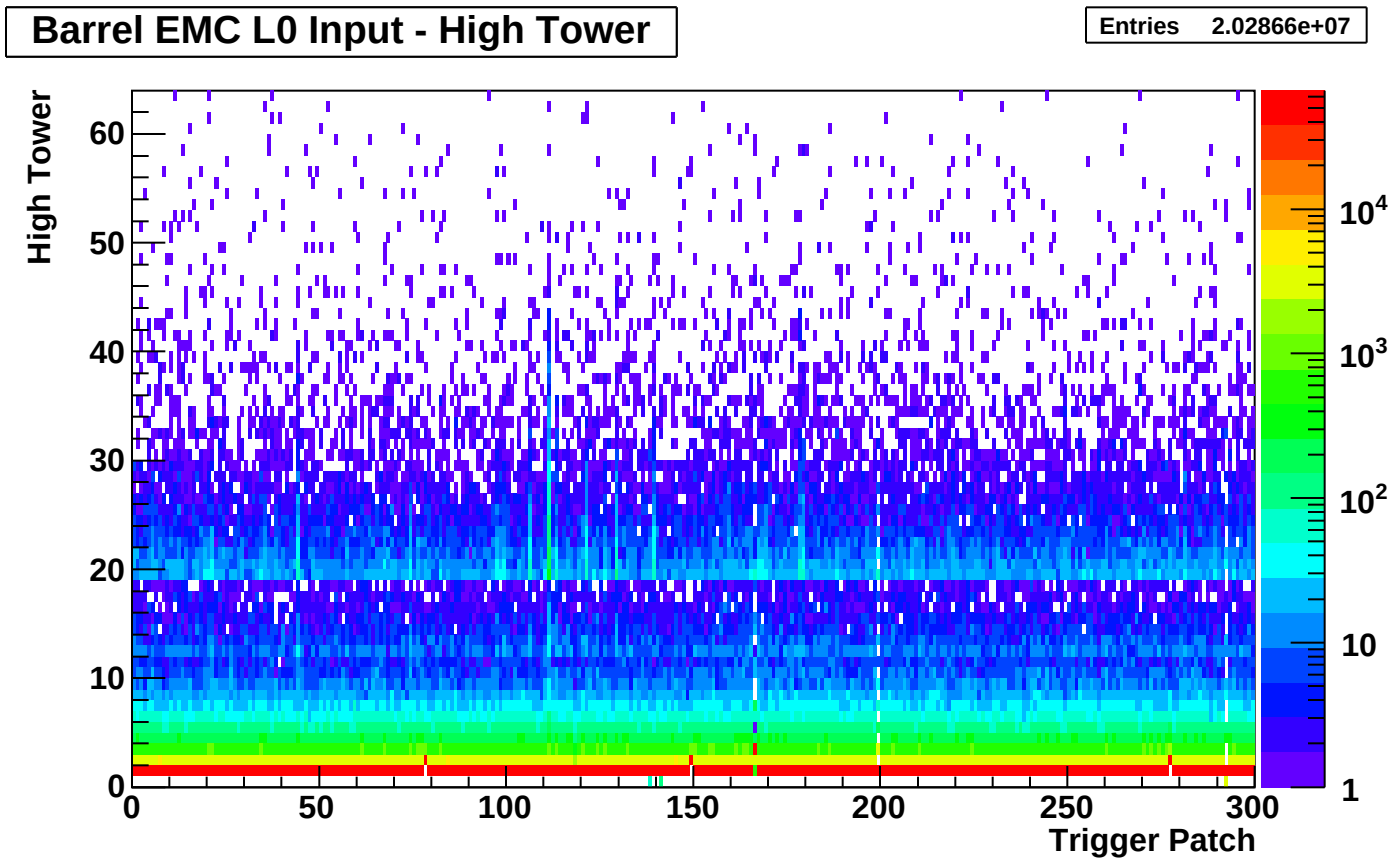

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

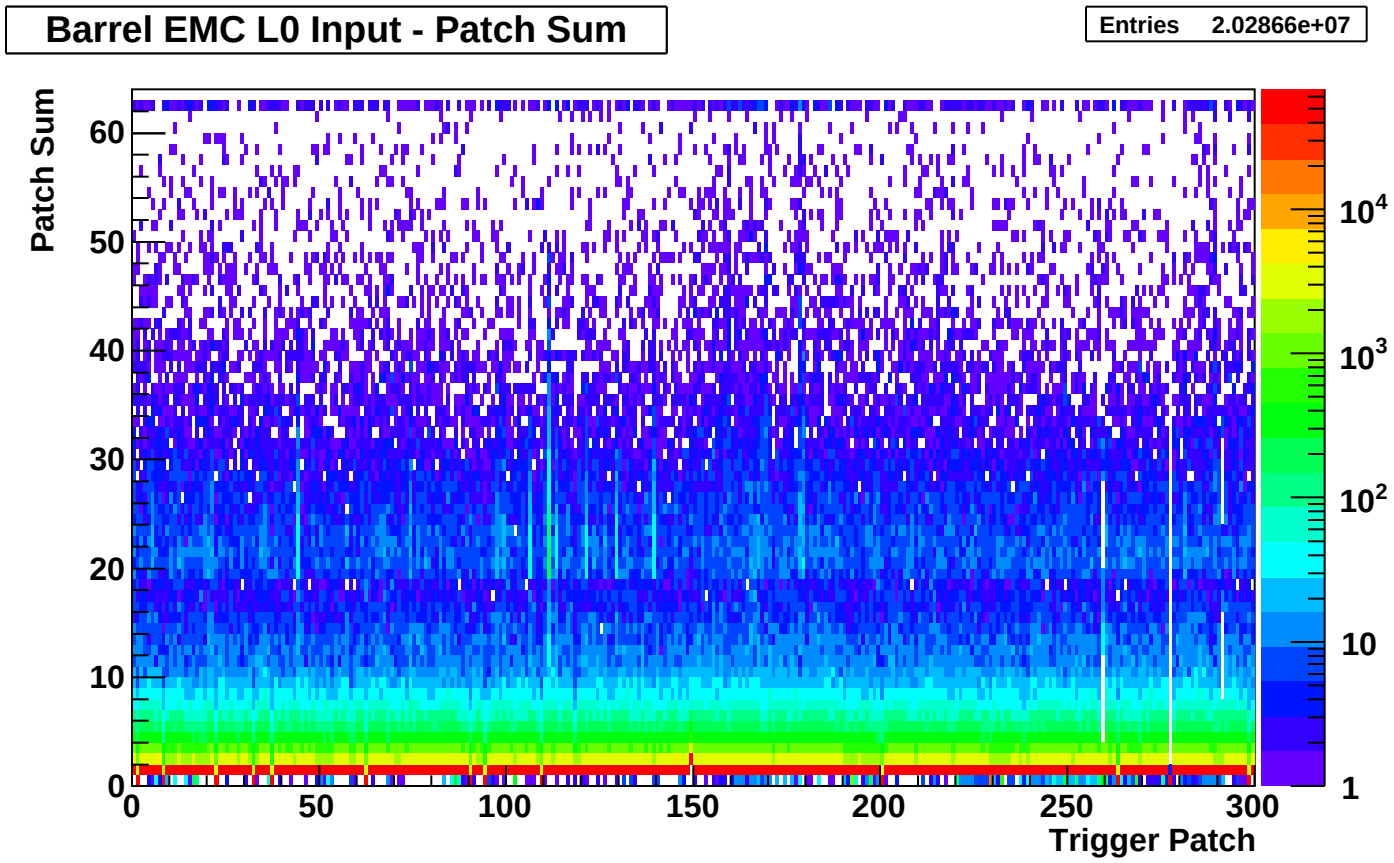

TCU bits
Entries 1081952
TCU bits
67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622 67622
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

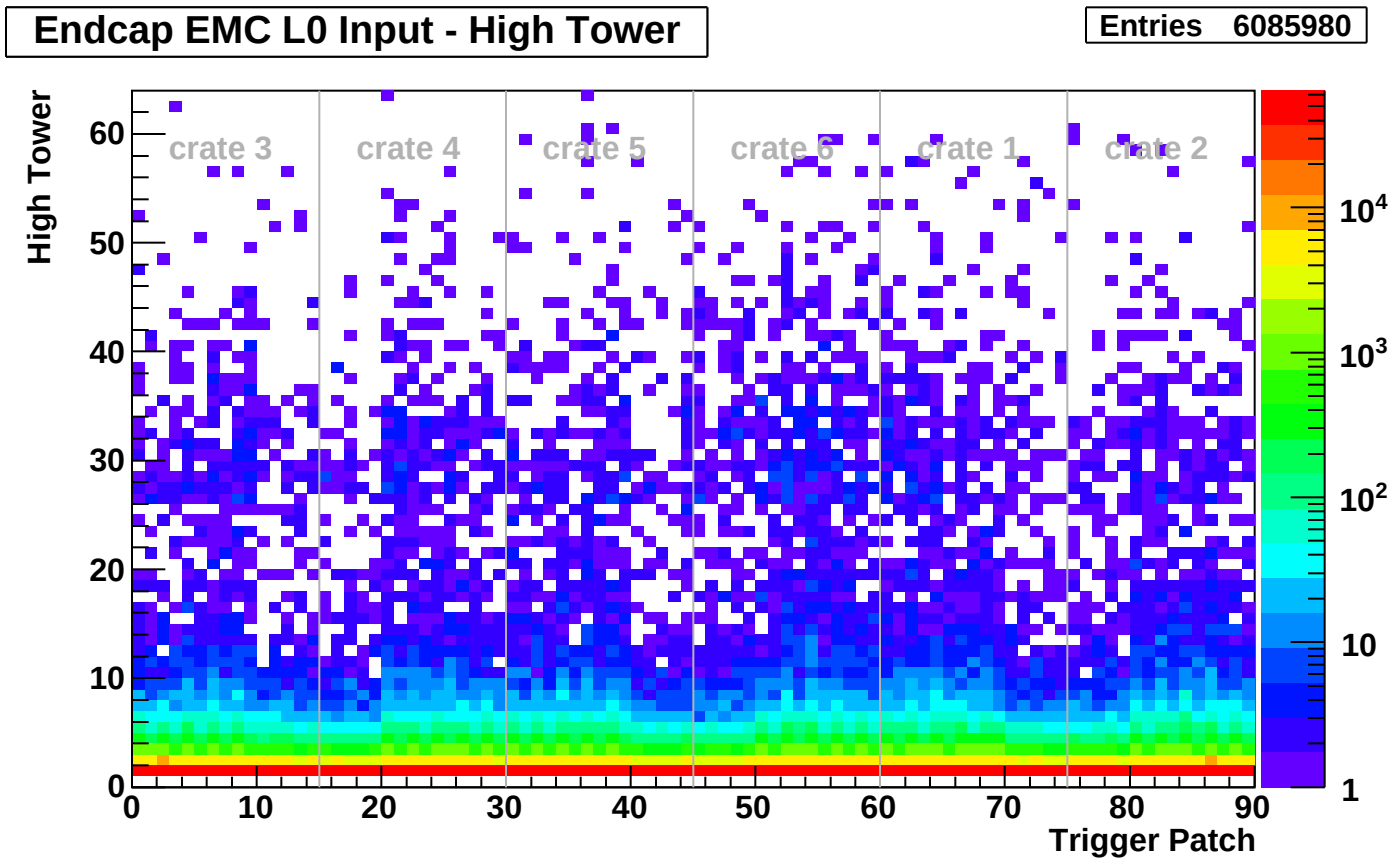

TCU bits
Entries 150421
TCU bits - Simulated
13418 27694 37315 1842 1733 293 67622 504
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

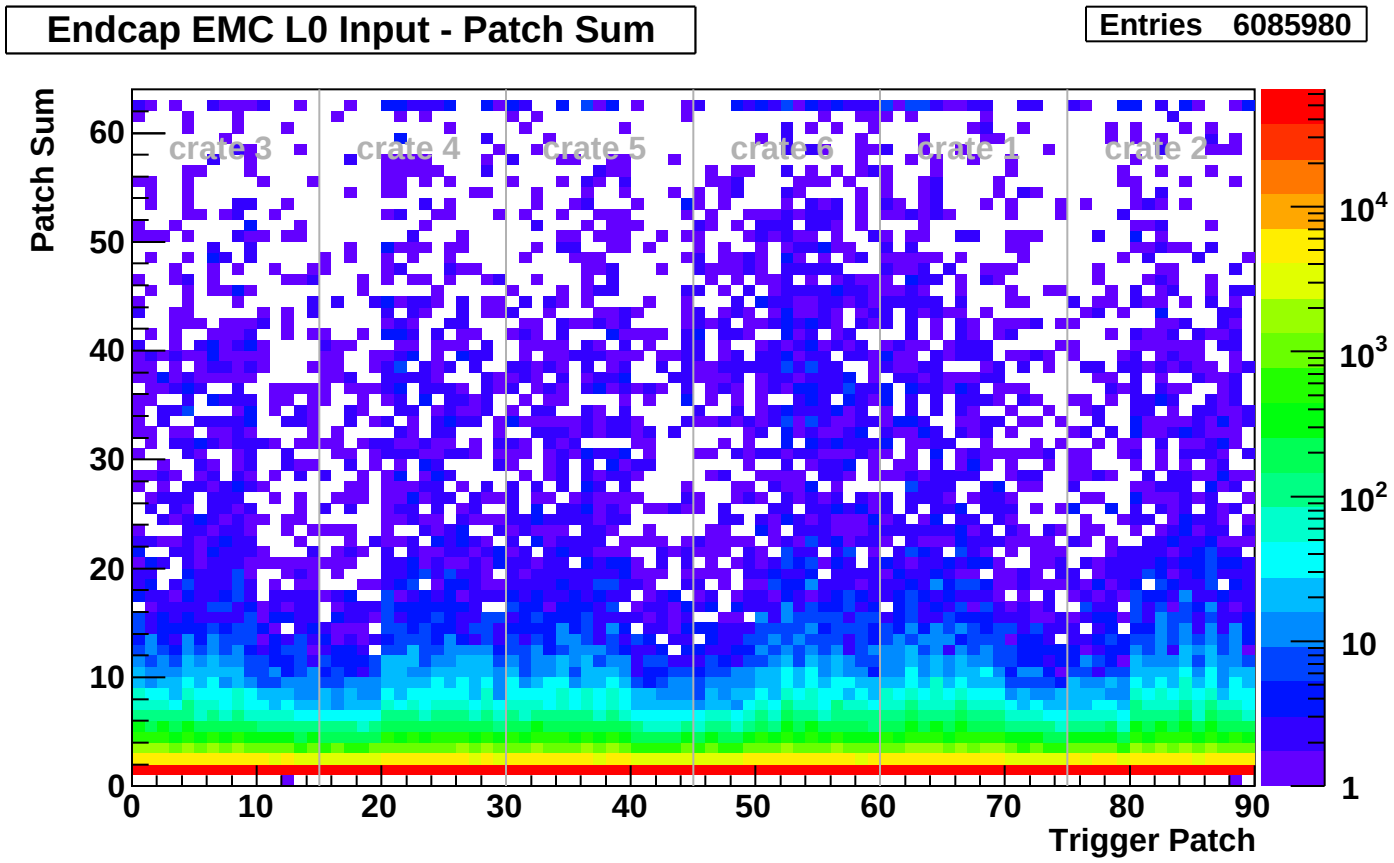

BunchId7Bit (BHT3)
Entries 2198

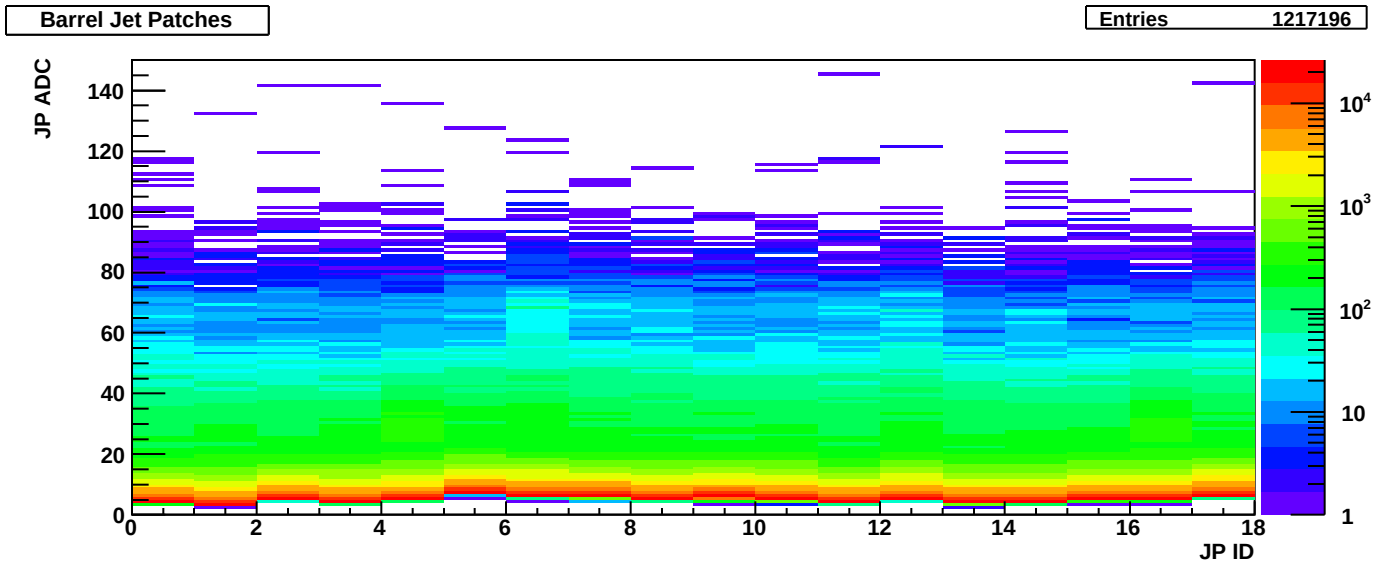

BunchId7Bit (all events)
Entries 67622

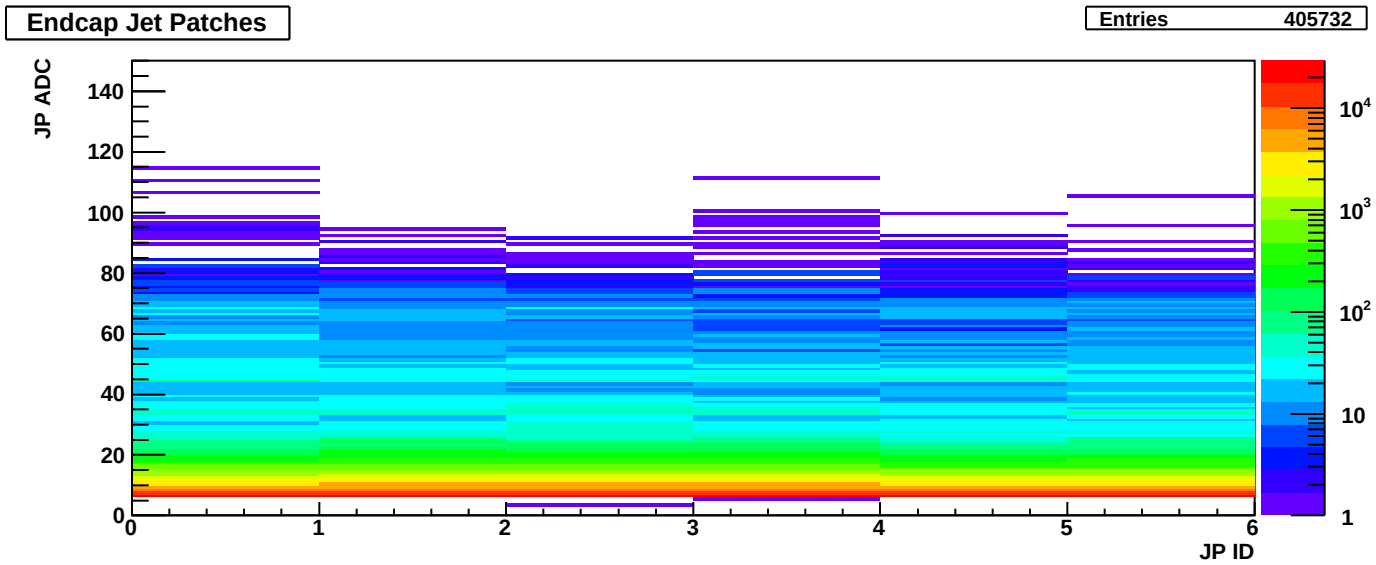

Barrel EMC L0 Input - High Tower
Entries 2.02866e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

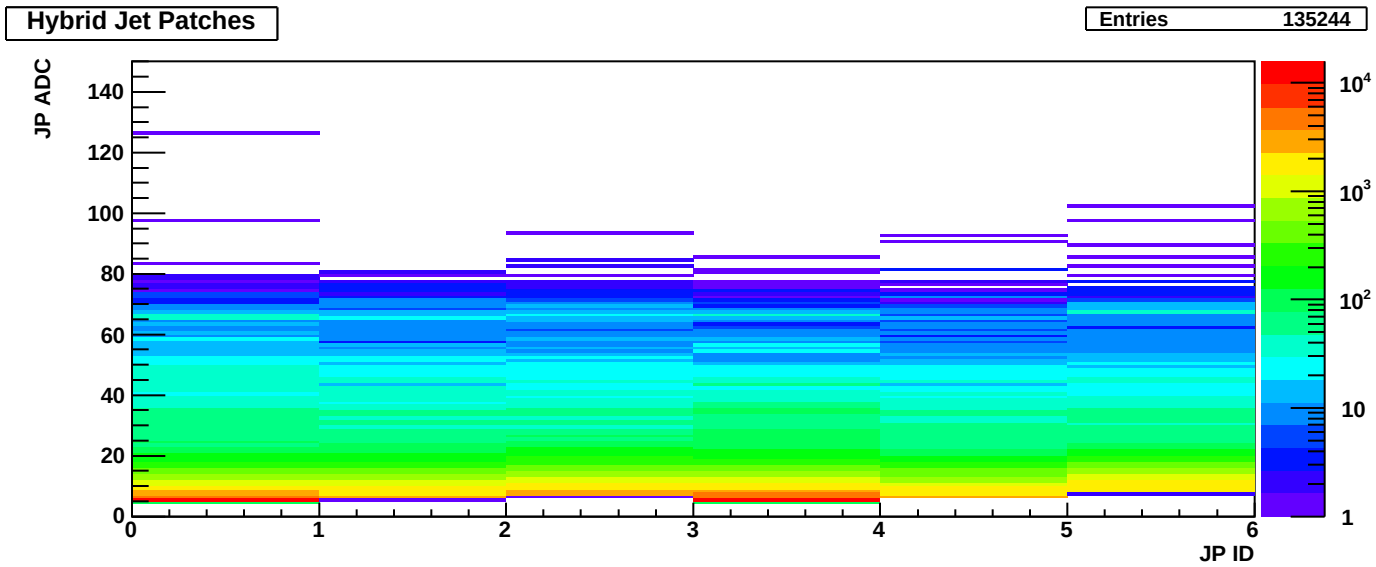

Barrel EMC L0 Input - Patch Sum
Entries 2.02866e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

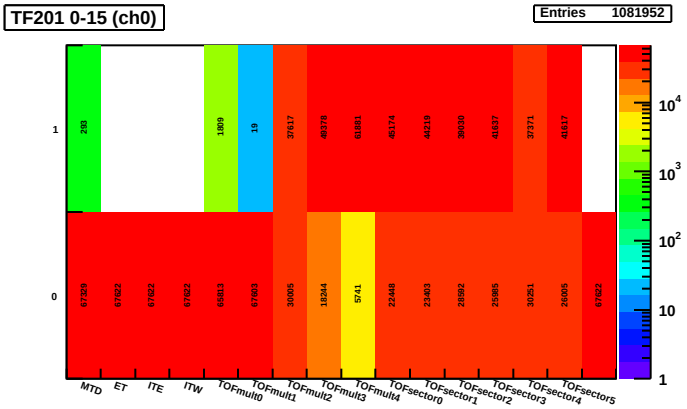

Endcap EMC L0 Input - High Tower
Entries 6085980
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

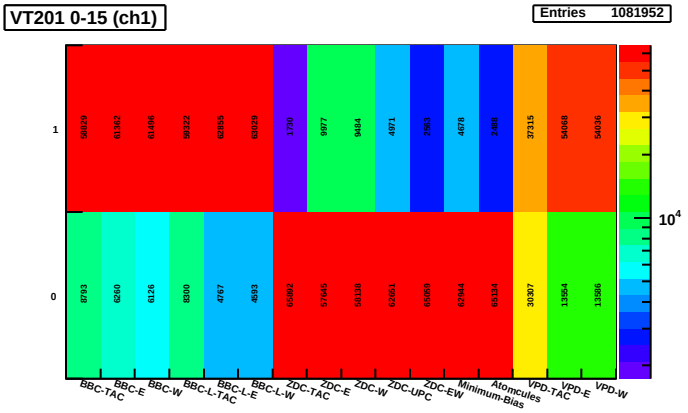

Endcap EMC L0 Input - Patch Sum
Entries 6085980
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

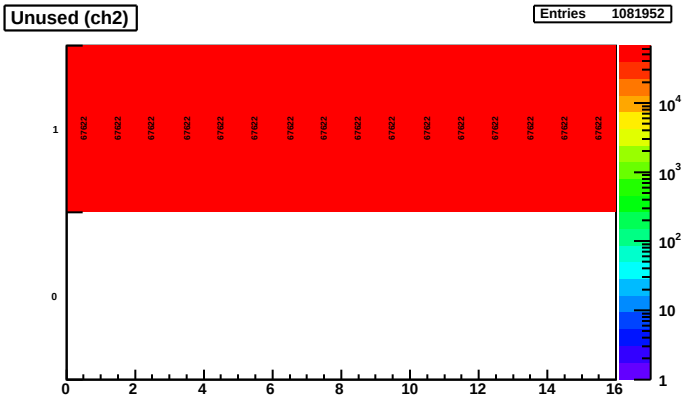

Barrel Jet Patches
Entries 1217196
JP ADC
JP ID

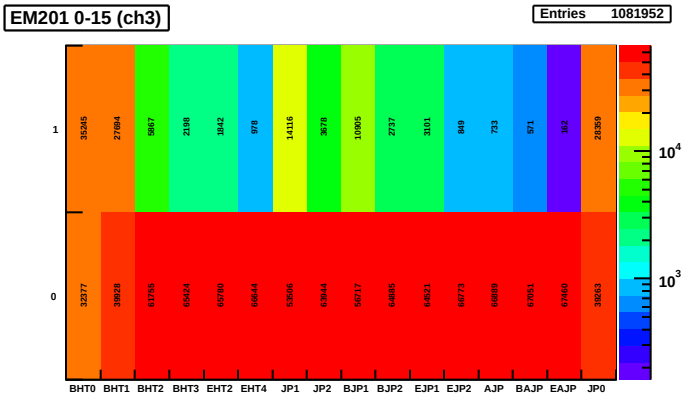

Endcap Jet Patches
Entries 405732
JP ADC
JP ID

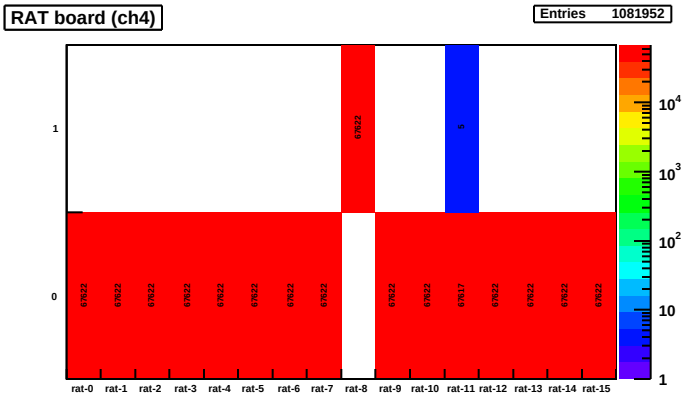

Hybrid Jet Patches
Entries 135244
JP ADC
JP ID

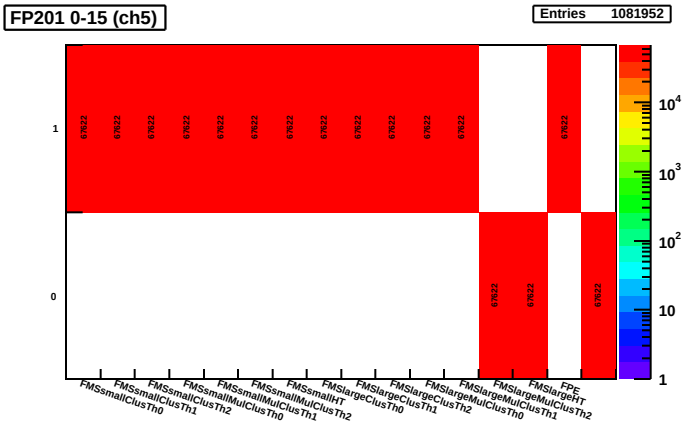

TF201 0-15 (ch0)
Entries 1081952
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

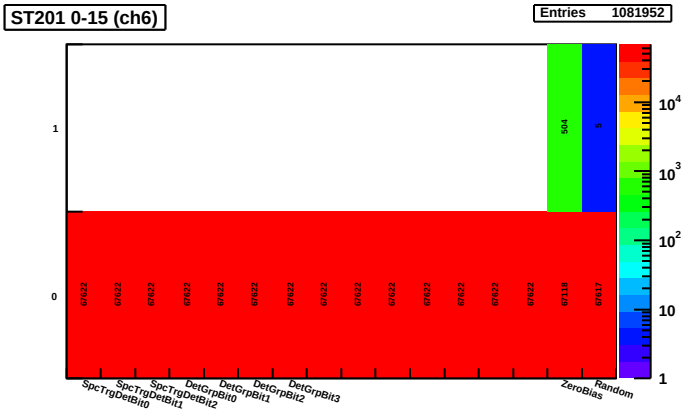

VT201 0-15 (ch1)
Entries 1081952
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

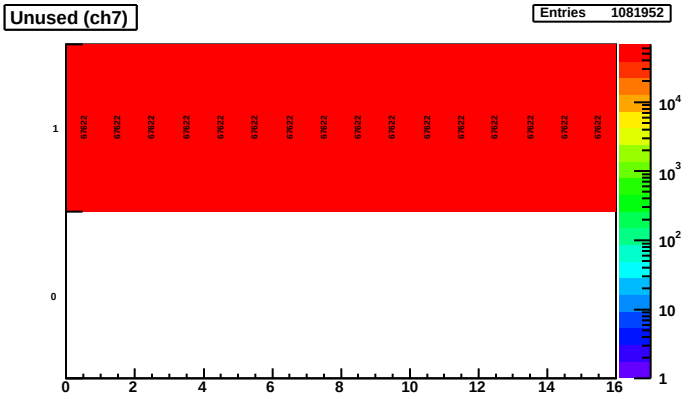

Unused (ch2)
Entries 1081952

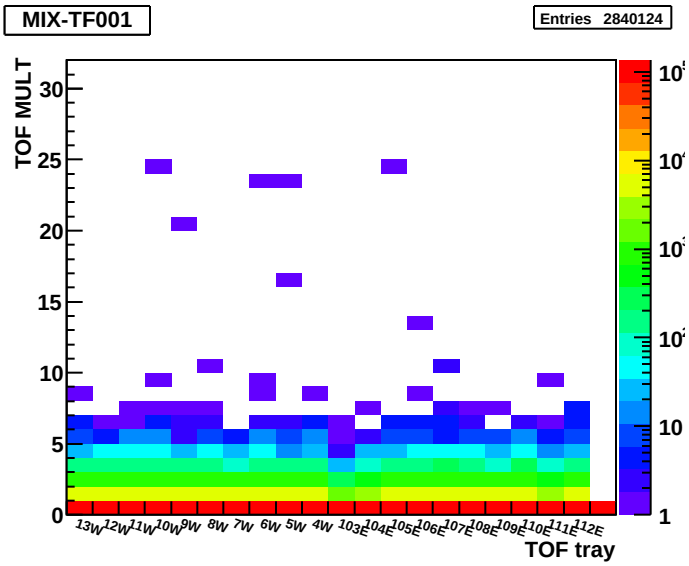

EM201 0-15 (ch3)
Entries 1081952
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

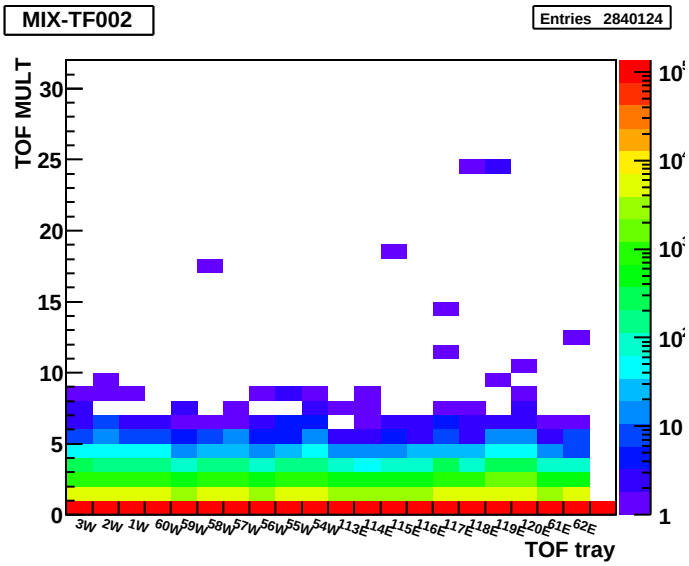

RAT board (ch4)
Entries 1081952
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

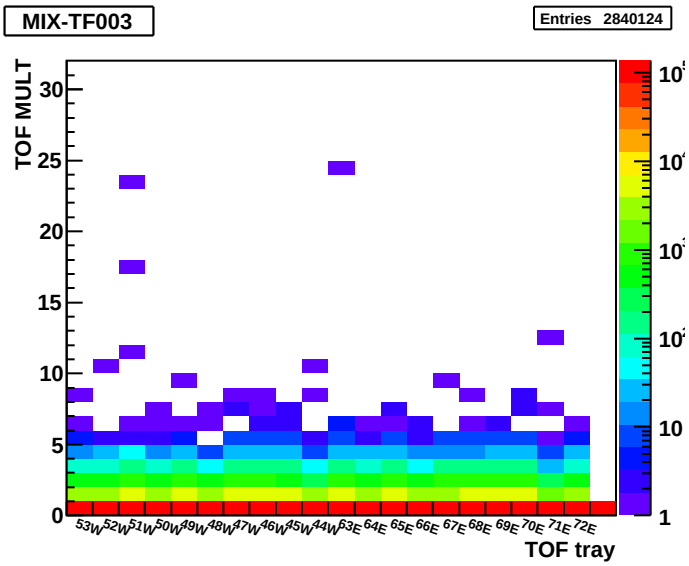

FP201 0-15 (ch5)
Entries 1081952
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

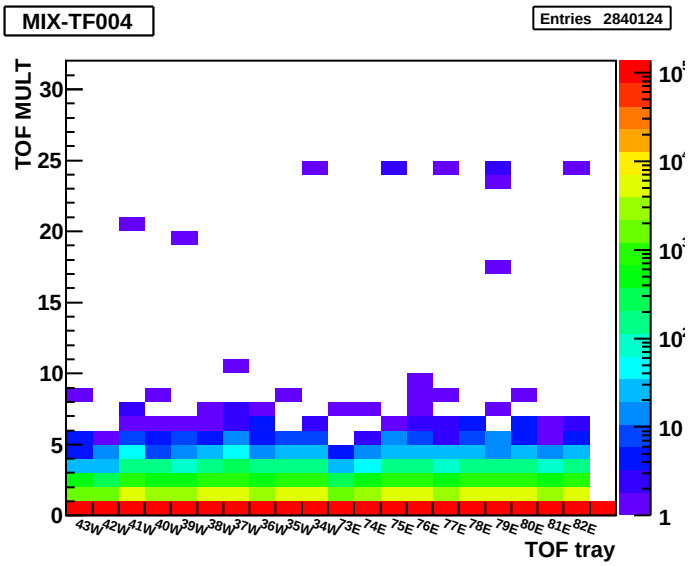

ST201 0-15 (ch6)
Entries 1081952
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

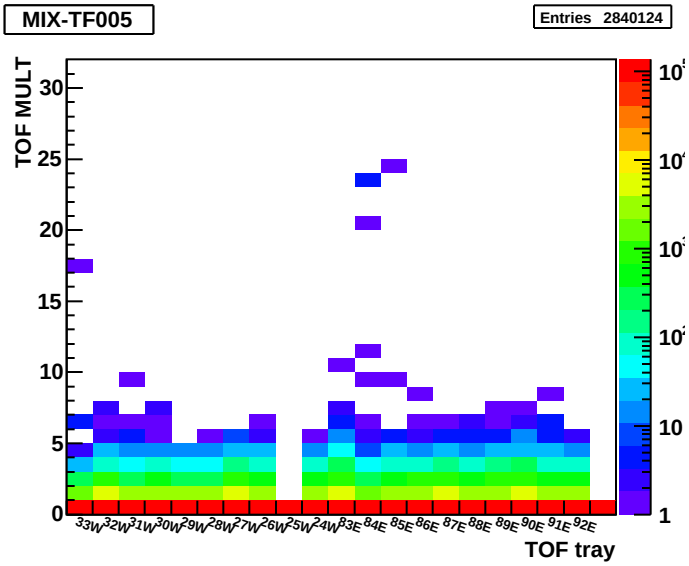

Unused (ch7)
Entries 1081952

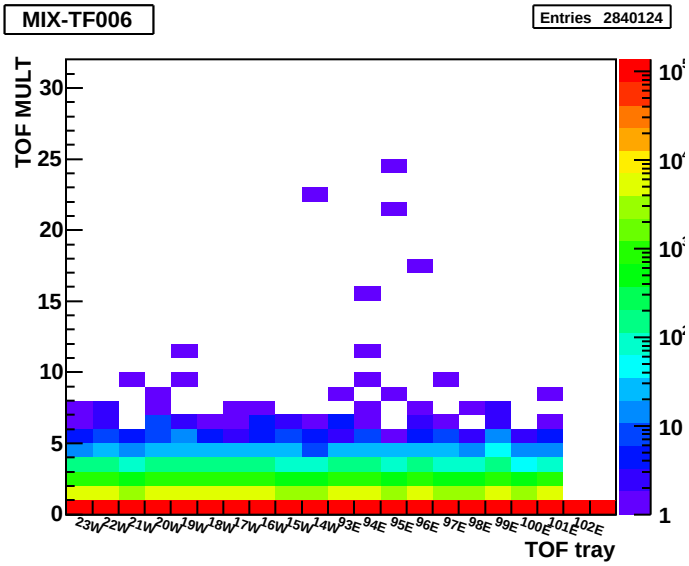

MIX-TF001
Entries 2840124
TOF MULT
TOF tray

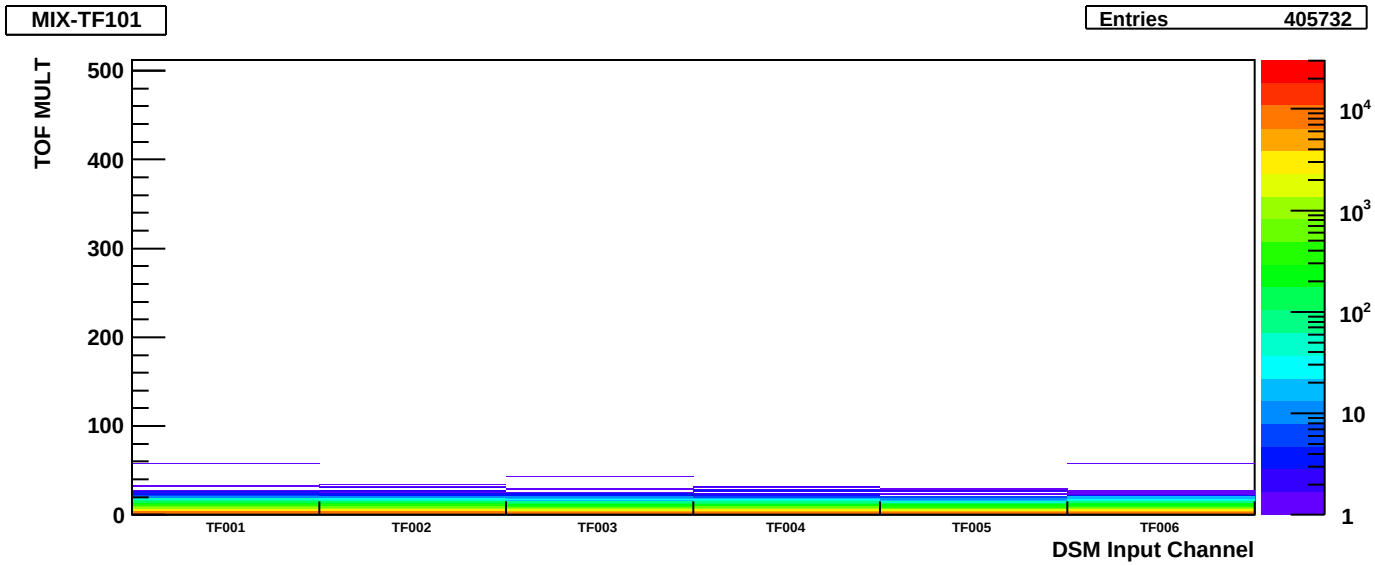

MIX-TF002
Entries 2840124
TOF MULT
TOF tray

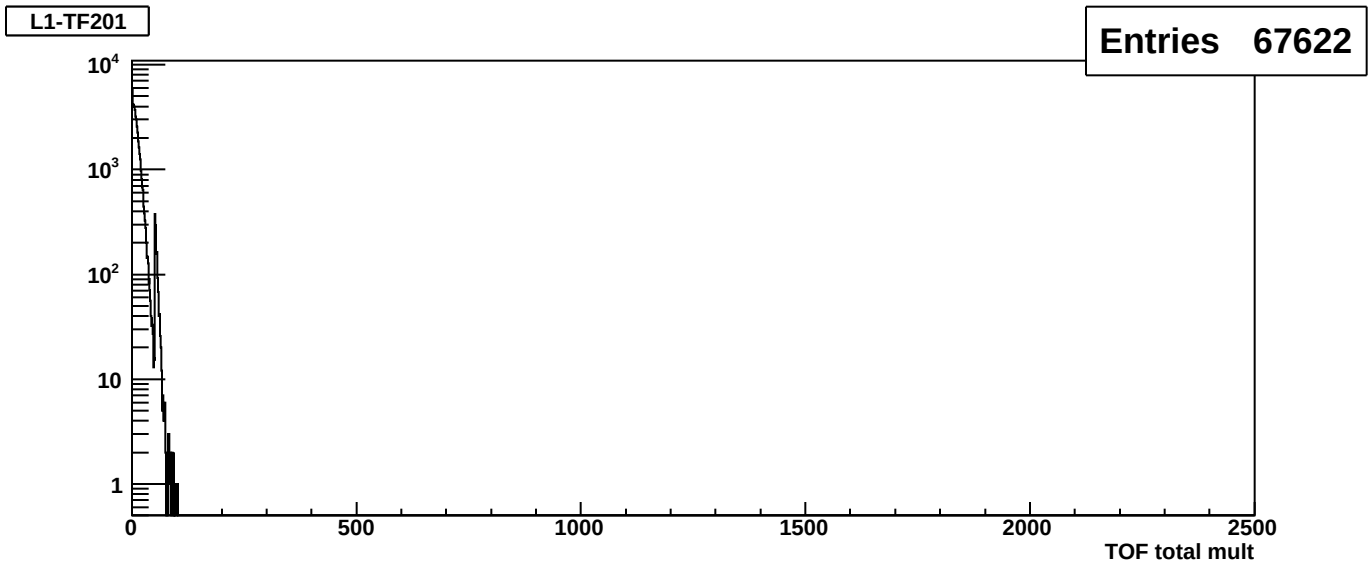

MIX-TF003
Entries 2840124
TOF MULT
TOF tray

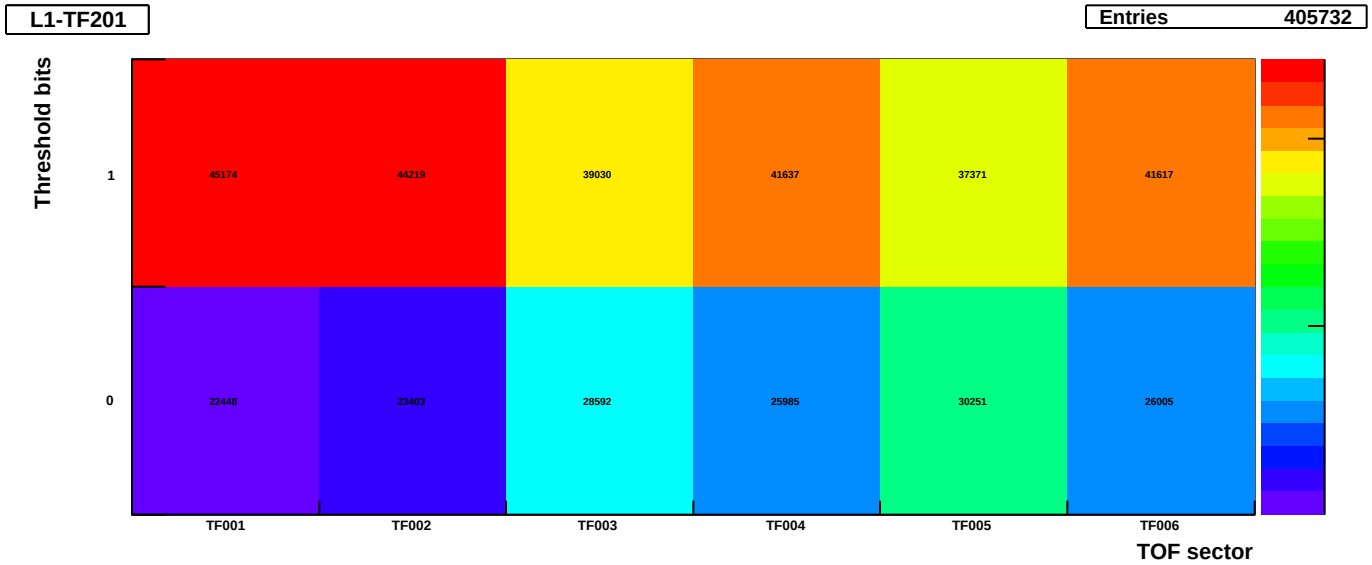

MIX-TF004
Entries 2840124
TOF MULT
TOF tray

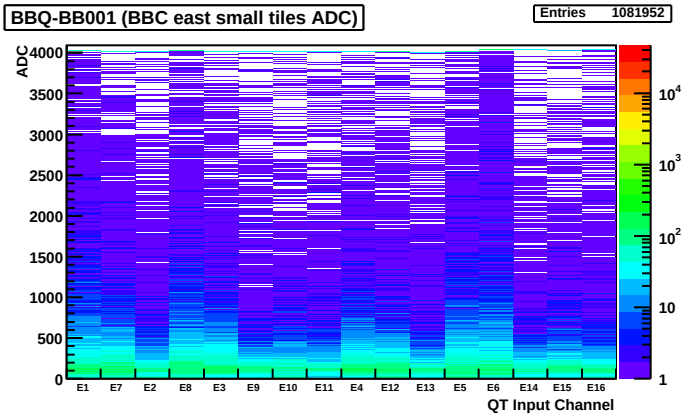

MIX-TF005
Entries 2840124
TOF MULT
TOF tray

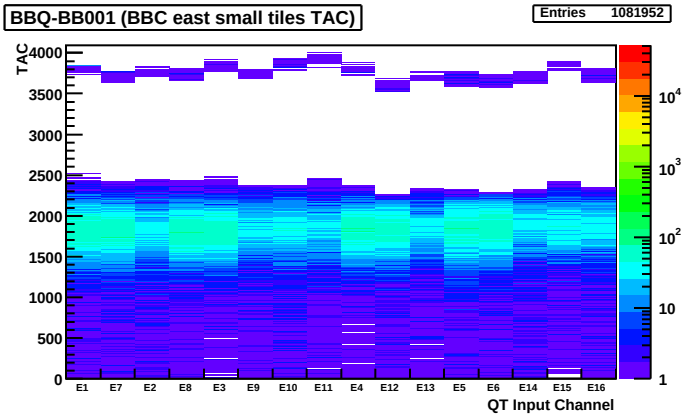

MIX-TF006
Entries 2840124
TOF MULT
TOF tray

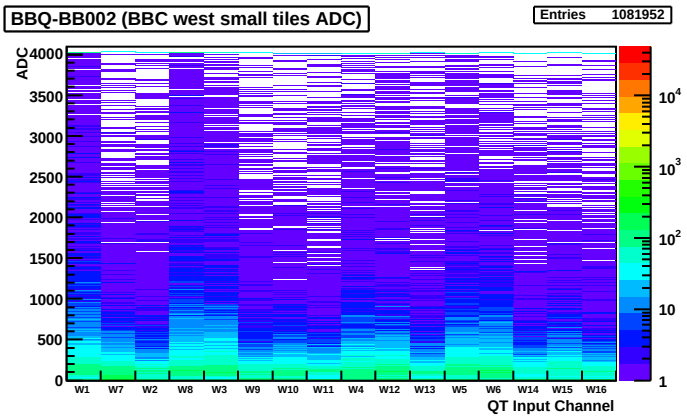

MIX-TF101
Entries 405732
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

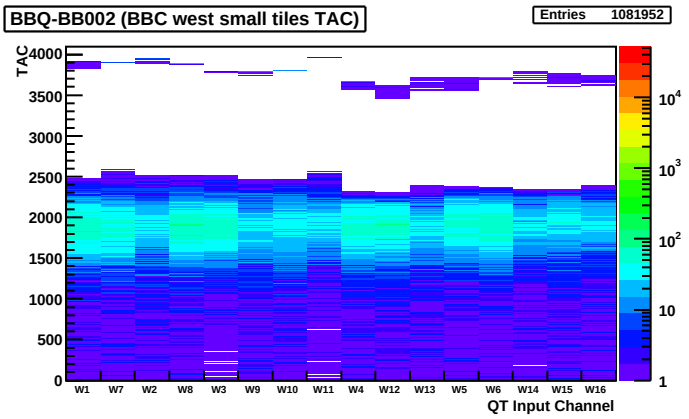

L1-TF201
Entries 67622
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

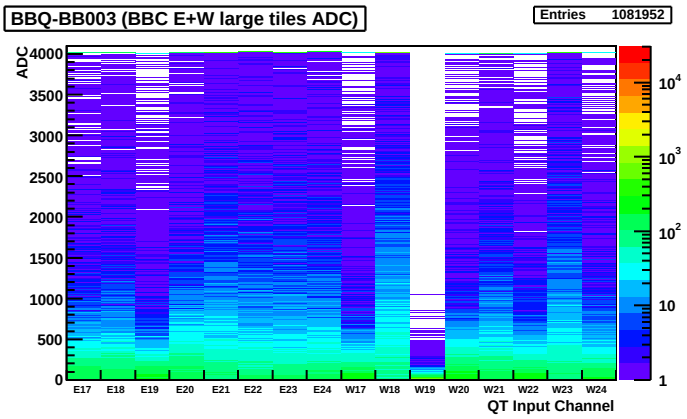

L1-TF201
Entries 405732
Threshold bits
1
0
45174
44219
39030
41637
37371
41617
22448
23403
28592
25985
30251
26005
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

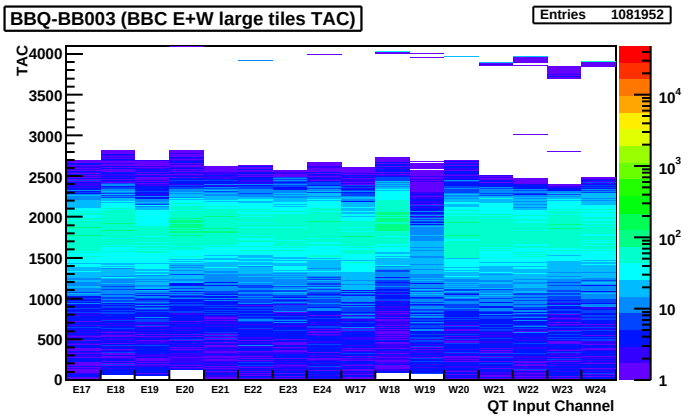

BBQ-BB001 (BBC east small tiles ADC)
Entries 1081952
ADC
QT Input Channel

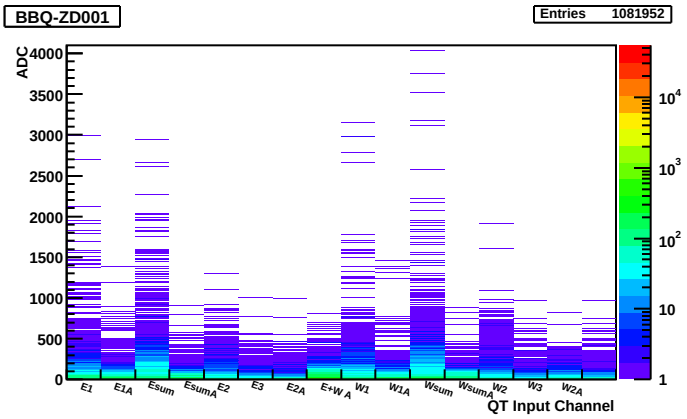

BBQ-BB001 (BBC east small tiles TAC)
Entries 1081952
TAC
QT Input Channel

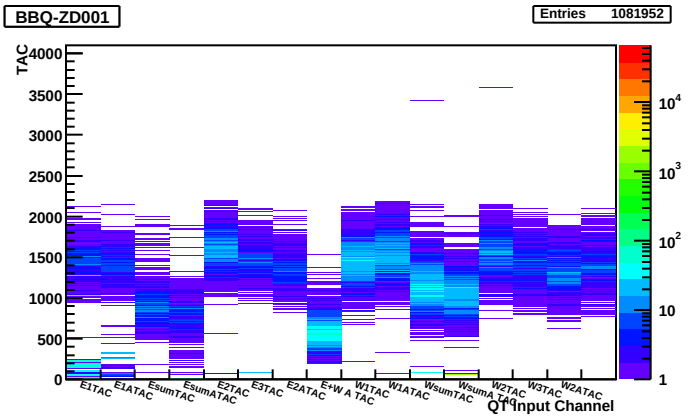

BBQ-BB002 (BBC west small tiles ADC)
Entries 1081952
ADC
QT Input Channel

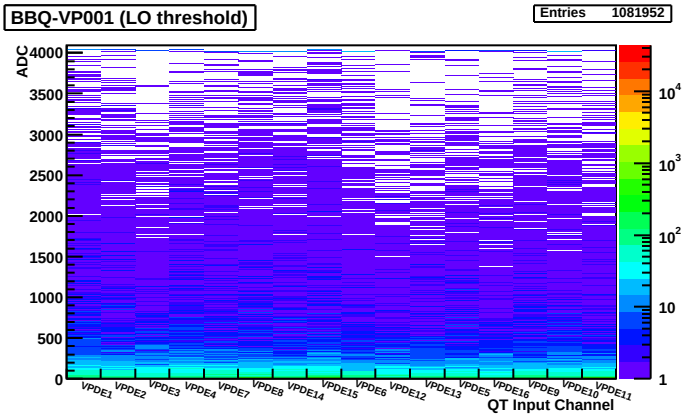

BBQ-BB002 (BBC west small tiles TAC)
Entries 1081952
TAC
QT Input Channel

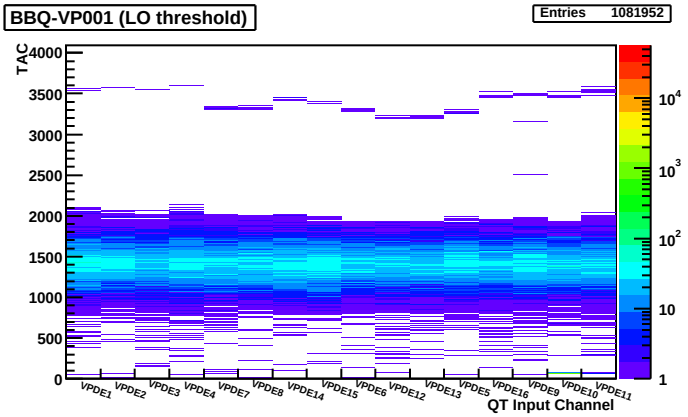

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1081952
ADC
QT Input Channel

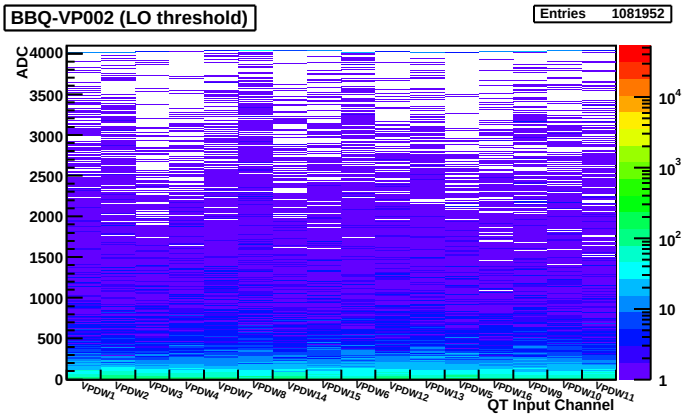

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1081952
TAC
QT Input Channel

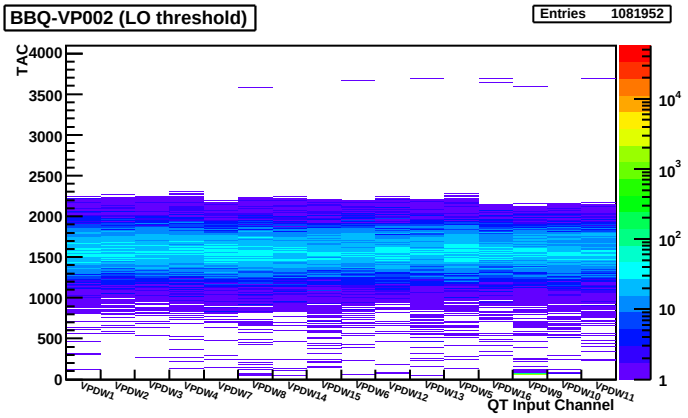

BBQ-ZD001
Entries 1081952
ADC
QT Input Channel

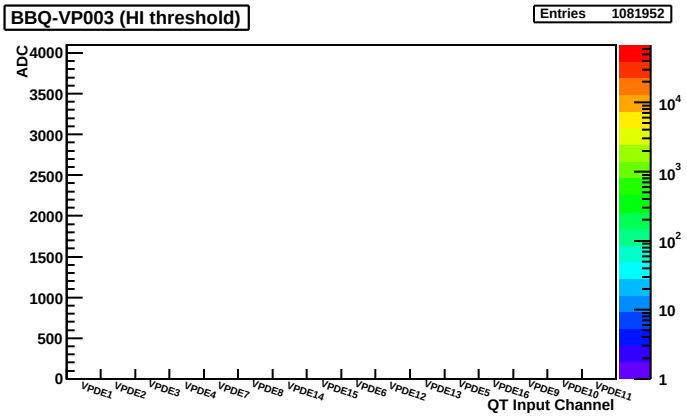

BBQ-ZD001
Entries 1081952
TAC
QT Input Channel

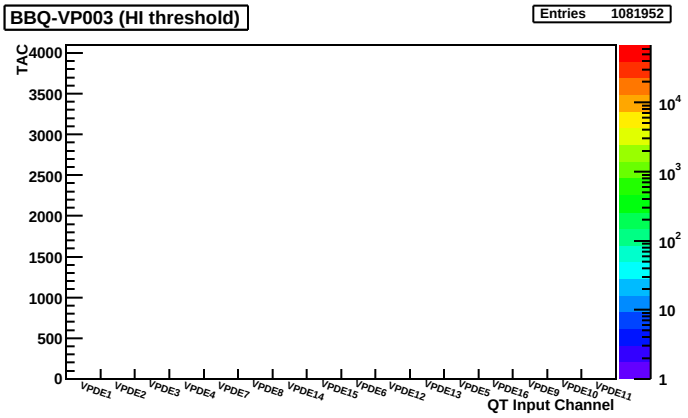

BBQ-VP001 (LO threshold)
Entries 1081952
ADC
QT Input Channel

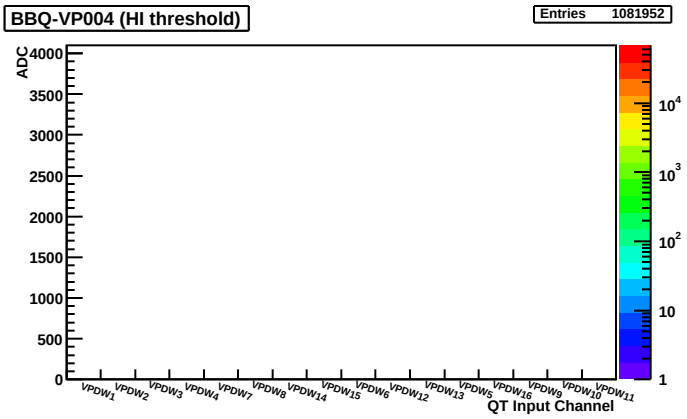

BBQ-VP001 (LO threshold)
Entries 1081952
TAC
QT Input Channel

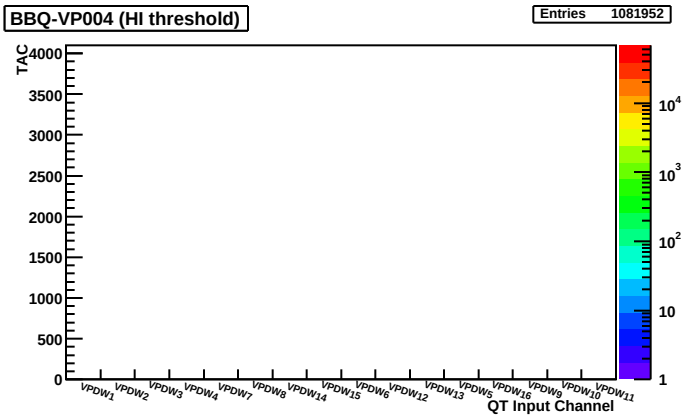

BBQ-VP002 (LO threshold)
Entries 1081952
ADC
QT Input Channel

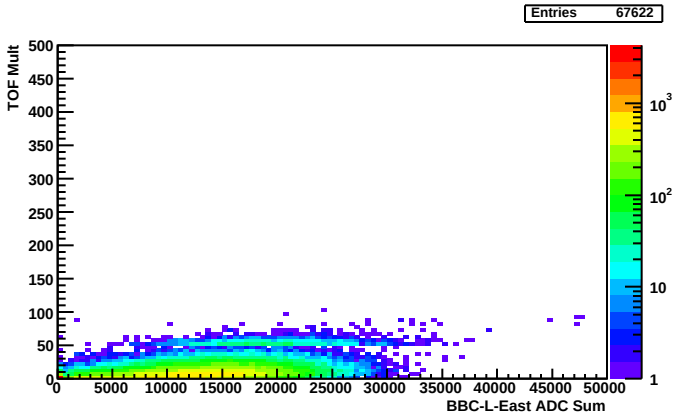

BBQ-VP002 (LO threshold)
Entries 1081952
TAC
QT Input Channel

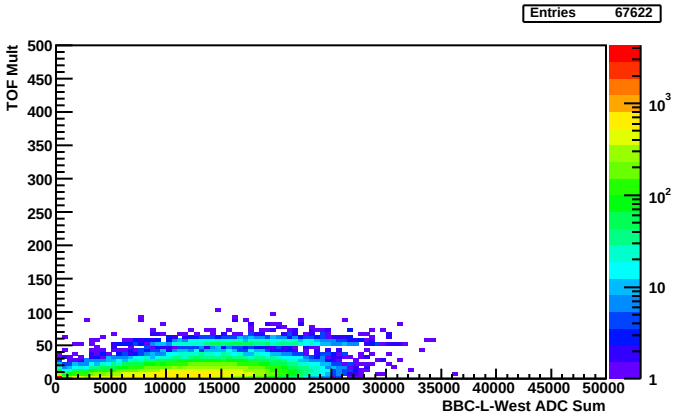

BBQ-VP003 (HI threshold)
Entries 1081952
ADC
QT Input Channel

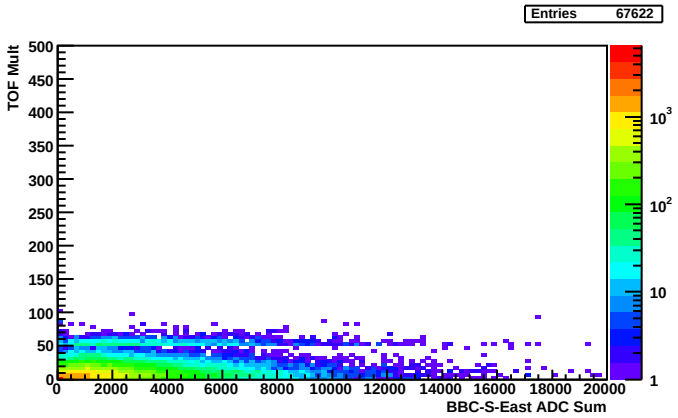

BBQ-VP003 (HI threshold)
Entries 1081952
TAC
QT Input Channel

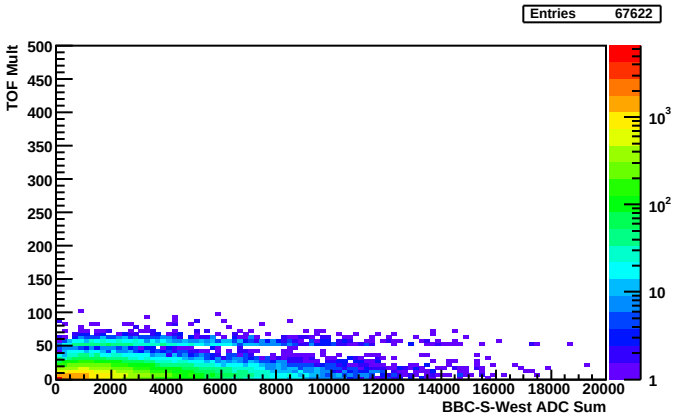

BBQ-VP004 (HI threshold)
Entries 1081952
ADC
QT Input Channel

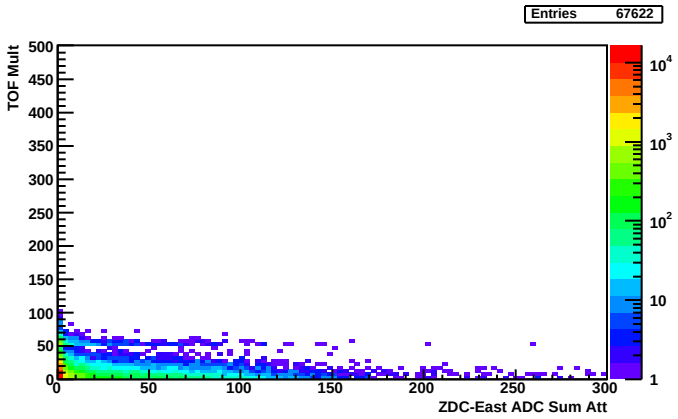

BBQ-VP004 (HI threshold)
Entries 1081952
TAC
QT Input Channel

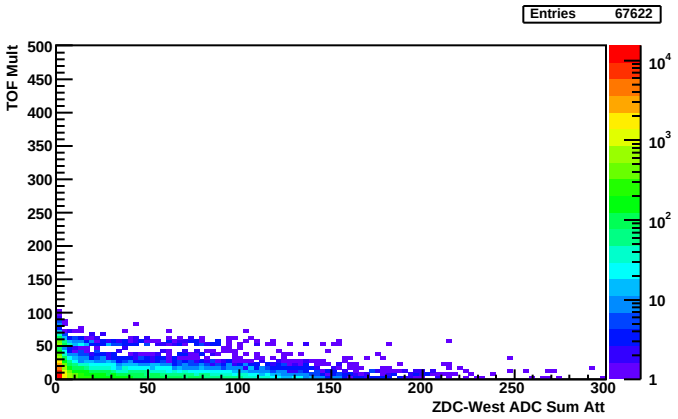

Entries 67622
TOF Mult
BBC-L-East ADC Sum

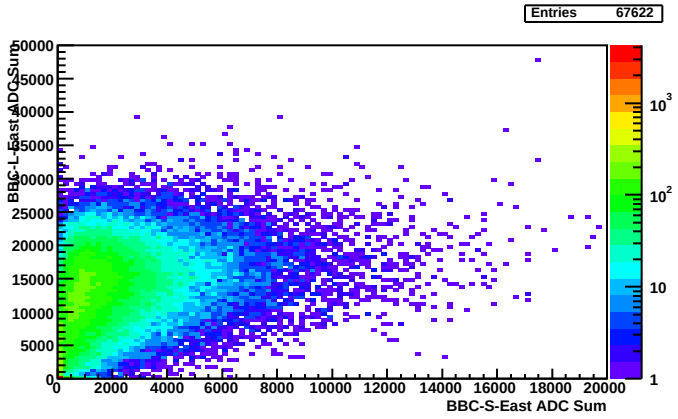

Entries 67622
TOF Mult
BBC-L-West ADC Sum

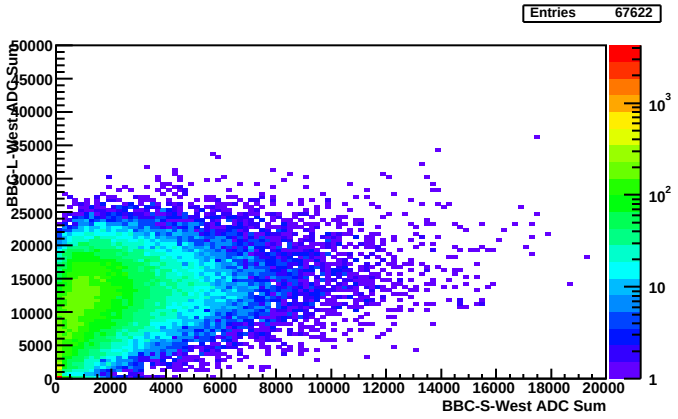

Entries 67622
TOF Mult
BBC-S-East ADC Sum

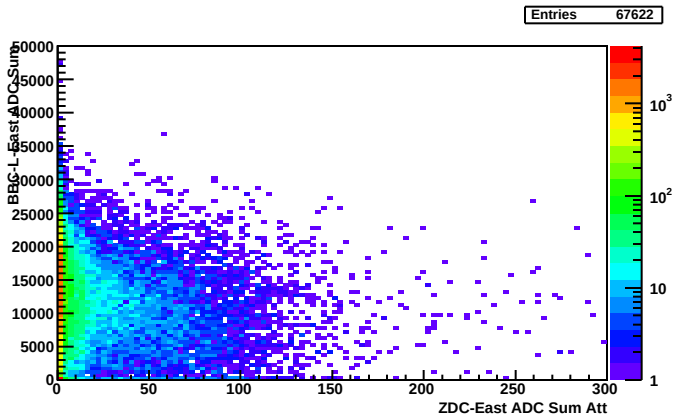

Entries 67622
TOF Mult
BBC-S-West ADC Sum

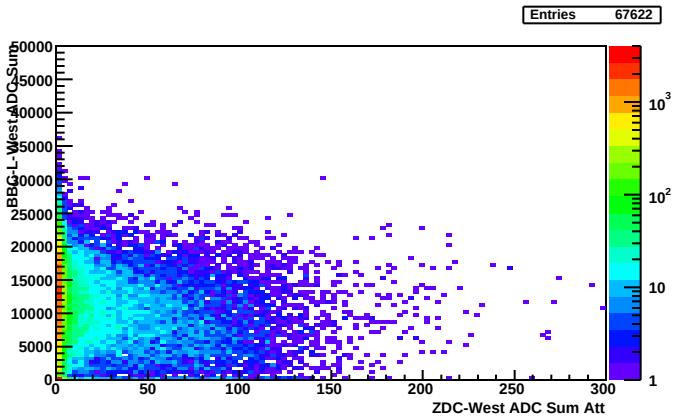

Entries 67622
TOF Mult
ZDC-East ADC Sum Att

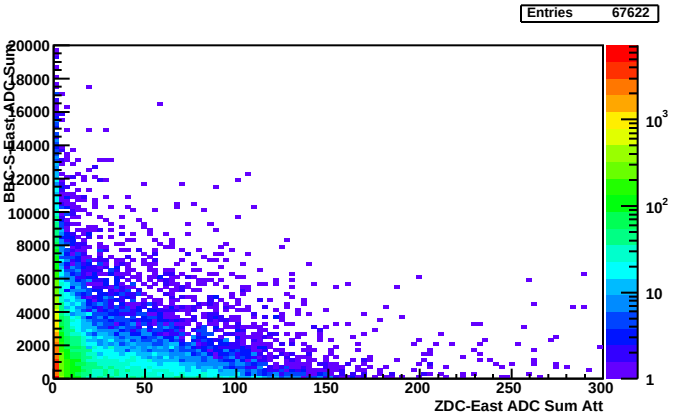

Entries 67622
TOF Mult
ZDC-West ADC Sum Att

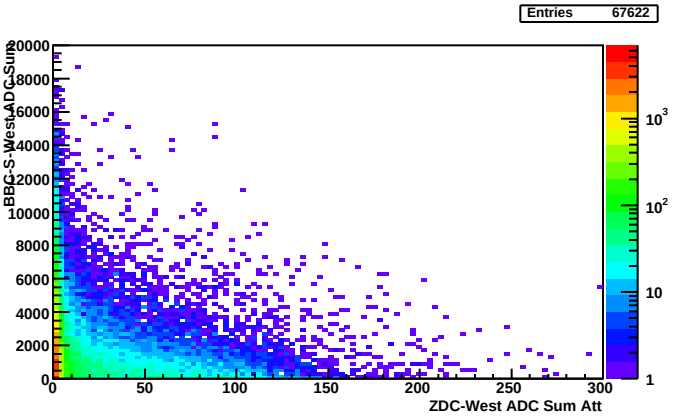

Entries 67622
BBC-L-East ADC Sum
BBC-S-East ADC Sum

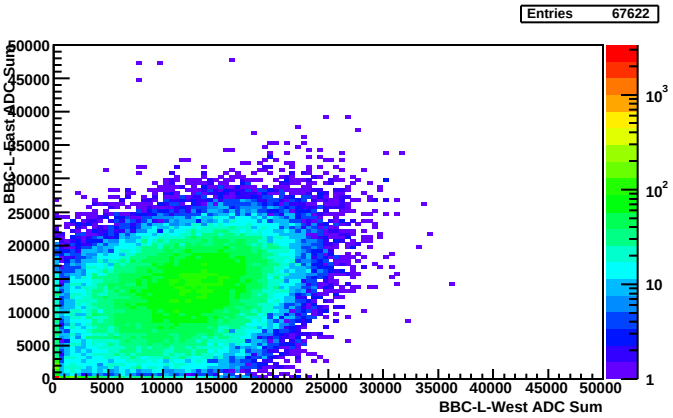

Entries 67622
BBC-L-West ADC Sum
BBC-S-West ADC Sum

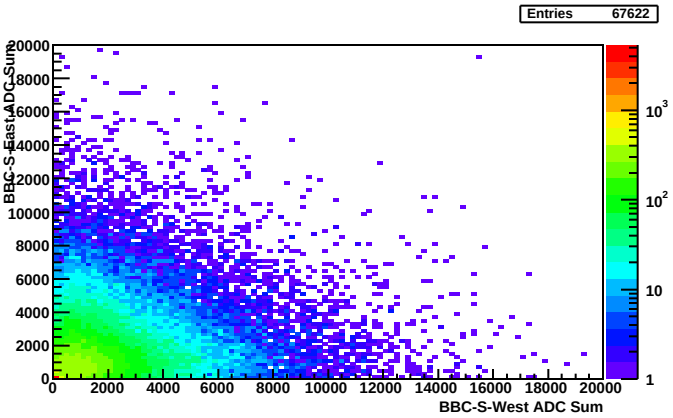

Entries 67622
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

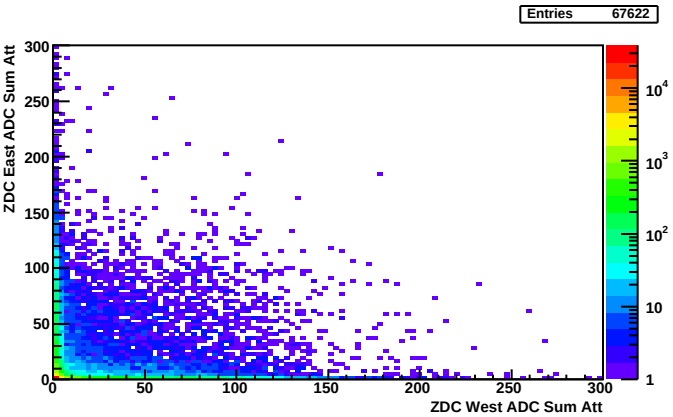

Entries 67622
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

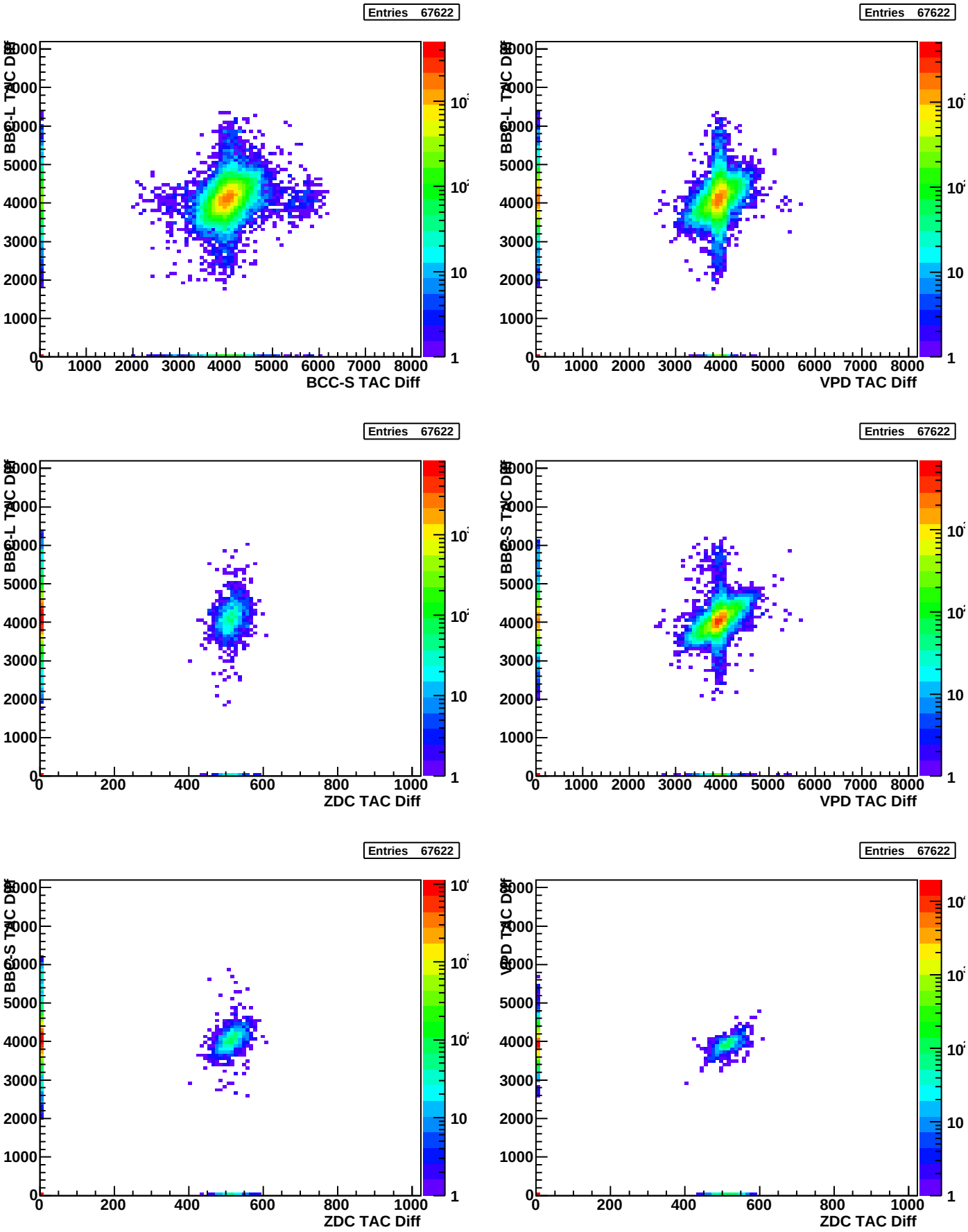

Entries 67622
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

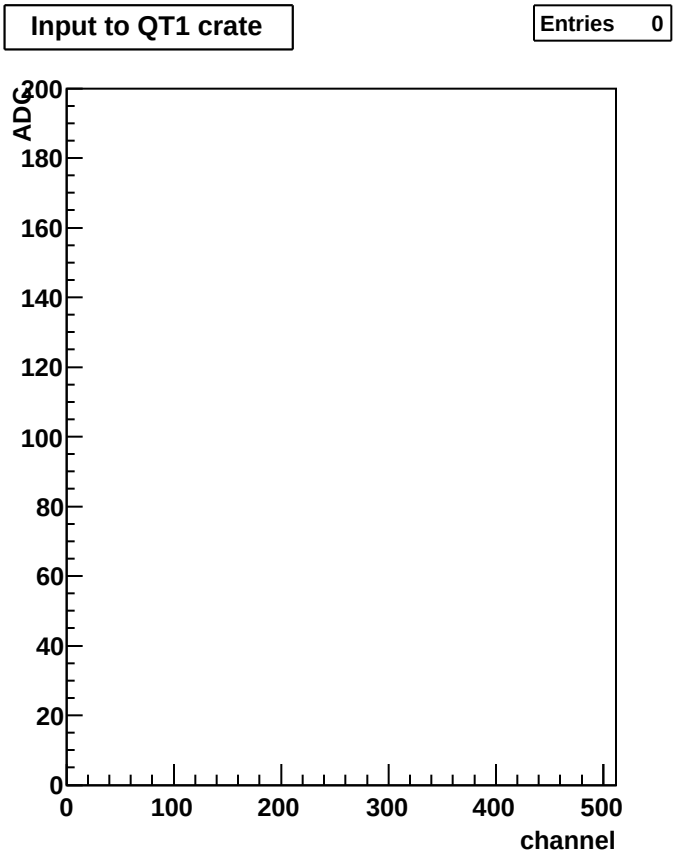

Entries 67622
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

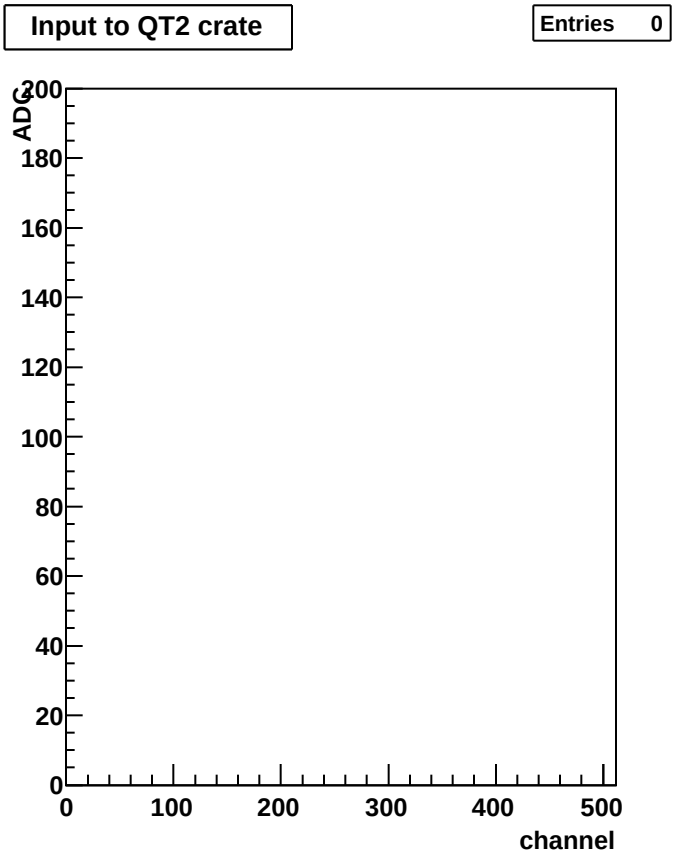

Entries 67622
BBC-L-East ADC Sum
BBC-L-West ADC Sum

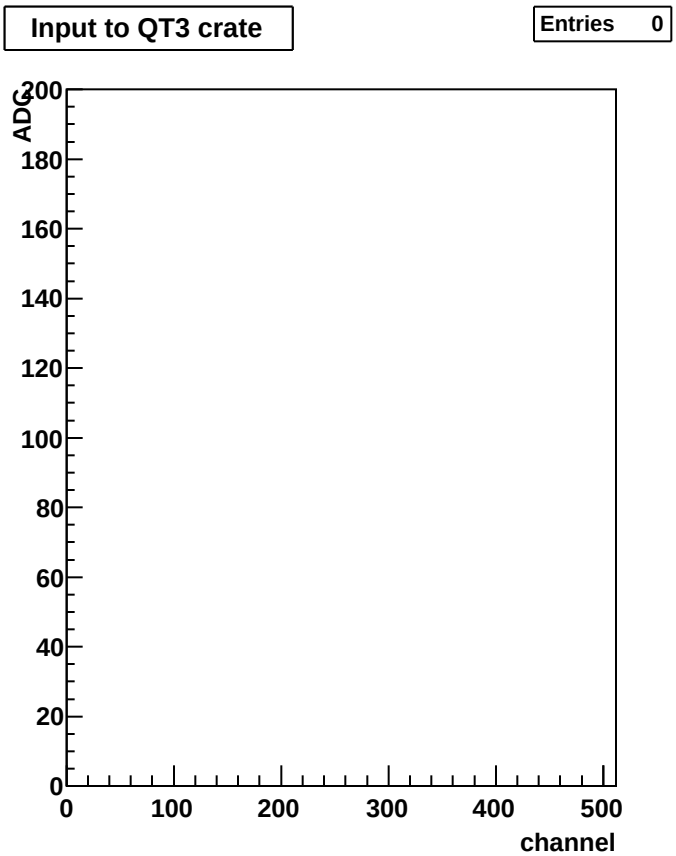

Entries 67622
BBC-S-East ADC Sum
BBC-S-West ADC Sum

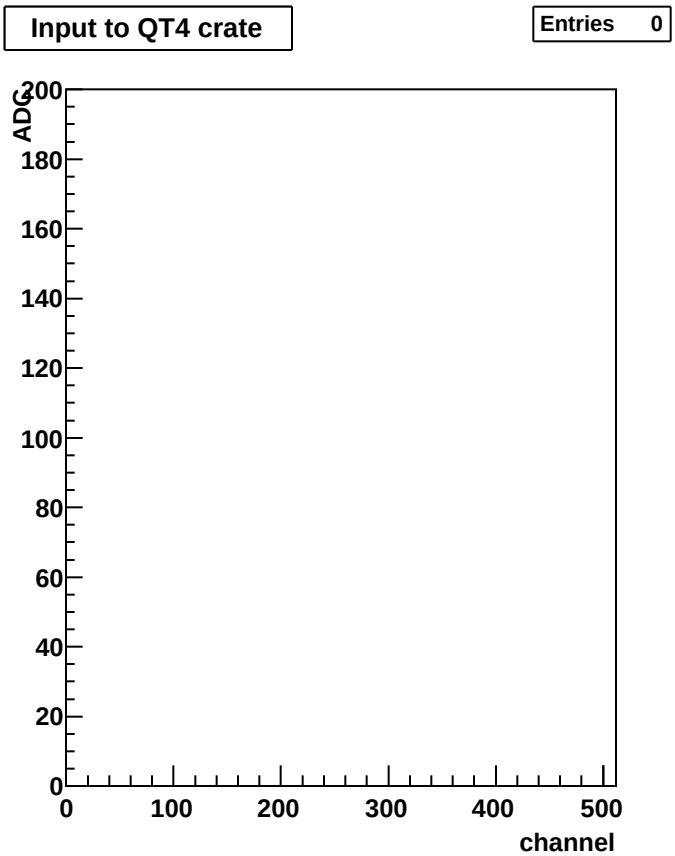

Entries 67622
ZDC East ADC Sum Att
ZDC West ADC Sum Att

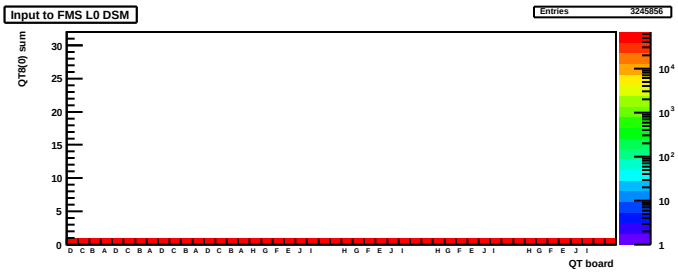

Entries 67622
BBC-L TAC Diff
BCC-S TAC Diff
Entries 67622
BBC-L TAC Diff
VPD TAC Diff
Entries 67622
BBC-L TAC Diff
ZDC TAC Diff
Entries 67622
BBC-S TAC Diff
VPD TAC Diff
Entries 67622
BBC-S TAC Diff
ZDC TAC Diff
Entries 67622
VPD TAC Diff
ZDC TAC Diff

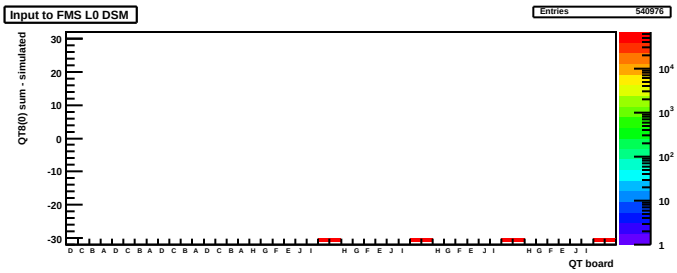

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

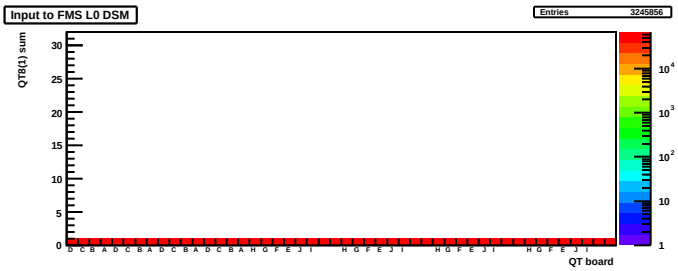

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

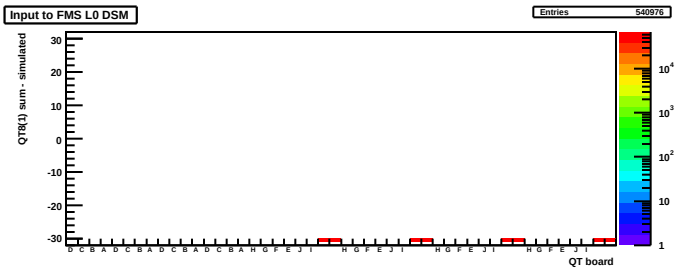

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

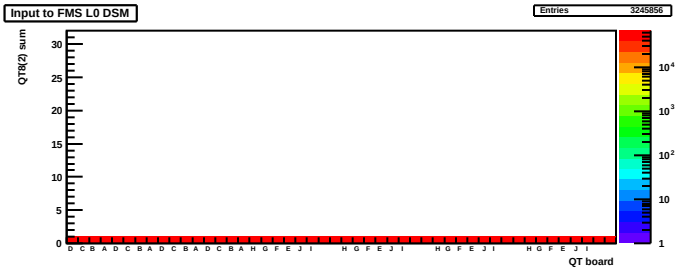

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

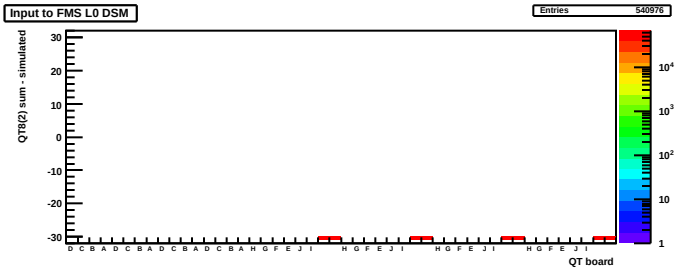

Input to FMS L0 DSM
Entries 3245856
QT8(0) sum
QT board

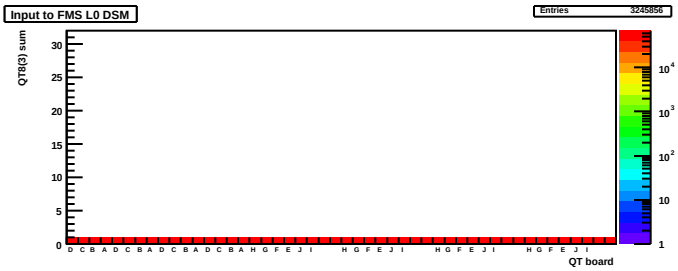

Input to FMS L0 DSM
Entries 540976
QT8(0) sum - simulated
QT board

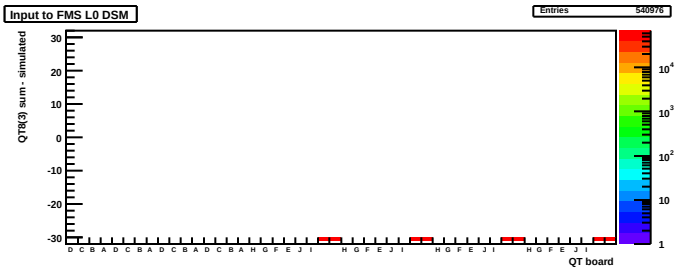

Input to FMS L0 DSM
Entries 3245856
QT8(1) sum
QT board

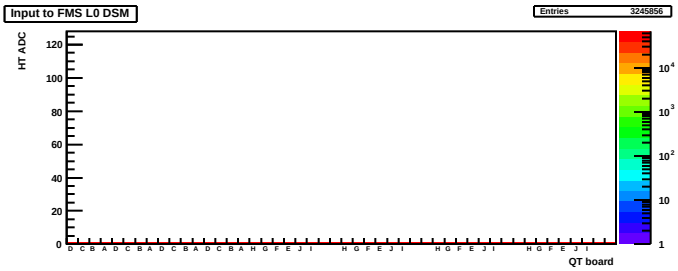

Input to FMS L0 DSM
Entries 540976
QT8(1) sum - simulated
QT board

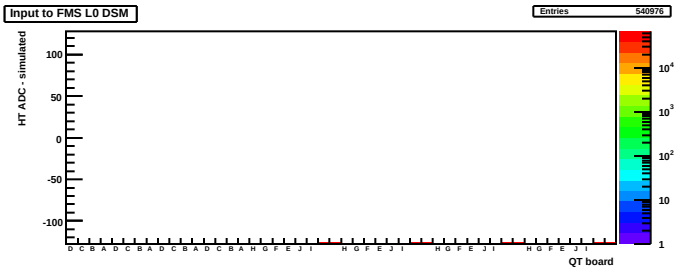

Input to FMS L0 DSM
Entries 3245856
QT8(2) sum
QT board

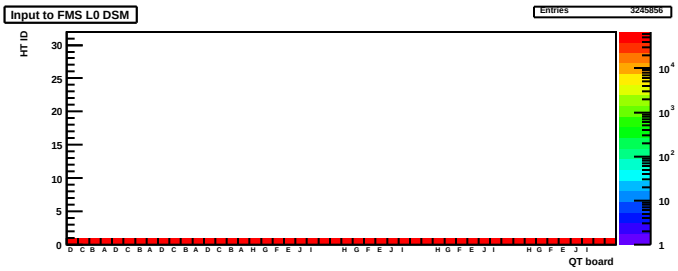

Input to FMS L0 DSM
Entries 540976
QT8(2) sum - simulated
QT board

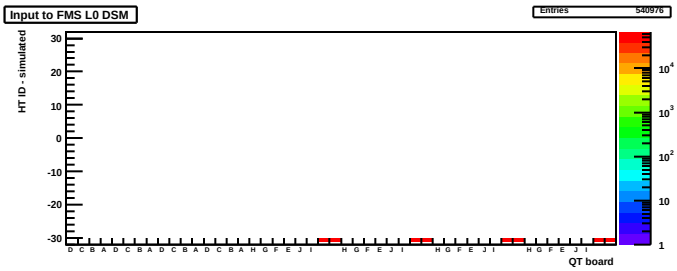

Input to FMS L0 DSM
Entries 3245856
QT8(3) sum
QT board

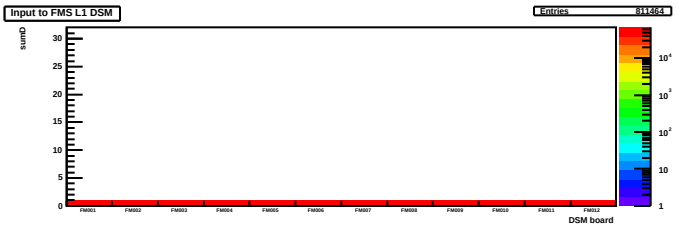

Input to FMS L0 DSM
Entries 540976
QT8(3) sum - simulated
QT board

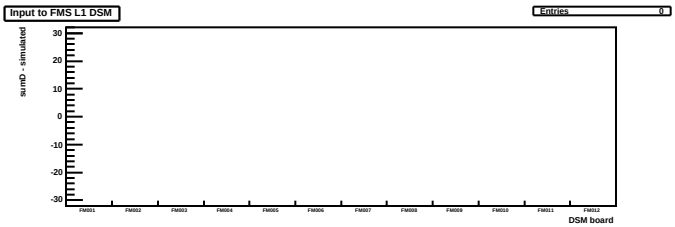

Input to FMS L0 DSM
Entries 3245856
HT ADC
QT board

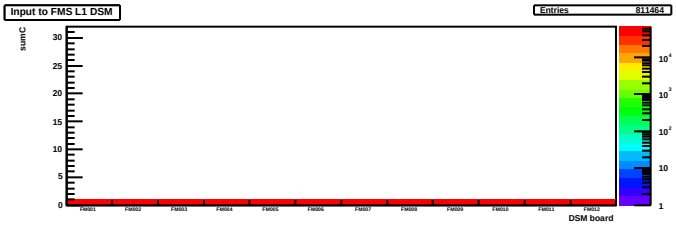

Input to FMS L0 DSM
Entries 540976
HT ADC - simulated
QT board

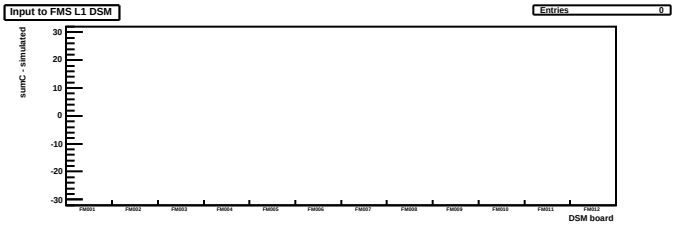

Input to FMS L0 DSM
Entries 3245856
HT ID
QT board

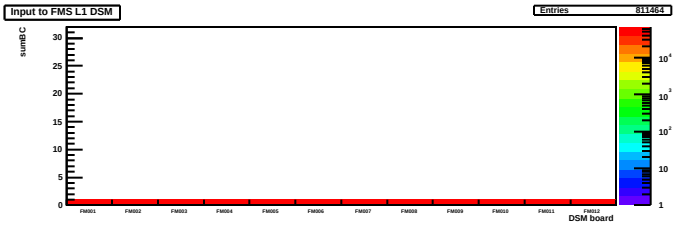

Input to FMS L0 DSM
Entries 540976
HT ID - simulated
QT board

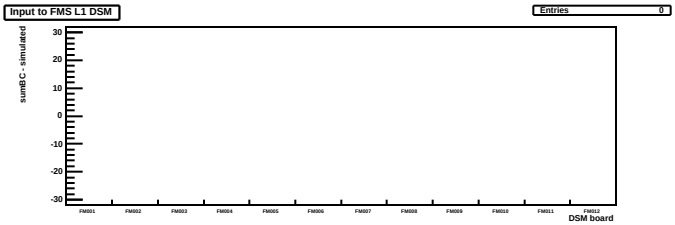

Input to FMS L1 DSM
Entries 811464
sumD
DSM board

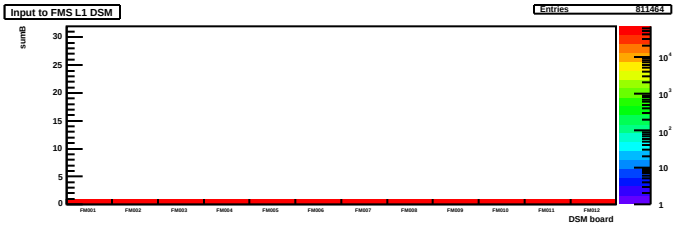

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

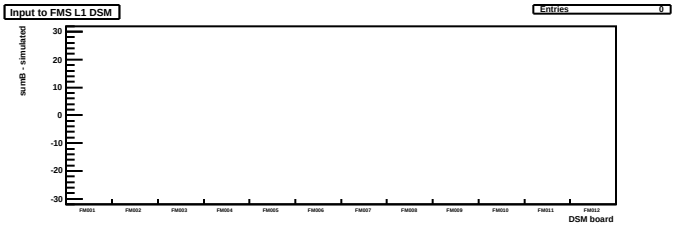

Input to FMS L1 DSM
Entries 811464
sumC
DSM board

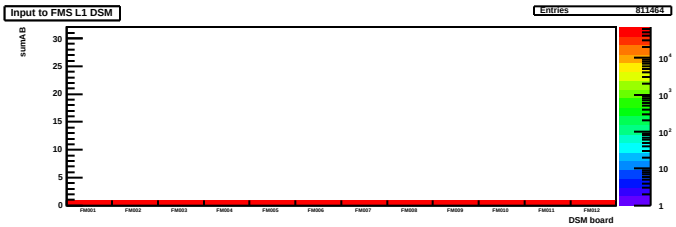

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

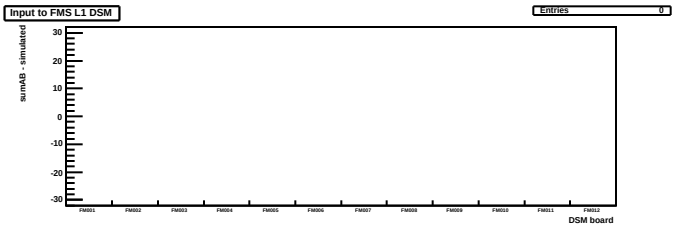

Input to FMS L1 DSM
Entries 811464
sumBC
DSM board

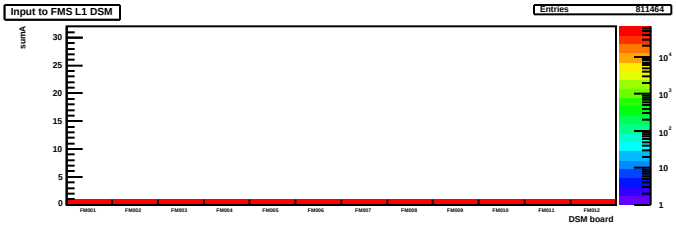

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

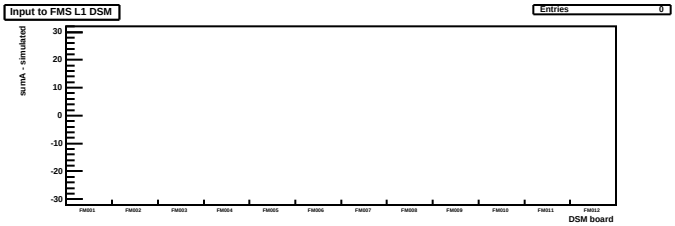

Input to FMS L1 DSM
Entries 811464
sumB
DSM board

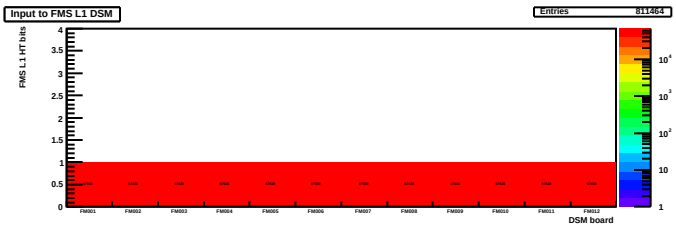

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

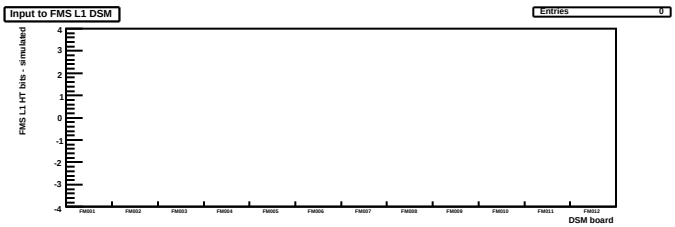

Input to FMS L1 DSM
Entries 811464
sumAB
DSM board

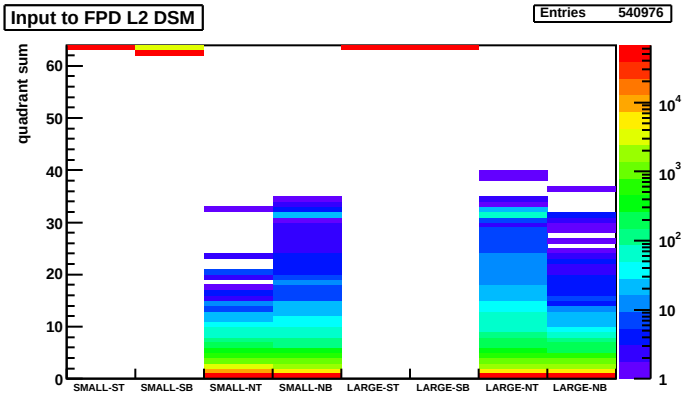

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

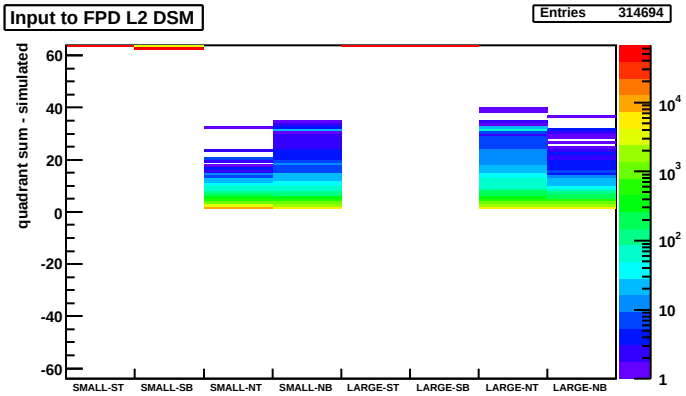

Input to FMS L1 DSM
Entries 811464
sumA
DSM board

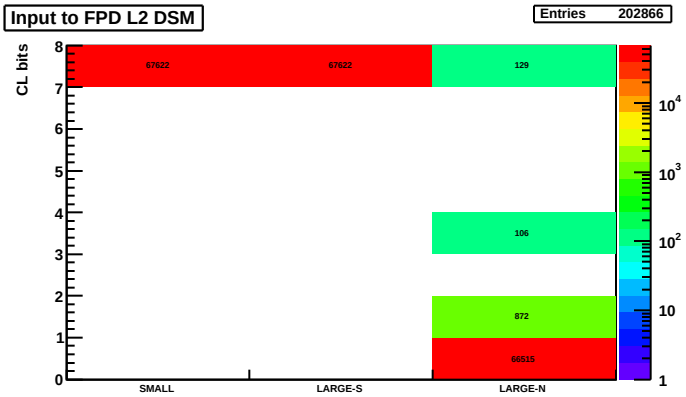

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

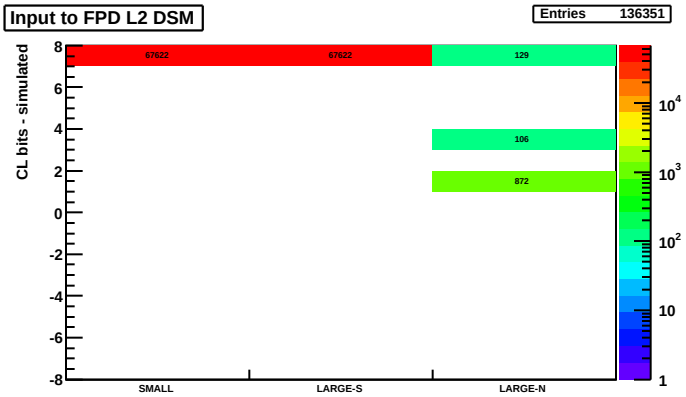

Input to FMS L1 DSM
Entries 811464
FMS L1 HT bits
DSM board

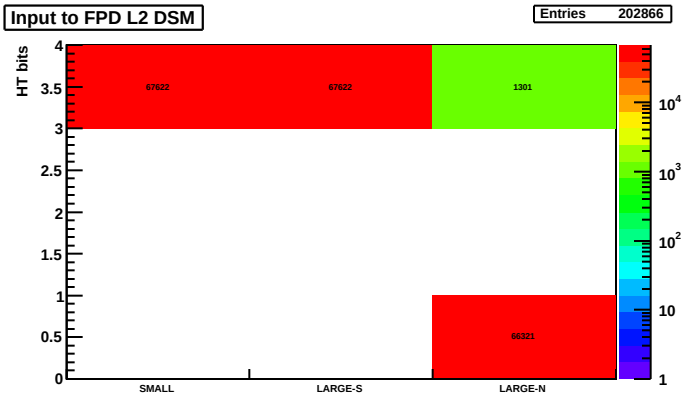

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

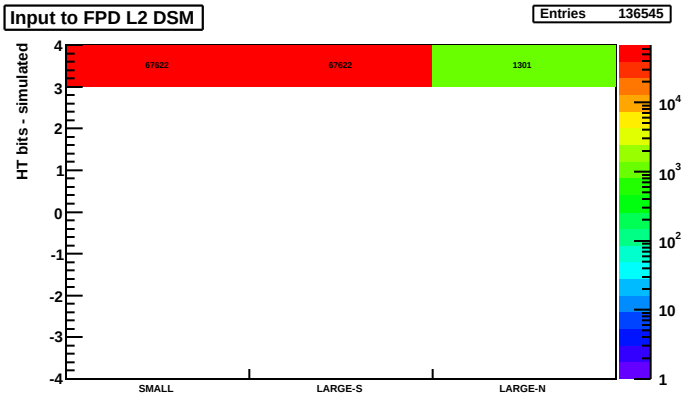

Input to FPD L2 DSM
Entries 540976
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

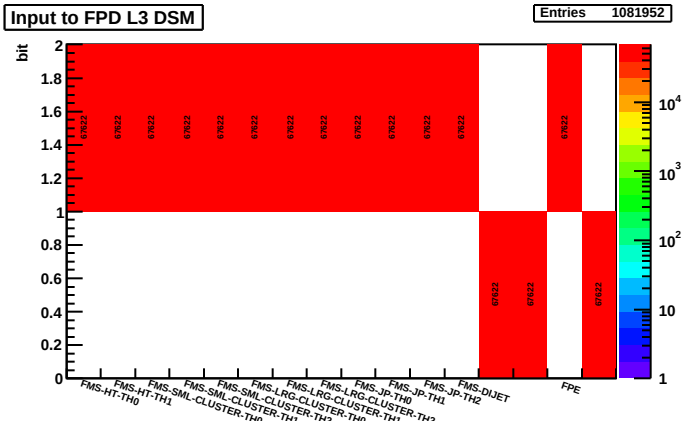

Input to FPD L2 DSM
Entries 314694
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

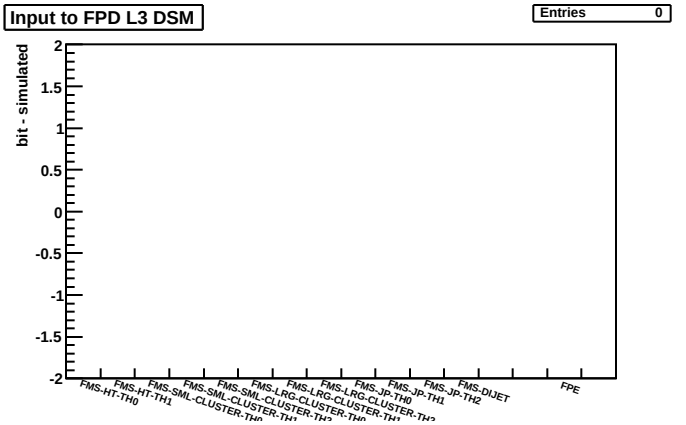

Input to FPD L2 DSM
Entries 202866
CL bits
67622
67622
129
106
872
66515
SMALL
LARGE-S
LARGE-N

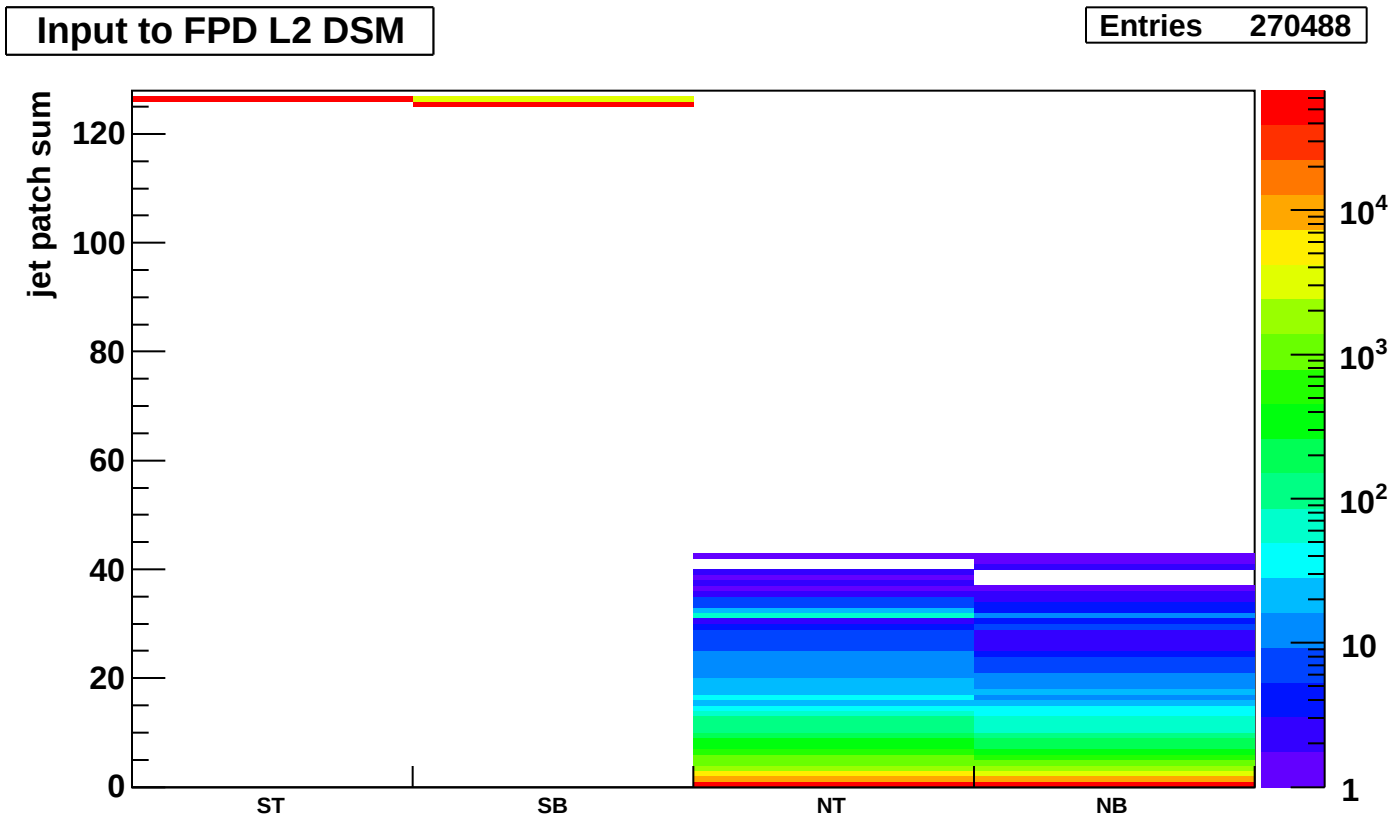

Input to FPD L2 DSM
Entries 136351
CL bits - simulated
67622
67622
129
106
872
SMALL
LARGE-S
LARGE-N

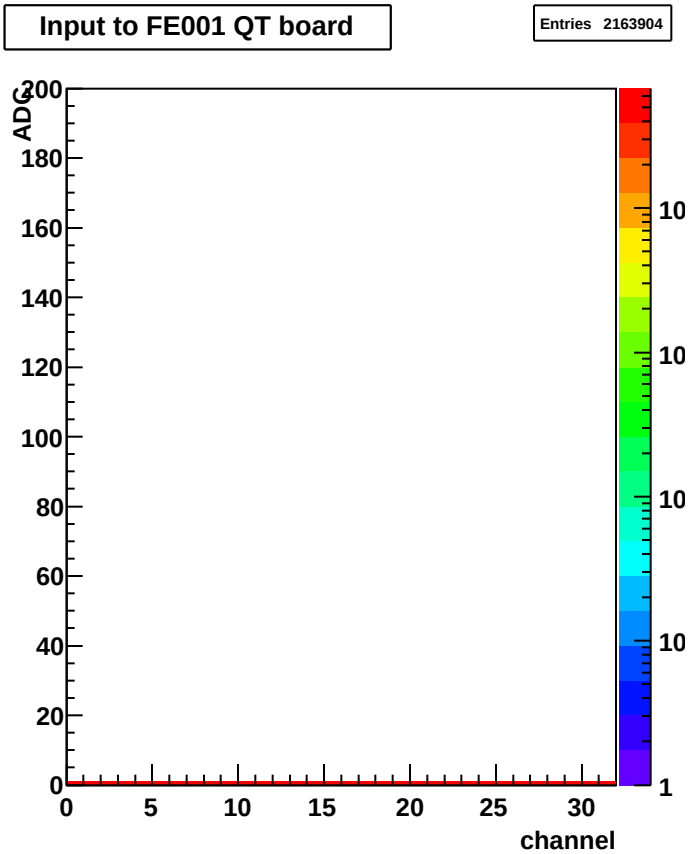

Input to FPD L2 DSM
Entries 202866
HT bits
67622
67622
1301
66321
SMALL
LARGE-S
LARGE-N

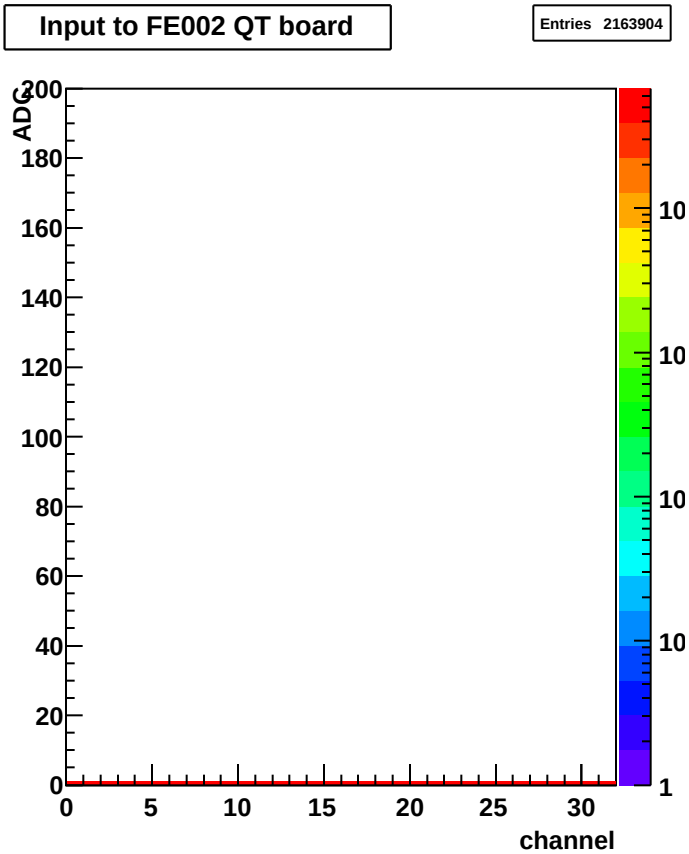

Input to FPD L2 DSM
Entries 136545
HT bits - simulated
67622
67622
1301
SMALL
LARGE-S
LARGE-N

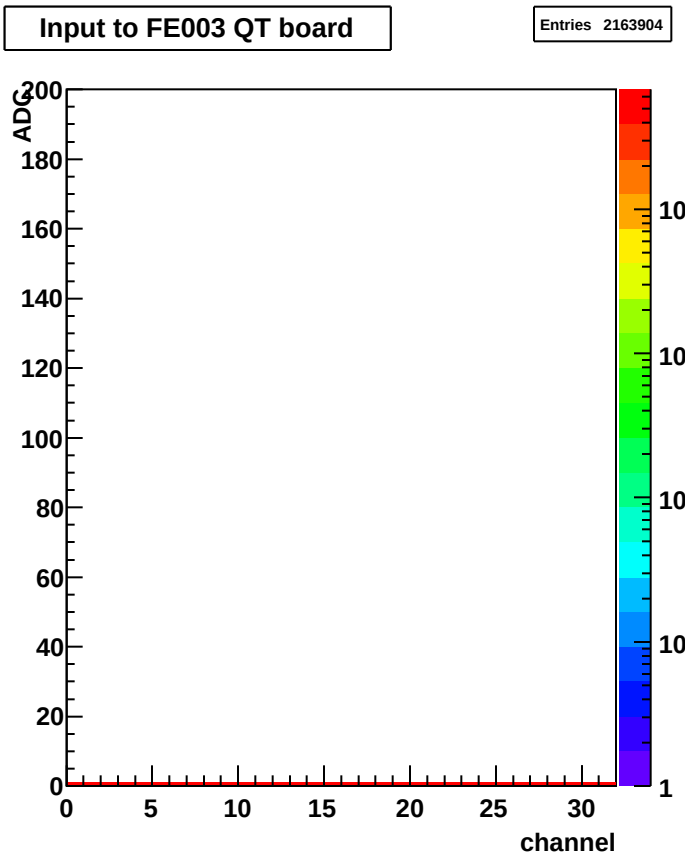

Input to FPD L3 DSM
Entries 1081952
bit
67622
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

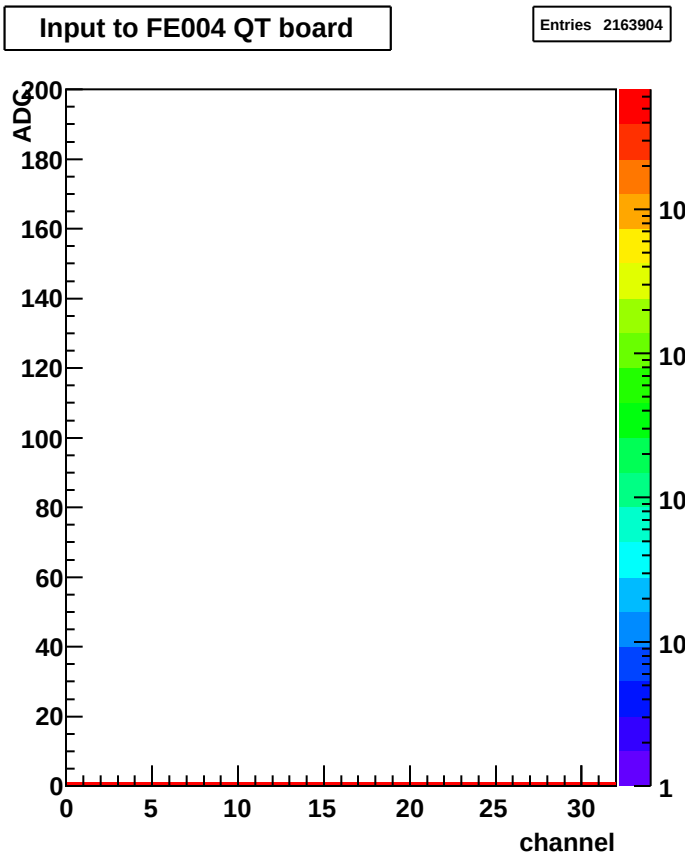

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

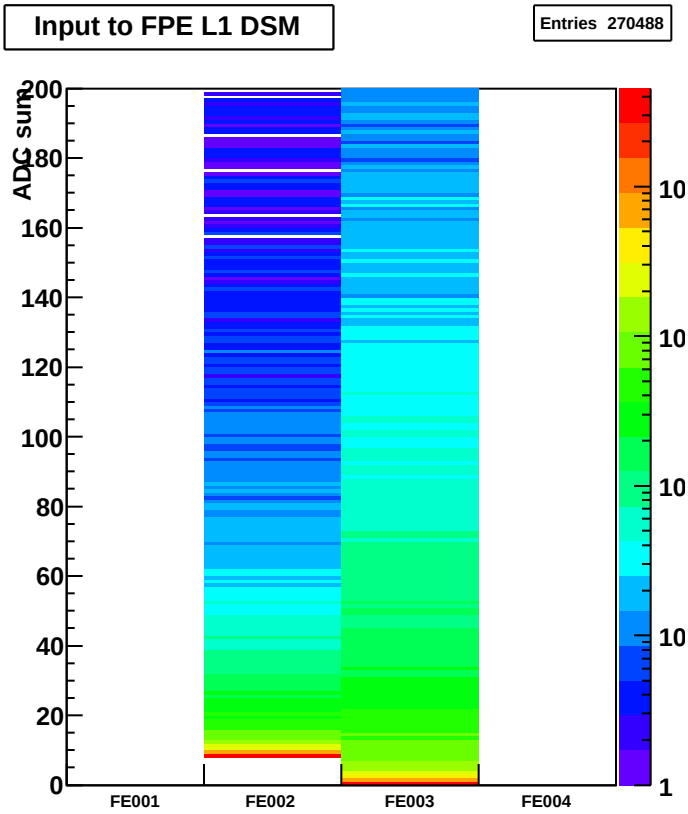

Input to FPD L2 DSM
Entries 270488
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

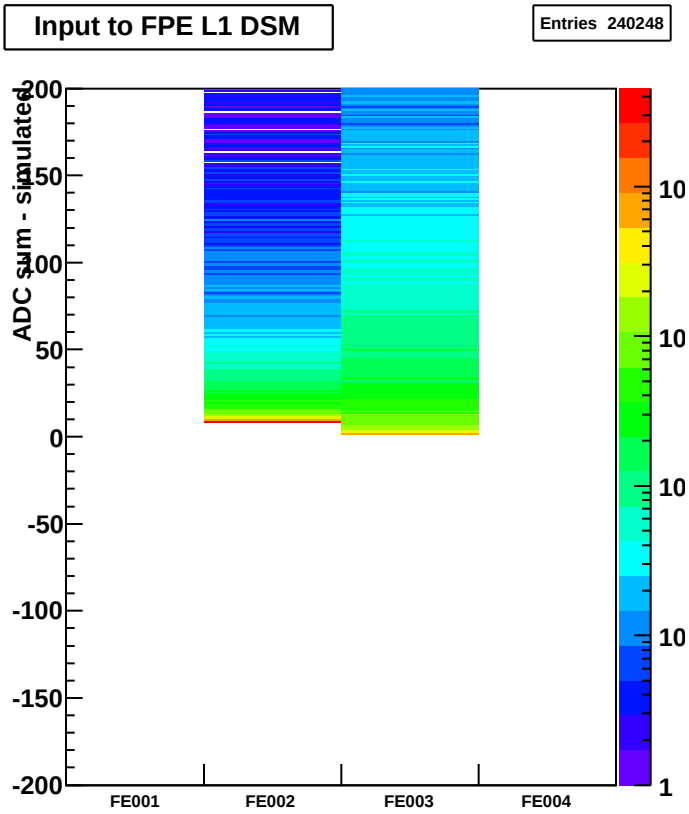

Input to FE001 QT board
Entries 2163904
ADC
channel

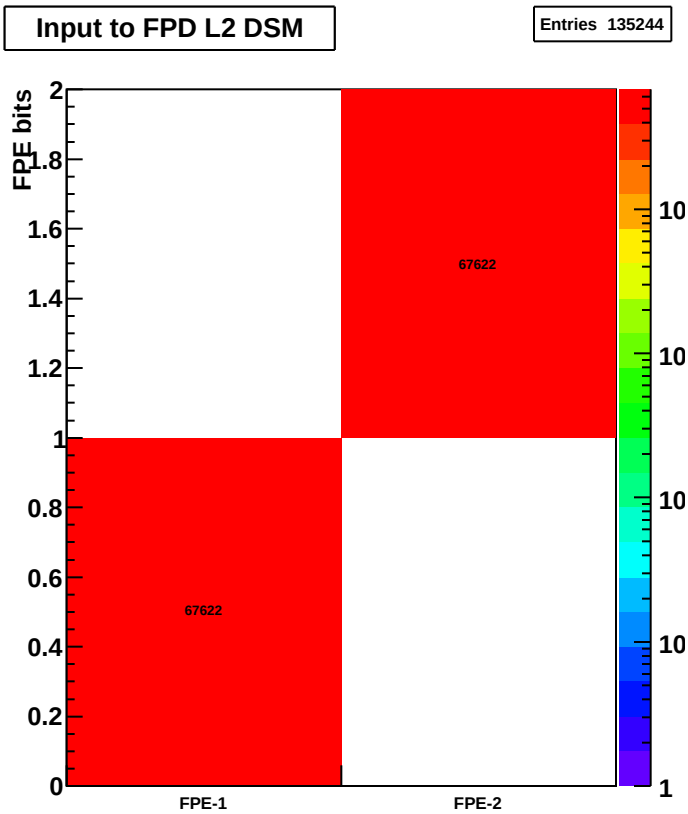

Input to FE002 QT board
Entries 2163904
ADC
channel

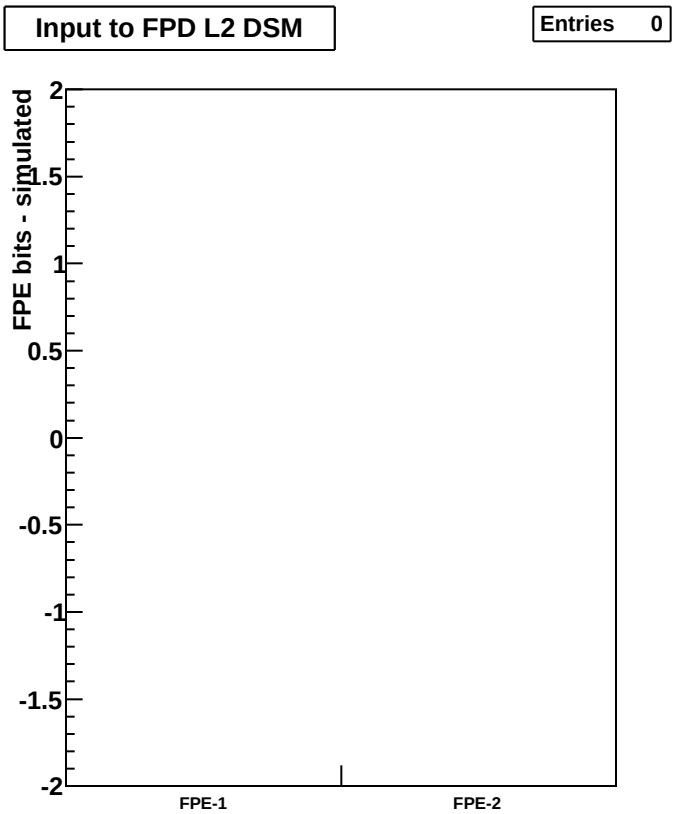

Input to FE003 QT board
Entries 2163904
ADC
channel


Input to FE004 QT board
Entries 2163904
ADC
channel


Input to FPE L1 DSM
Entries 270488
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 240248
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 135244
FPE bits
67622
67622
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2