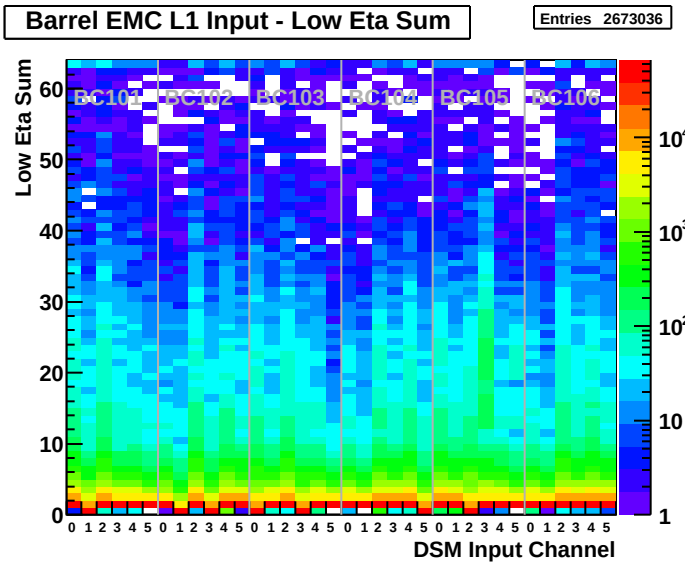
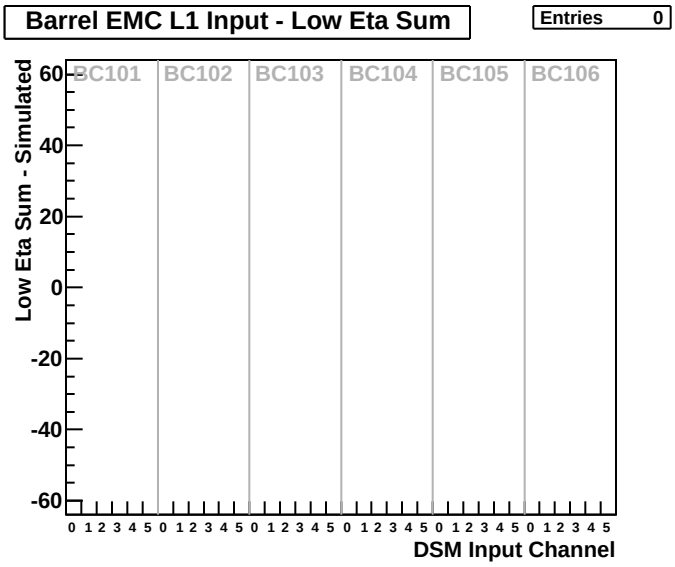
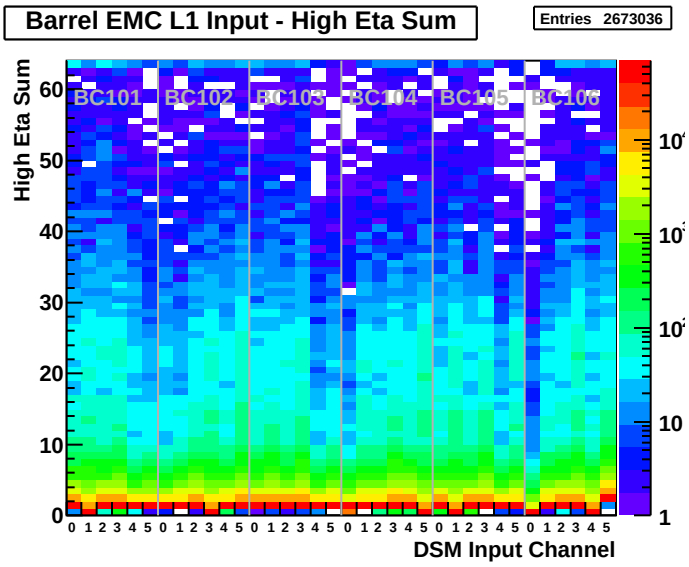
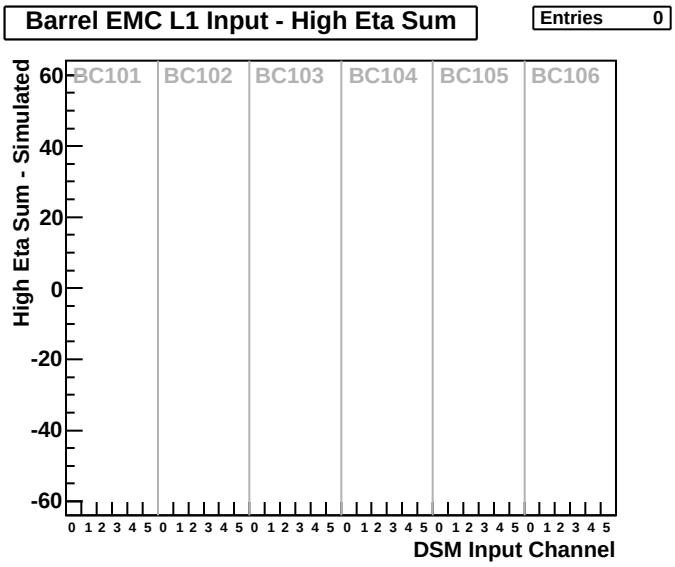
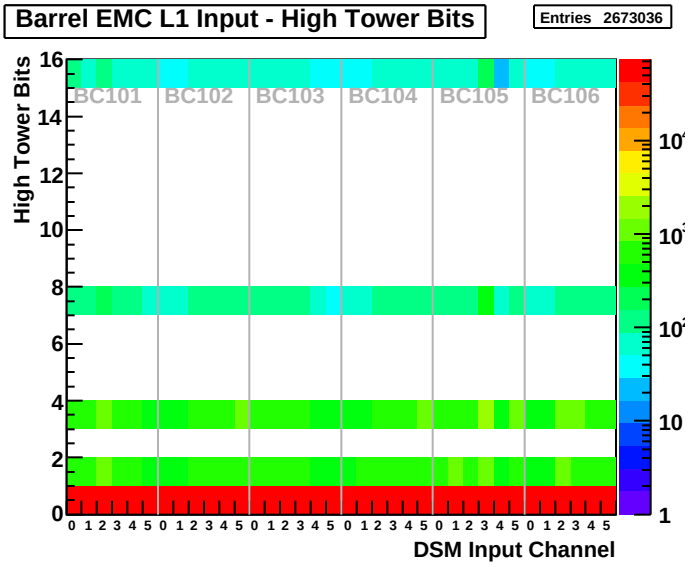
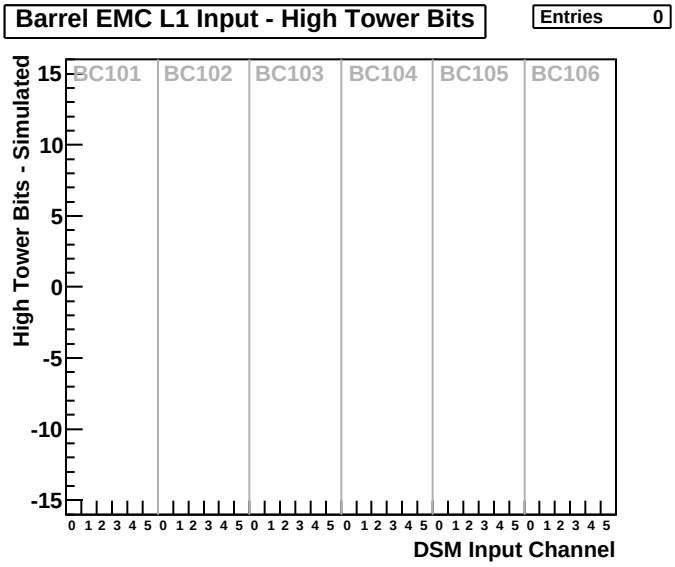

Barrel EMC L1 Input - Low Eta Sum
Entries 2673036
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

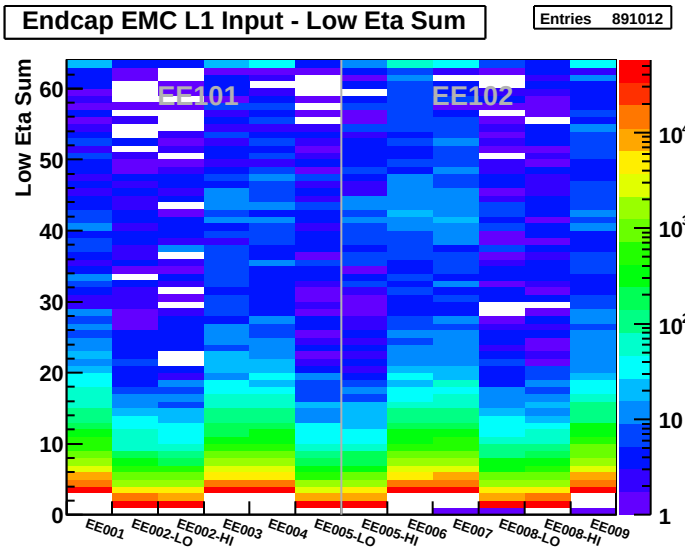

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

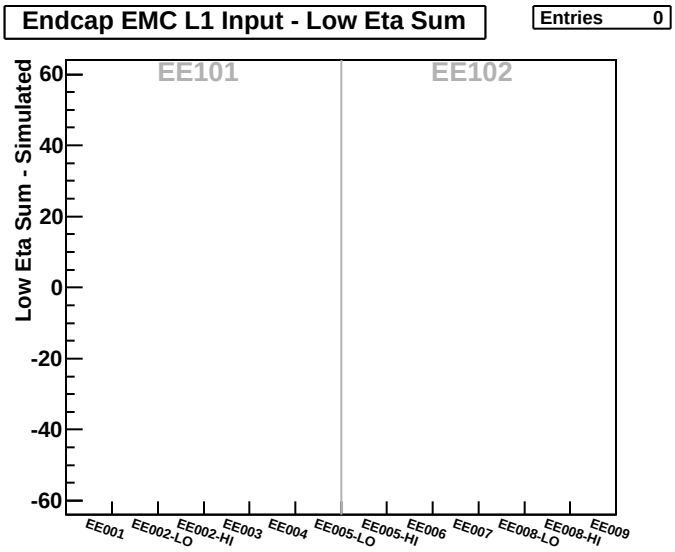

Barrel EMC L1 Input - High Eta Sum
Entries 2673036
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

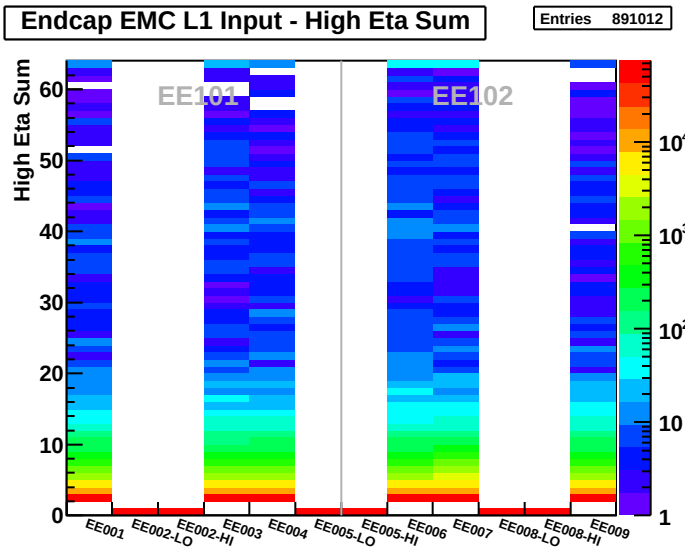

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

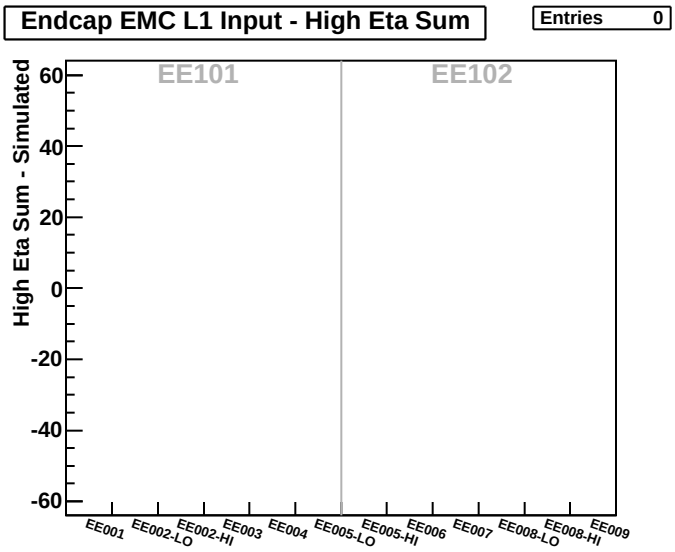

Barrel EMC L1 Input - High Tower Bits
Entries 2673036
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

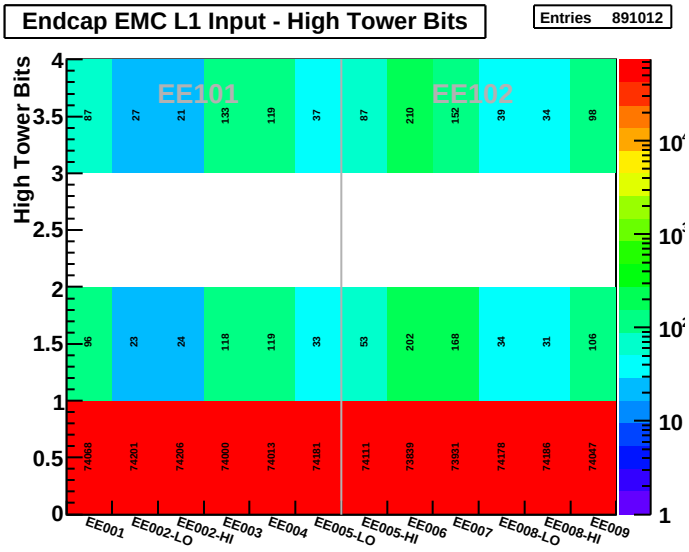

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

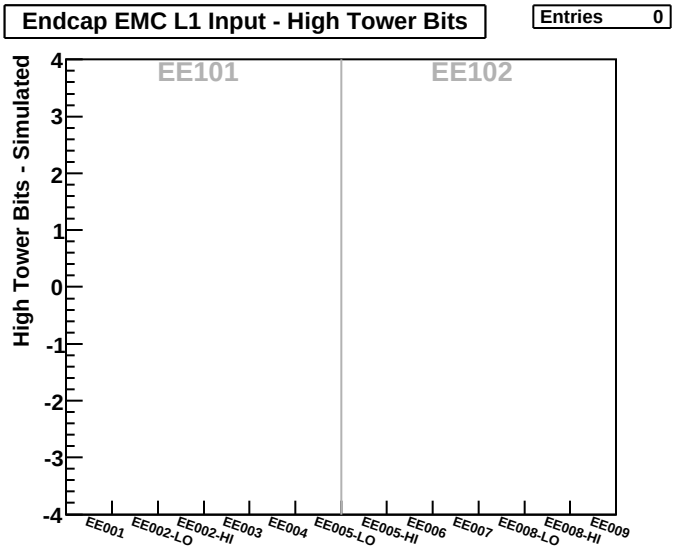

Endcap EMC L1 Input - Low Eta Sum
Entries 891012
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

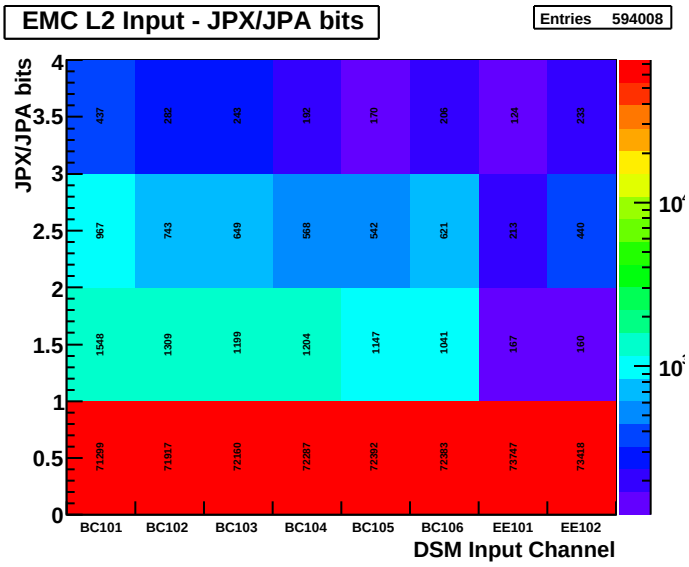

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

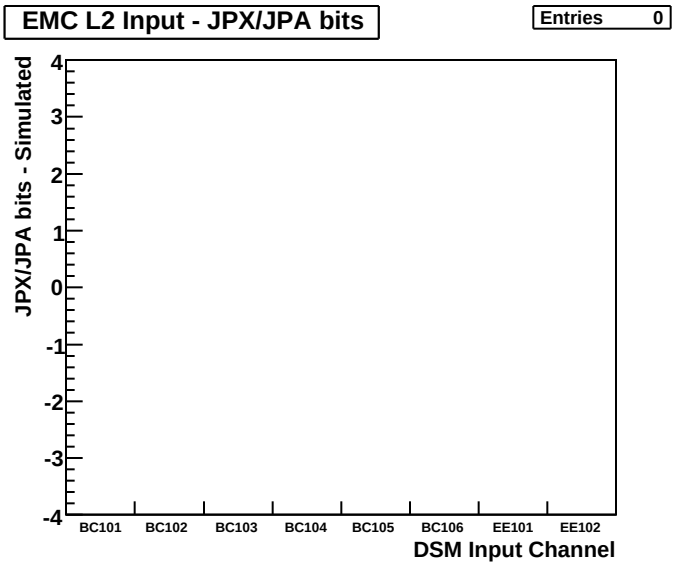

Endcap EMC L1 Input - High Eta Sum
Entries 891012
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

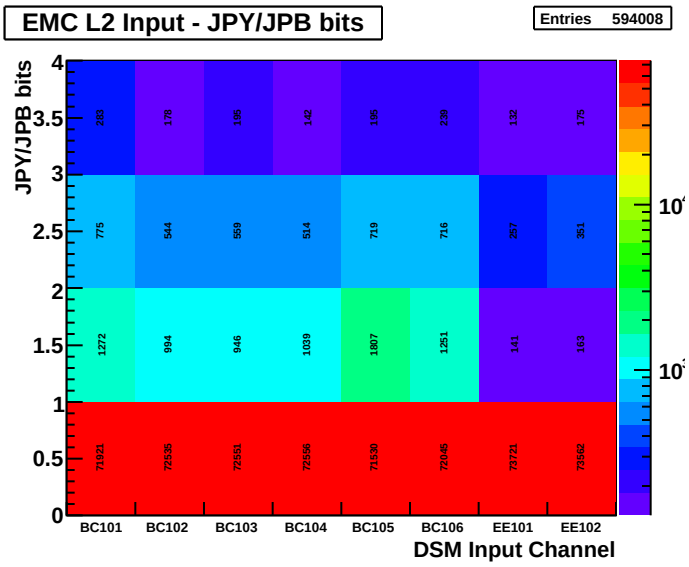

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

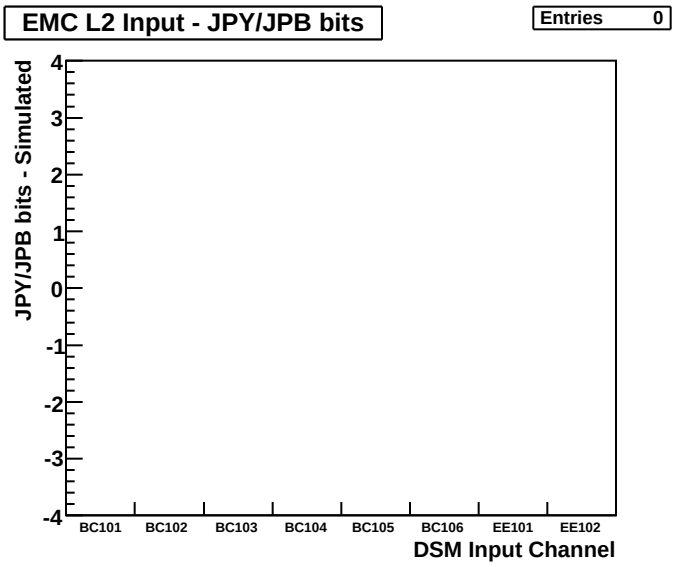

Endcap EMC L1 Input - High Tower Bits
Entries 891012
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

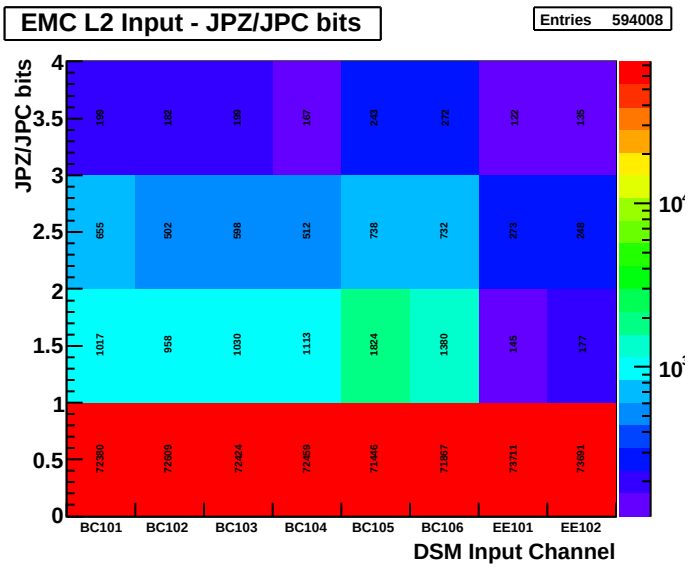

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

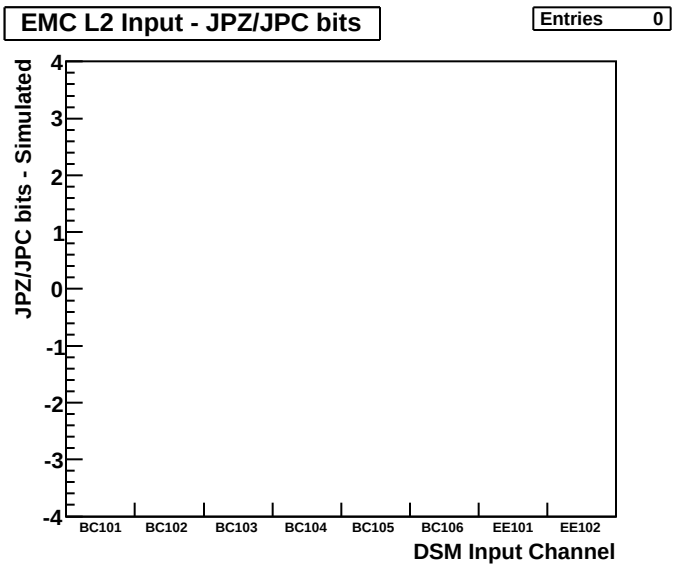

EMC L2 Input - JPX/JPA bits
Entries 594008
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
437 282 243 192 170 206 124 233
967 743 649 568 542 621 213 440
1548 1309 1199 1204 1147 1041 167 160
71299 71917 72160 72287 72392 72383 73747 73418

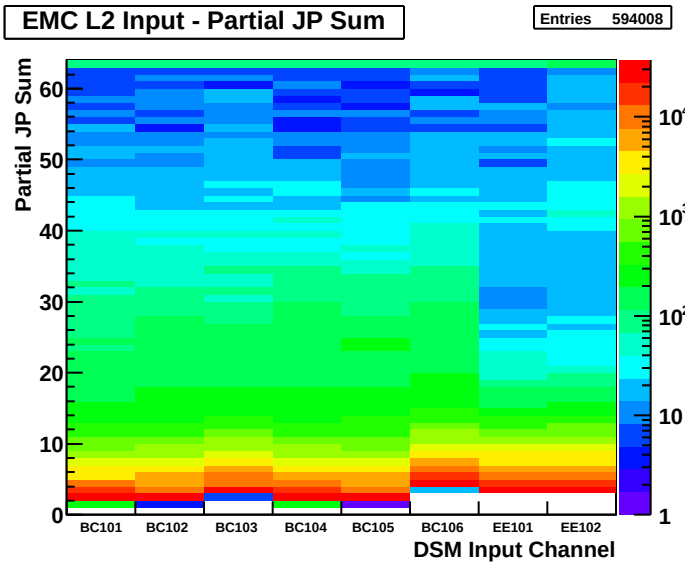

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

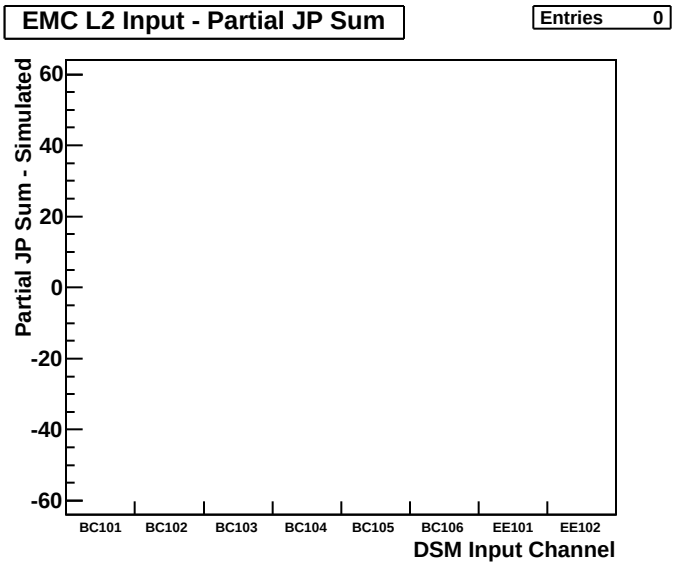

EMC L2 Input - JPY/JPB bits
Entries 594008
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
233 178 195 142 195 239 132 175
775 544 559 514 719 716 257 351
1272 994 946 1039 1807 1251 141 163
71921 72535 72551 72556 71530 72045 73721 73562

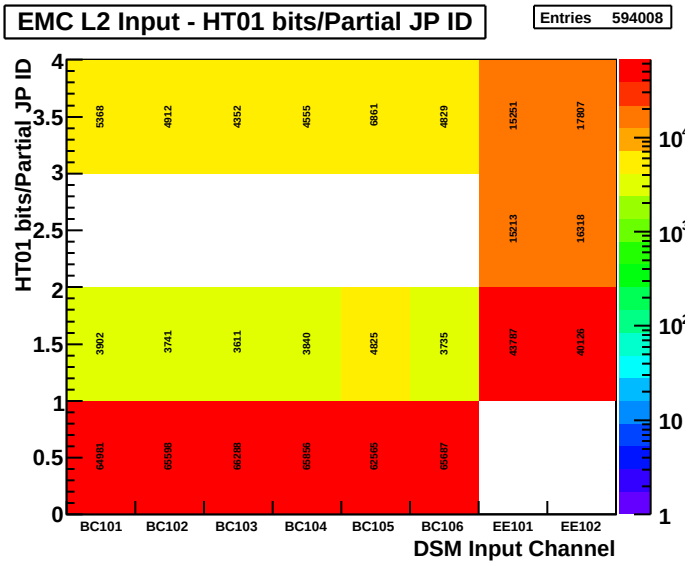

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

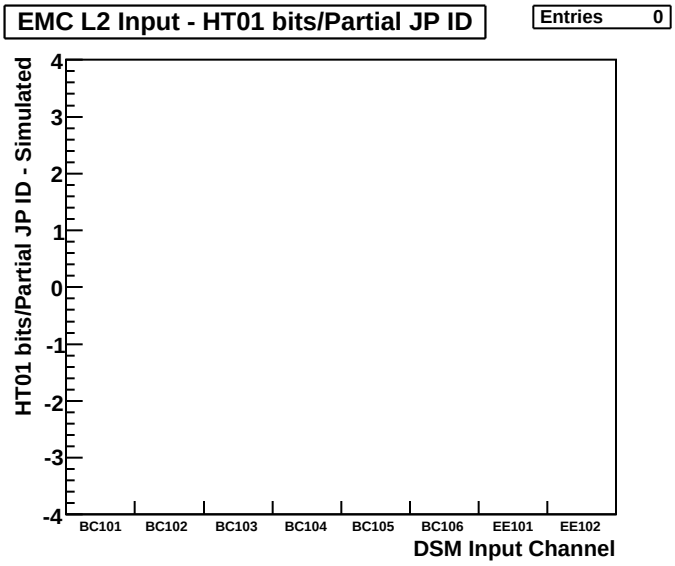

EMC L2 Input - JPZ/JPC bits
Entries 594008
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
139 182 199 167 243 272 122 135
655 502 598 512 738 732 273 248
1017 958 1030 1113 1824 1380 145 177
72380 72609 72424 72459 71446 71867 73711 73691

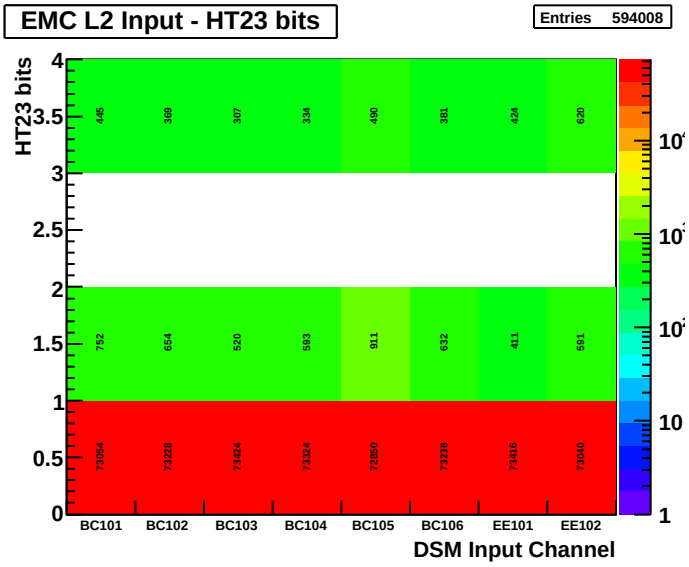

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

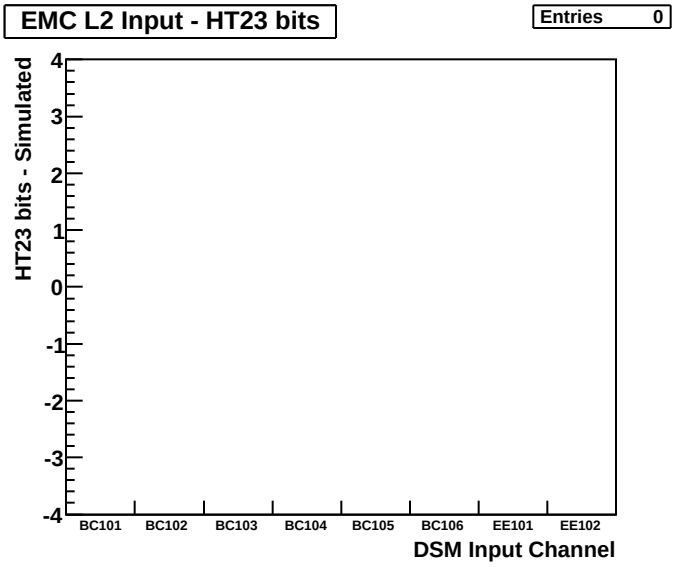

EMC L2 Input - Partial JP Sum
Entries 594008
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

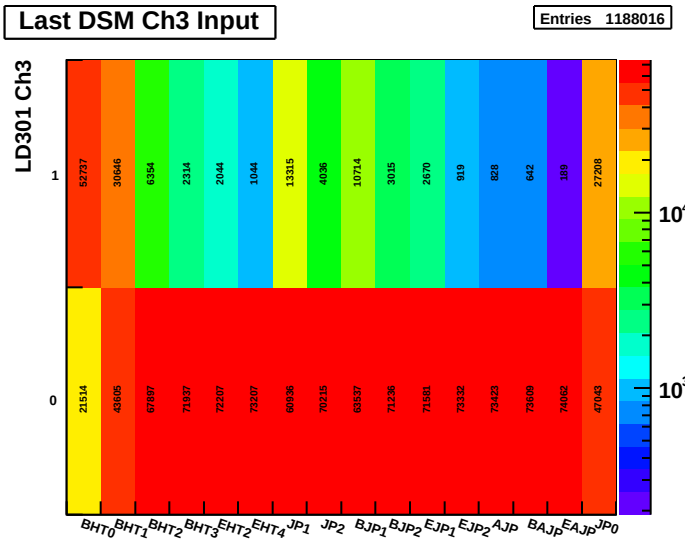

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

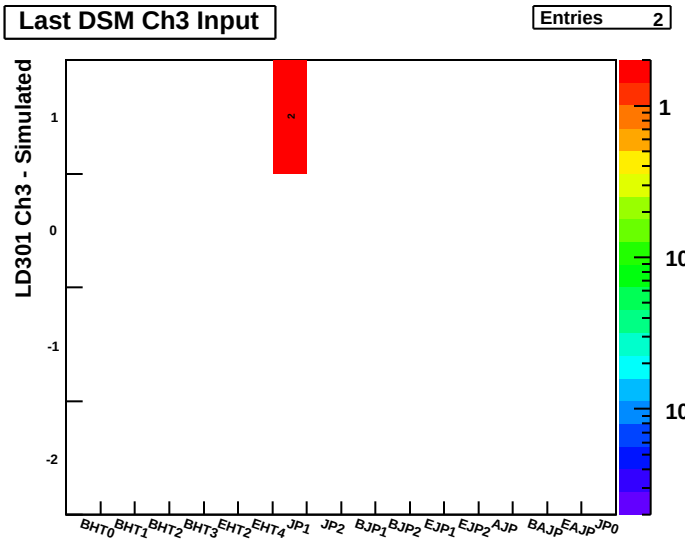

EMC L2 Input - HT01 bits/Partial JP ID
Entries 594008
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

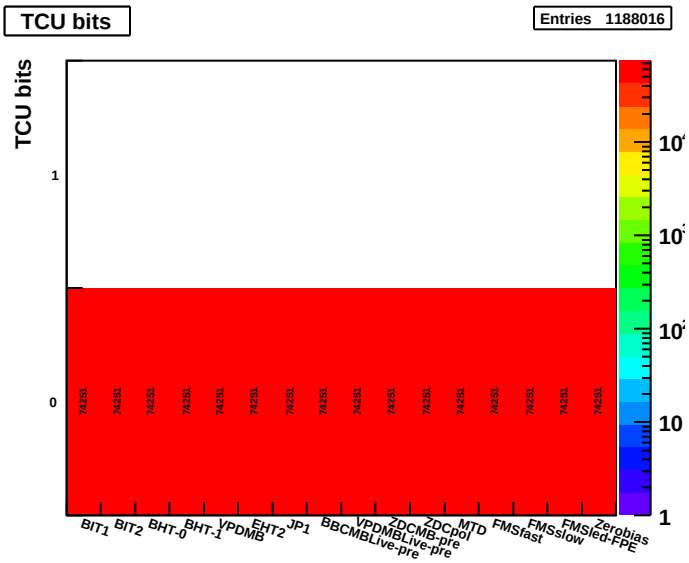

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

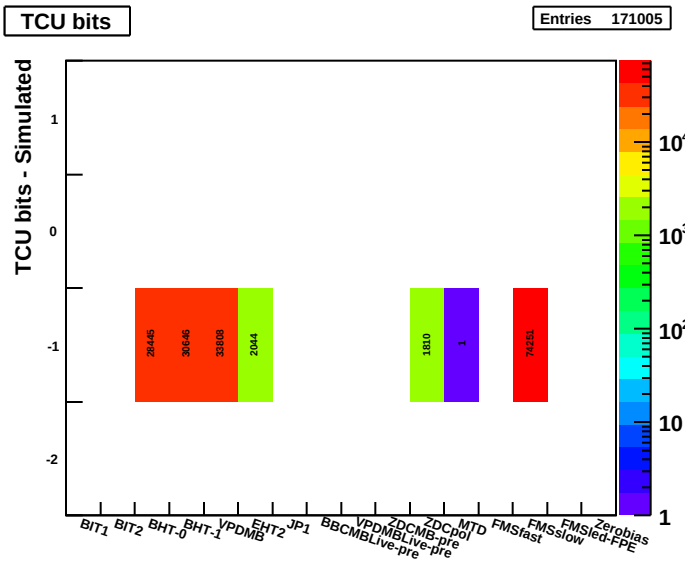

EMC L2 Input - HT23 bits
Entries 594008
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

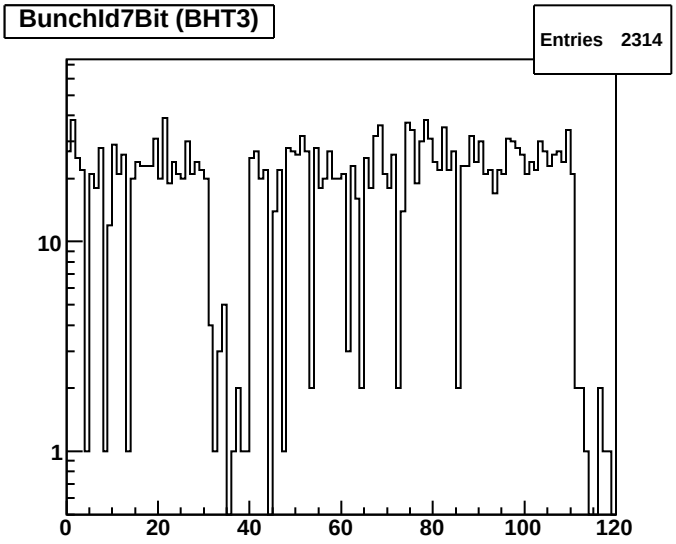

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

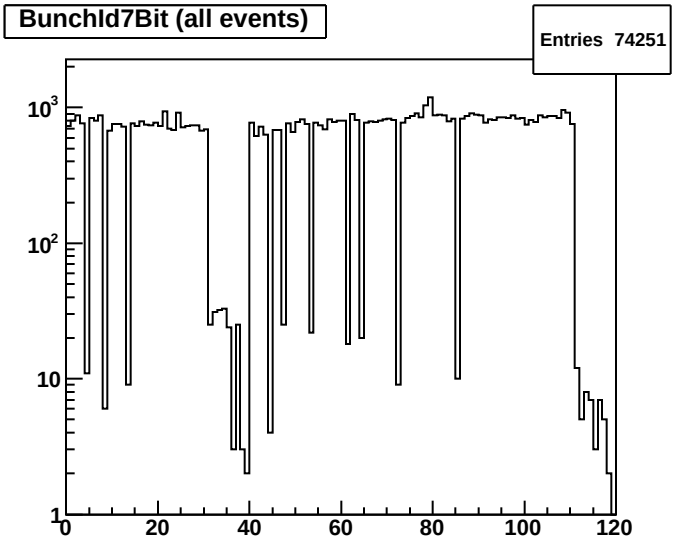

Last DSM Ch3 Input
Entries 1188016
LD301 Ch3
52737 30646 6354 2314 2044 1044 13315 4036 10714 3015 2670 919 828 642 189 27208
21514 43605 67897 71937 72207 73207 60936 70215 63537 71236 71581 73332 73423 73609 74062 47043
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

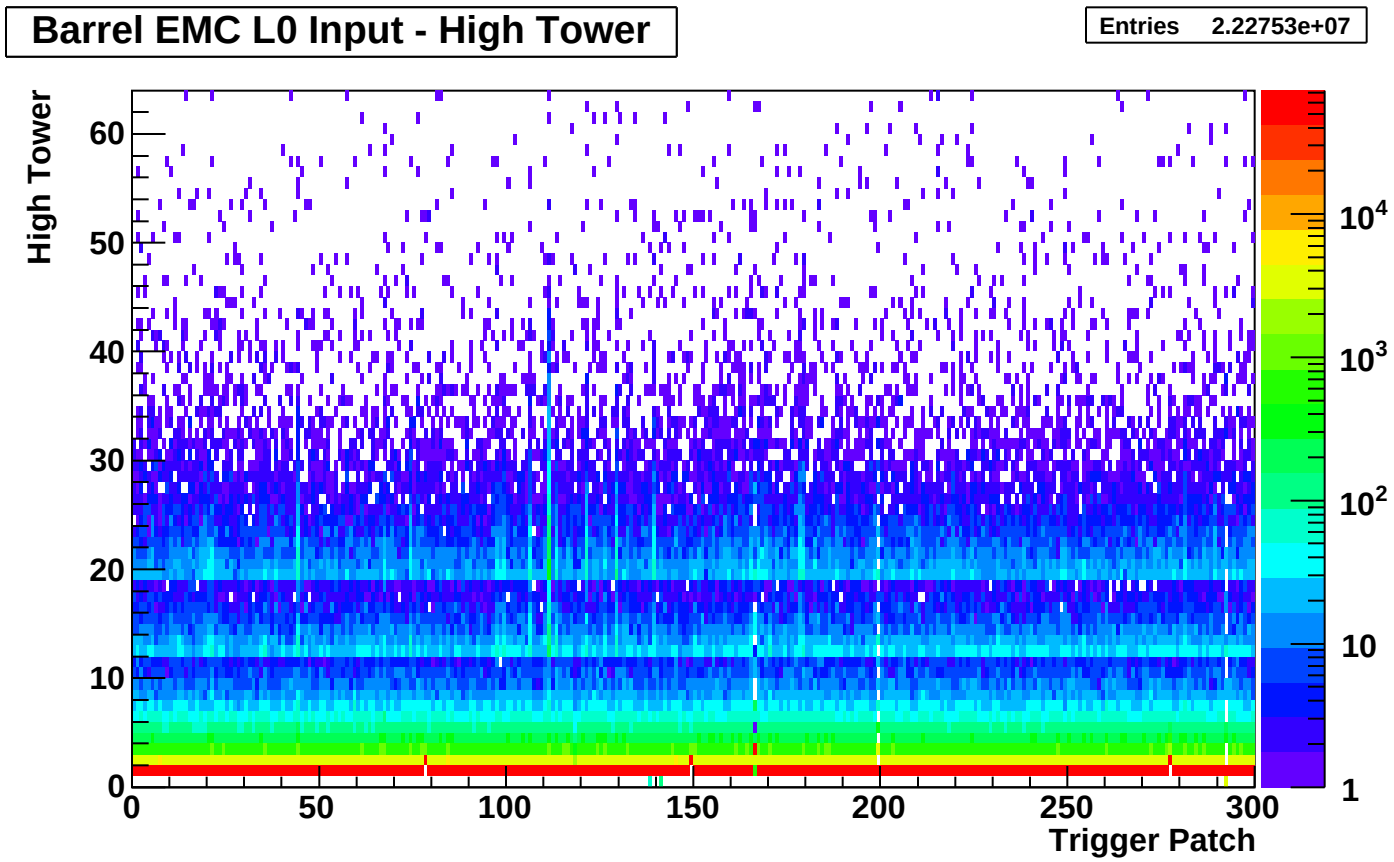

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

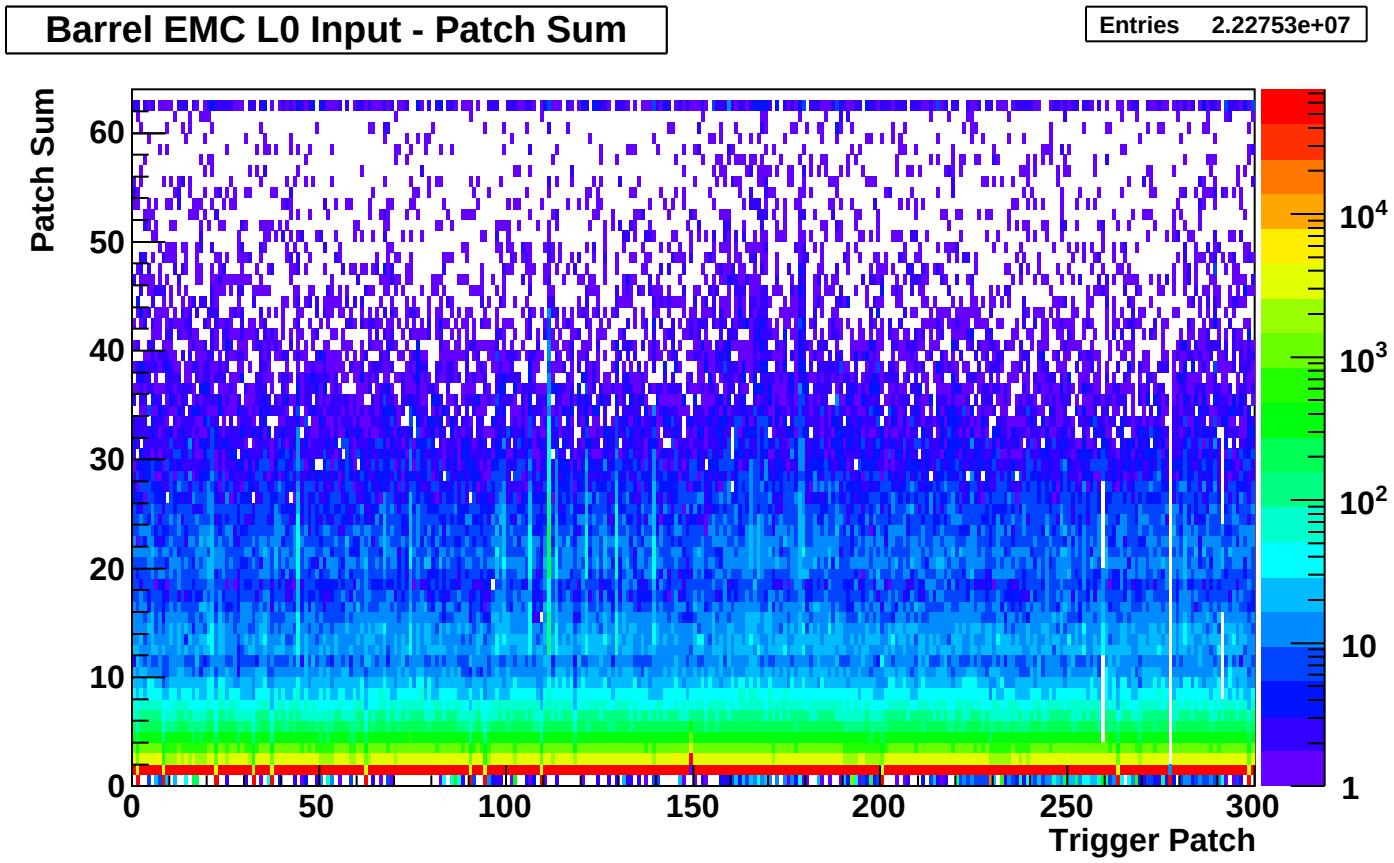

TCU bits
Entries 1188016
TCU bits
74251
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

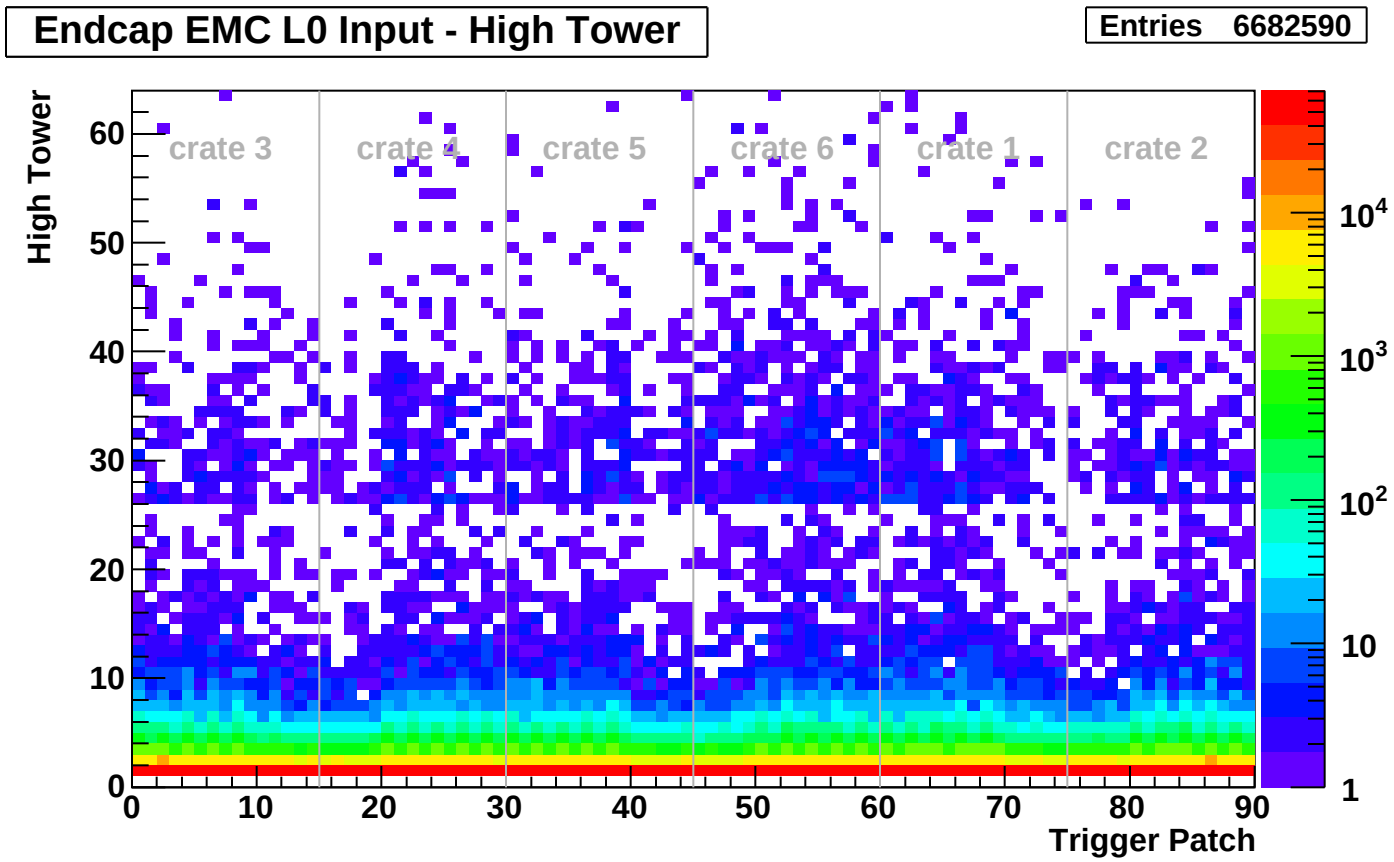

TCU bits
Entries 171005
TCU bits - Simulated
28445 30646 33808 2044 1810 1 74251
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

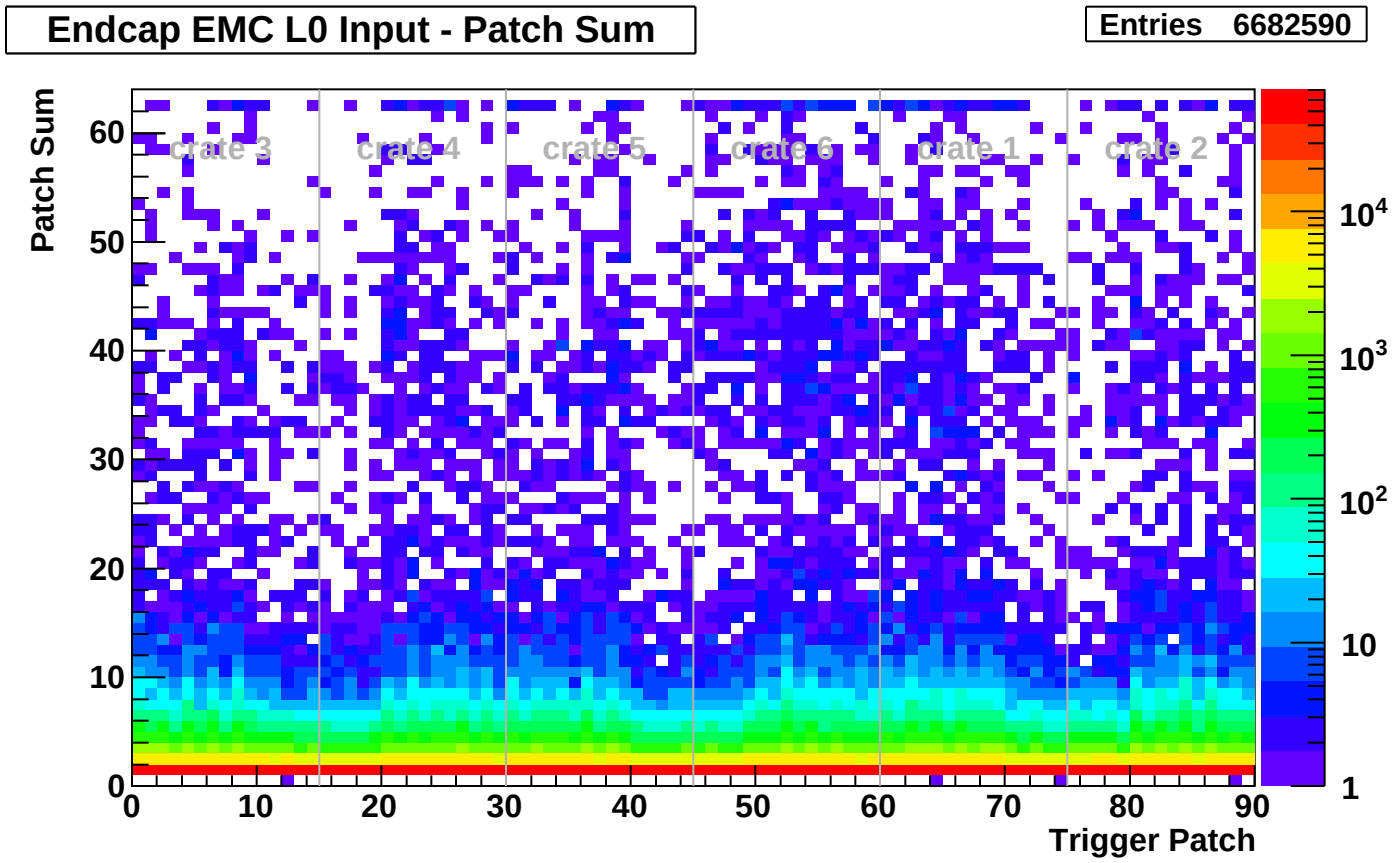

BunchId7Bit (BHT3)
Entries 2314

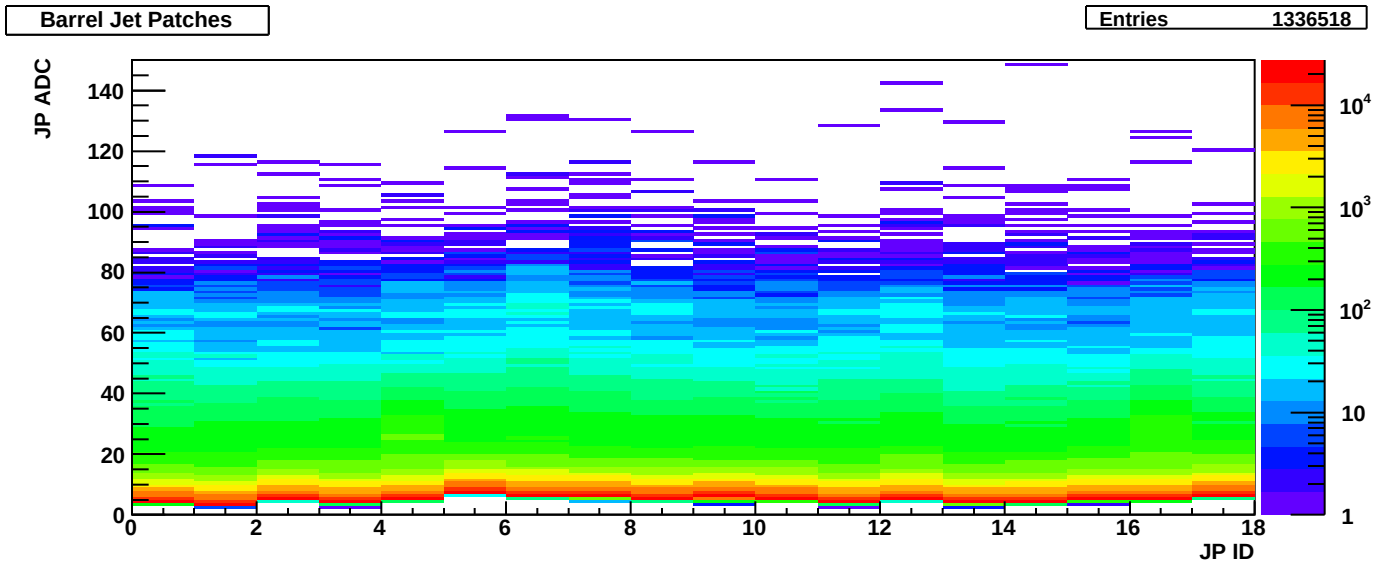

BunchId7Bit (all events)
Entries 74251

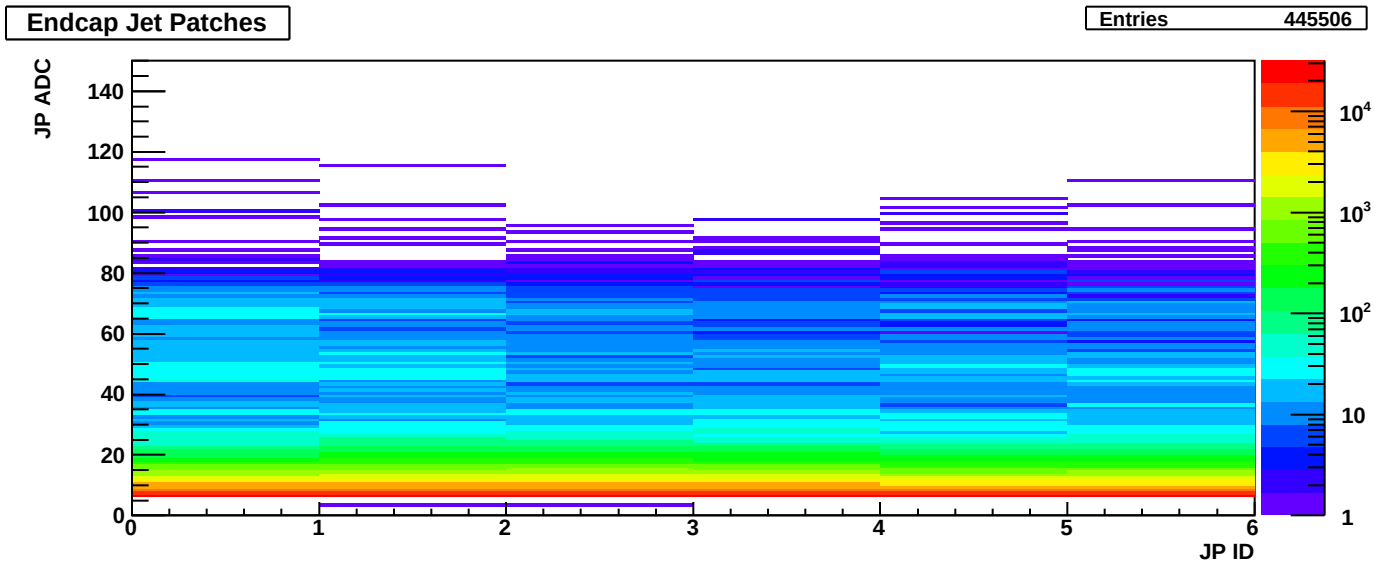

Barrel EMC L0 Input - High Tower
Entries 2.22753e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

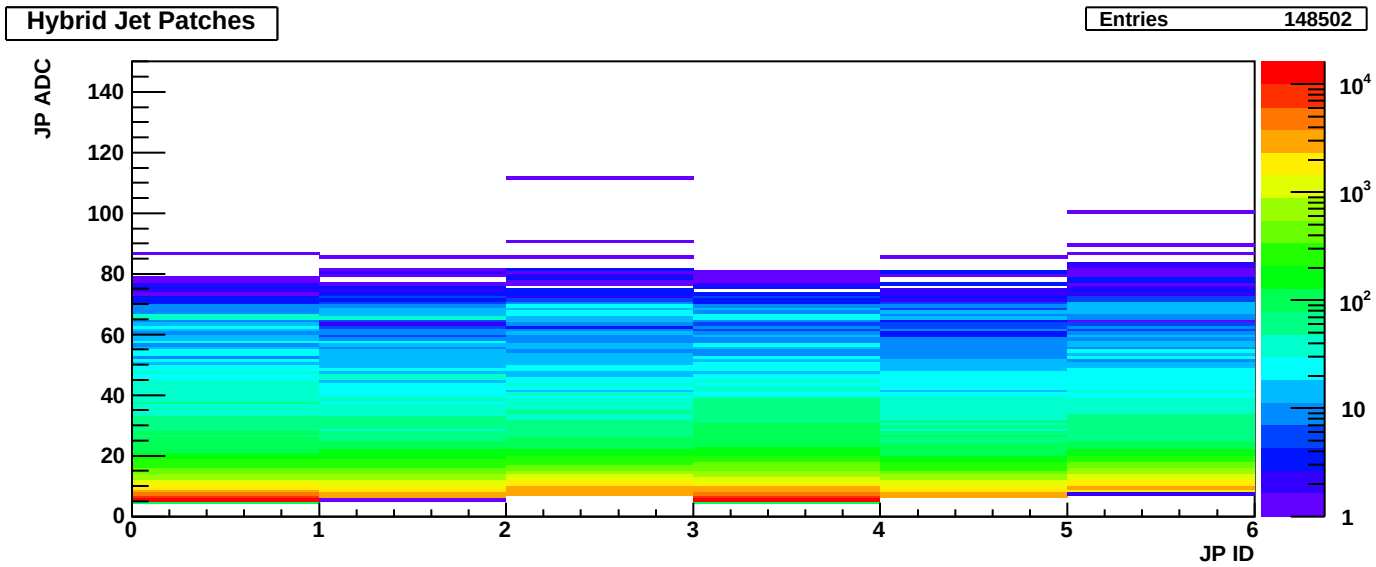

Barrel EMC L0 Input - Patch Sum
Entries 2.22753e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

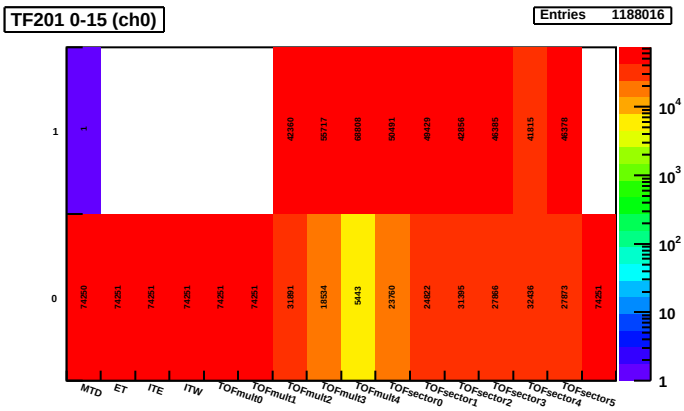

Endcap EMC L0 Input - High Tower
Entries 6682590
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

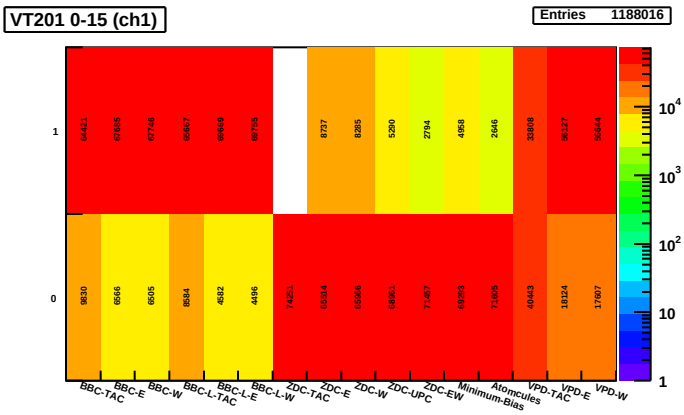

Endcap EMC L0 Input - Patch Sum
Entries 6682590
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

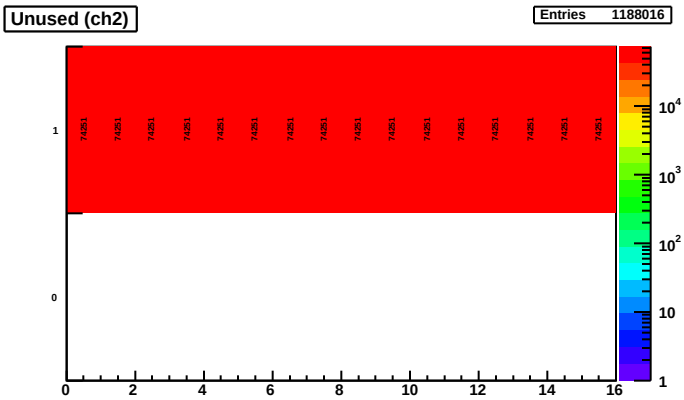

Barrel Jet Patches
Entries 1336518
JP ADC
JP ID

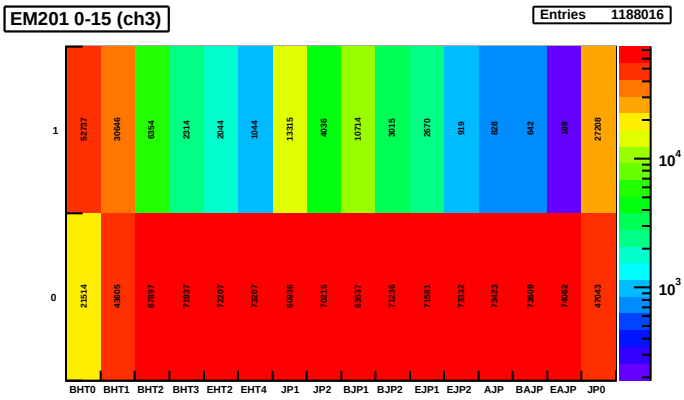

Endcap Jet Patches
Entries 445506
JP ADC
JP ID

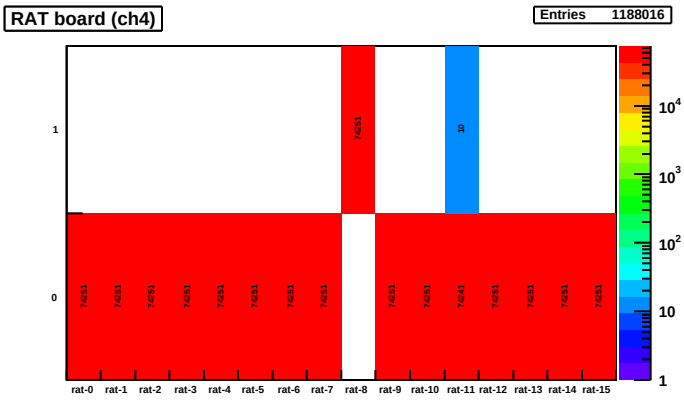

Hybrid Jet Patches
Entries 148502
JP ADC
JP ID

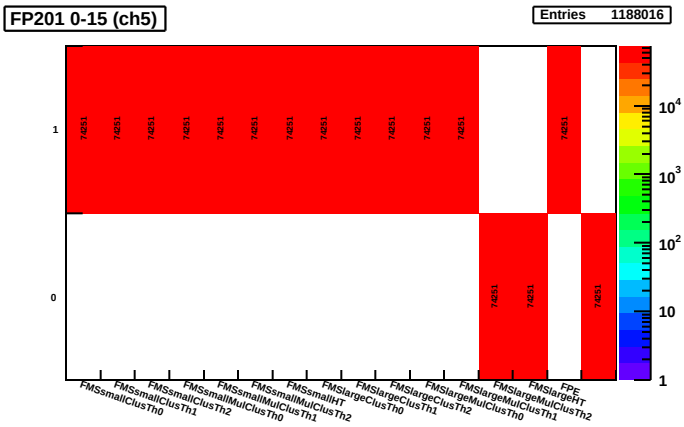

TF201 0-15 (ch0)
Entries 1188016
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

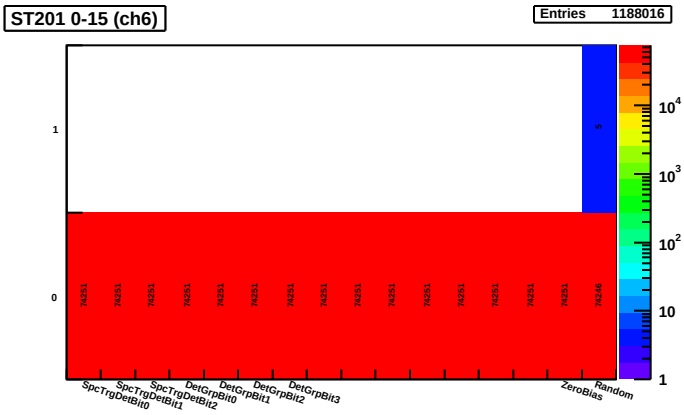

VT201 0-15 (ch1)
Entries 1188016
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

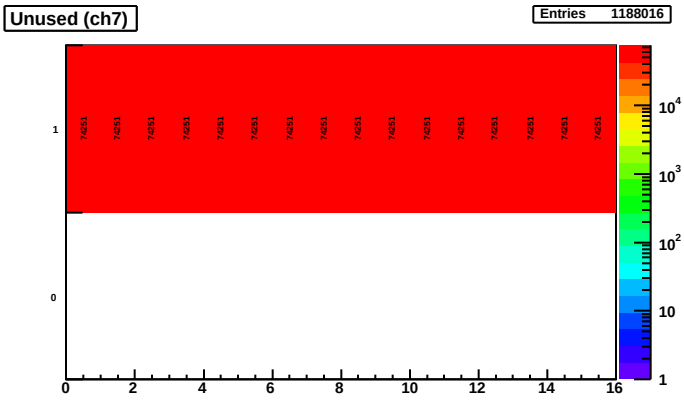

Unused (ch2)
Entries 1188016

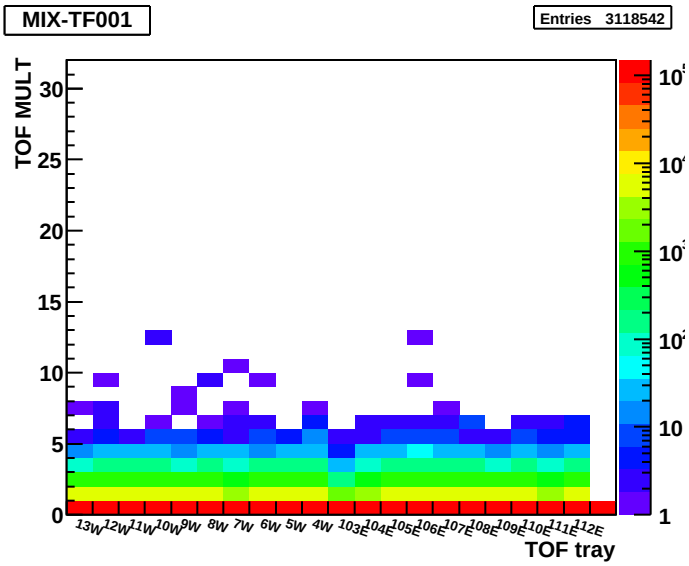

EM201 0-15 (ch3)
Entries 1188016
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

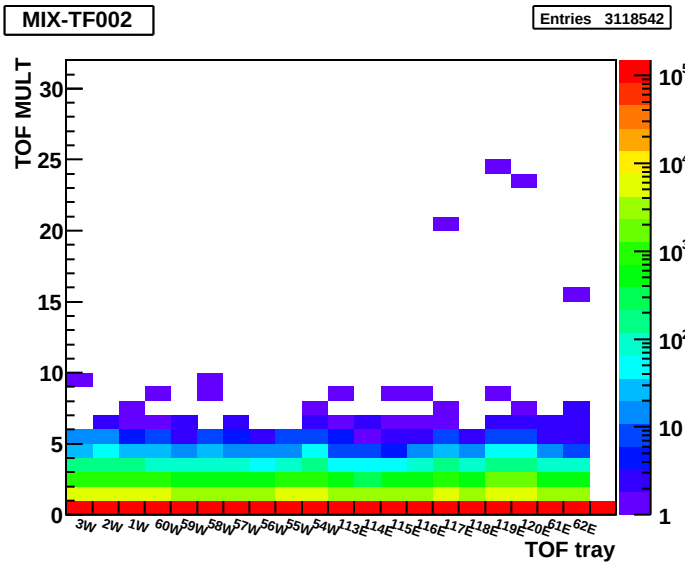

RAT board (ch4)
Entries 1188016
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

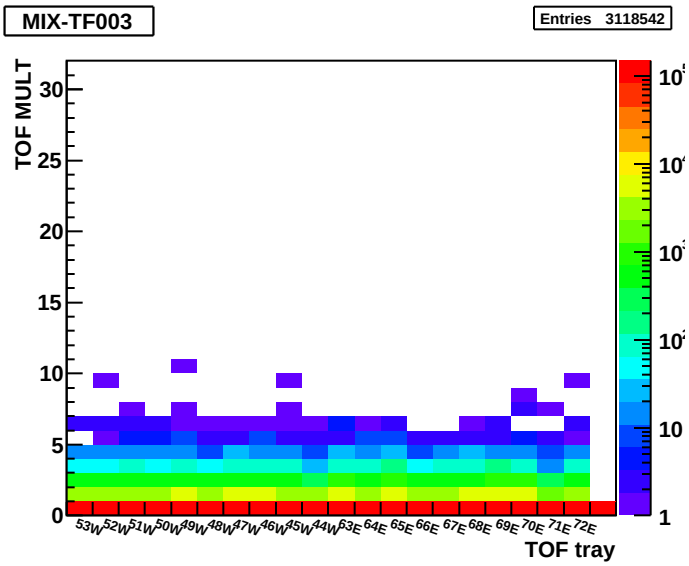

FP201 0-15 (ch5)
Entries 1188016
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

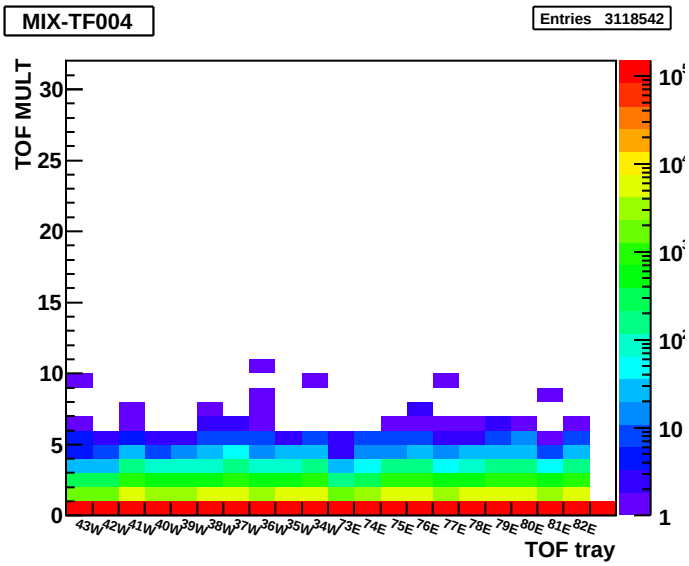

ST201 0-15 (ch6)
Entries 1188016
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

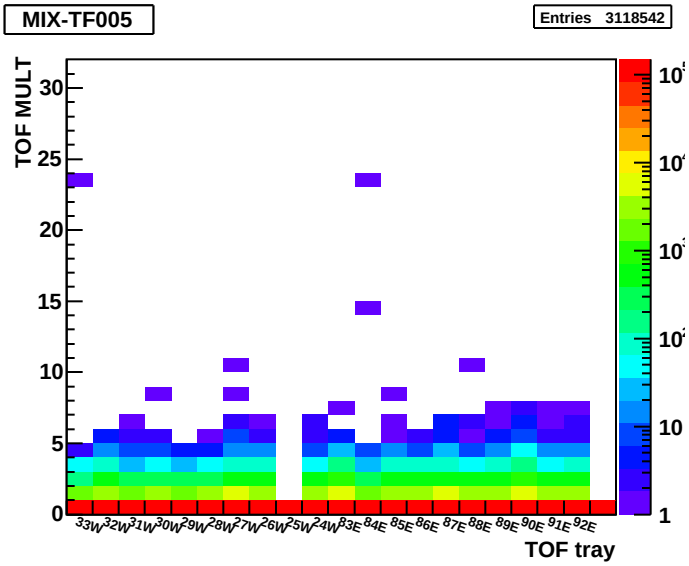

Unused (ch7)
Entries 1188016

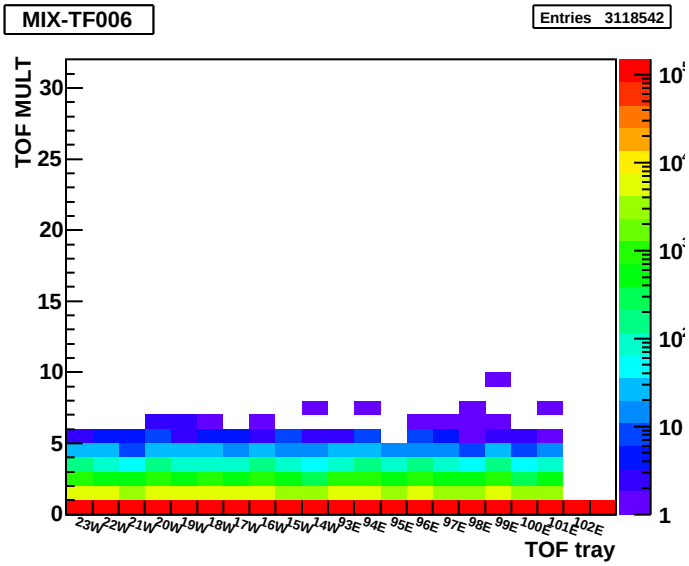

MIX-TF001
Entries 3118542
TOF MULT
TOF tray

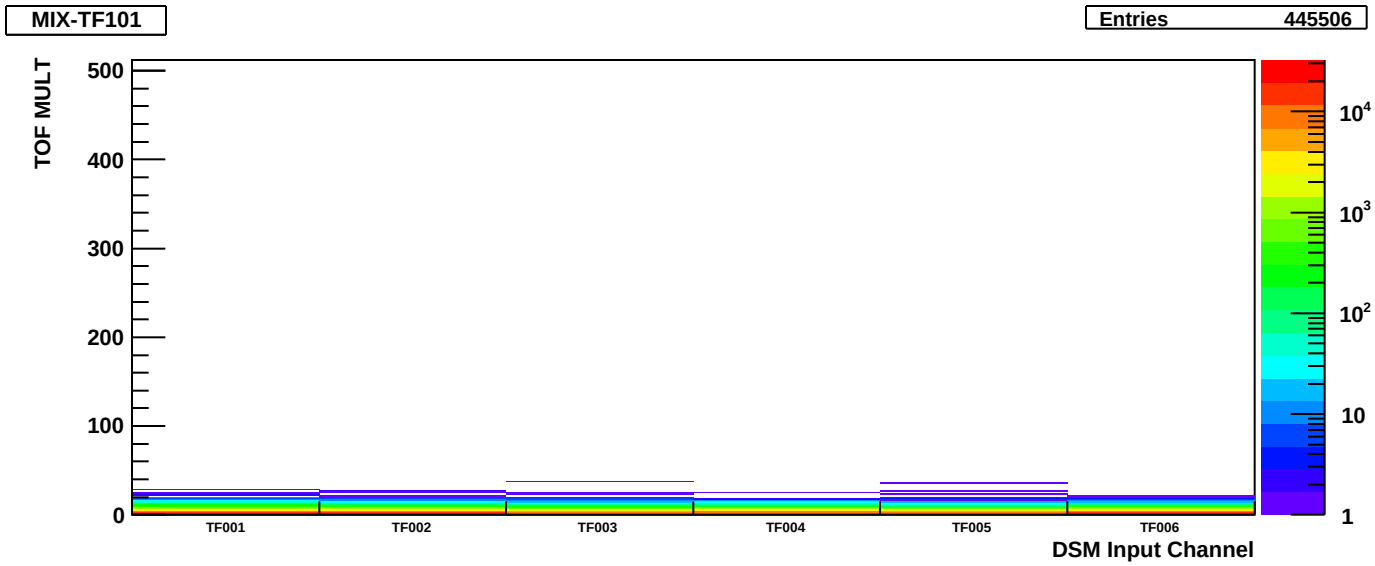

MIX-TF002
Entries 3118542
TOF MULT
TOF tray

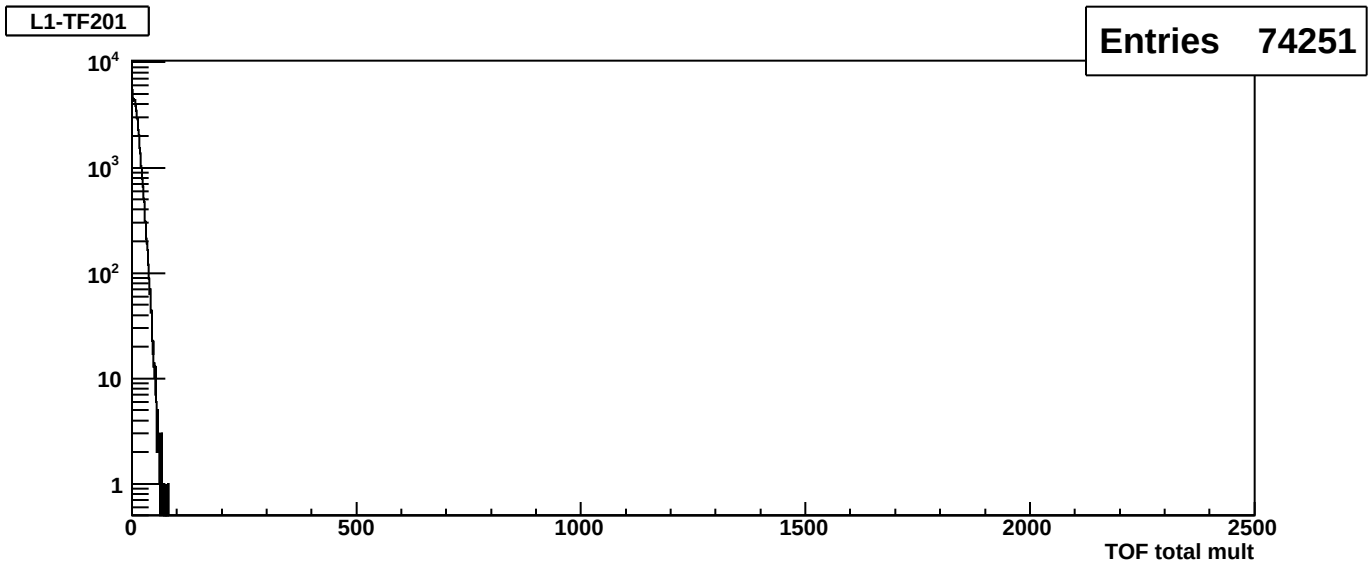

MIX-TF003
Entries 3118542
TOF MULT
TOF tray

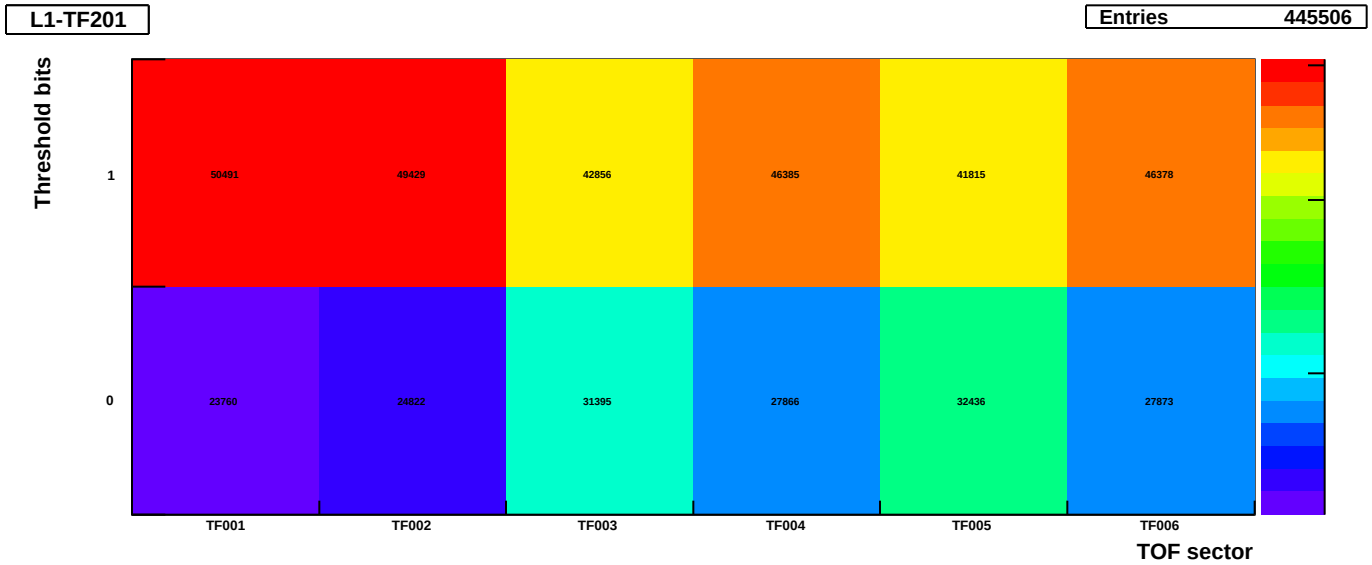

MIX-TF004
Entries 3118542
TOF MULT
TOF tray

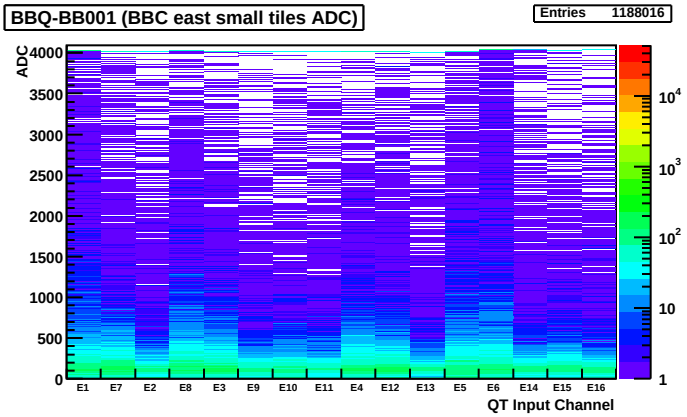

MIX-TF005
Entries 3118542
TOF MULT
TOF tray

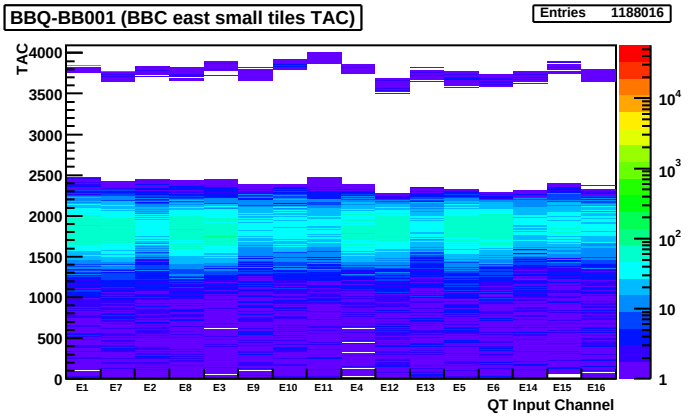

MIX-TF006
Entries 3118542
TOF MULT
TOF tray

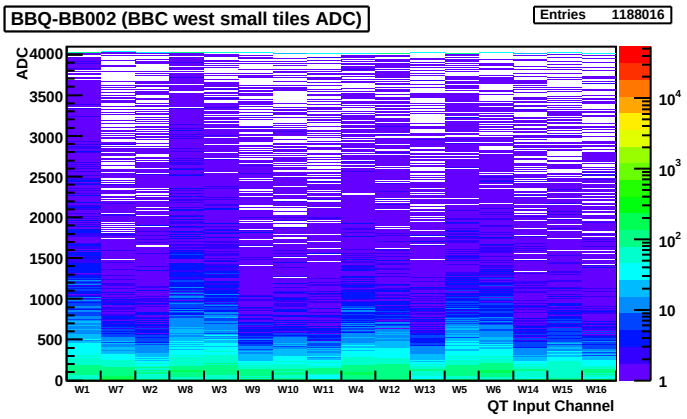

MIX-TF101
Entries 445506
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

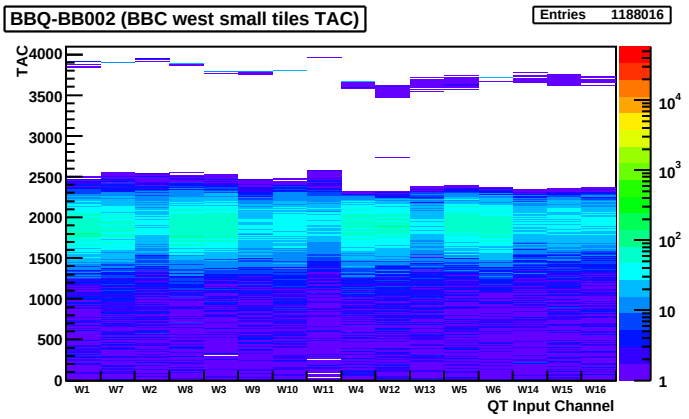

L1-TF201
Entries 74251
$10^4$
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

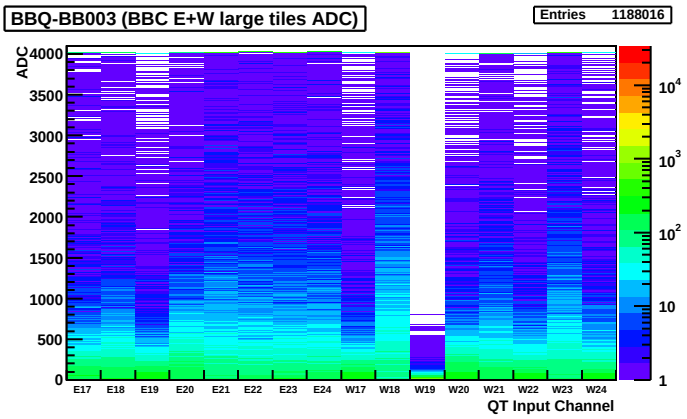

L1-TF201
Entries 445506
Threshold bits
1
0
50491
49429
42856
46385
41815
46378
23760
24822
31395
27866
32436
27873
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

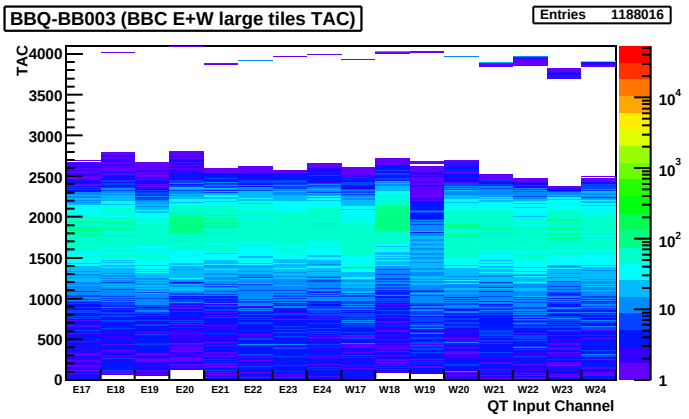

BBQ-BB001 (BBC east small tiles ADC)
Entries 1188016
ADC
QT Input Channel

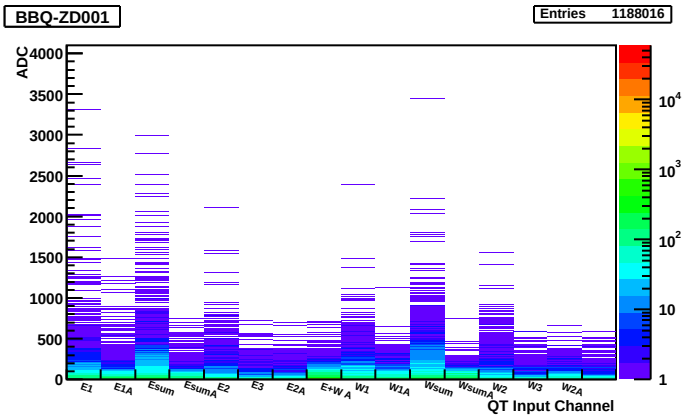

BBQ-BB001 (BBC east small tiles TAC)
Entries 1188016
TAC
QT Input Channel

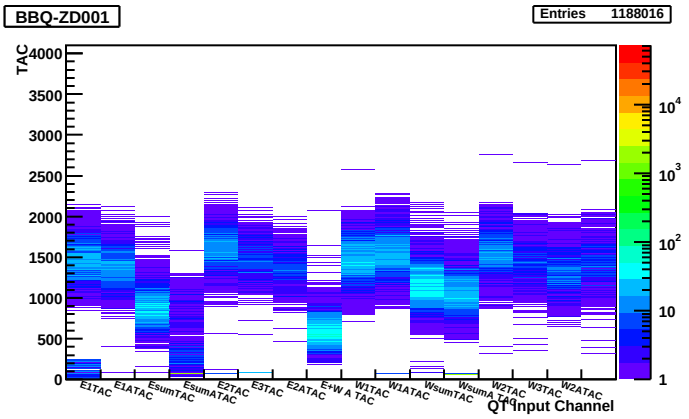

BBQ-BB002 (BBC west small tiles ADC)
Entries 1188016
ADC
QT Input Channel

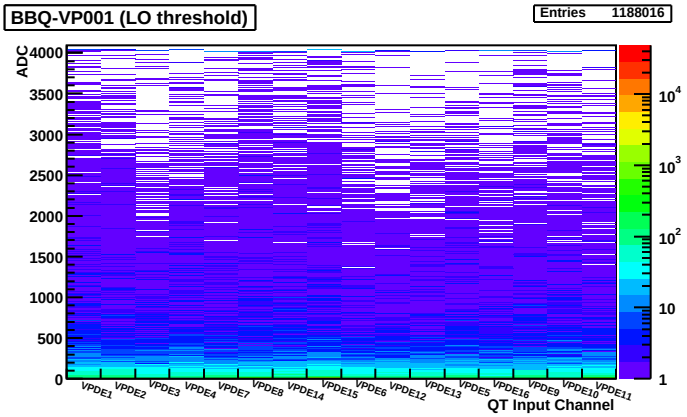

BBQ-BB002 (BBC west small tiles TAC)
Entries 1188016
TAC
QT Input Channel

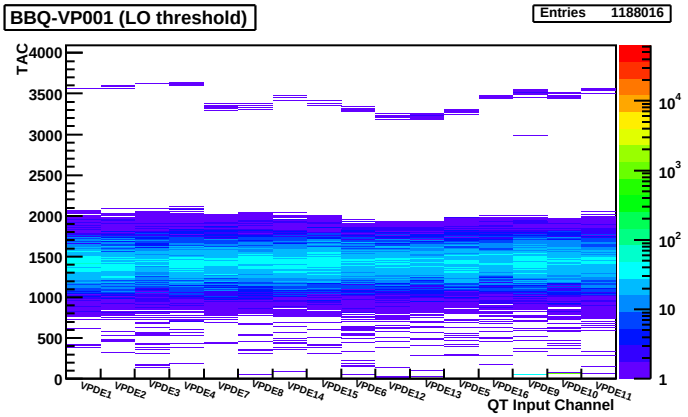

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1188016
ADC
QT Input Channel

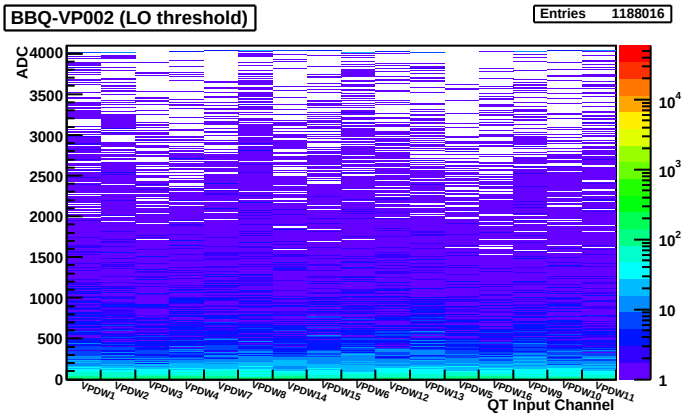

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1188016
TAC
QT Input Channel

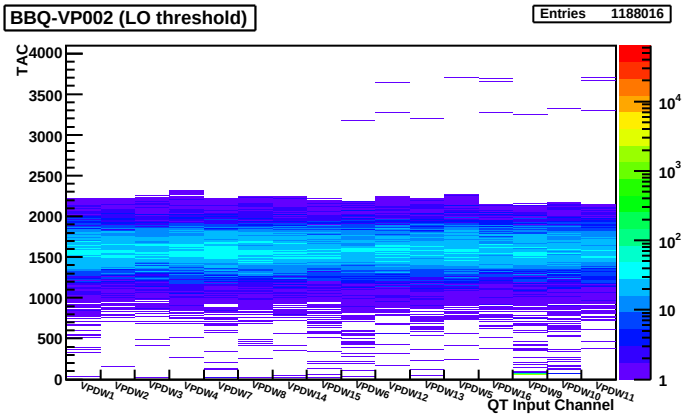

BBQ-ZD001
Entries 1188016
ADC
QT Input Channel

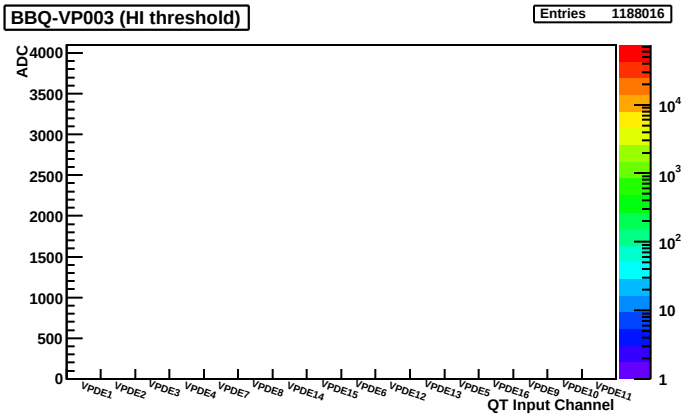

BBQ-ZD001
Entries 1188016
TAC
QT Input Channel

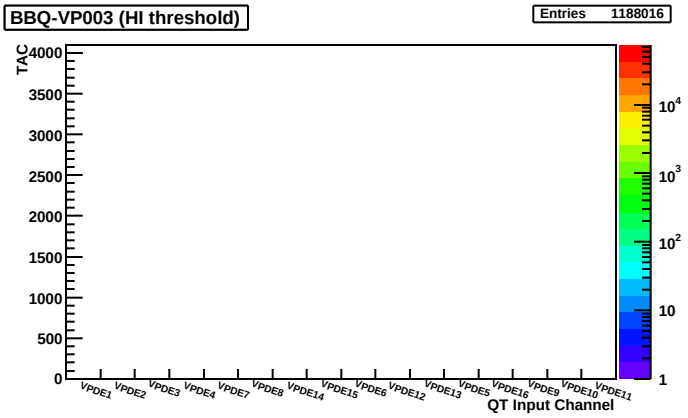

BBQ-VP001 (LO threshold)
Entries 1188016
ADC
QT Input Channel

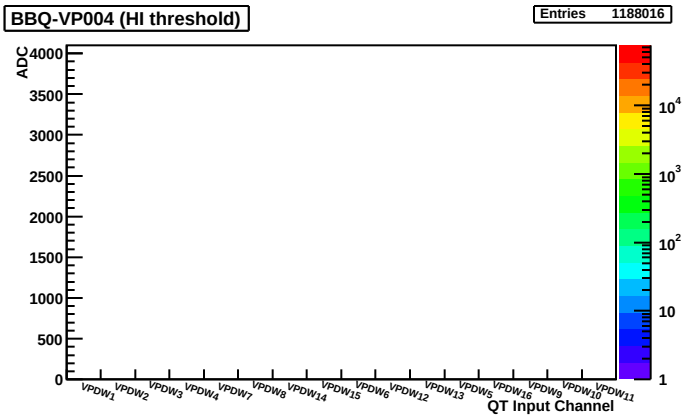

BBQ-VP001 (LO threshold)
Entries 1188016
TAC
QT Input Channel

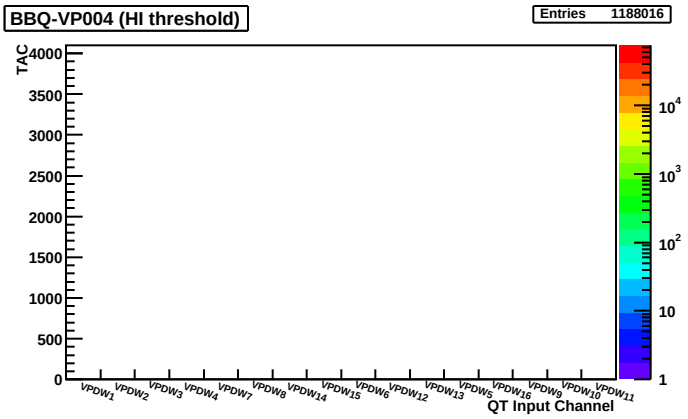

BBQ-VP002 (LO threshold)
Entries 1188016
ADC
QT Input Channel

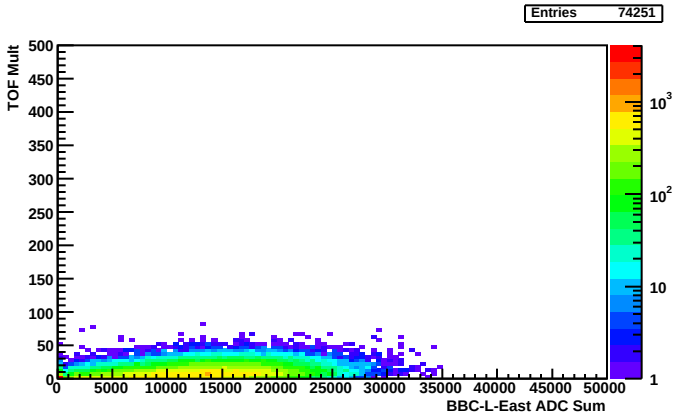

BBQ-VP002 (LO threshold)
Entries 1188016
TAC
QT Input Channel

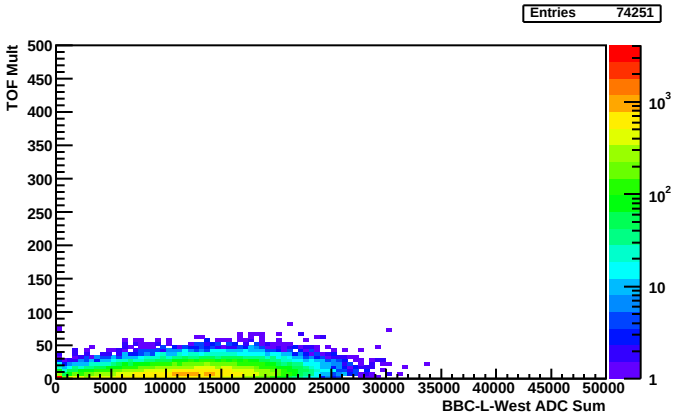

BBQ-VP003 (HI threshold)
Entries 1188016
ADC
QT Input Channel

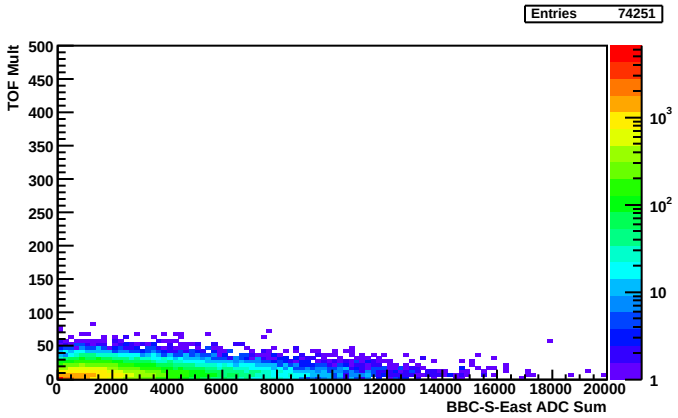

BBQ-VP003 (HI threshold)
Entries 1188016
TAC
QT Input Channel

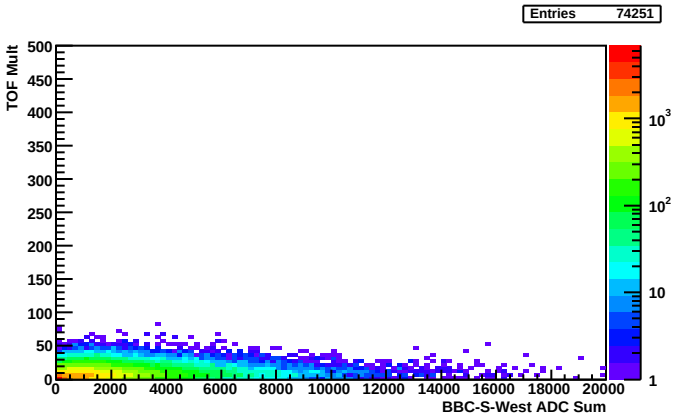

BBQ-VP004 (HI threshold)
Entries 1188016
ADC
QT Input Channel

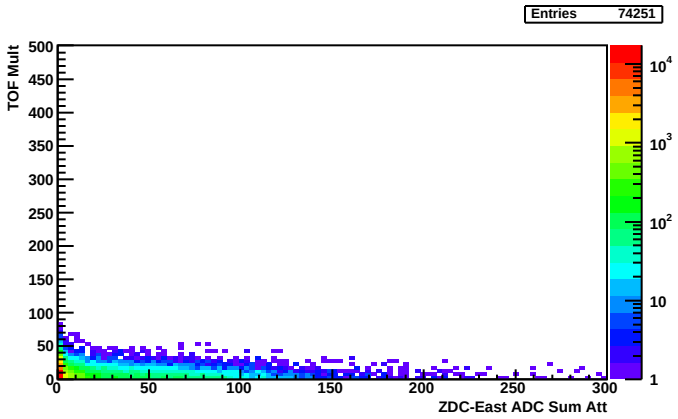

BBQ-VP004 (HI threshold)
Entries 1188016
TAC
QT Input Channel

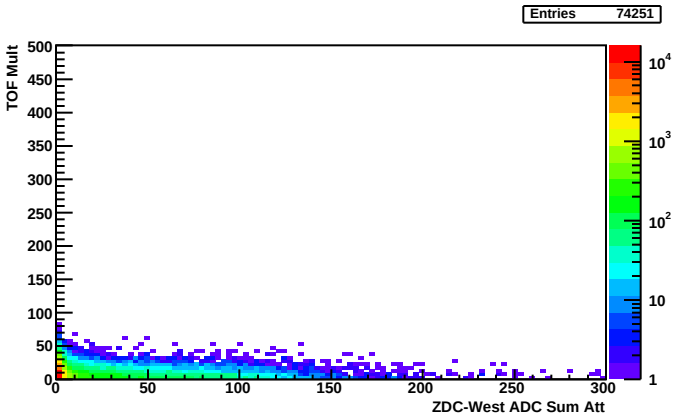

Entries 74251
TOF Mult
BBC-L-East ADC Sum

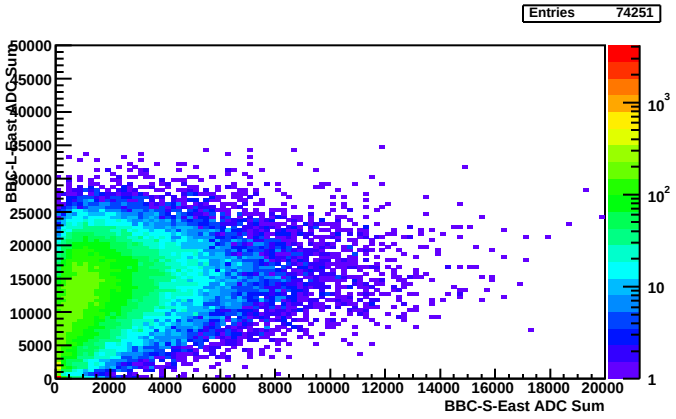

Entries 74251
TOF Mult
BBC-L-West ADC Sum

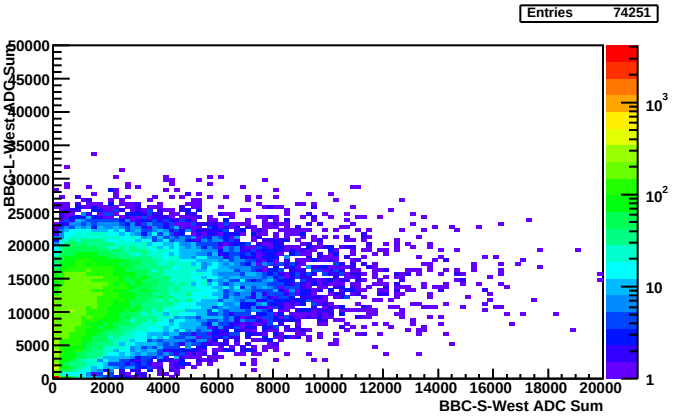

Entries 74251
TOF Mult
BBC-S-East ADC Sum

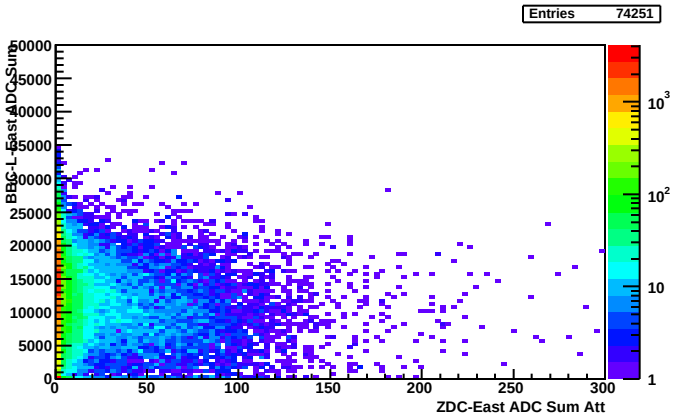

Entries 74251
TOF Mult
BBC-S-West ADC Sum

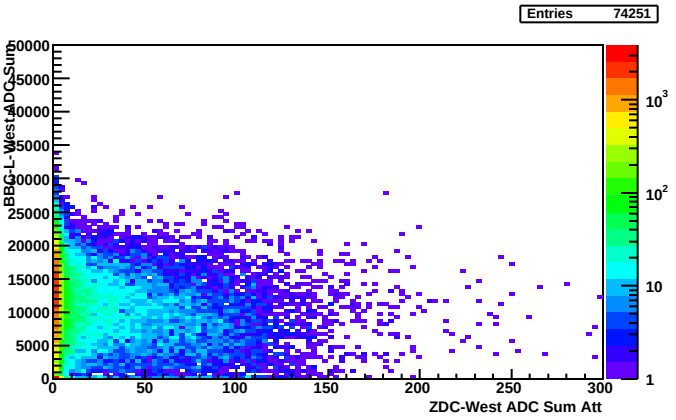

Entries 74251
TOF Mult
ZDC-East ADC Sum Att

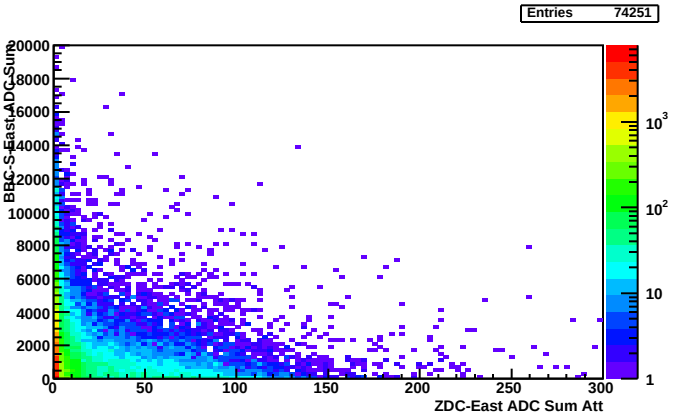

Entries 74251
TOF Mult
ZDC-West ADC Sum Att

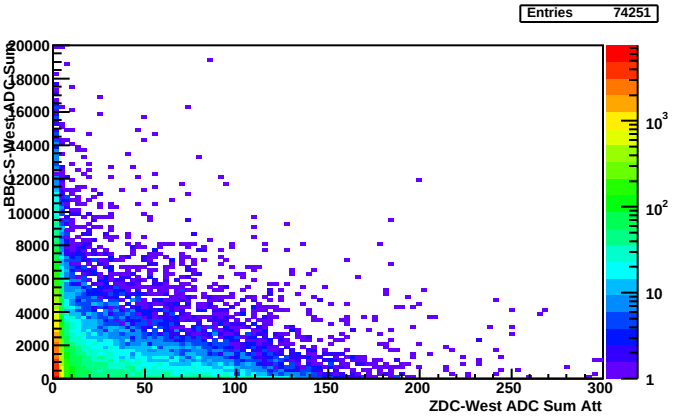

Entries 74251
BBC-L-East ADC Sum
BBC-S-East ADC Sum

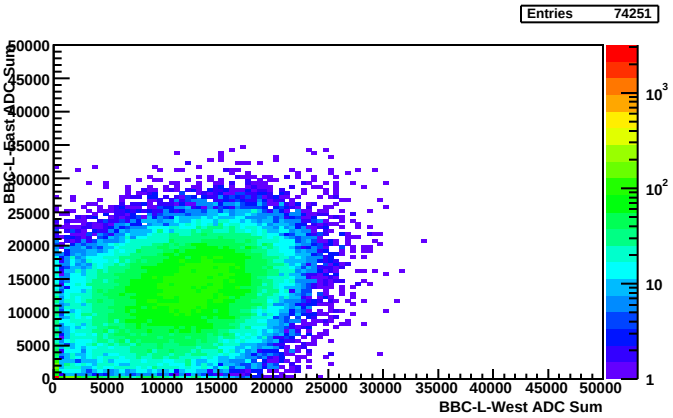

Entries 74251
BBC-L-West ADC Sum
BBC-S-West ADC Sum

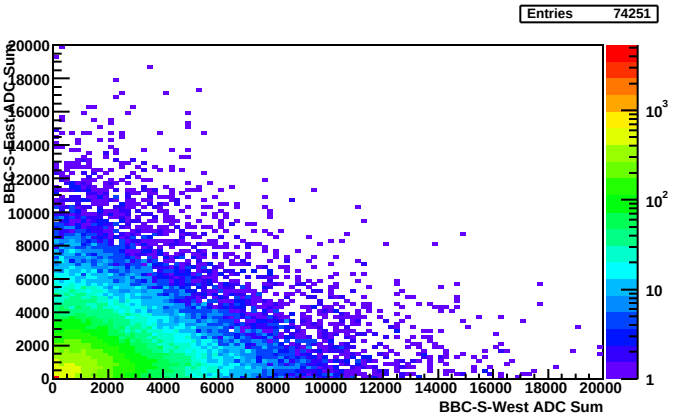

Entries 74251
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

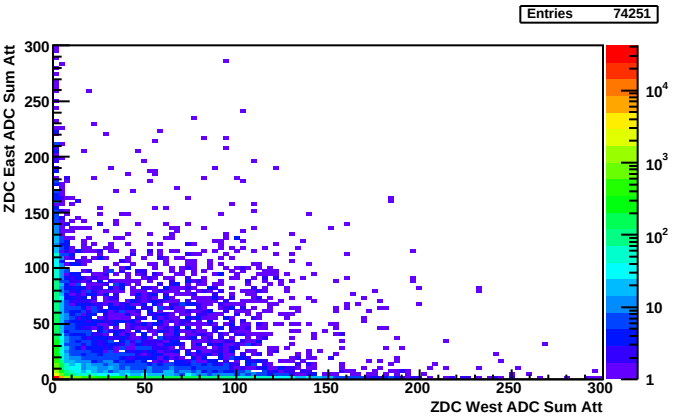

Entries 74251
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

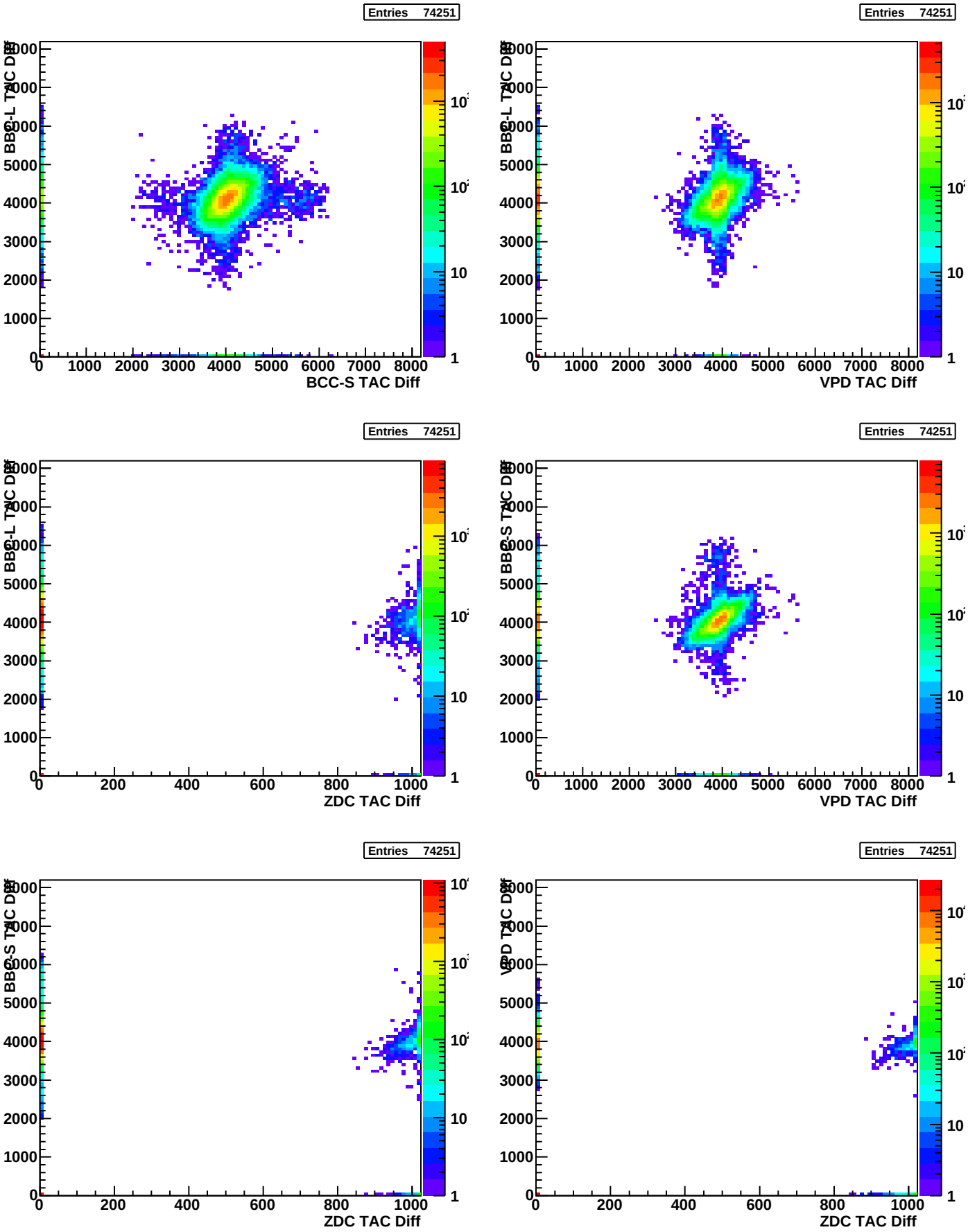

Entries 74251
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

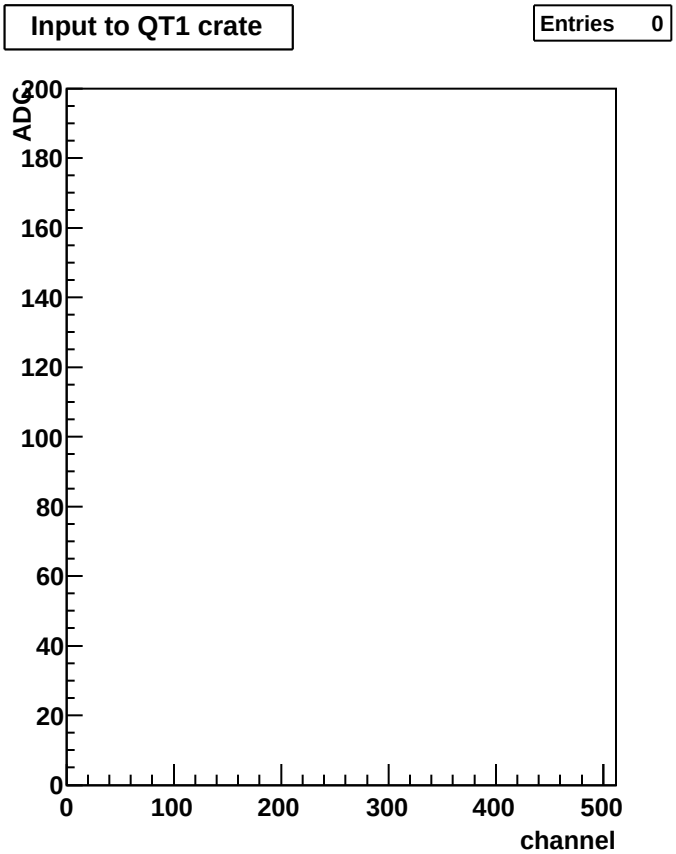

Entries 74251
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

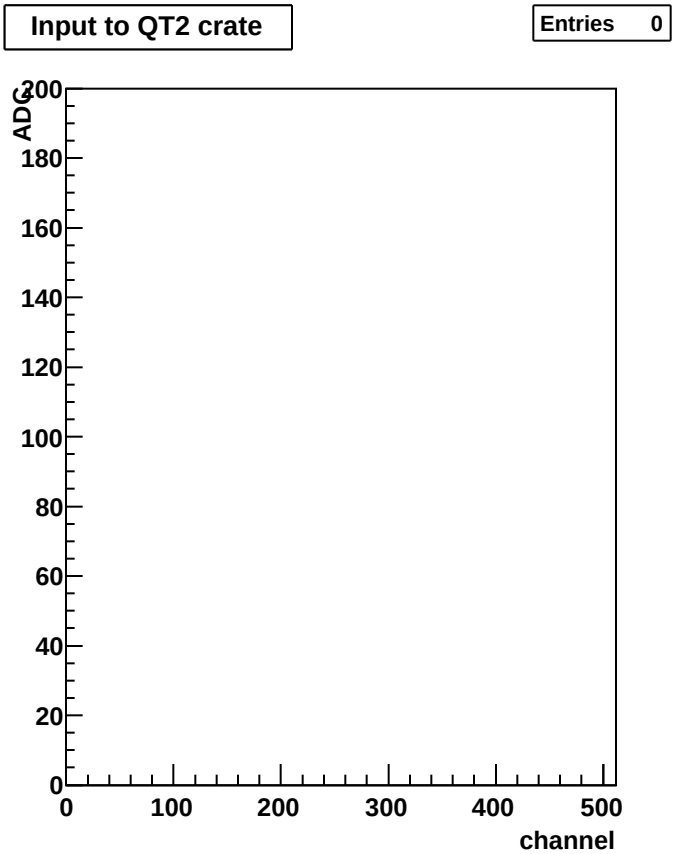

Entries 74251
BBC-L-East ADC Sum
BBC-L-West ADC Sum

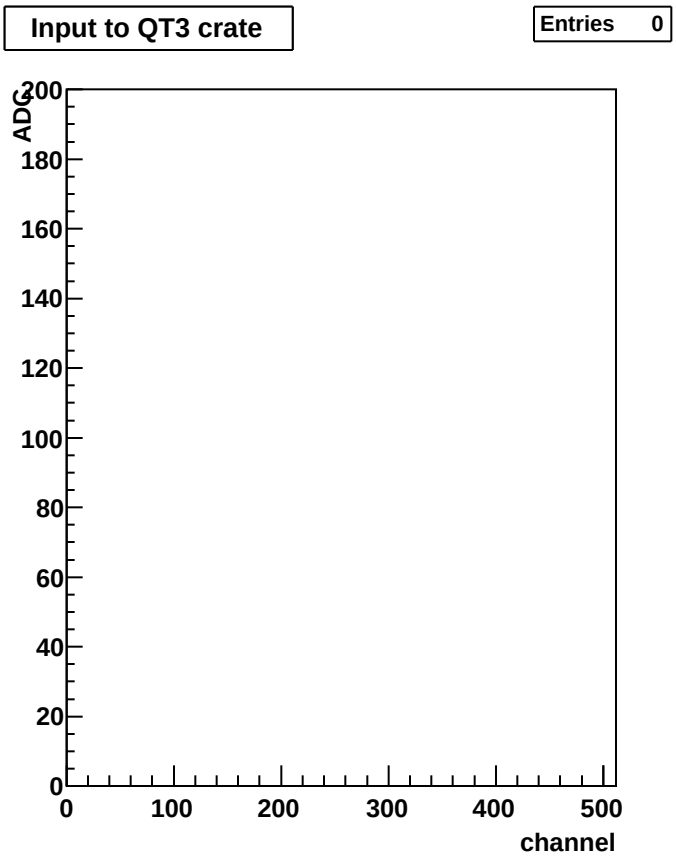

Entries 74251
BBC-S-East ADC Sum
BBC-S-West ADC Sum

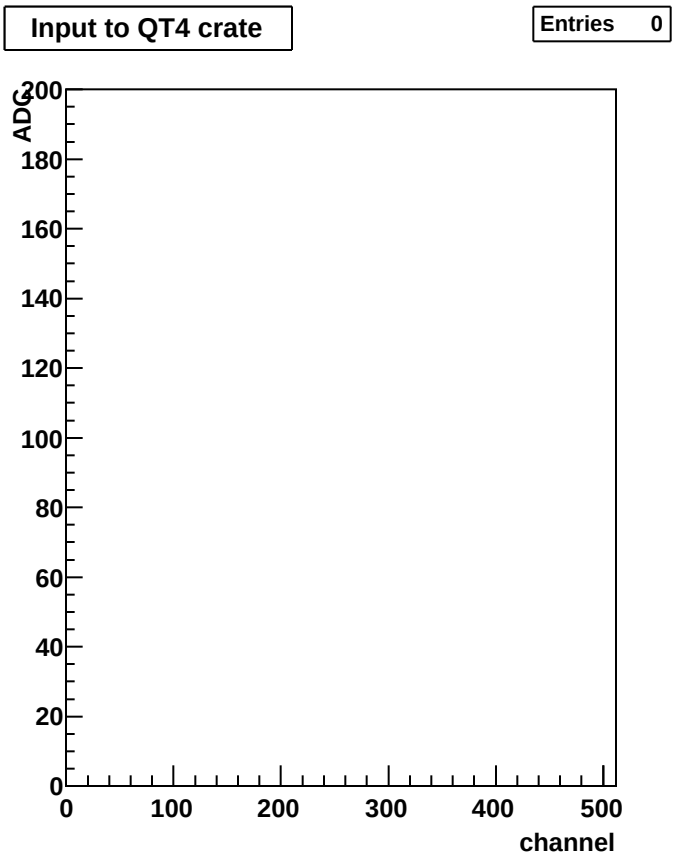

Entries 74251
ZDC East ADC Sum Att
ZDC West ADC Sum Att

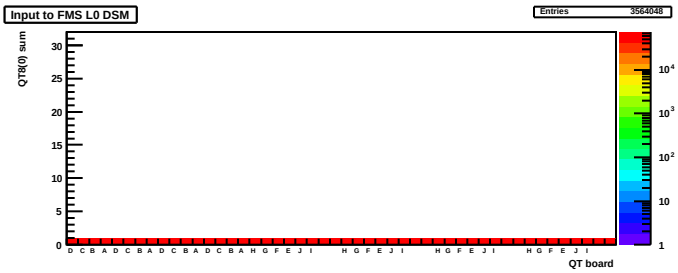

Entries 74251
BBC-L TAC Diff
BCC-S TAC Diff
Entries 74251
BBC-L TAC Diff
VPD TAC Diff
Entries 74251
BBC-L TAC Diff
ZDC TAC Diff
Entries 74251
BBC-S TAC Diff
VPD TAC Diff
Entries 74251
BBC-S TAC Diff
ZDC TAC Diff
Entries 74251
VPD TAC Diff
ZDC TAC Diff

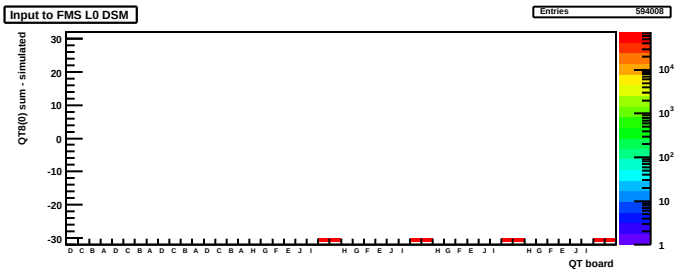

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

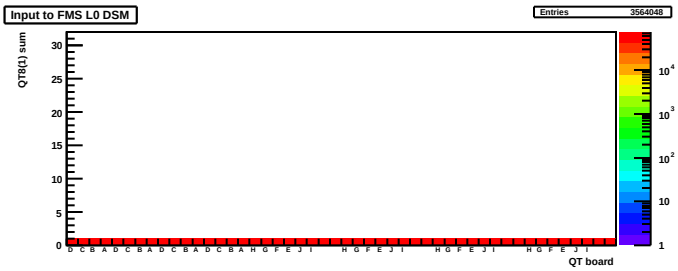

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

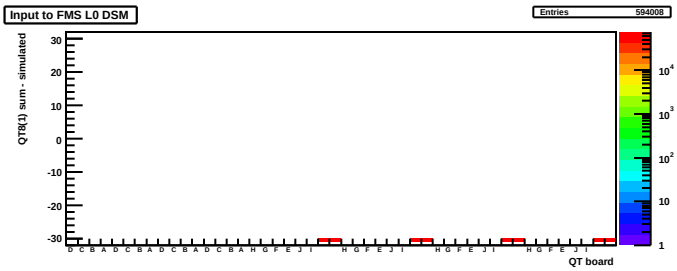

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

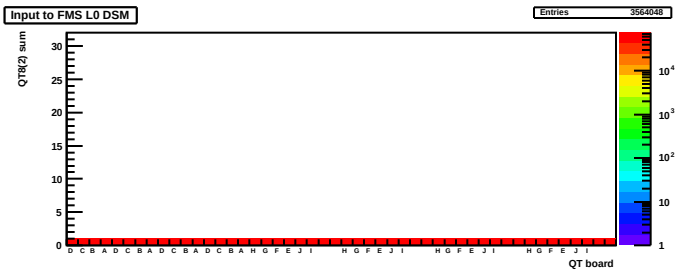

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

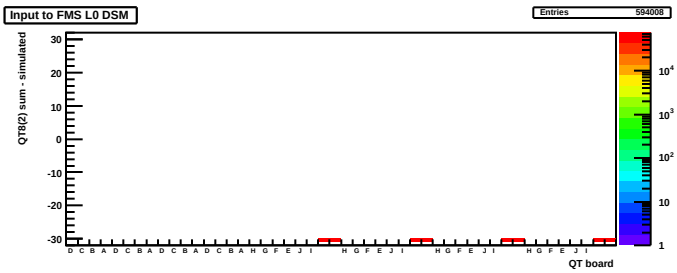

Input to FMS L0 DSM
Entries 3564048
QT8(0) sum
QT board

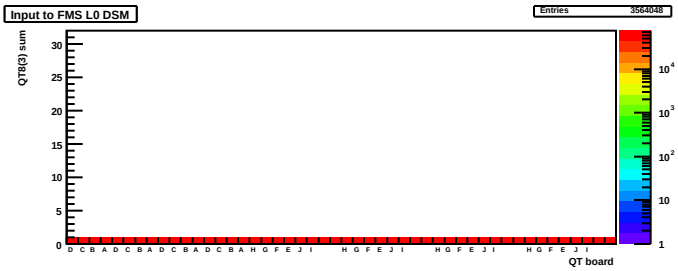

Input to FMS L0 DSM
Entries 594008
QT8(0) sum - simulated
QT board

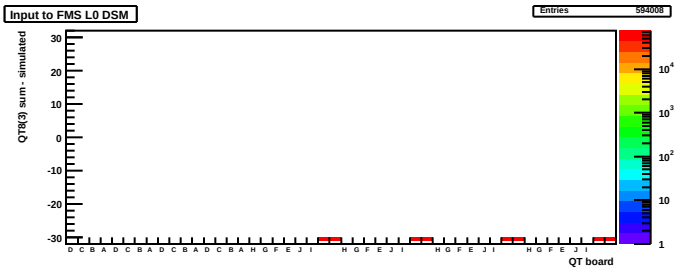

Input to FMS L0 DSM
Entries 3564048
QT8(1) sum
QT board

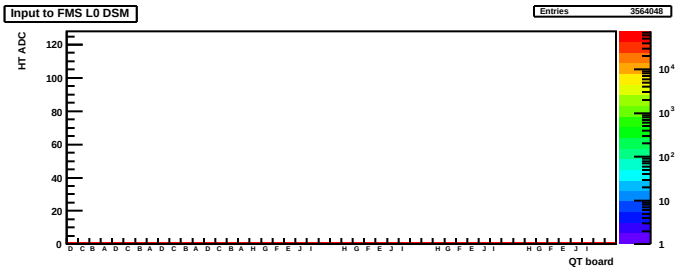

Input to FMS L0 DSM
Entries 594008
QT8(1) sum - simulated
QT board

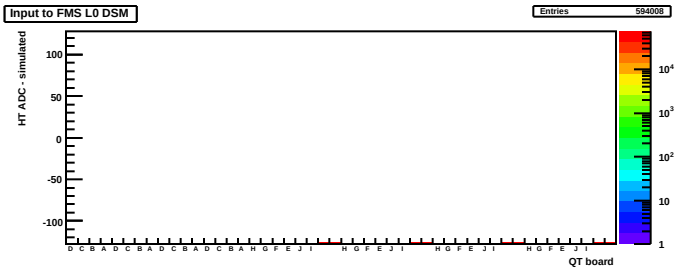

Input to FMS L0 DSM
Entries 3564048
QT8(2) sum
QT board

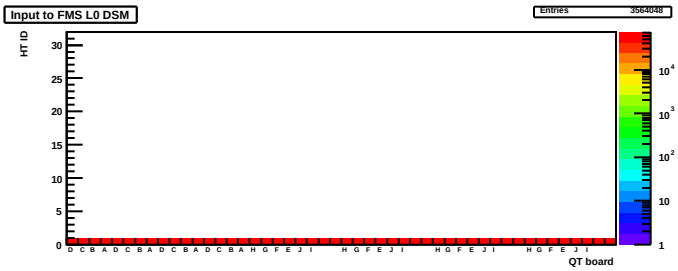

Input to FMS L0 DSM
Entries 594008
QT8(2) sum - simulated
QT board

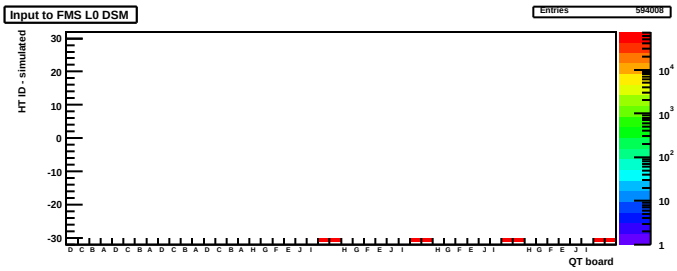

Input to FMS L0 DSM
Entries 3564048
QT8(3) sum
QT board

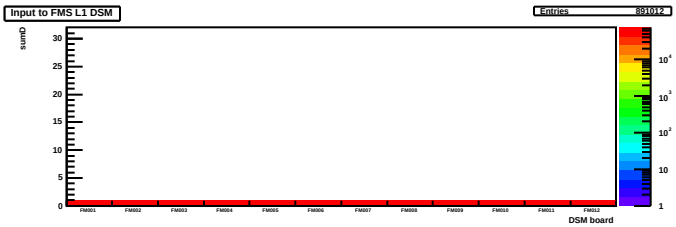

Input to FMS L0 DSM
Entries 594008
QT8(3) sum - simulated
QT board

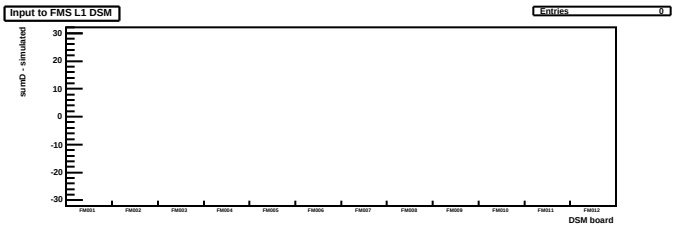

Input to FMS L0 DSM
Entries 3564048
HT ADC
QT board

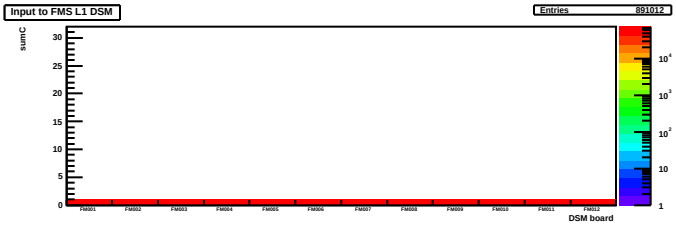

Input to FMS L0 DSM
Entries 594008
HT ADC - simulated
QT board

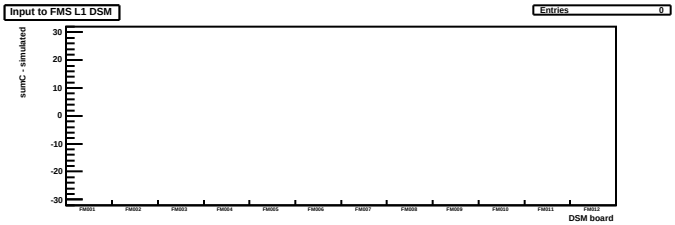

Input to FMS L0 DSM
Entries 3564048
HT ID
QT board

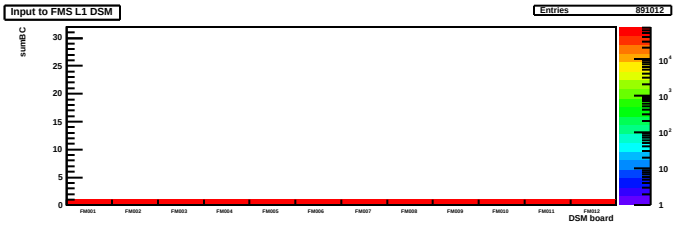

Input to FMS L0 DSM
Entries 594008
HT ID - simulated
QT board

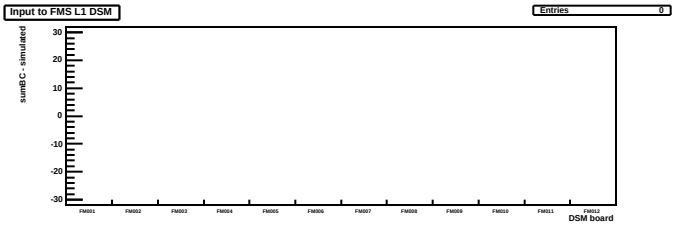

Input to FMS L1 DSM
Entries 891012
sumD
DSM board

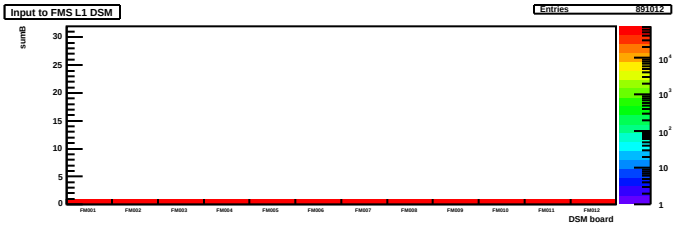

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

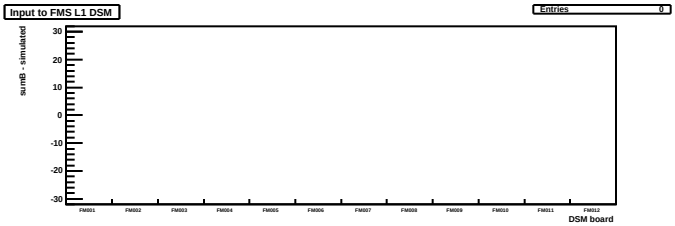

Input to FMS L1 DSM
Entries 891012
sumC
DSM board

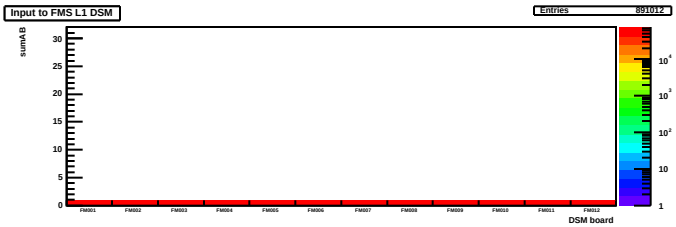

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

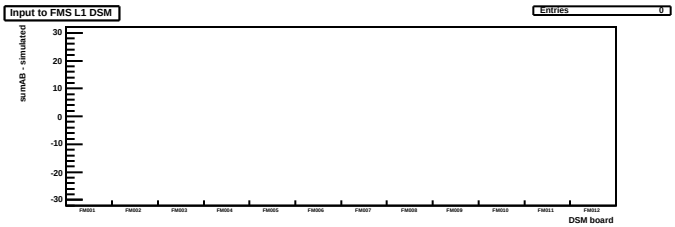

Input to FMS L1 DSM
Entries 891012
sumBC
DSM board

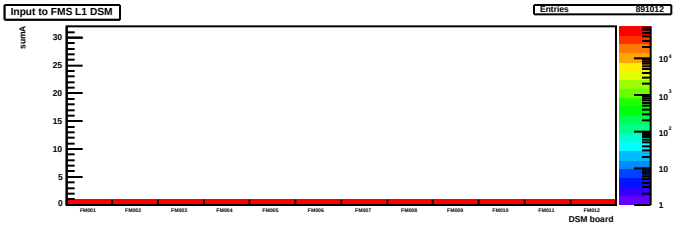

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

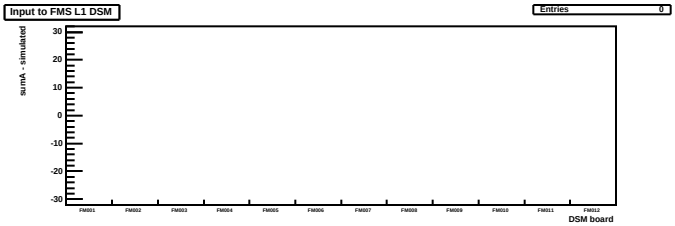

Input to FMS L1 DSM
Entries 891012
sumB
DSM board

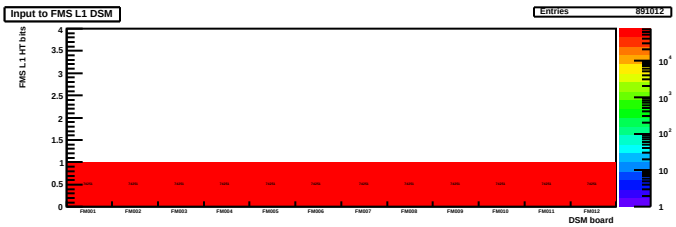

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

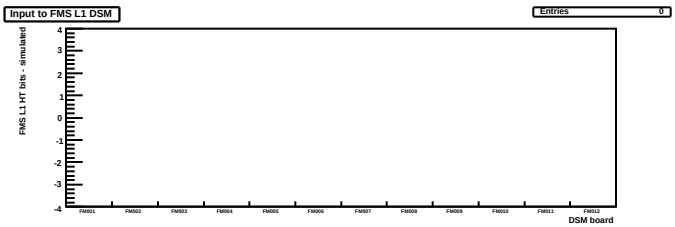

Input to FMS L1 DSM
Entries 891012
sumAB
DSM board

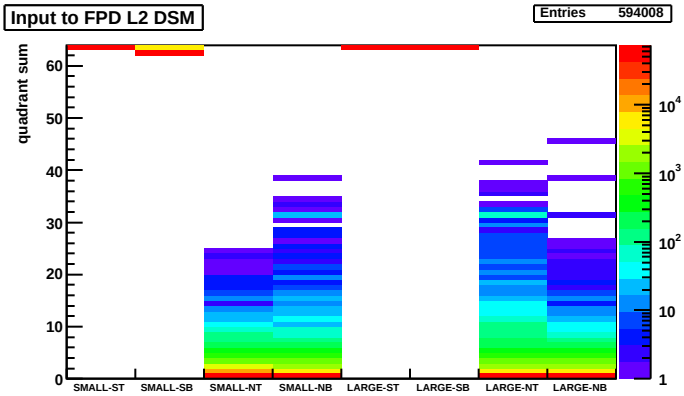

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

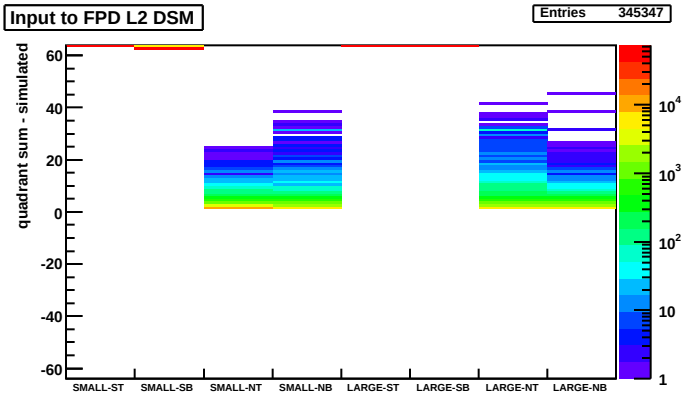

Input to FMS L1 DSM
Entries 891012
sumA
DSM board

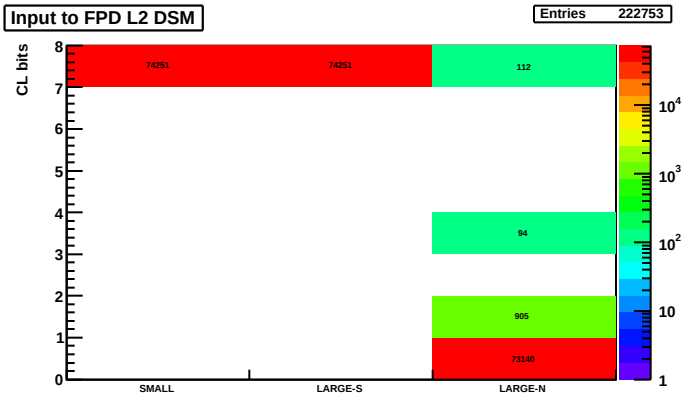

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

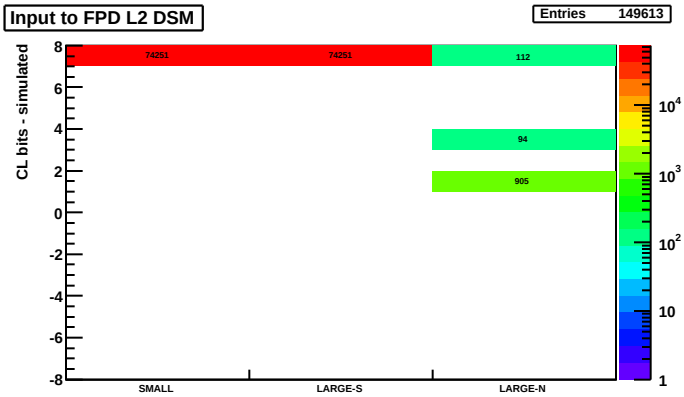

Input to FMS L1 DSM
Entries 891012
FMS L1 HT bits
DSM board

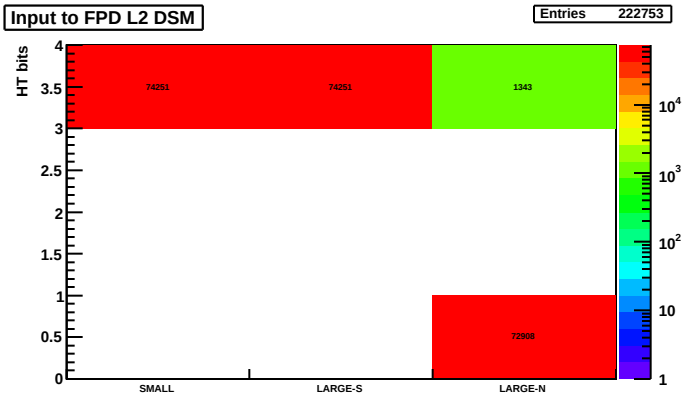

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

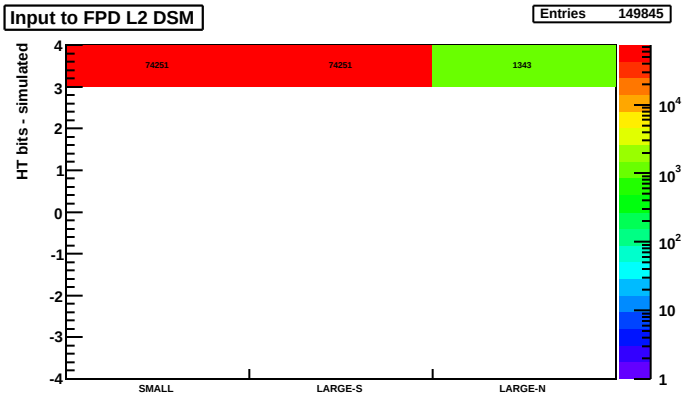

Input to FPD L2 DSM
Entries 594008
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

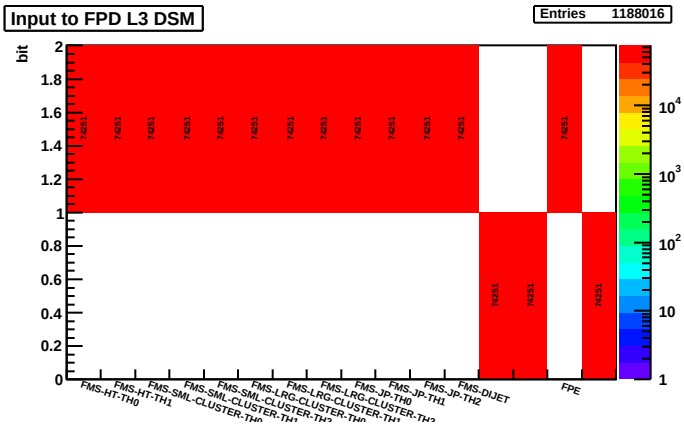

Input to FPD L2 DSM
Entries 345347
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

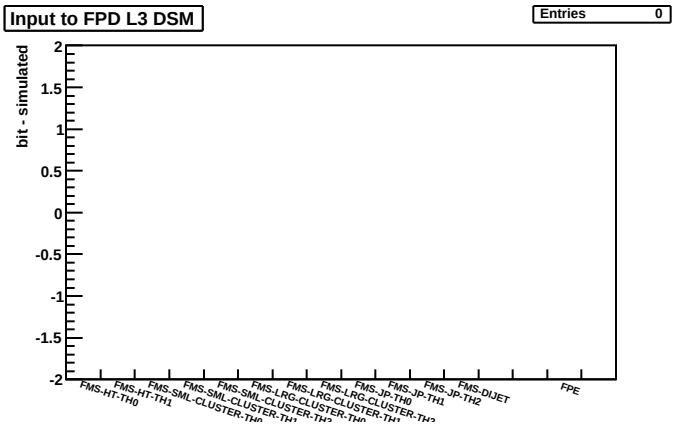

Input to FPD L2 DSM
Entries 222753
CL bits
74251
74251
112
94
905
73140
SMALL
LARGE-S
LARGE-N

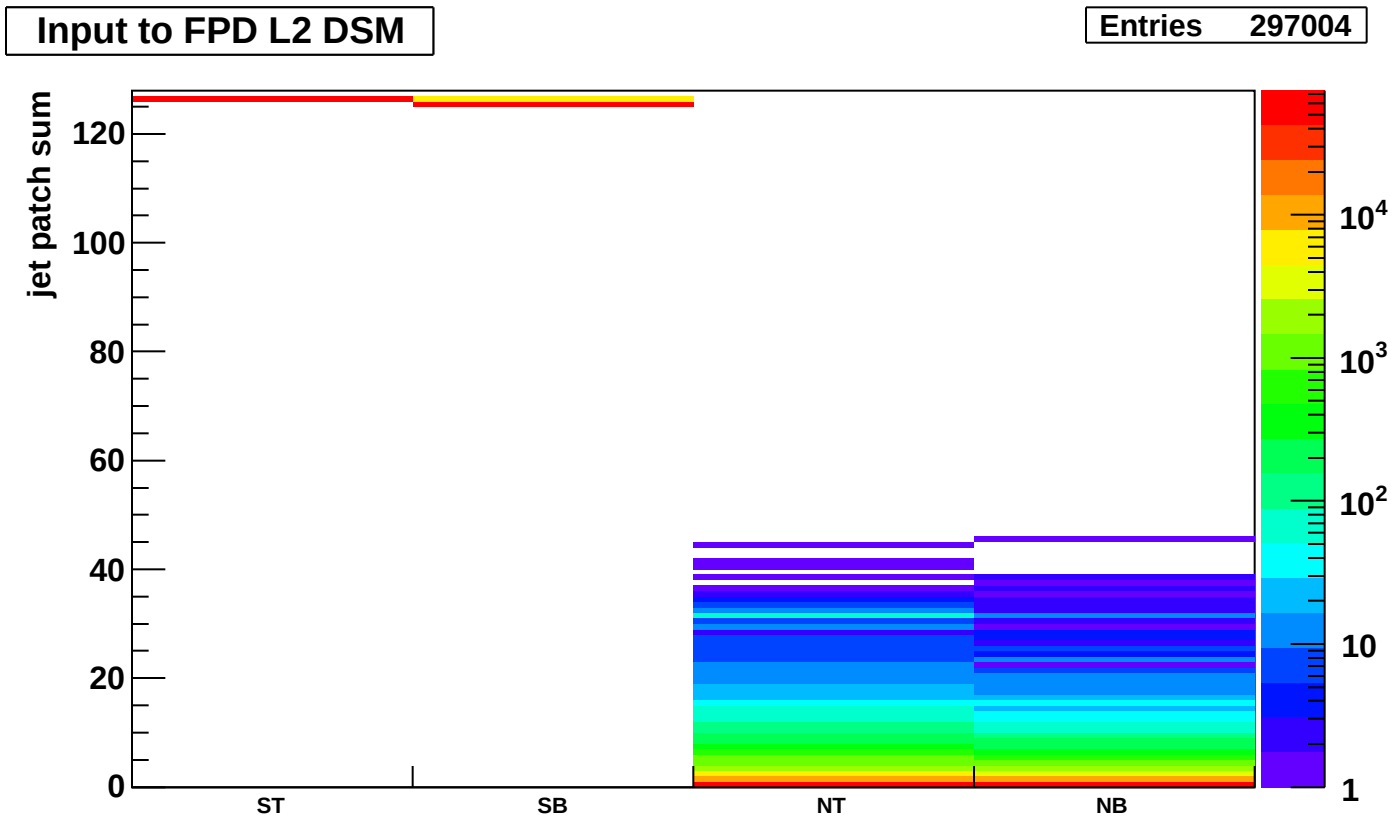

Input to FPD L2 DSM
Entries 149613
CL bits - simulated
74251
74251
112
94
905
SMALL
LARGE-S
LARGE-N

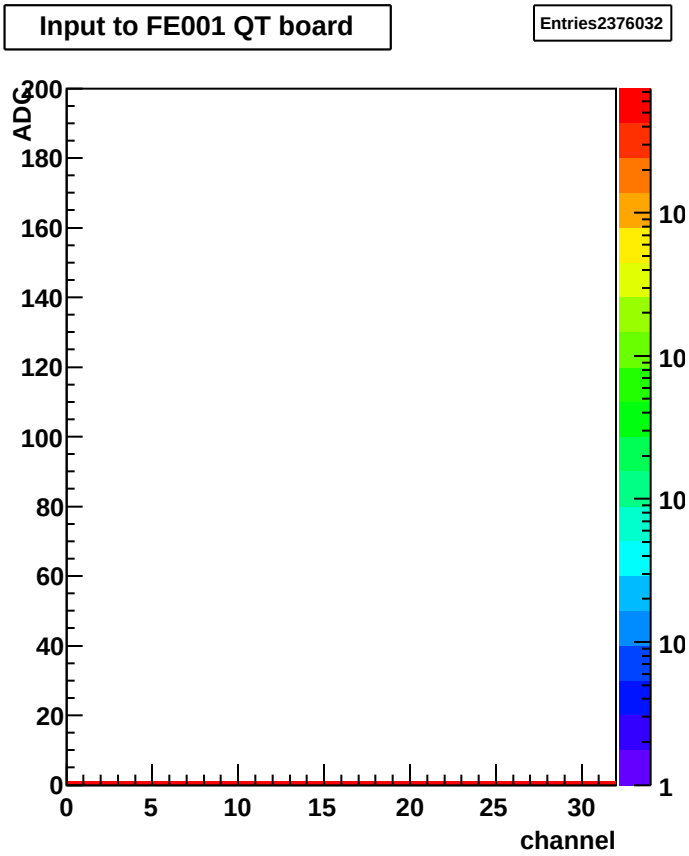

Input to FPD L2 DSM
Entries 222753
HT bits
74251
74251
1343
72908
SMALL
LARGE-S
LARGE-N

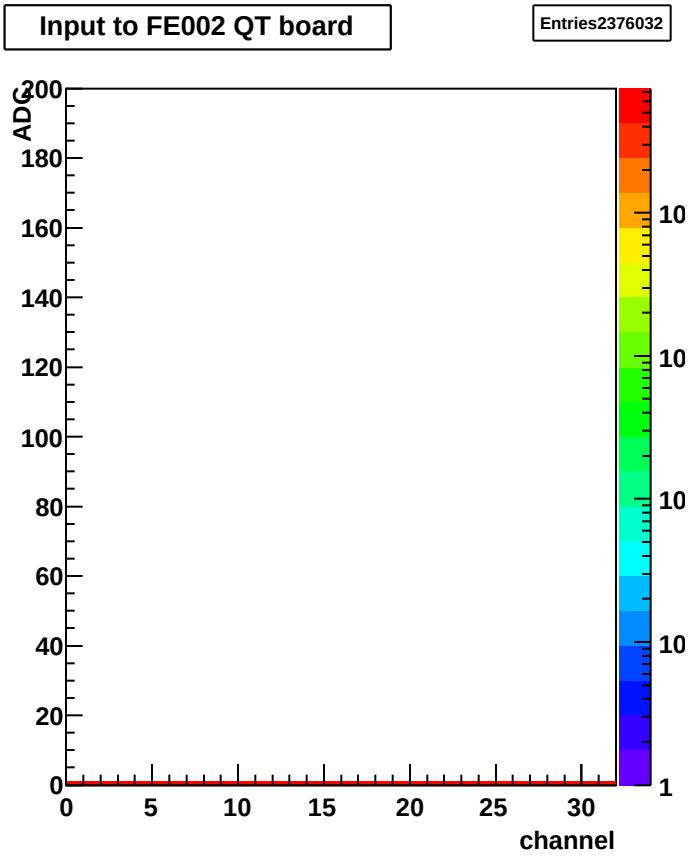

Input to FPD L2 DSM
Entries 149845
HT bits - simulated
74251
74251
1343
SMALL
LARGE-S
LARGE-N

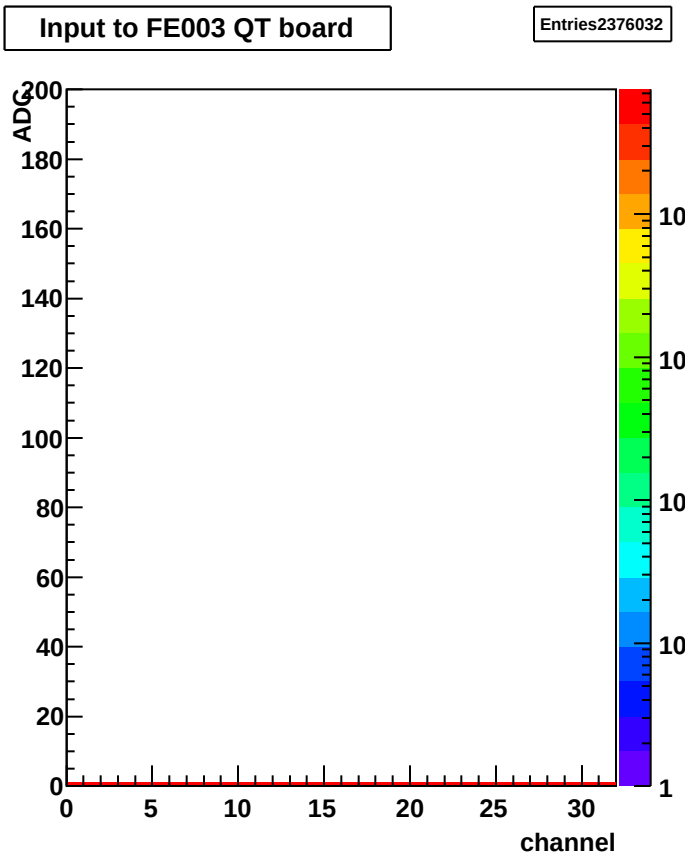

Input to FPD L3 DSM
Entries 1188016
bit
74251
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

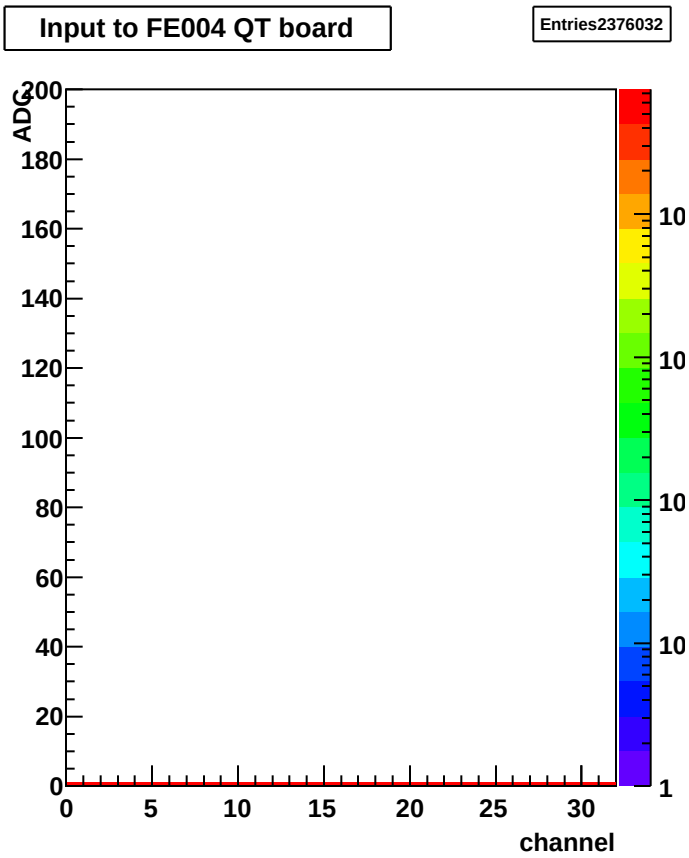

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

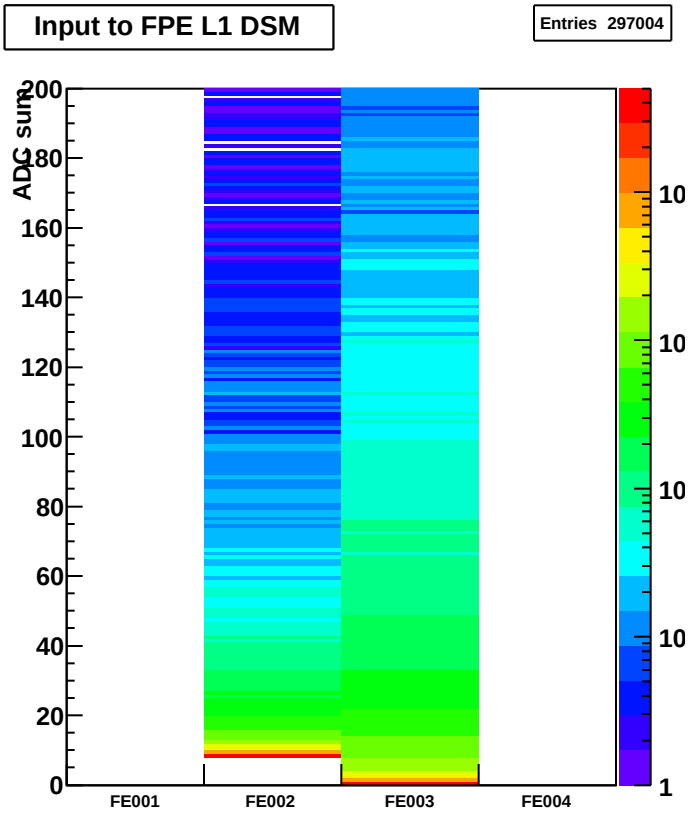

Input to FPD L2 DSM
Entries 297004
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$

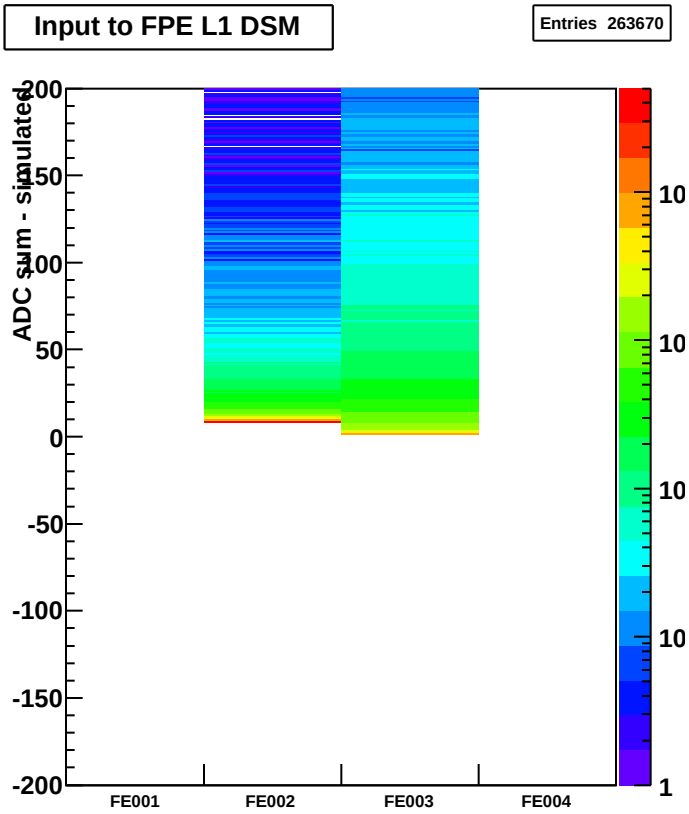

Input to FE001 QT board
Entries2376032
ADC
channel

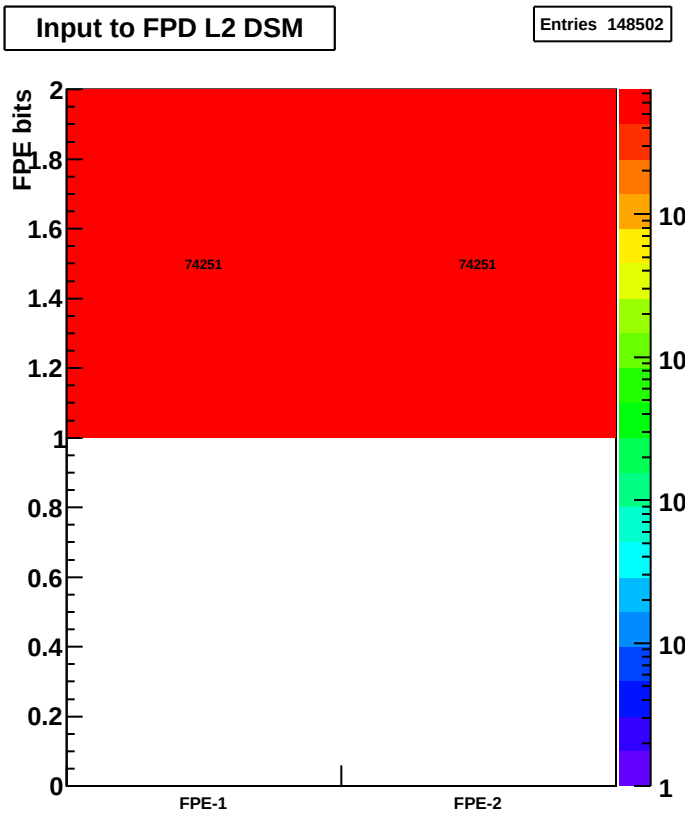

Input to FE002 QT board
Entries2376032
ADC
channel

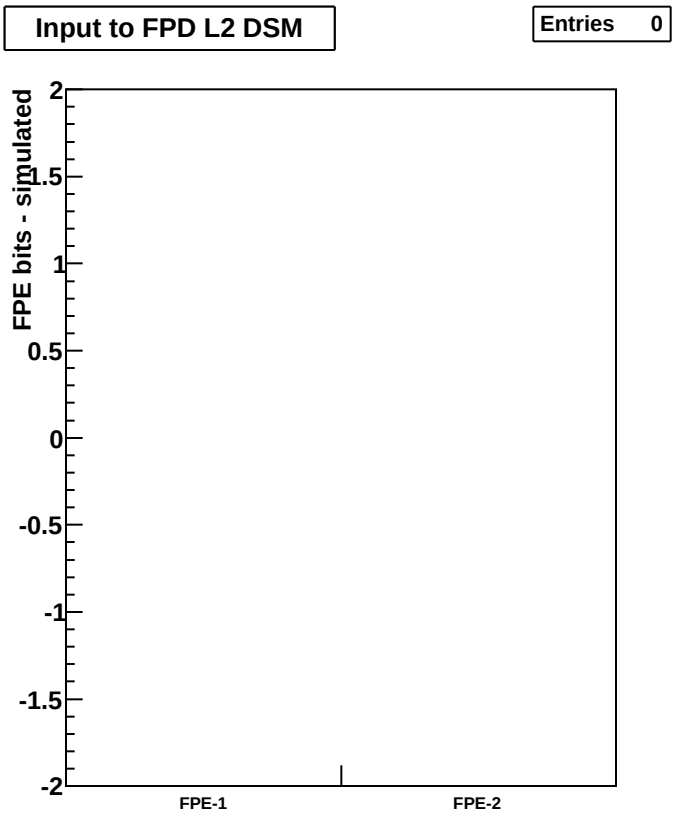

Input to FE003 QT board
Entries2376032
ADC
channel


Input to FE004 QT board
Entries2376032
ADC
channel


Input to FPE L1 DSM
Entries 297004
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 263670
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 148502
FPE bits
74251
74251
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2