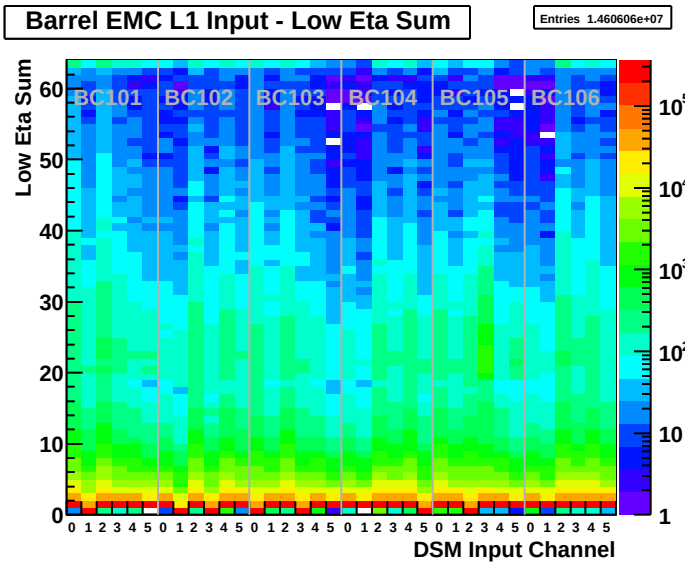
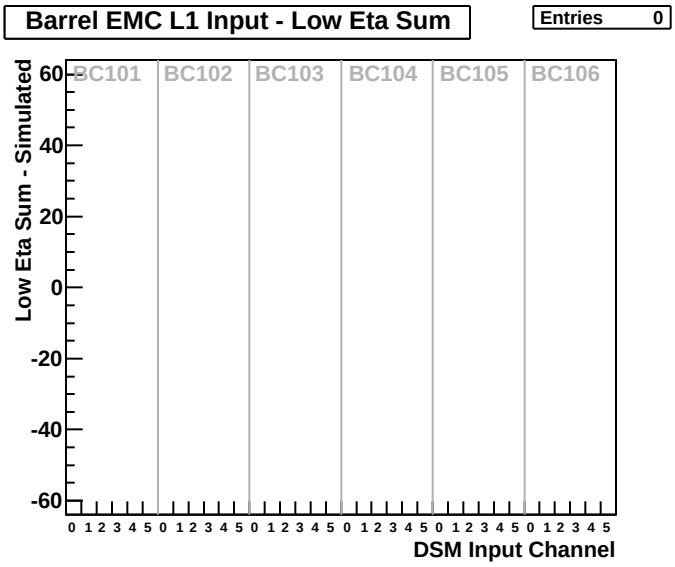
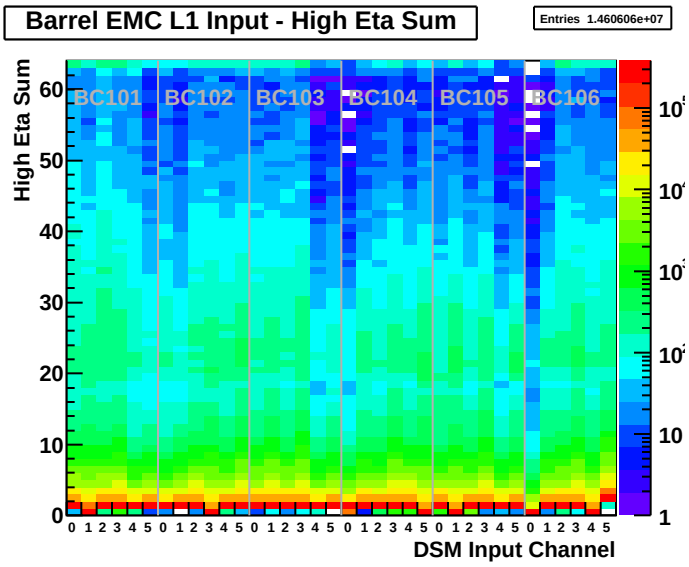
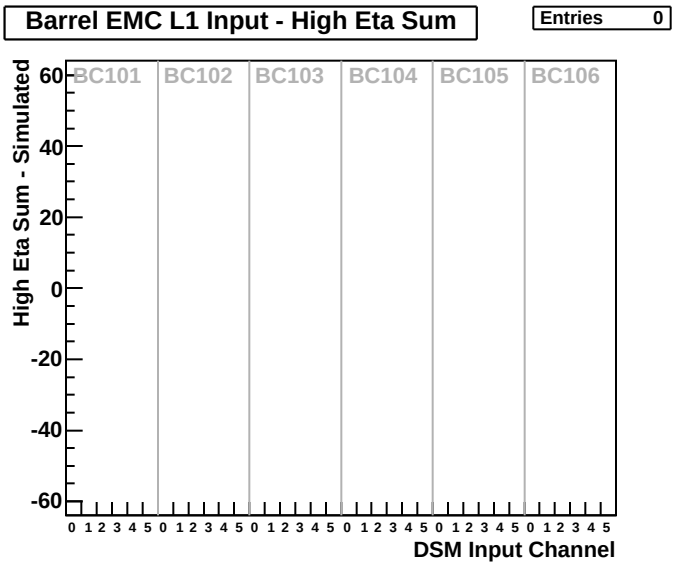
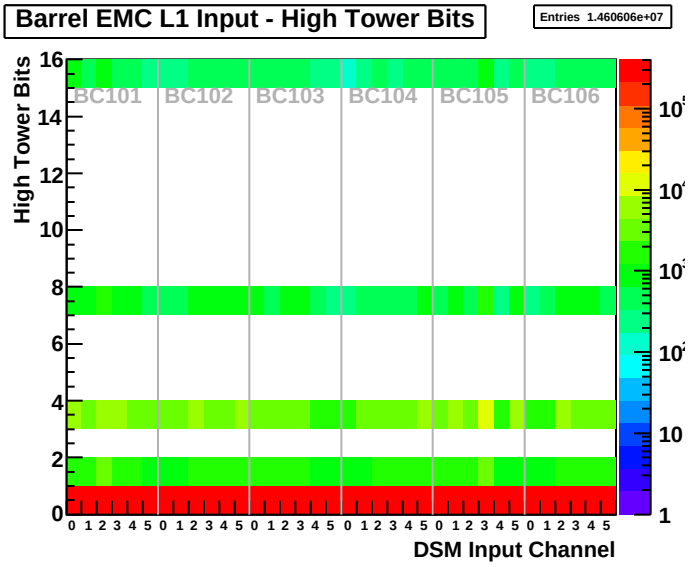
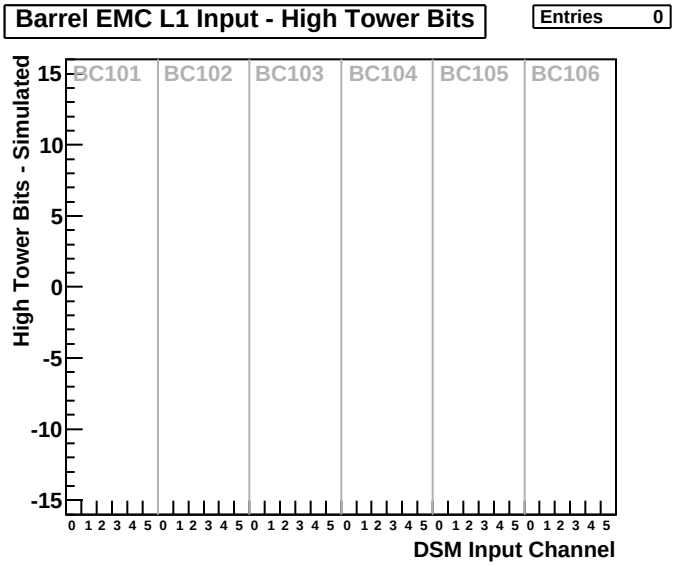

Barrel EMC L1 Input - Low Eta Sum
Entries 1.460606e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

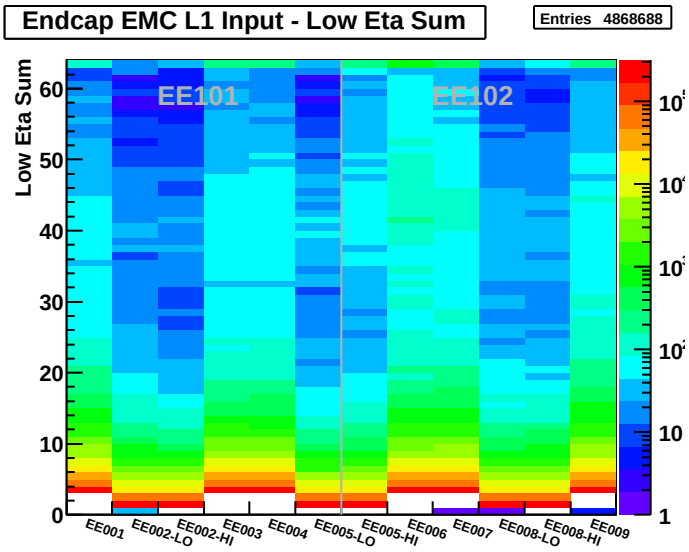

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

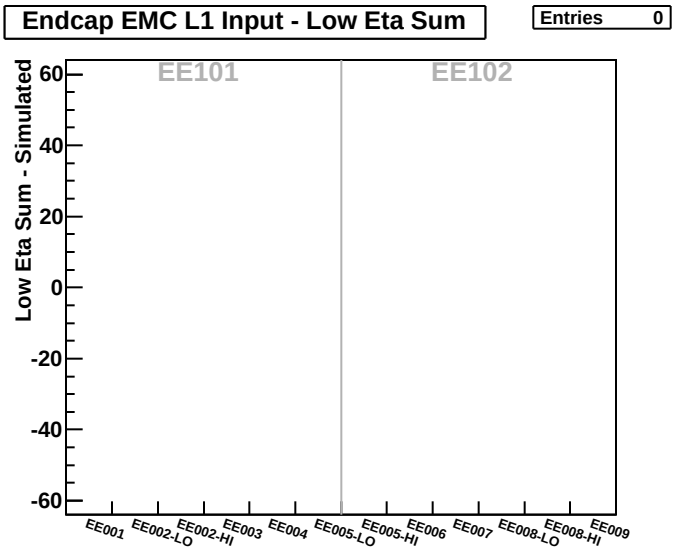

Barrel EMC L1 Input - High Eta Sum
Entries 1.460606e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

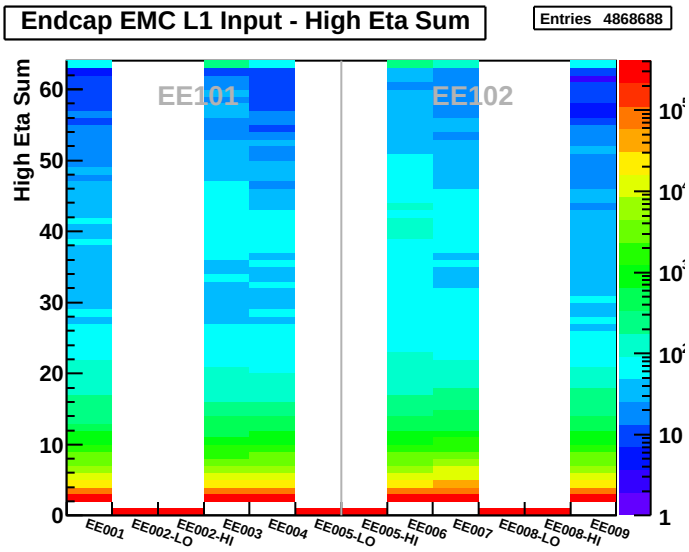

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

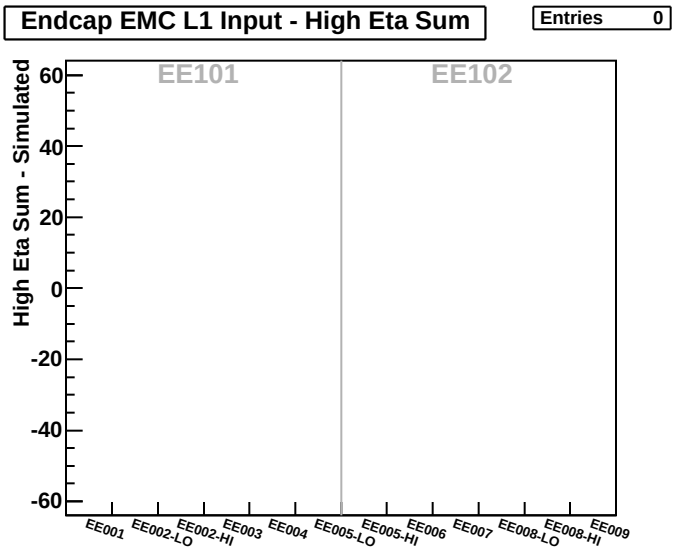

Barrel EMC L1 Input - High Tower Bits
Entries 1.460606e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

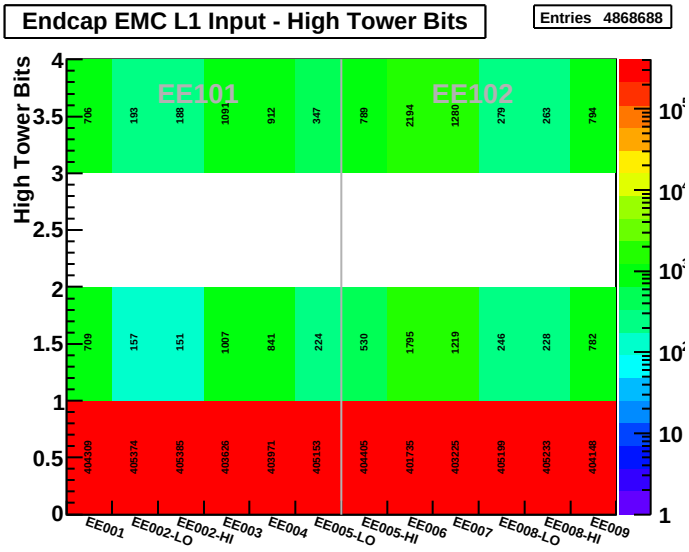

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

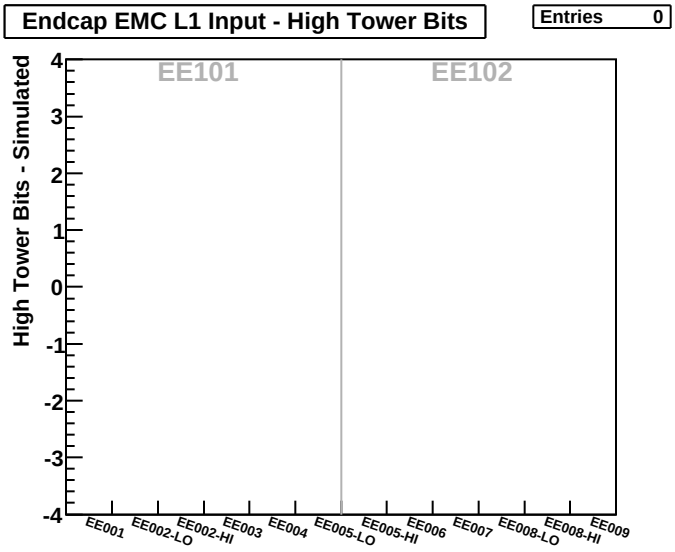

Endcap EMC L1 Input - Low Eta Sum
Entries 4868688
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

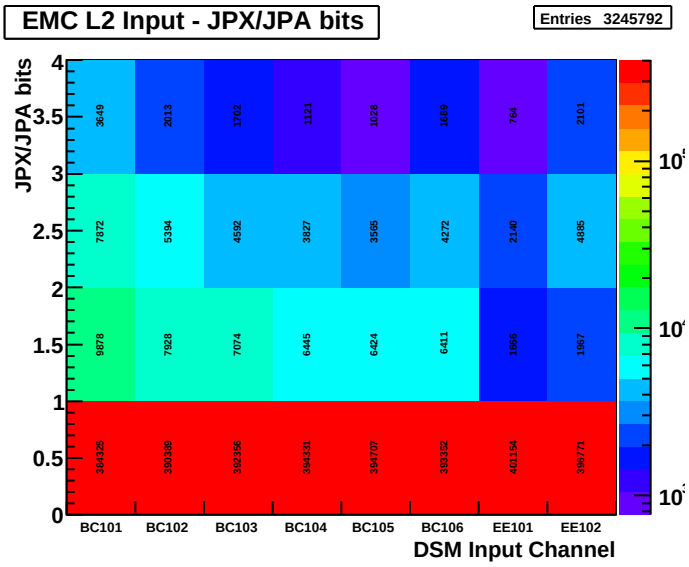

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

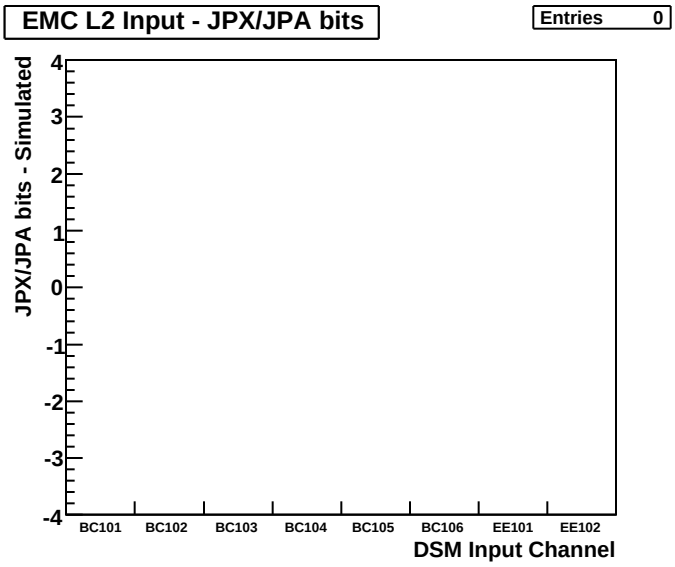

Endcap EMC L1 Input - High Eta Sum
Entries 4868688
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

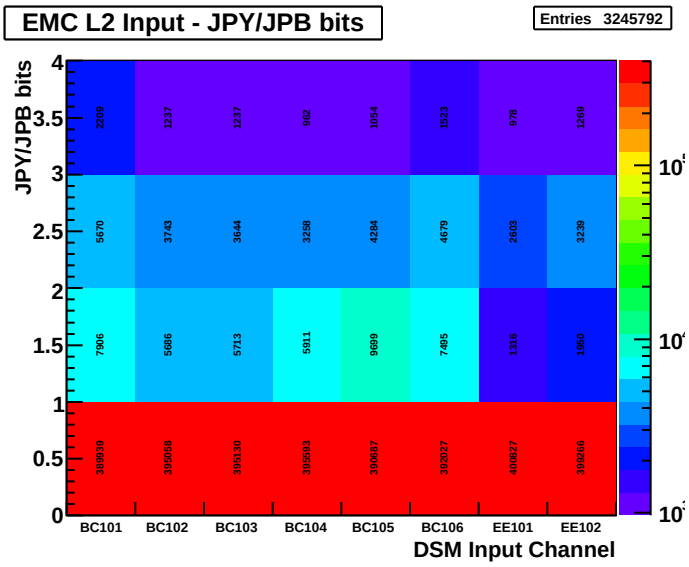

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

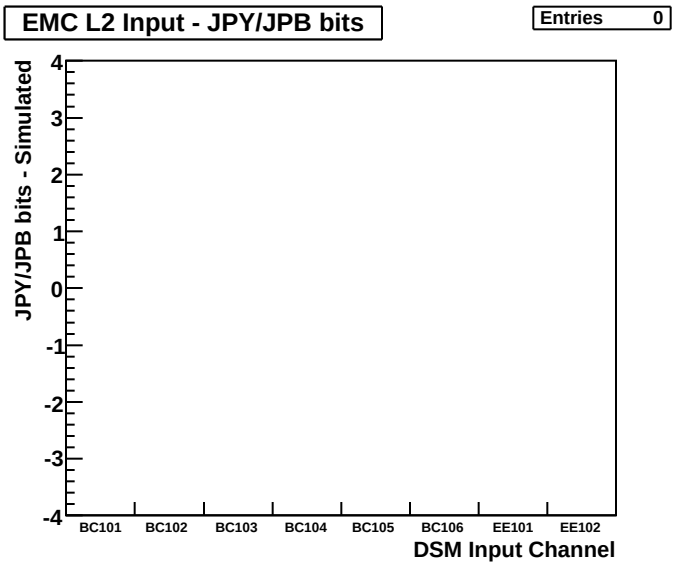

Endcap EMC L1 Input - High Tower Bits
Entries 4868688
High Tower Bits
EE101
EE102
706 193 188 1093 912 347 789 2194 1280 279 263 794
709 157 151 1007 841 224 530 1795 1219 246 228 782
404309 405374 405386 403926 403971 405153 404405 401795 403225 405199 405233 404148
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

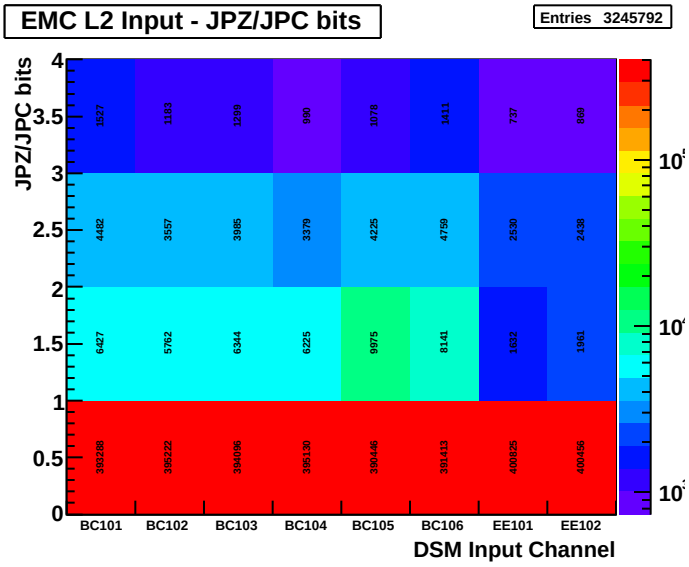

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

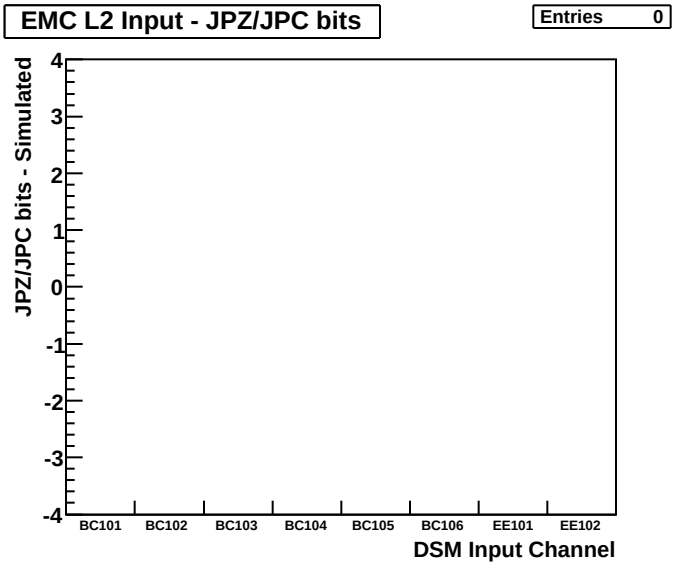

EMC L2 Input - JPX/JPA bits
Entries 3245792
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3649 2013 1702 1121 1028 1689 764 2101
7872 5394 4592 3827 3565 4272 2140 4885
9878 7928 7074 6445 6424 6411 1666 1967
384325 390389 392356 394331 394707 393352 401154 398771

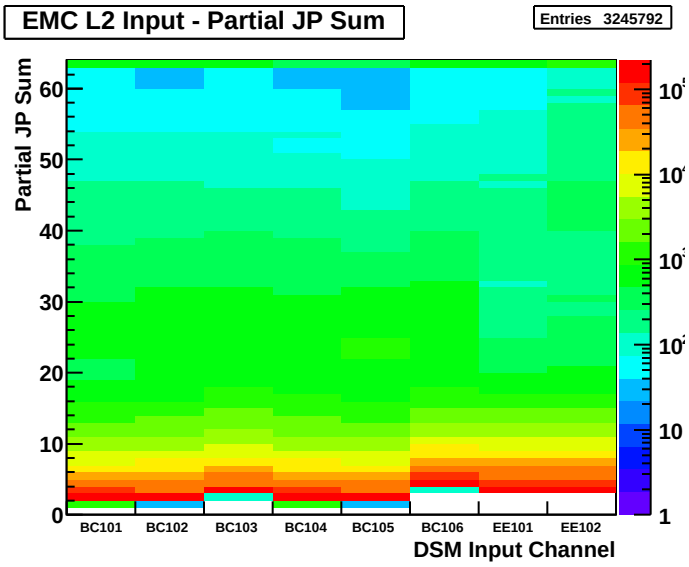

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

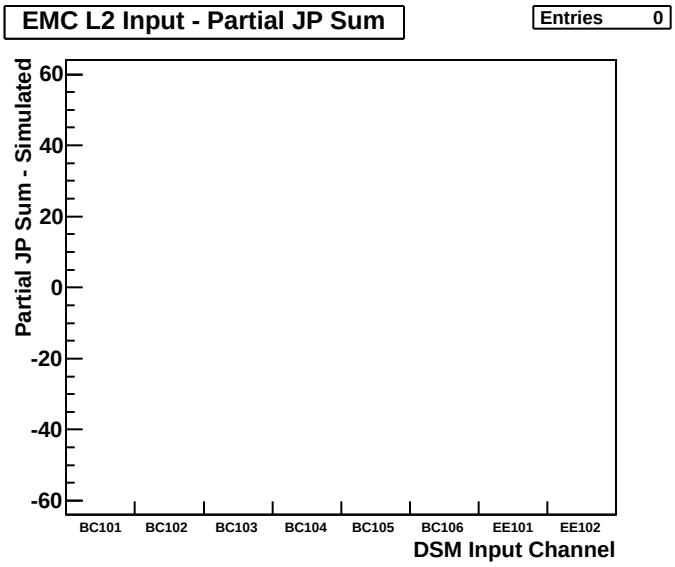

EMC L2 Input - JPY/JPB bits
Entries 3245792
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2209 1237 1237 962 1054 1523 978 1269
5670 3743 3644 3258 4284 4679 2603 3239
7906 5686 5713 5911 9699 7495 1316 1950
389939 390058 390130 390593 390687 392027 400827 399266

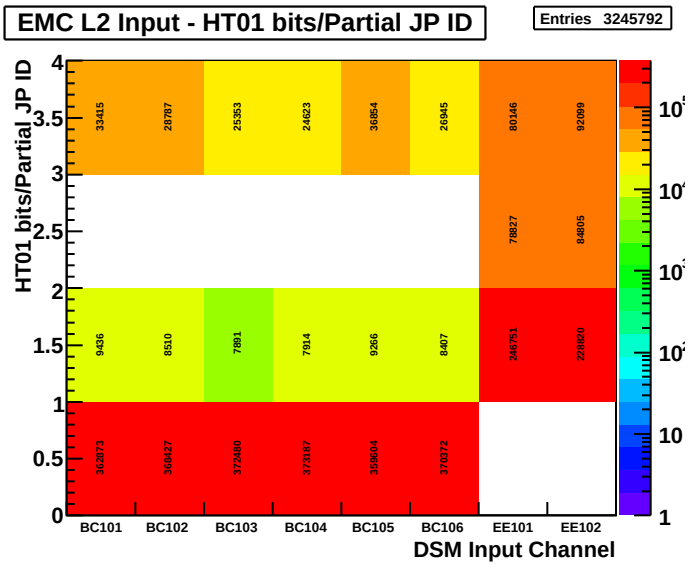

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

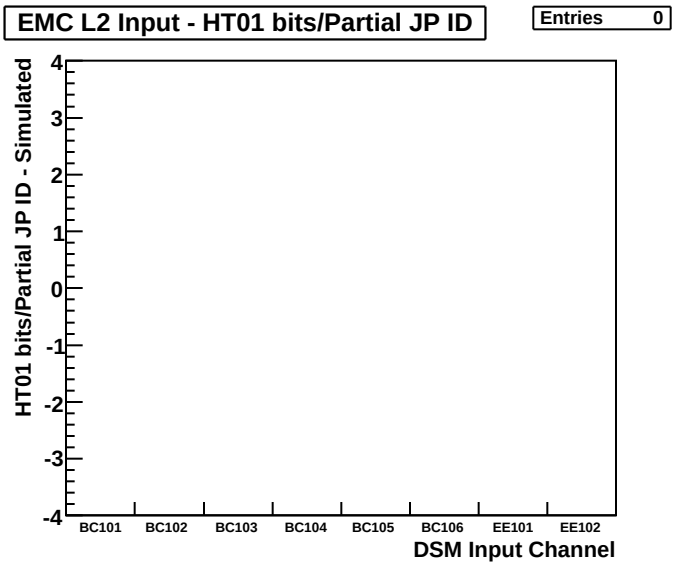

EMC L2 Input - JPZ/JPC bits
Entries 3245792
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1527 1183 1299 990 1078 1411 737 869
4482 3557 3985 3379 4225 4759 2530 2438
6427 5762 6344 6225 9975 8141 1632 1961
393288 395222 394096 395130 390446 391413 400325 400456

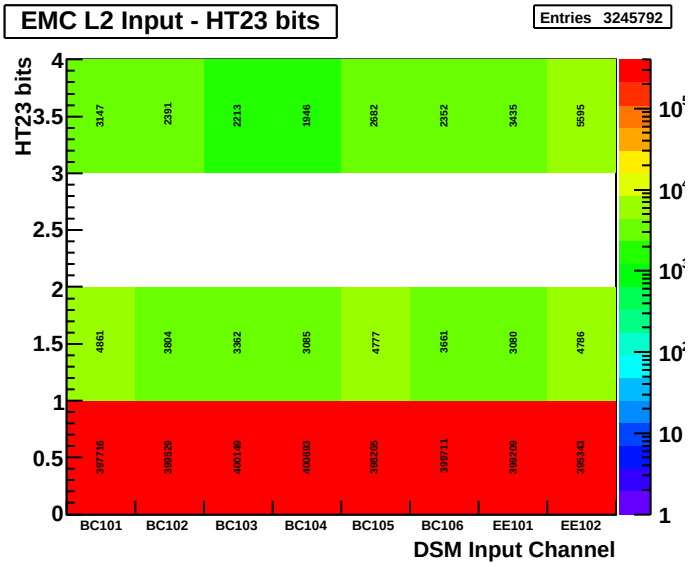

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

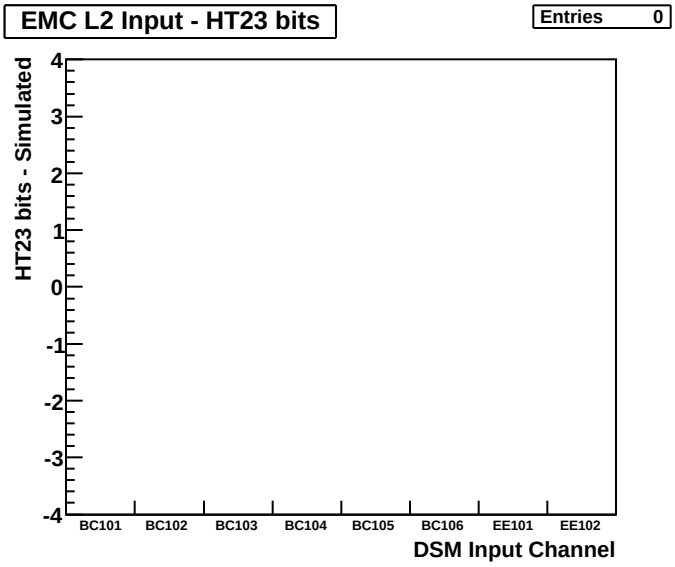

EMC L2 Input - Partial JP Sum
Entries 3245792
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

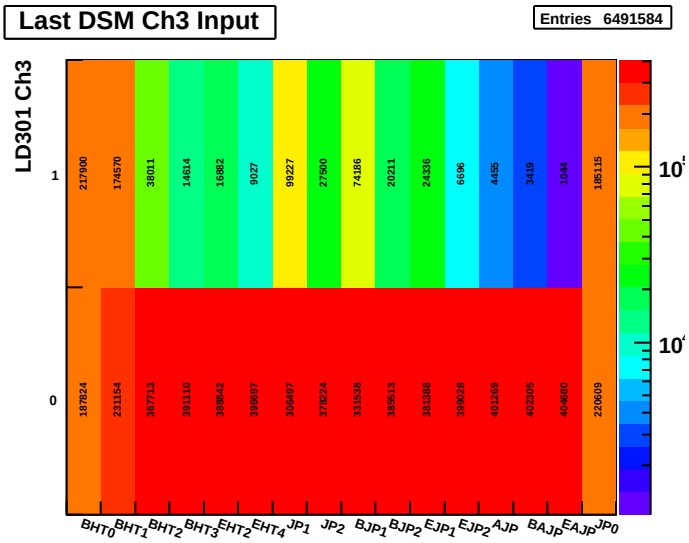

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

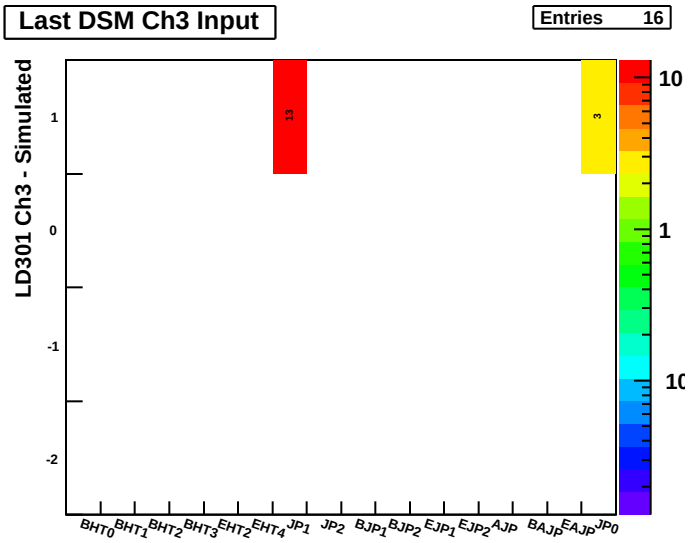

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3245792
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

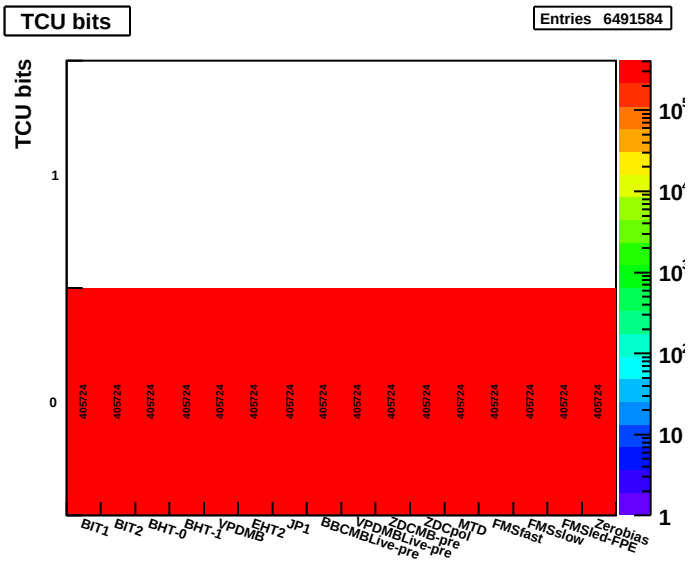

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

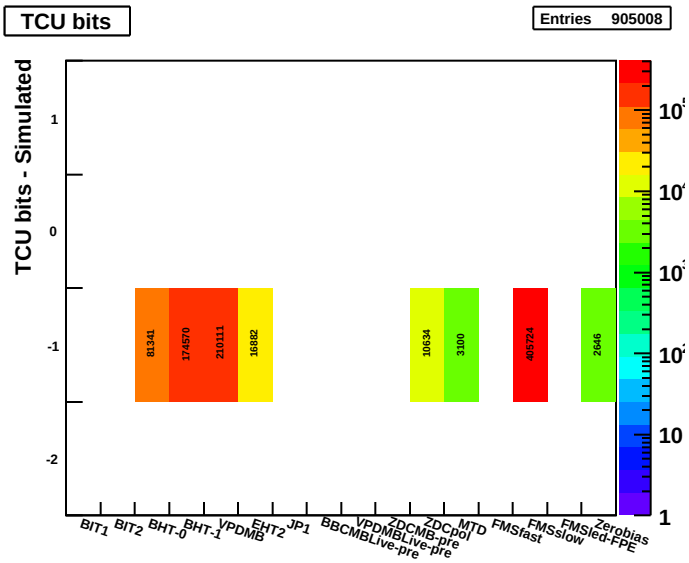

EMC L2 Input - HT23 bits
Entries 3245792
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

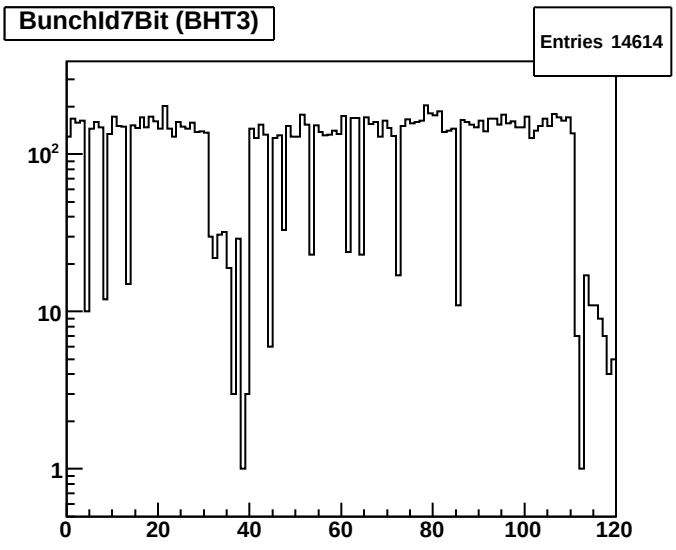

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

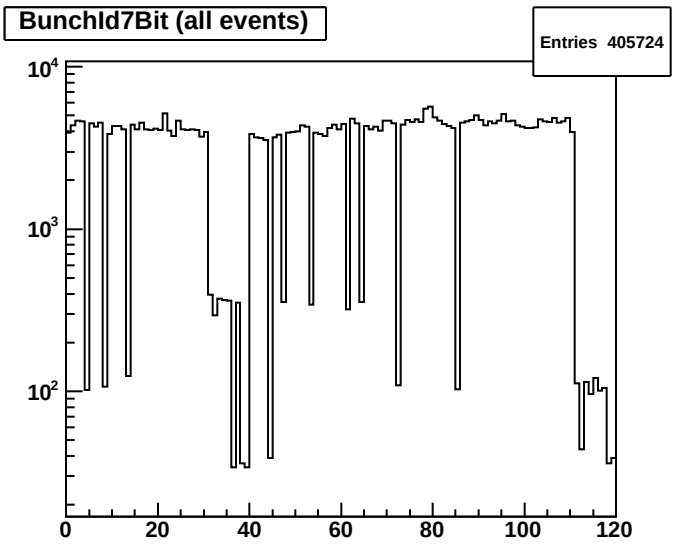

Last DSM Ch3 Input
Entries 6491584
LD301 Ch3
217900 174570 38011 14614 16882 9027 99227 27500 74186 20211 24336 6696 4455 3419 1044 185135
187824 231154 367713 391110 388842 396697 306497 378224 331538 385513 381388 399028 401269 402305 404680 220609
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

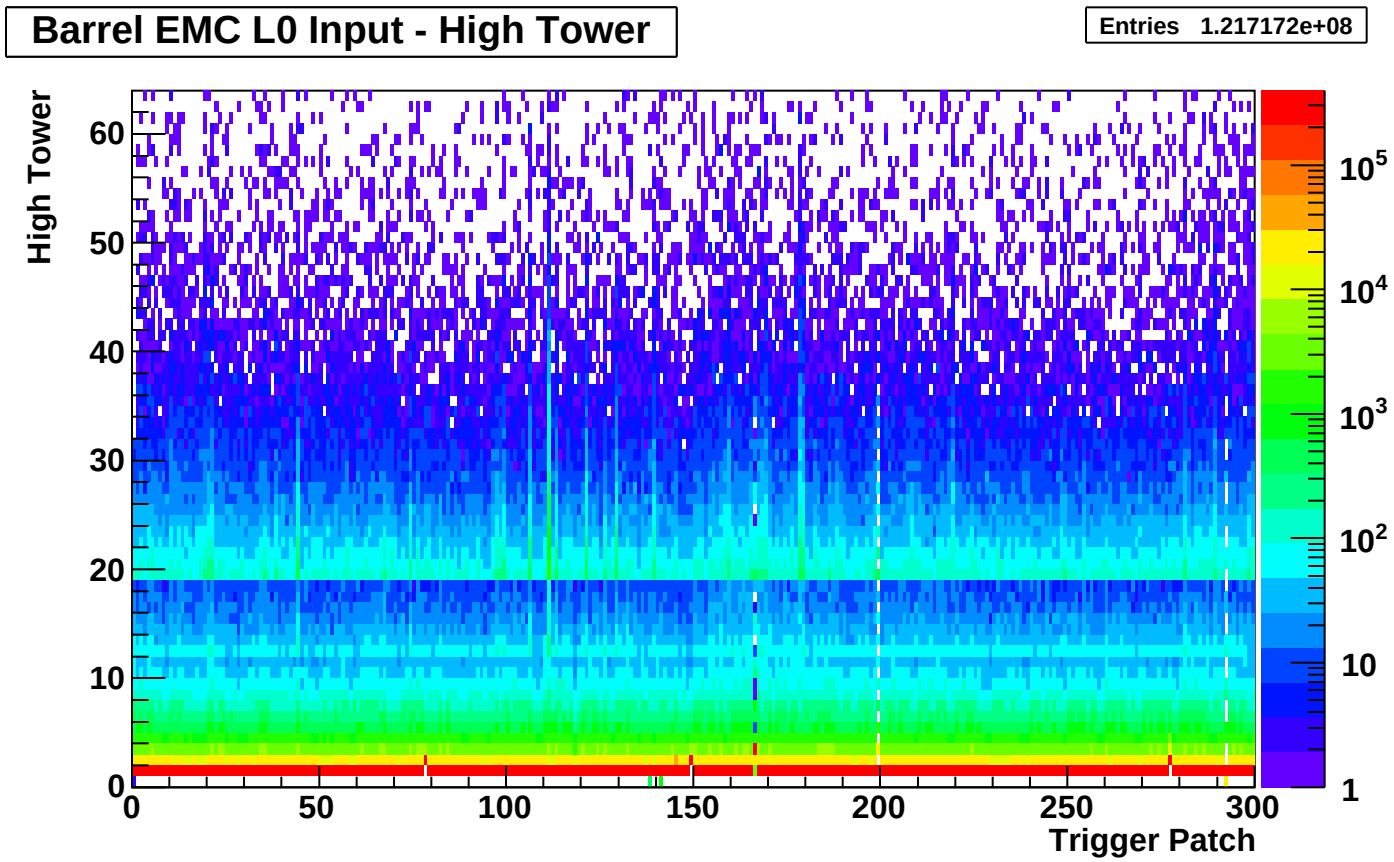

Last DSM Ch3 Input
Entries 16
LD301 Ch3 - Simulated
13
3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

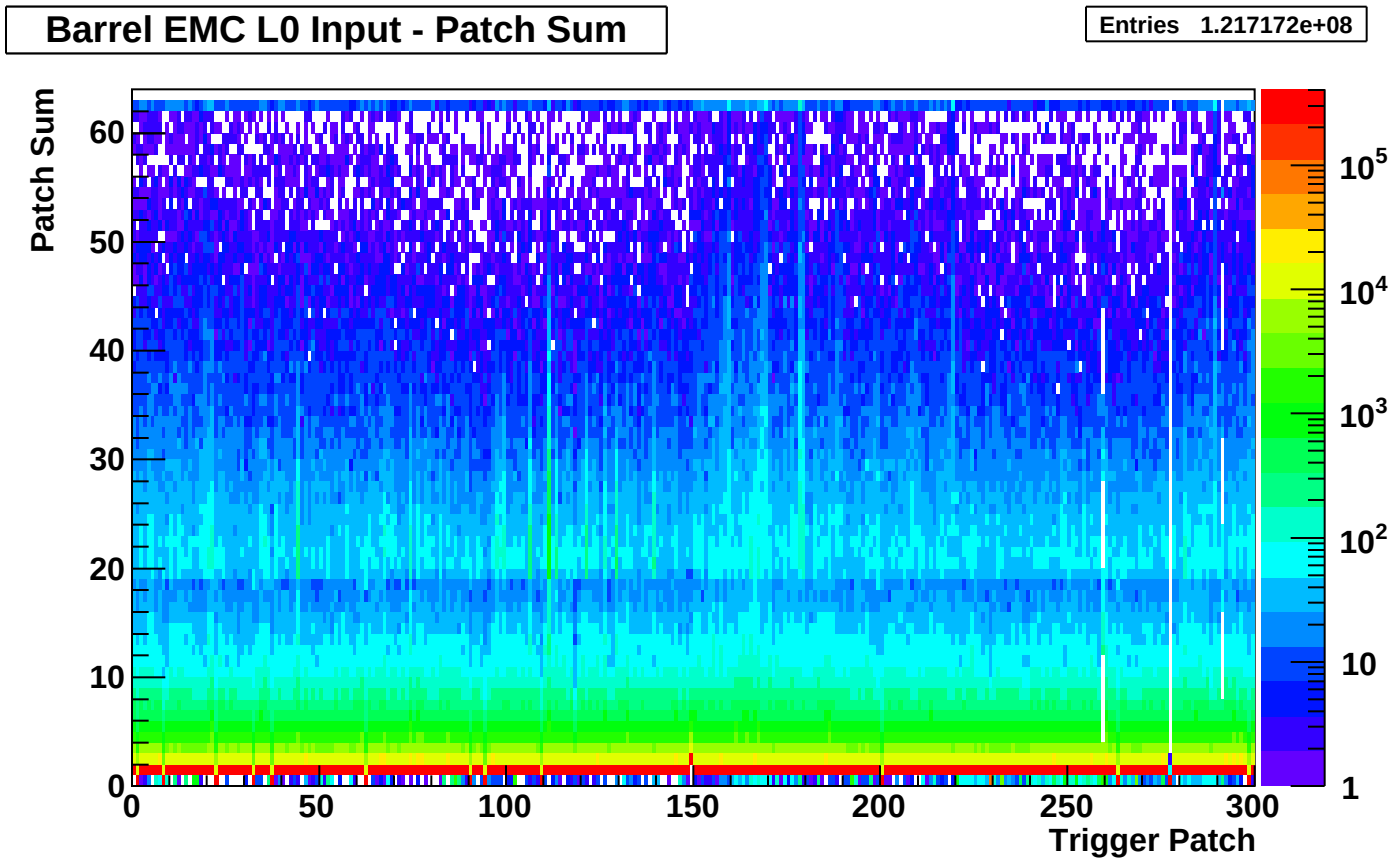

TCU bits
Entries 6491584
TCU bits
405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724 405724
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

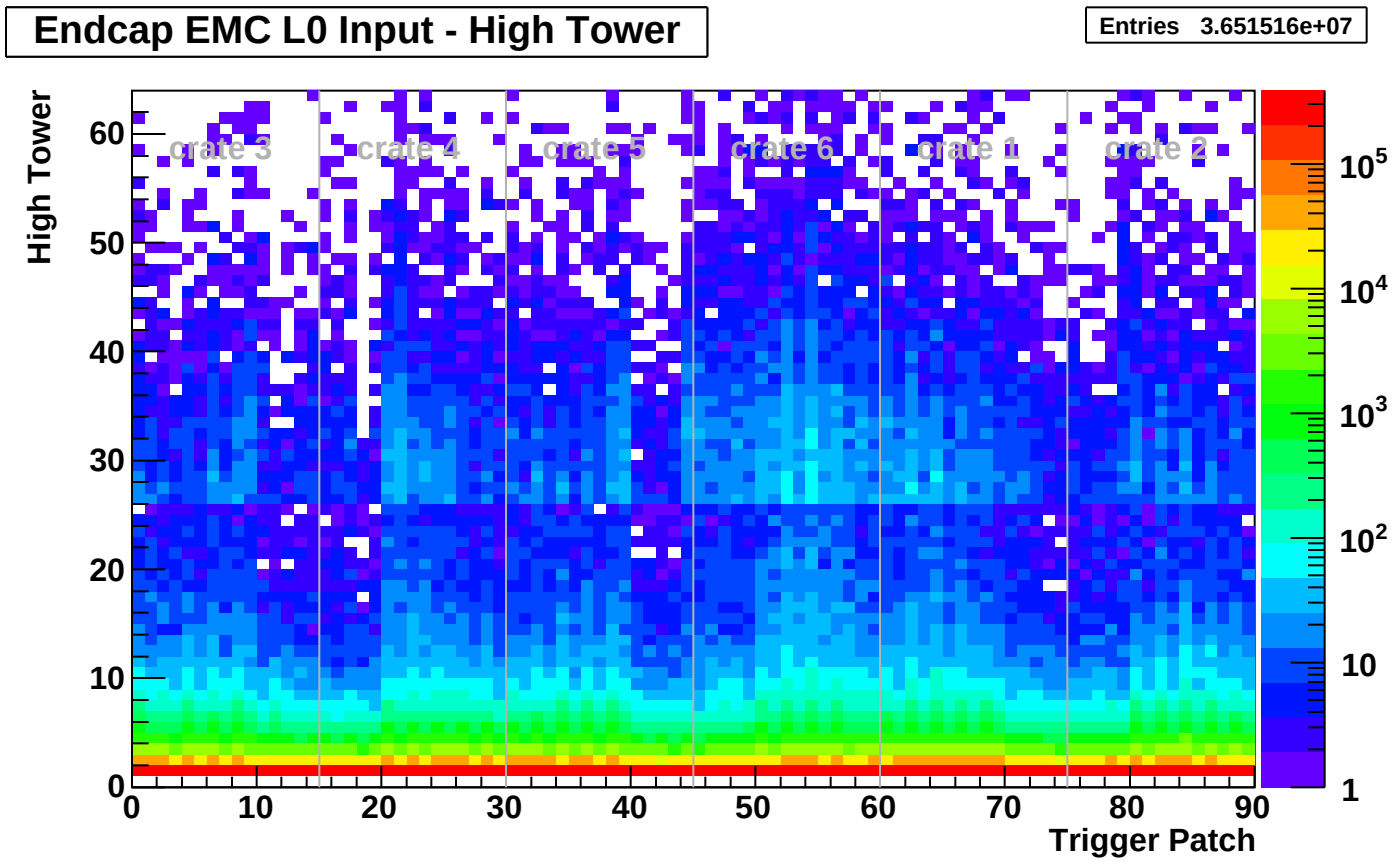

TCU bits
Entries 905008
TCU bits - Simulated
81341 174570 210111 16882 10634 3100 405724 2646
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

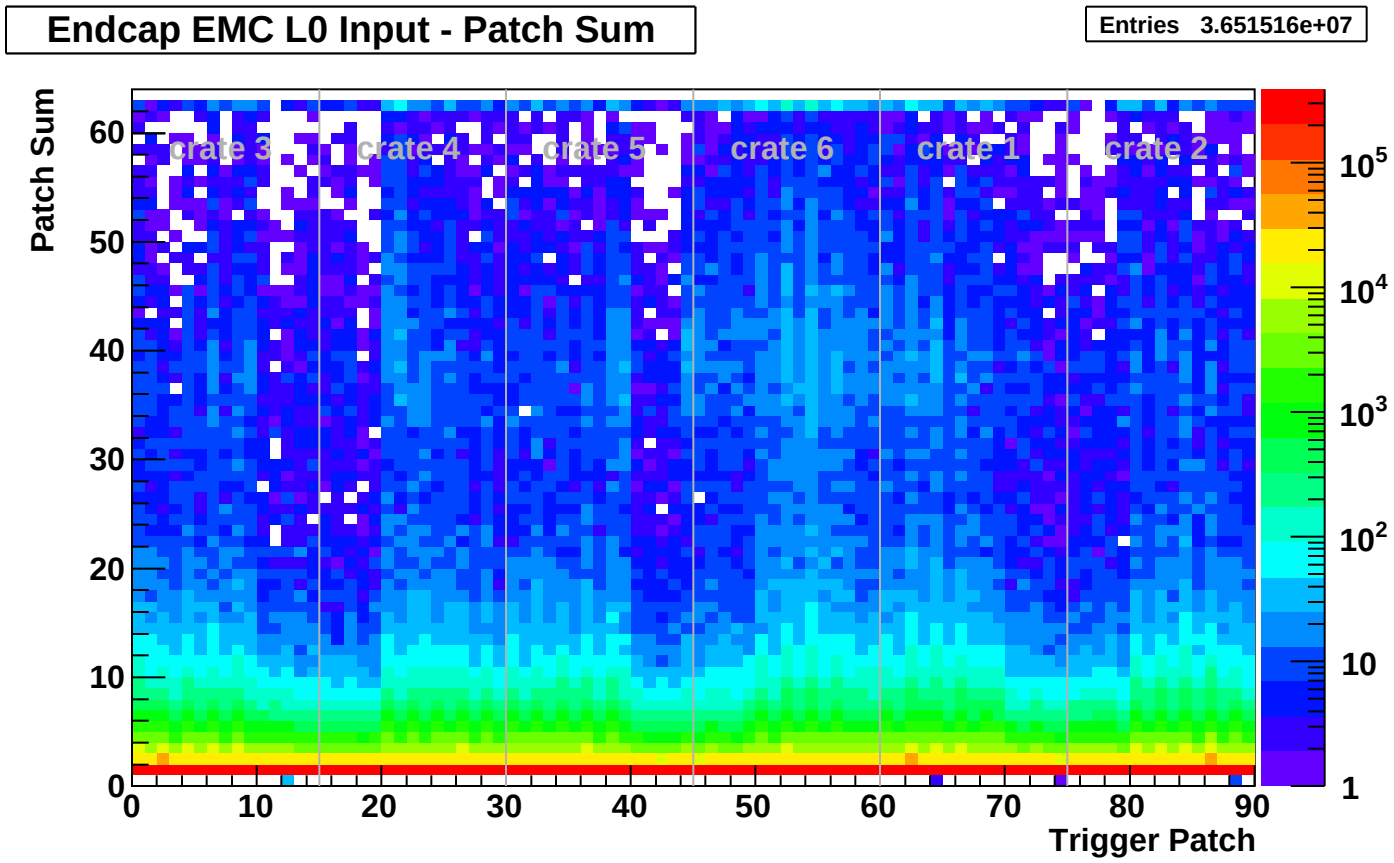

BunchId7Bit (BHT3)
Entries 14614

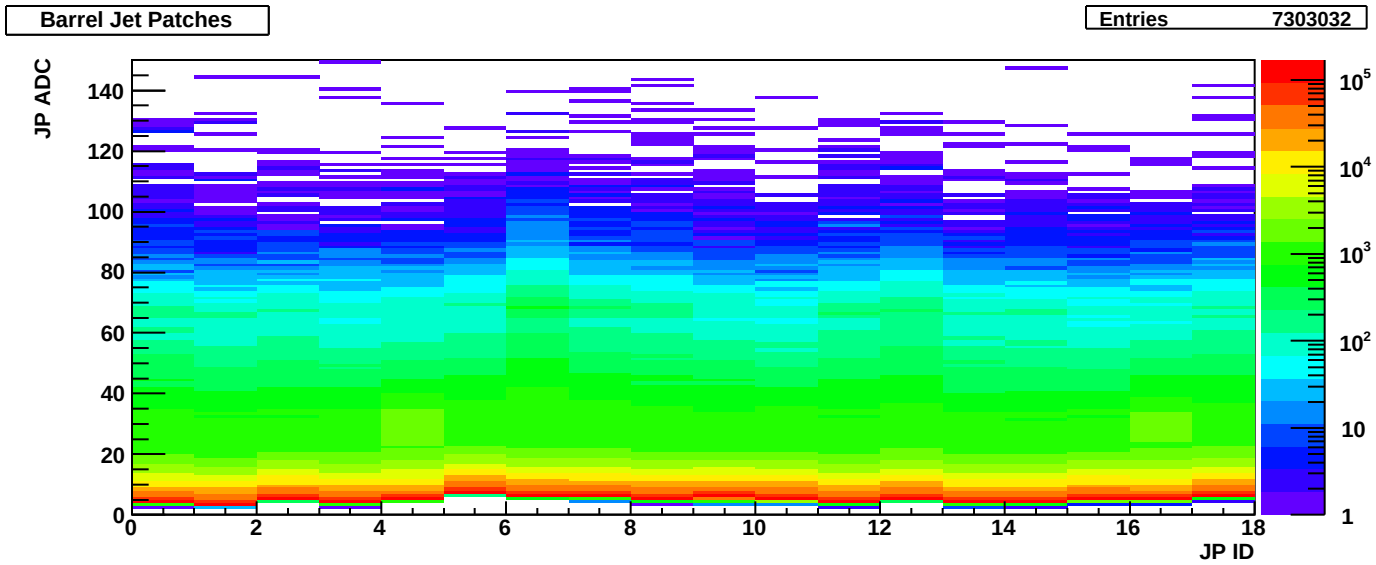

BunchId7Bit (all events)
Entries 405724

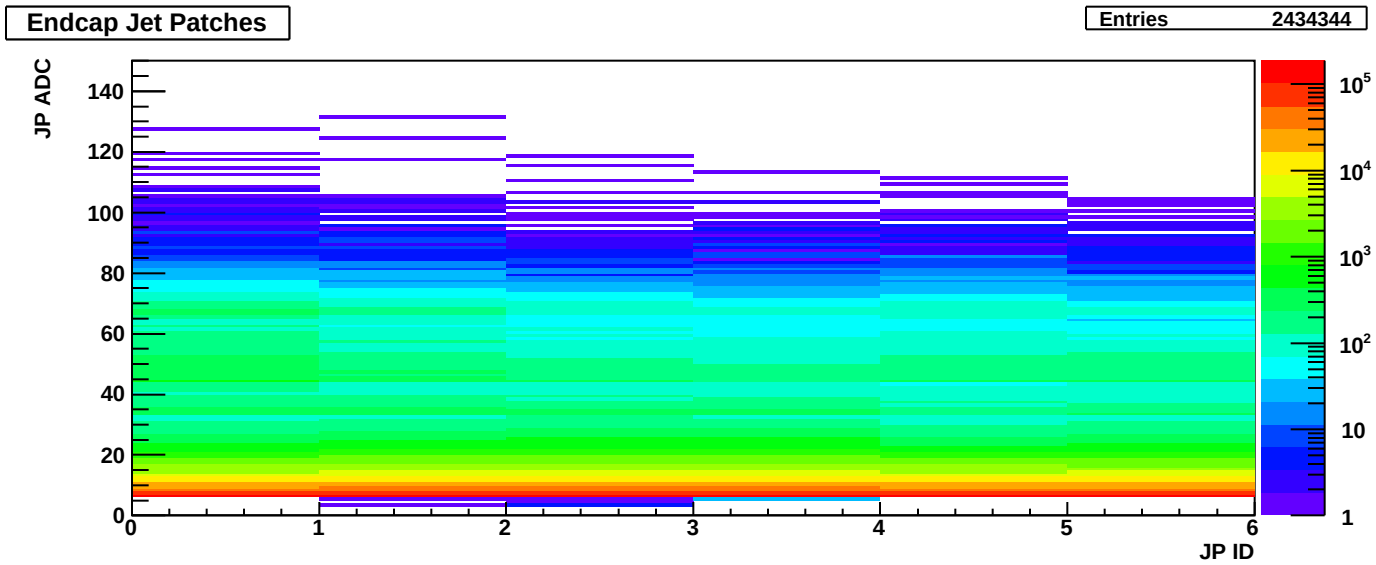

Barrel EMC L0 Input - High Tower
Entries 1.217172e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

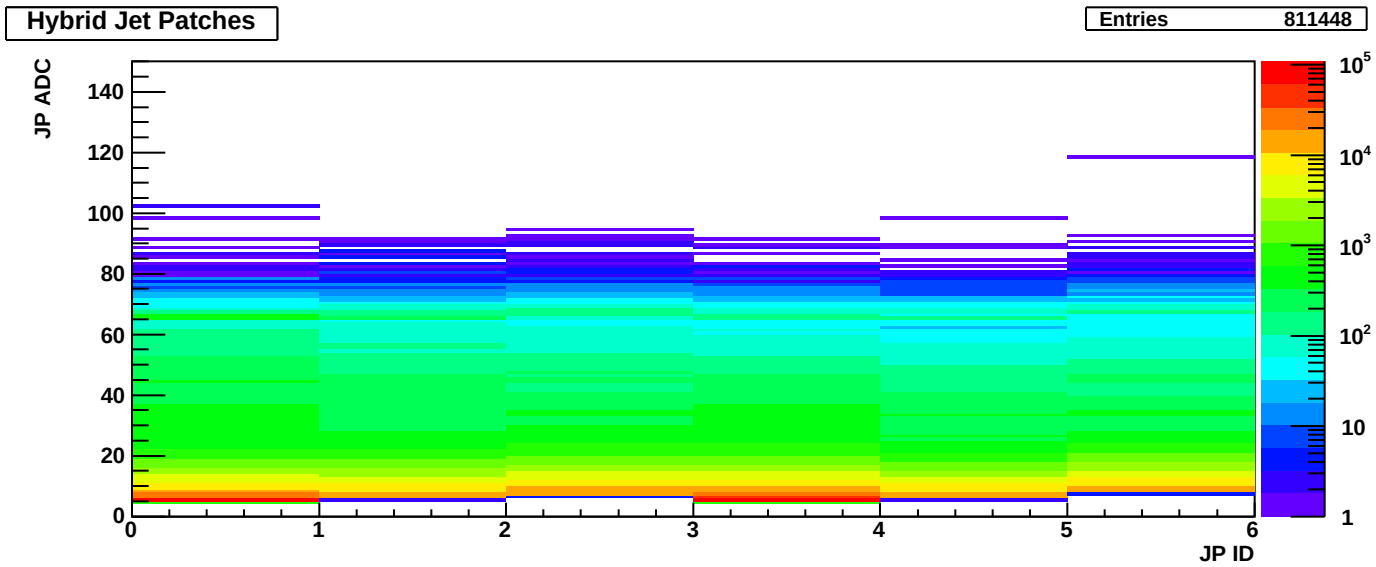

Barrel EMC L0 Input - Patch Sum
Entries 1.217172e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

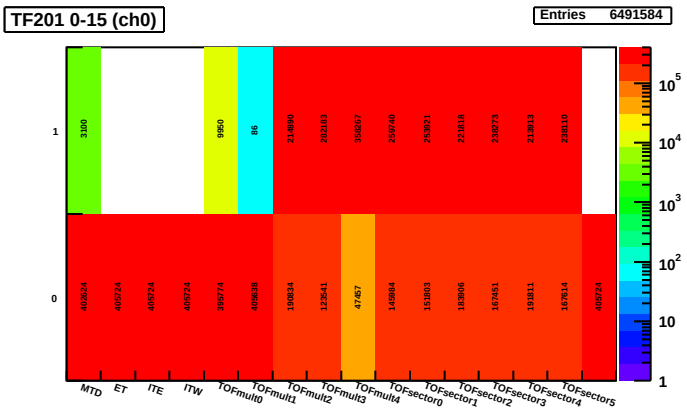

Endcap EMC L0 Input - High Tower
Entries 3.651516e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

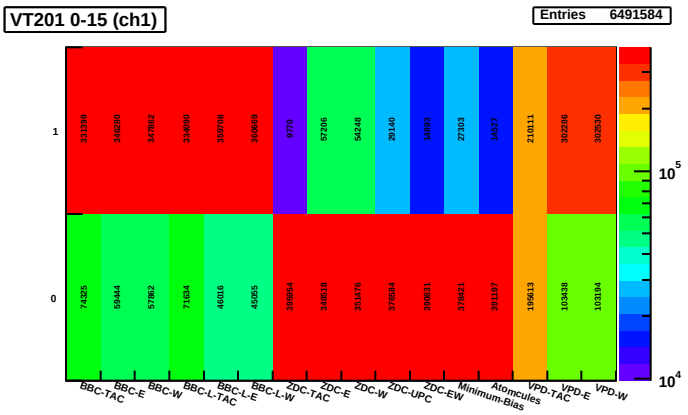

Endcap EMC L0 Input - Patch Sum
Entries 3.651516e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

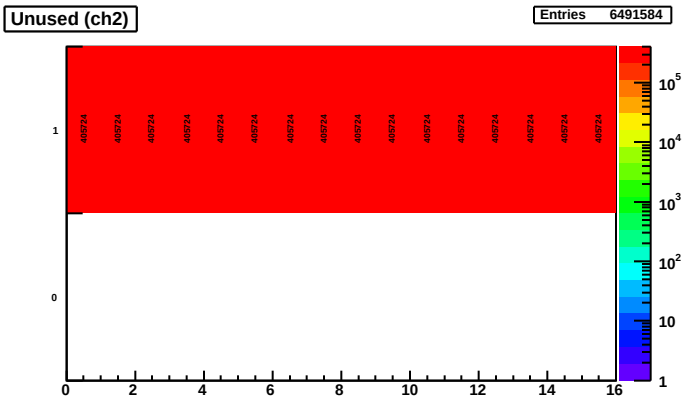

Barrel Jet Patches
Entries 7303032
JP ADC
JP ID

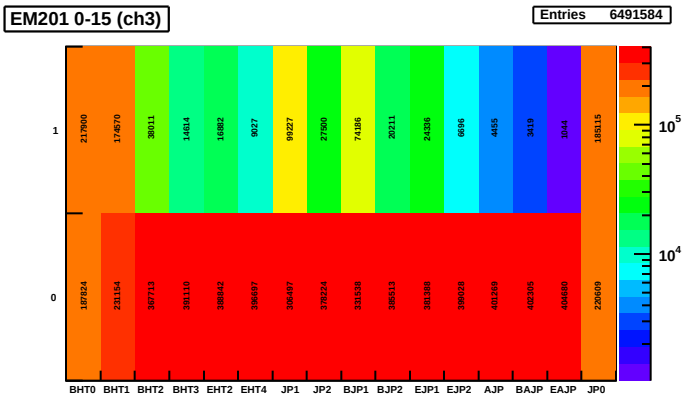

Endcap Jet Patches
Entries 2434344
JP ADC
JP ID

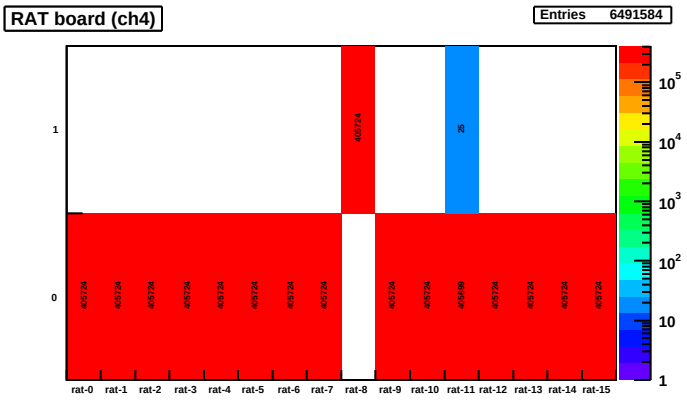

Hybrid Jet Patches
Entries 811448
JP ADC
JP ID

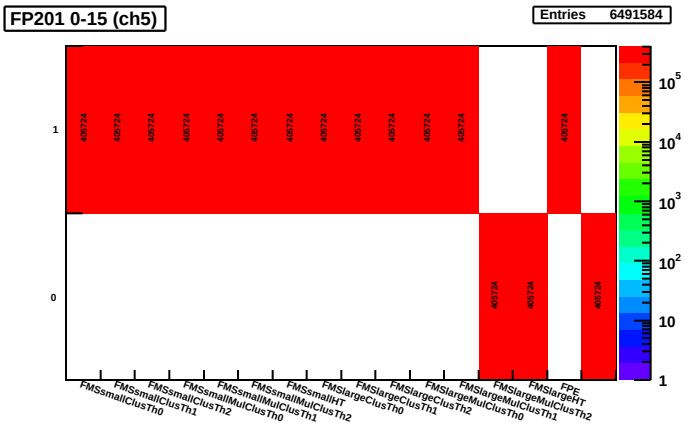

TF201 0-15 (ch0)
Entries 6491584
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

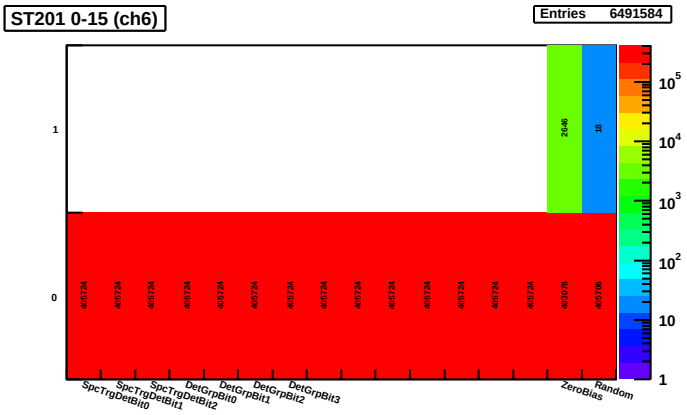

VT201 0-15 (ch1)
Entries 6491584
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

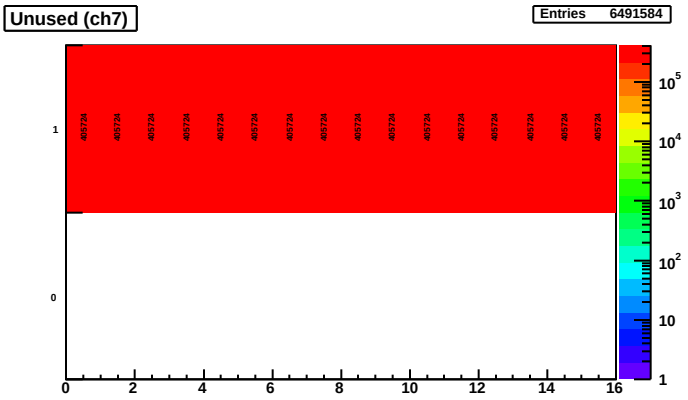

Unused (ch2)
Entries 6491584

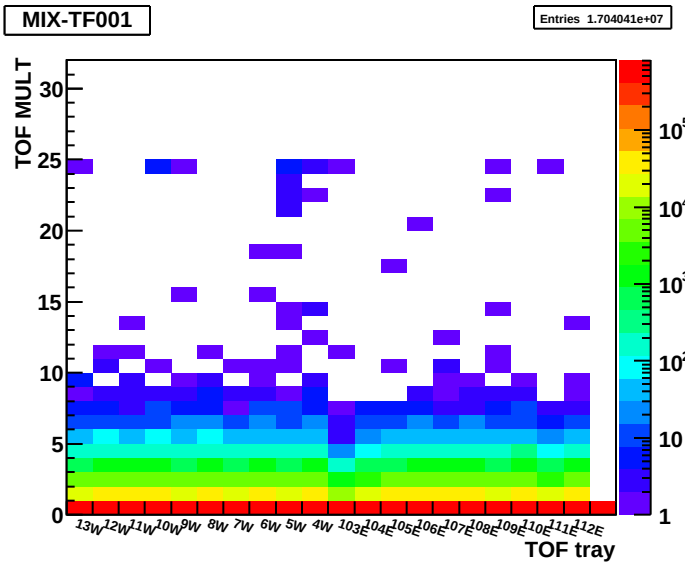

EM201 0-15 (ch3)
Entries 6491584
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

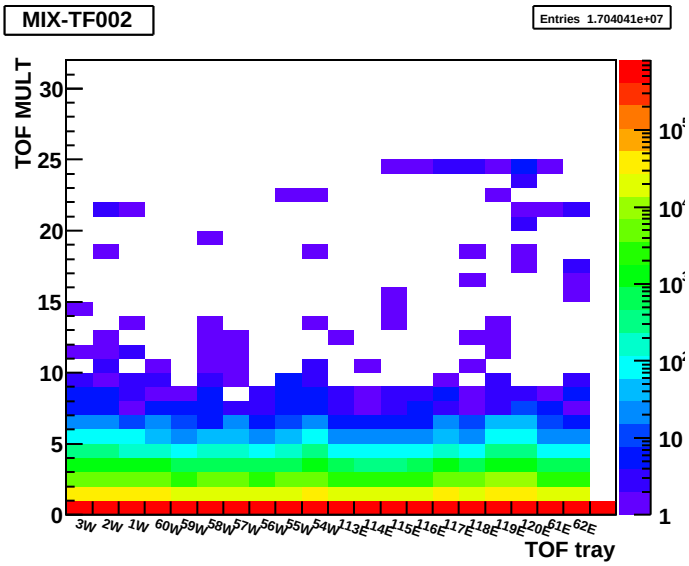

RAT board (ch4)
Entries 6491584
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

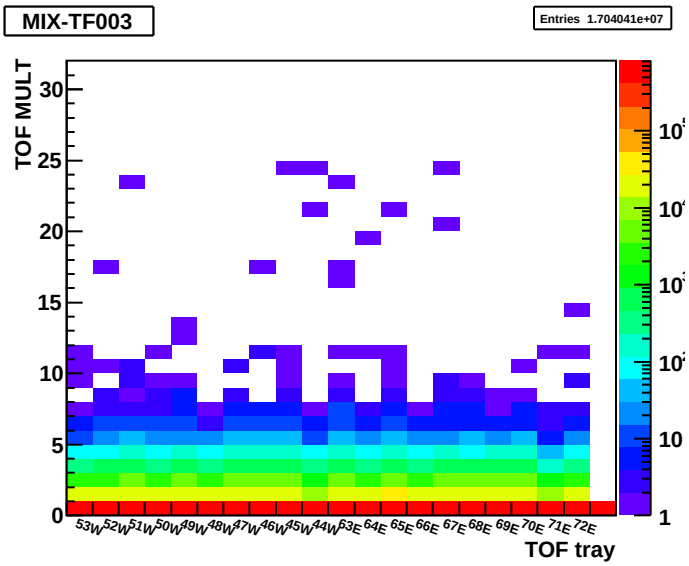

FP201 0-15 (ch5)
Entries 6491584
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

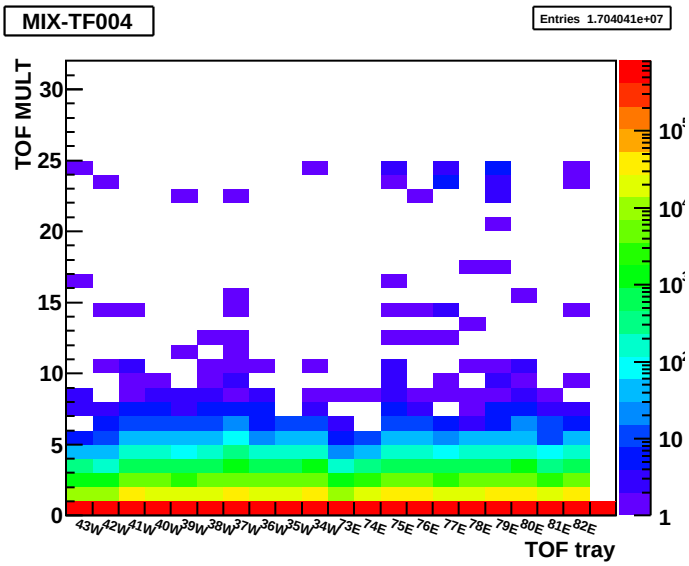

ST201 0-15 (ch6)
Entries 6491584
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

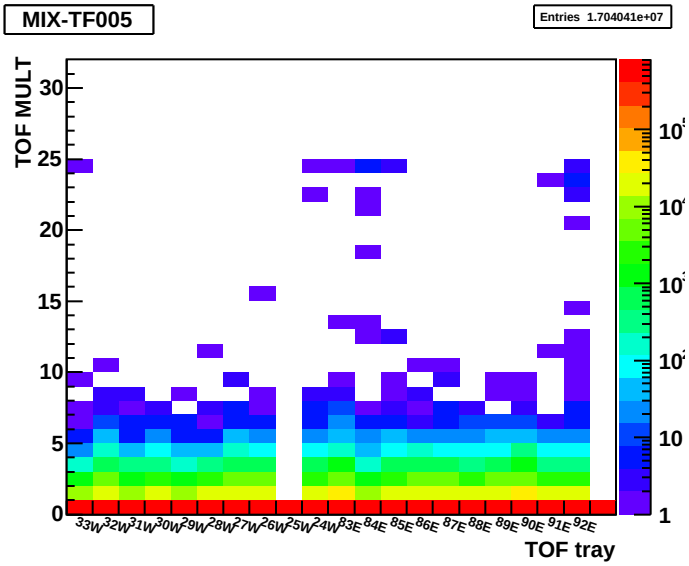

Unused (ch7)
Entries 6491584

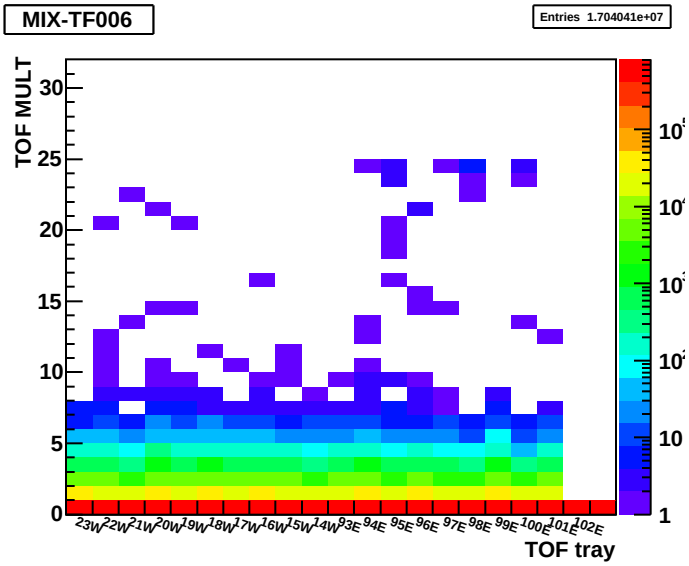

MIX-TF001
Entries 1.704041e+07
TOF MULT
TOF tray

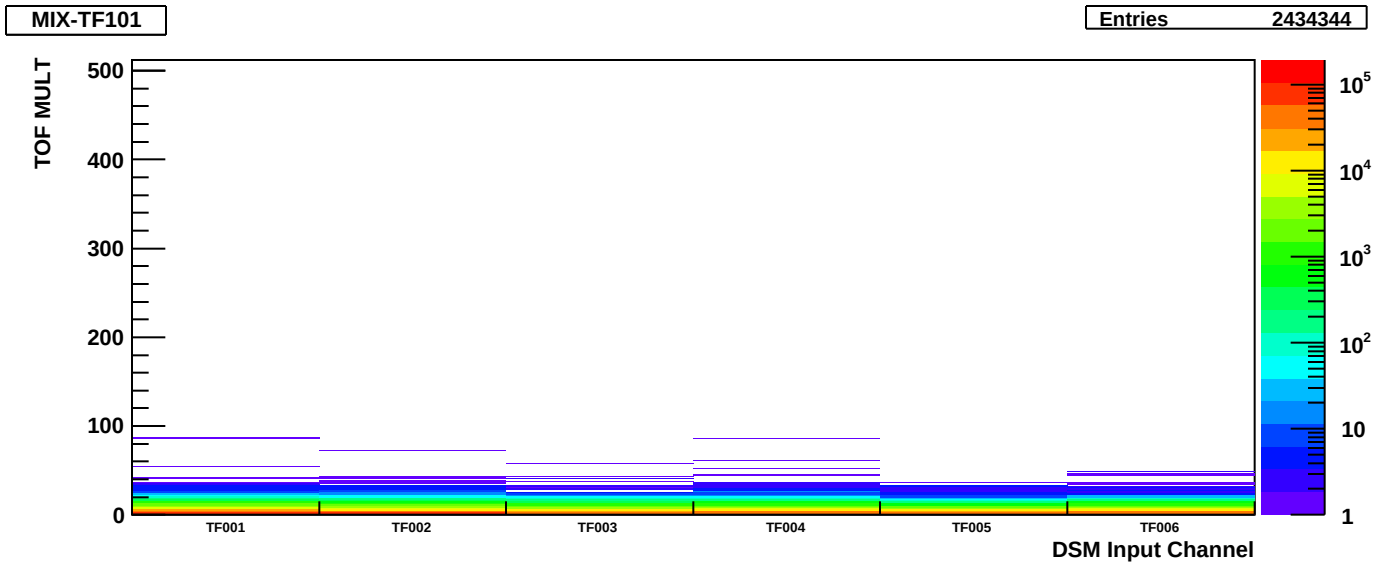

MIX-TF002
Entries 1.704041e+07
TOF MULT
TOF tray

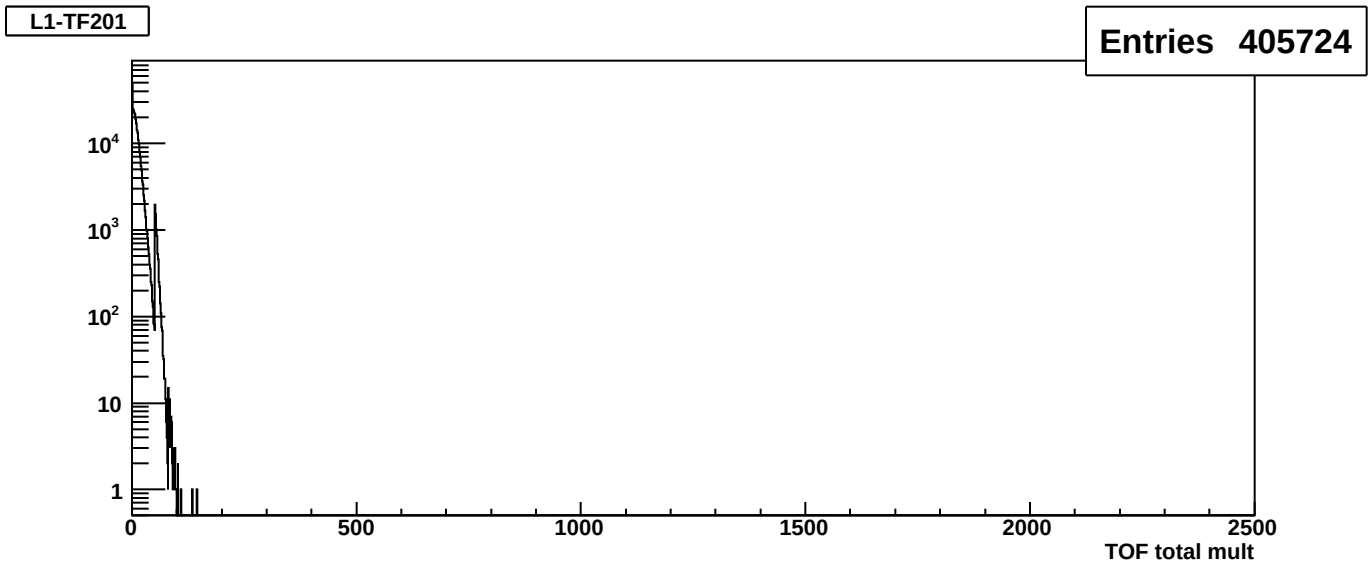

MIX-TF003
Entries 1.704041e+07
TOF MULT
TOF tray

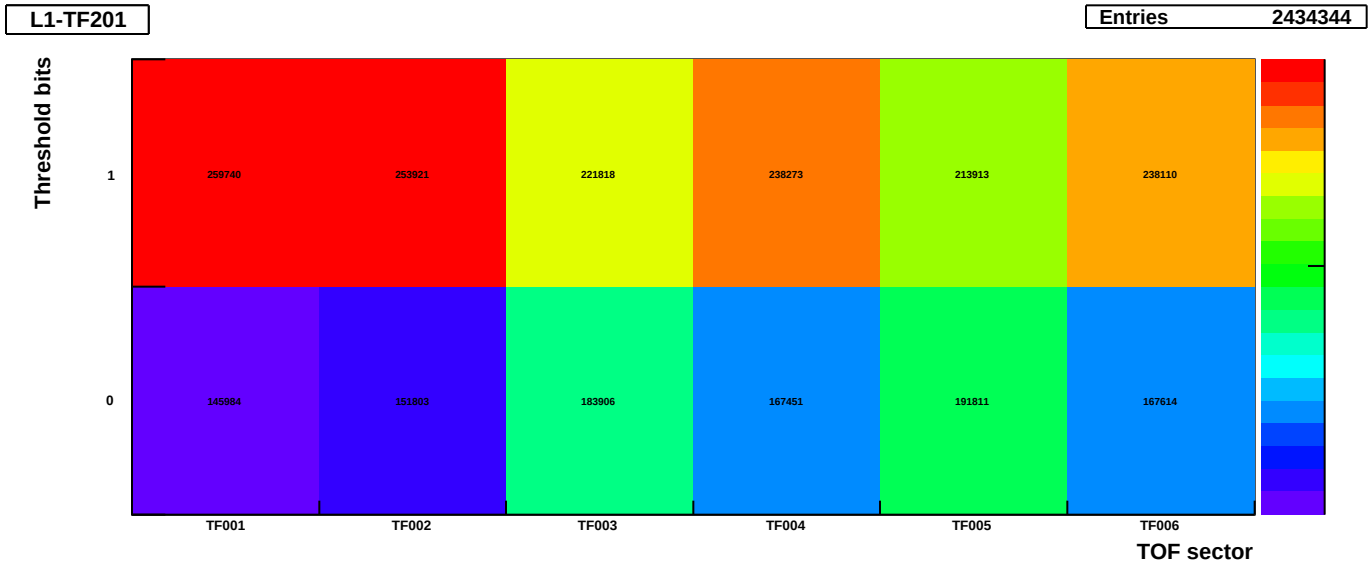

MIX-TF004
Entries 1.704041e+07
TOF MULT
TOF tray

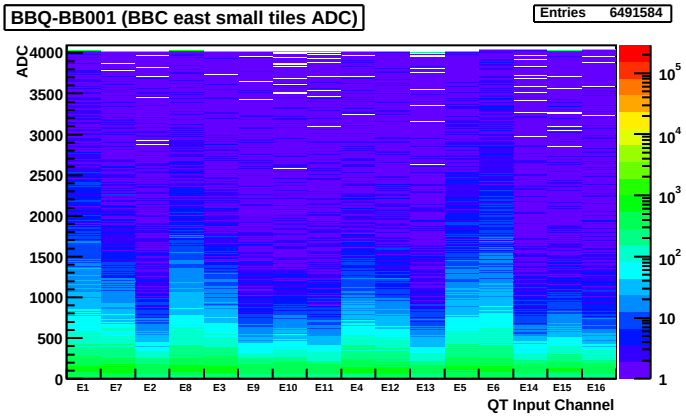

MIX-TF005
Entries 1.704041e+07
TOF MULT
TOF tray

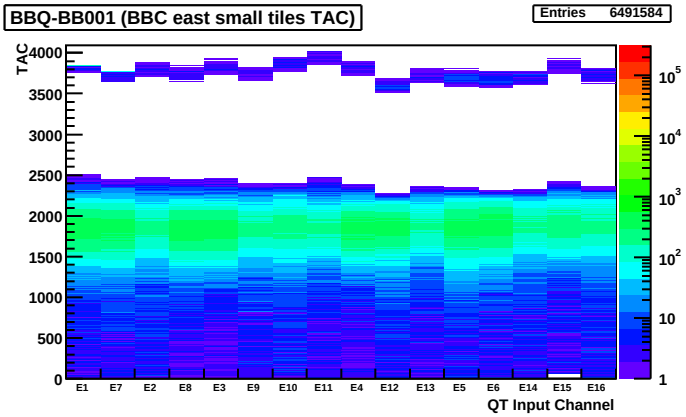

MIX-TF006
Entries 1.704041e+07
TOF MULT
TOF tray

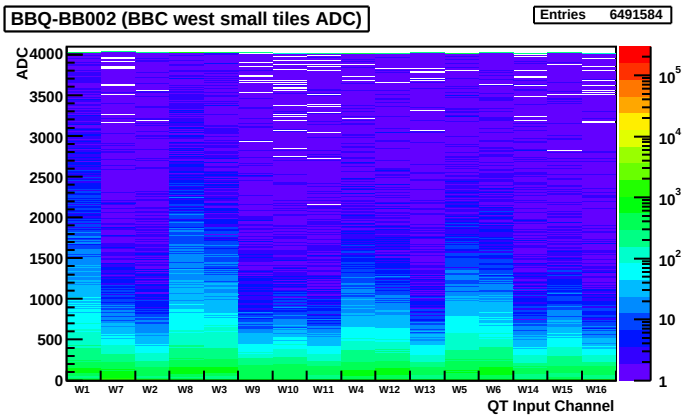

MIX-TF101
Entries 2434344
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

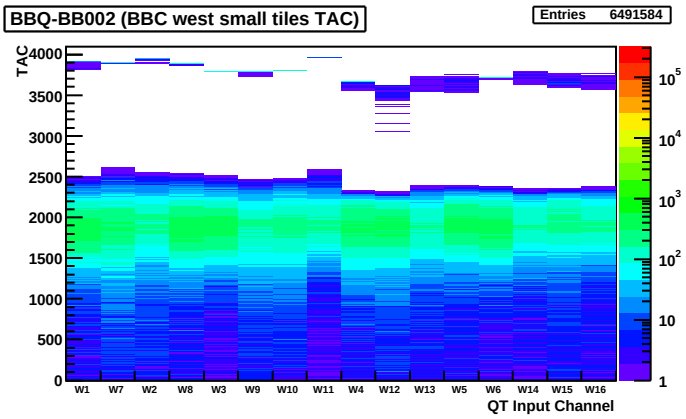

L1-TF201
Entries 405724
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

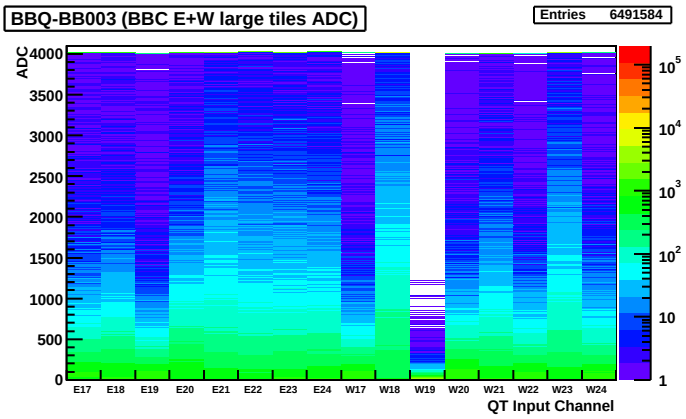

L1-TF201
Entries 2434344
Threshold bits
1
0
259740
253921
221818
238273
213913
238110
145984
151803
183906
167451
191811
167614
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

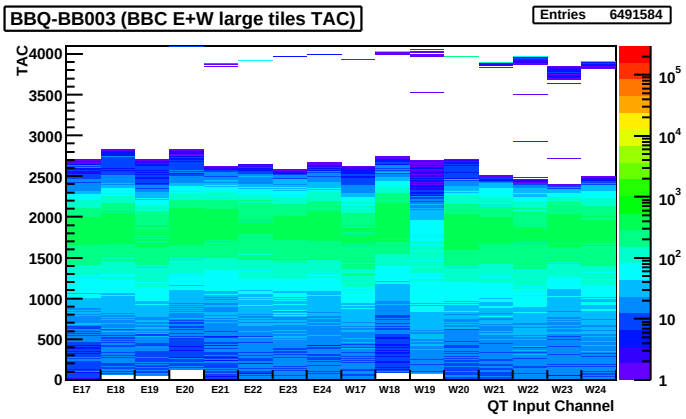

BBQ-BB001 (BBC east small tiles ADC)
Entries 6491584
ADC
QT Input Channel

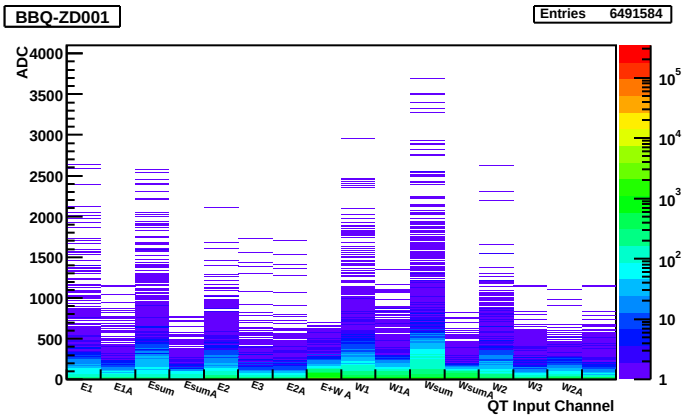

BBQ-BB001 (BBC east small tiles TAC)
Entries 6491584
TAC
QT Input Channel

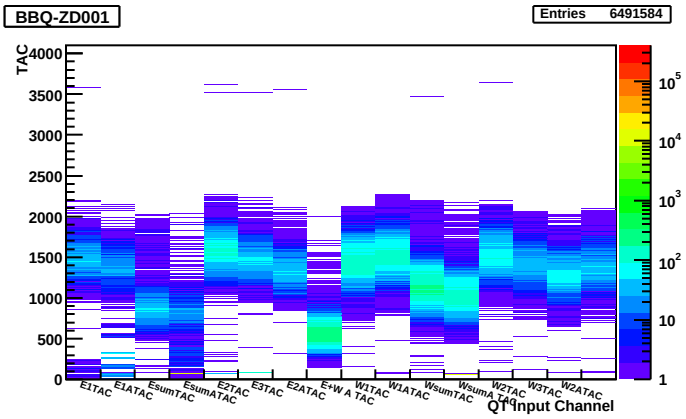

BBQ-BB002 (BBC west small tiles ADC)
Entries 6491584
ADC
QT Input Channel

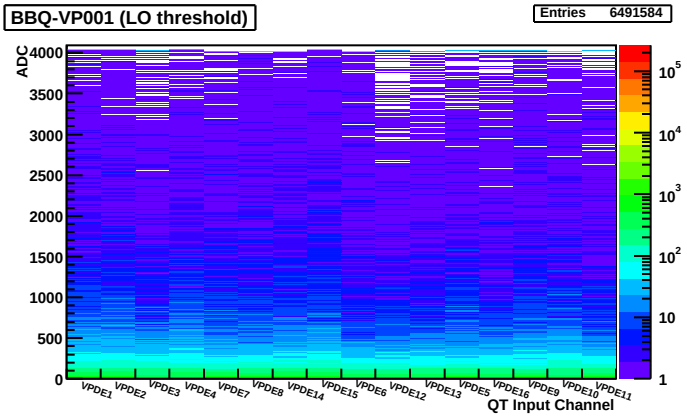

BBQ-BB002 (BBC west small tiles TAC)
Entries 6491584
TAC
QT Input Channel

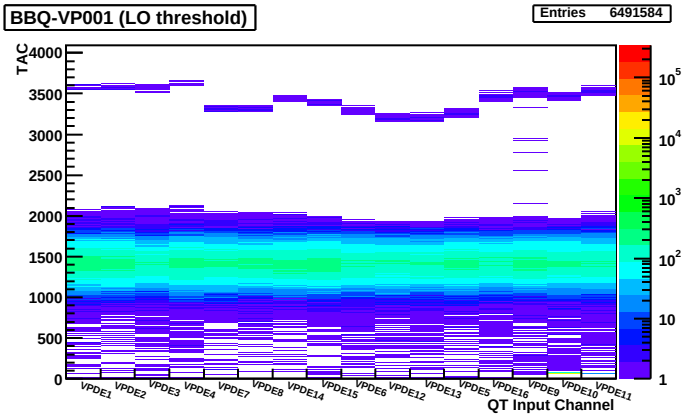

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6491584
ADC
QT Input Channel

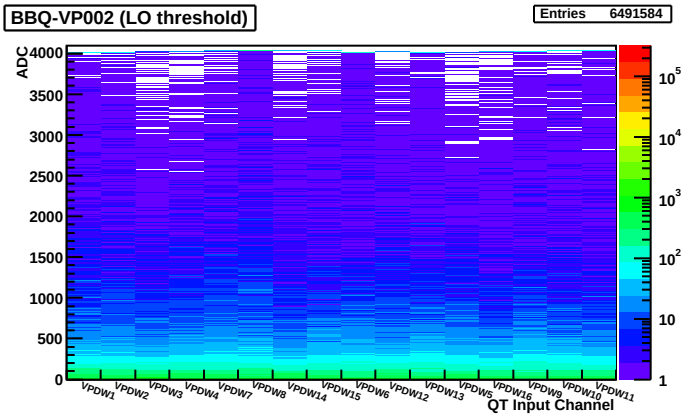

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6491584
TAC
QT Input Channel

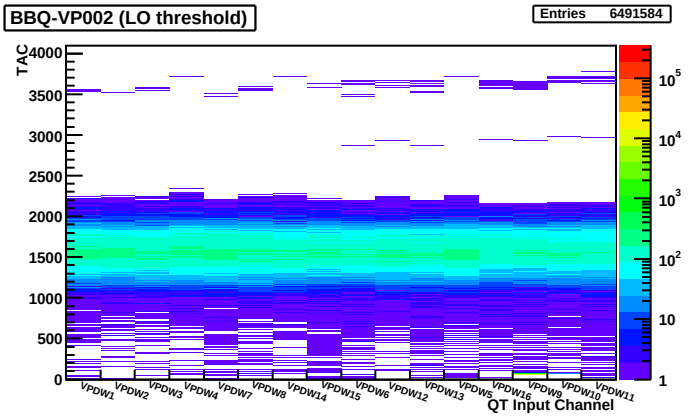

BBQ-ZD001
Entries 6491584
ADC
QT Input Channel

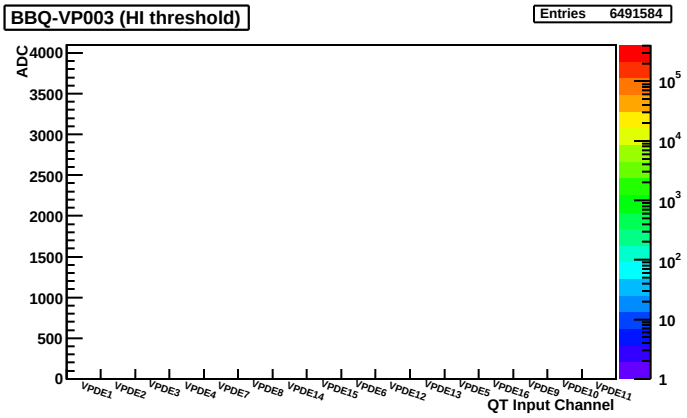

BBQ-ZD001
Entries 6491584
TAC
QT Input Channel

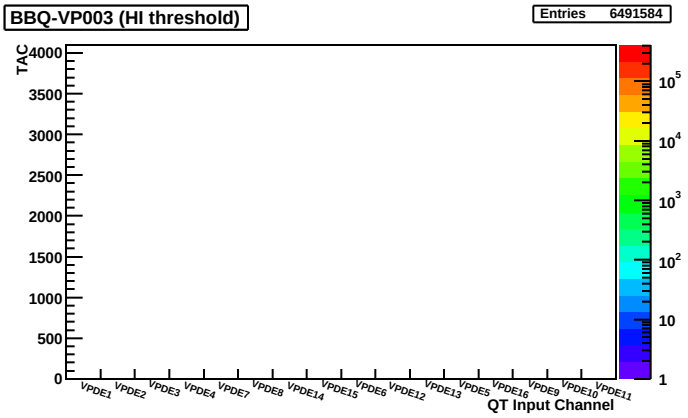

BBQ-VP001 (LO threshold)
Entries 6491584
ADC
QT Input Channel

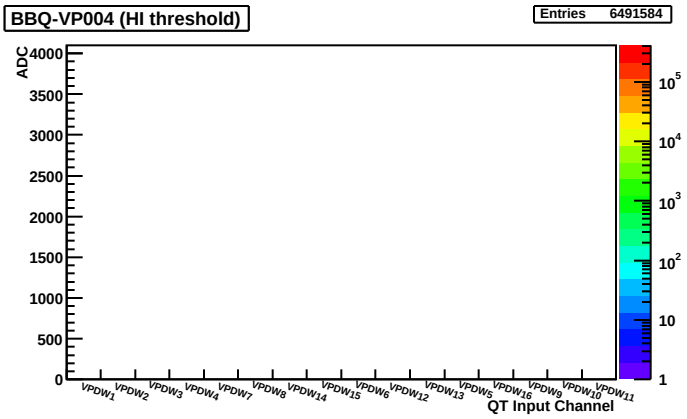

BBQ-VP001 (LO threshold)
Entries 6491584
TAC
QT Input Channel

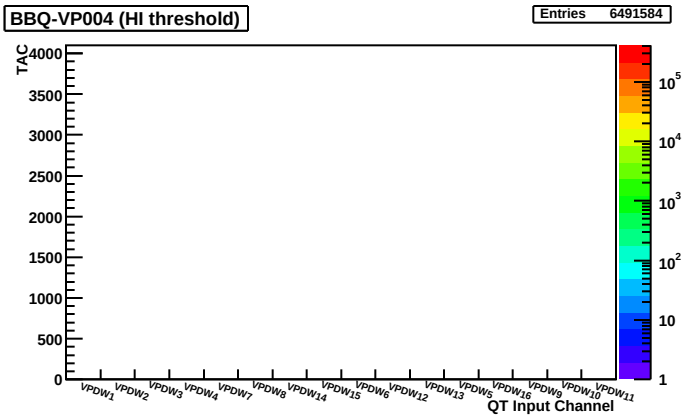

BBQ-VP002 (LO threshold)
Entries 6491584
ADC
QT Input Channel

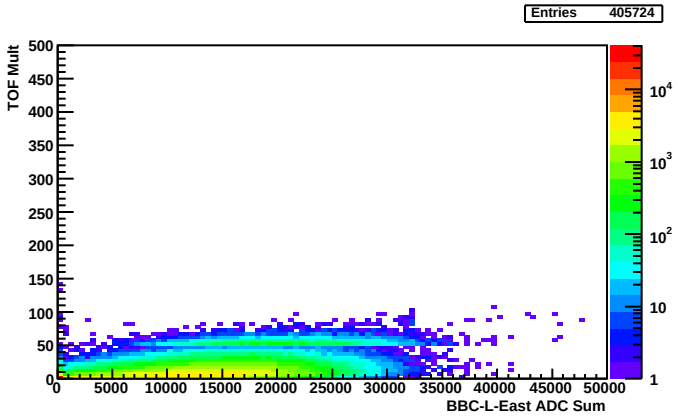

BBQ-VP002 (LO threshold)
Entries 6491584
TAC
QT Input Channel

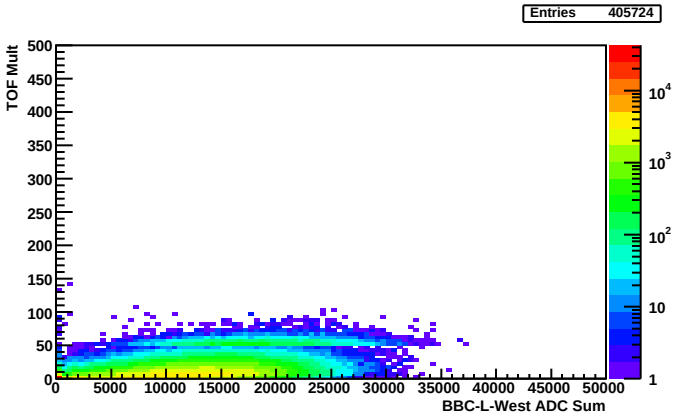

BBQ-VP003 (HI threshold)
Entries 6491584
ADC
QT Input Channel

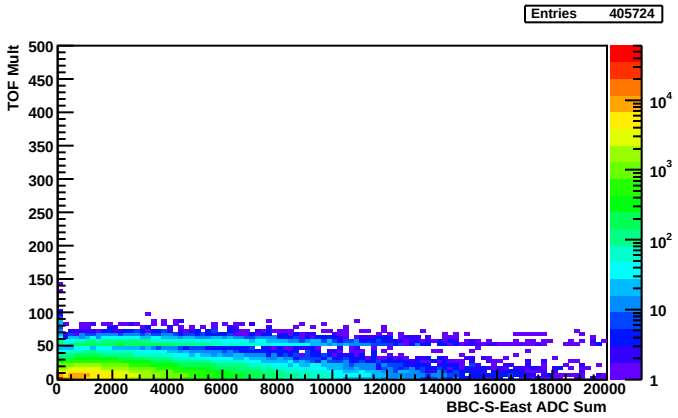

BBQ-VP003 (HI threshold)
Entries 6491584
TAC
QT Input Channel

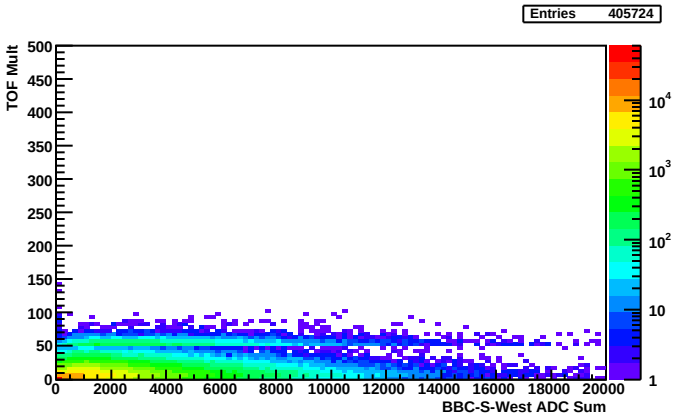

BBQ-VP004 (HI threshold)
Entries 6491584
ADC
QT Input Channel

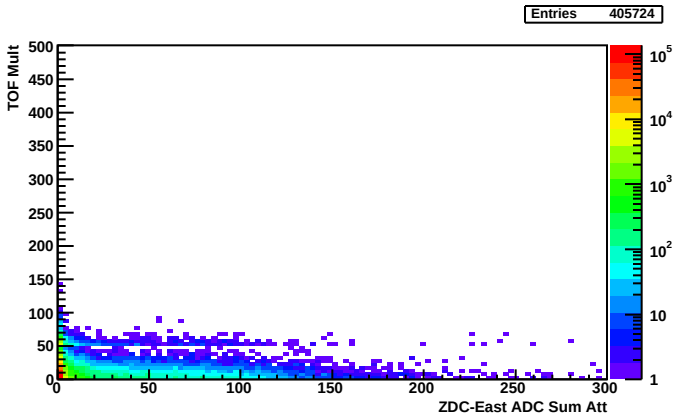

BBQ-VP004 (HI threshold)
Entries 6491584
TAC
QT Input Channel

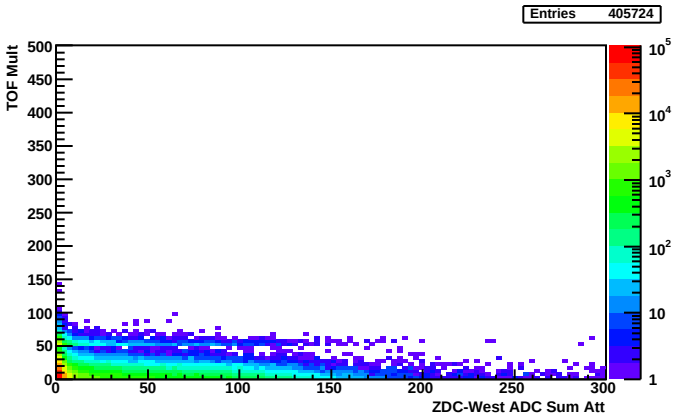

Entries 405724
TOF Mult
BBC-L-East ADC Sum

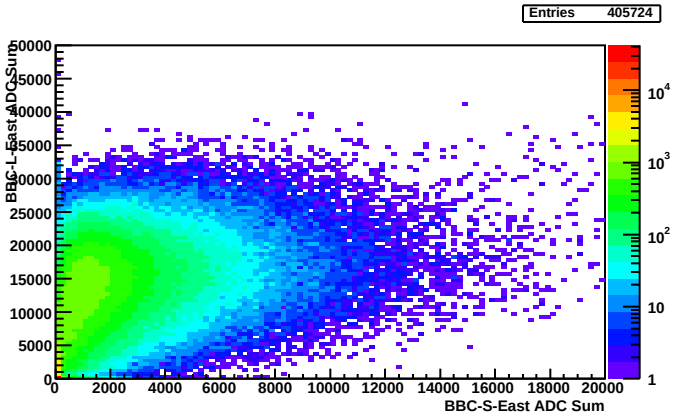

Entries 405724
TOF Mult
BBC-L-West ADC Sum

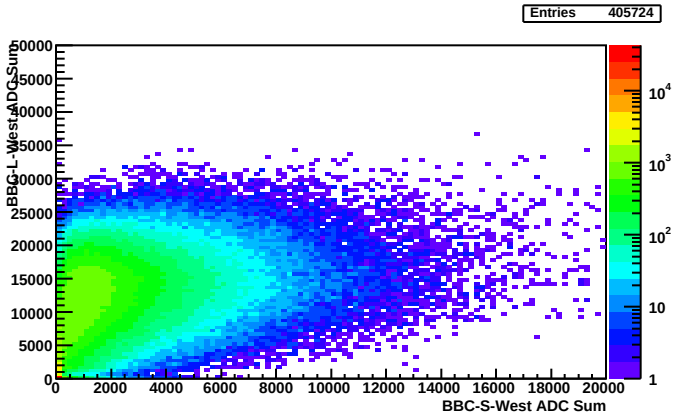

Entries 405724
TOF Mult
BBC-S-East ADC Sum

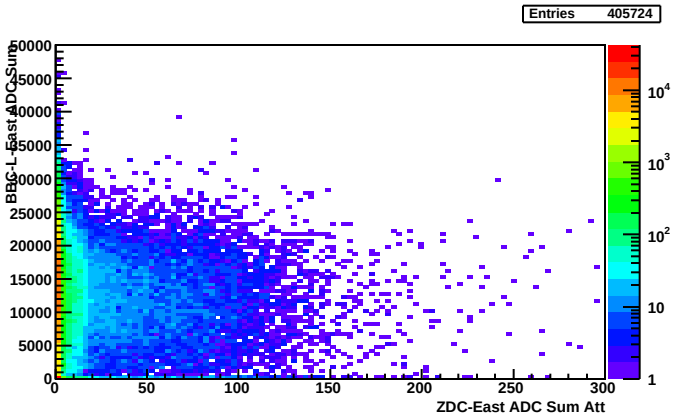

Entries 405724
TOF Mult
BBC-S-West ADC Sum

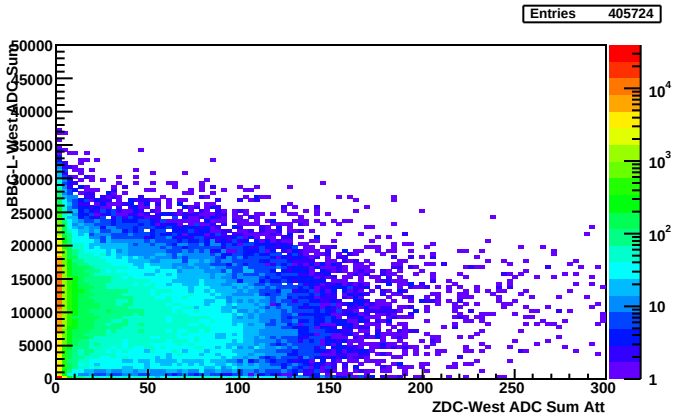

Entries 405724
TOF Mult
ZDC-East ADC Sum Att

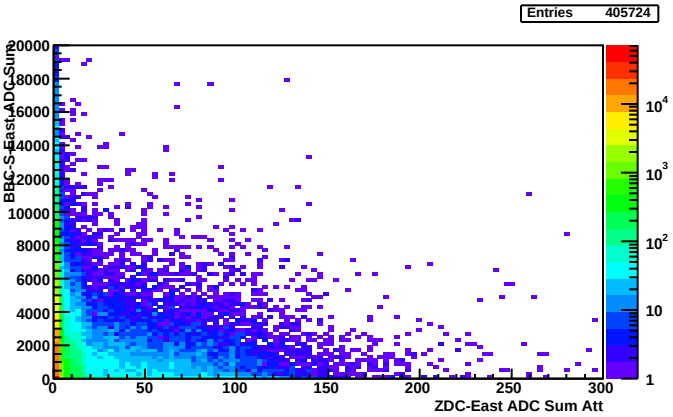

Entries 405724
TOF Mult
ZDC-West ADC Sum Att

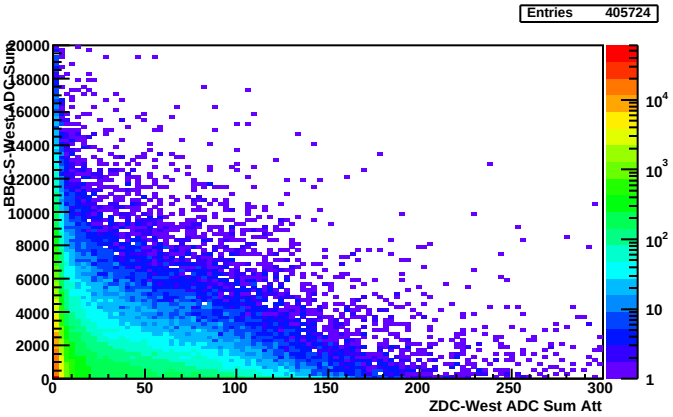

Entries 405724
BBC-L-East ADC Sum
BBC-S-East ADC Sum

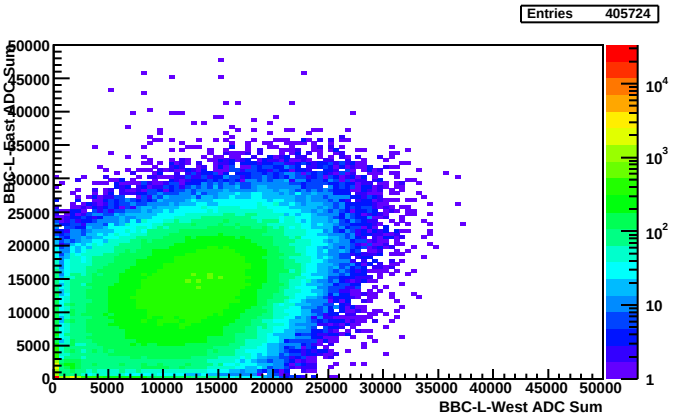

Entries 405724
BBC-L-West ADC Sum
BBC-S-West ADC Sum

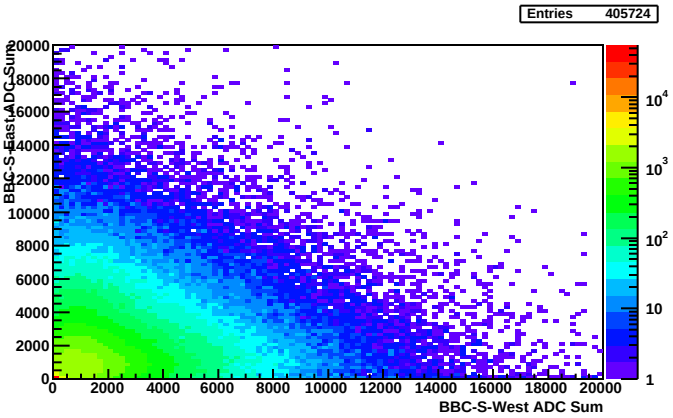

Entries 405724
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

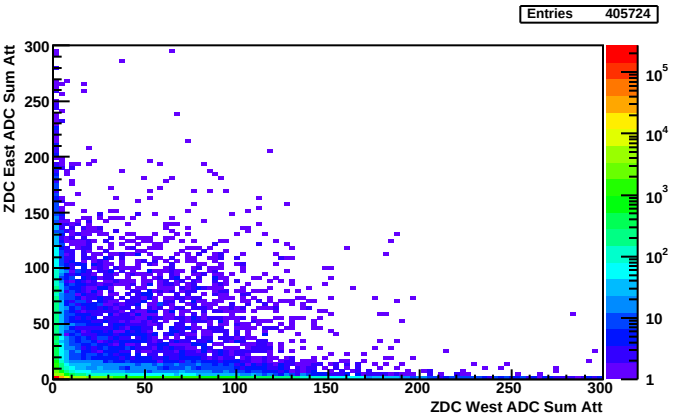

Entries 405724
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

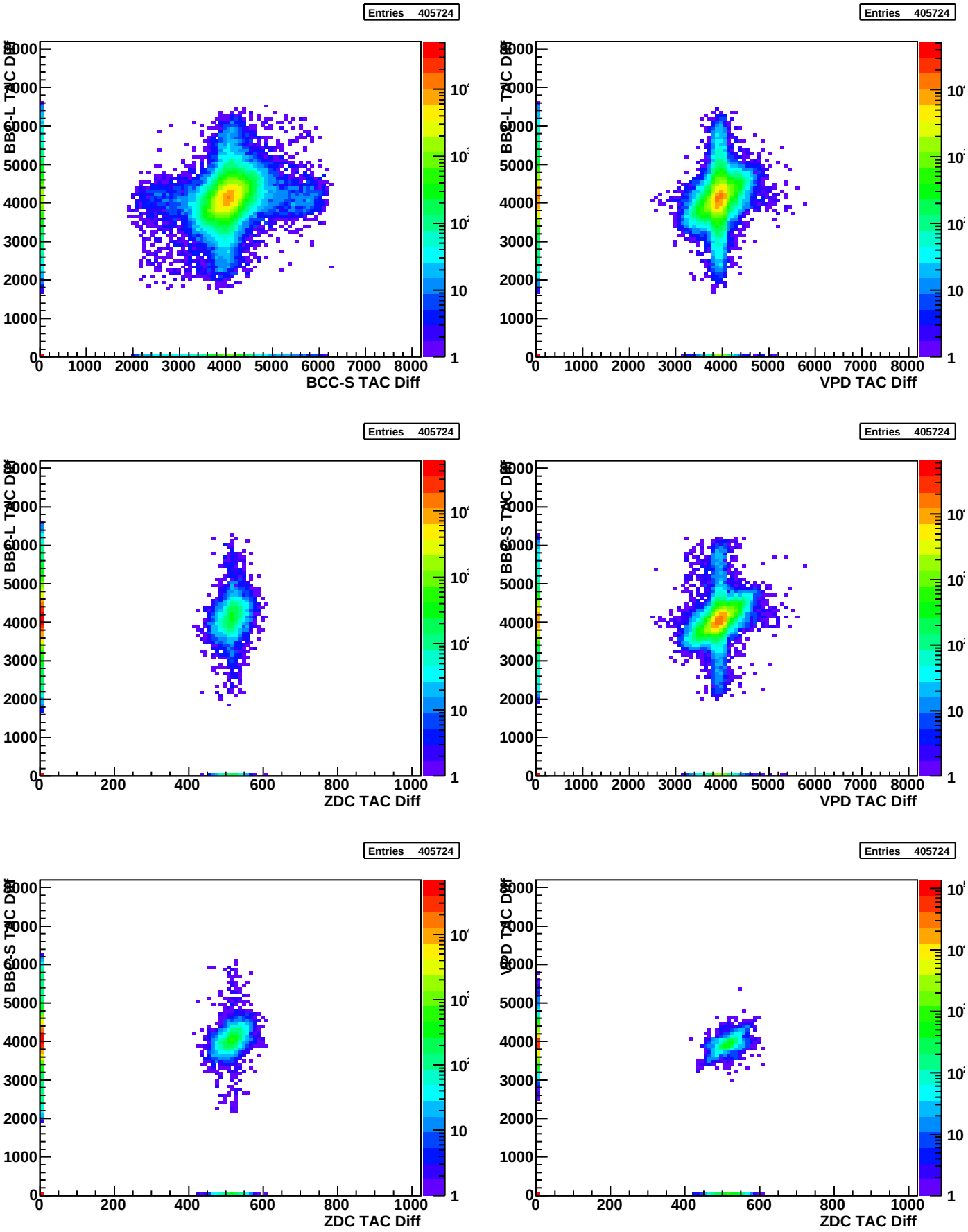

Entries 405724
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

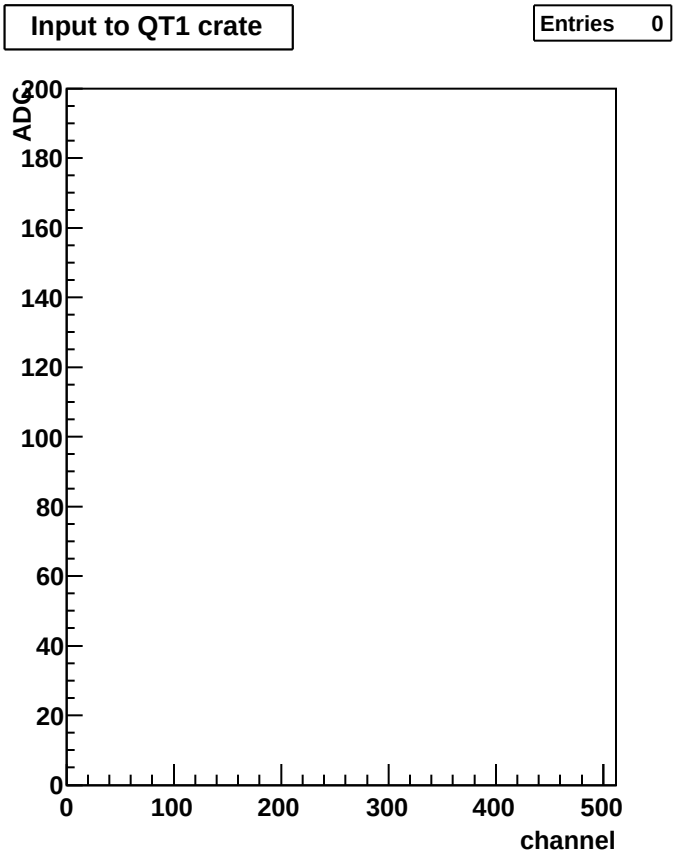

Entries 405724
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

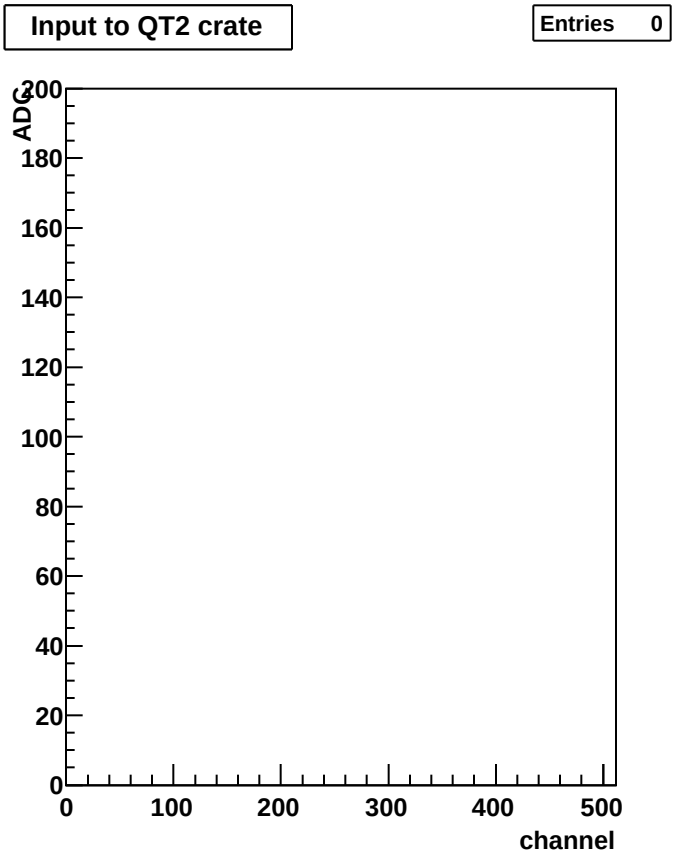

Entries 405724
BBC-L-East ADC Sum
BBC-L-West ADC Sum

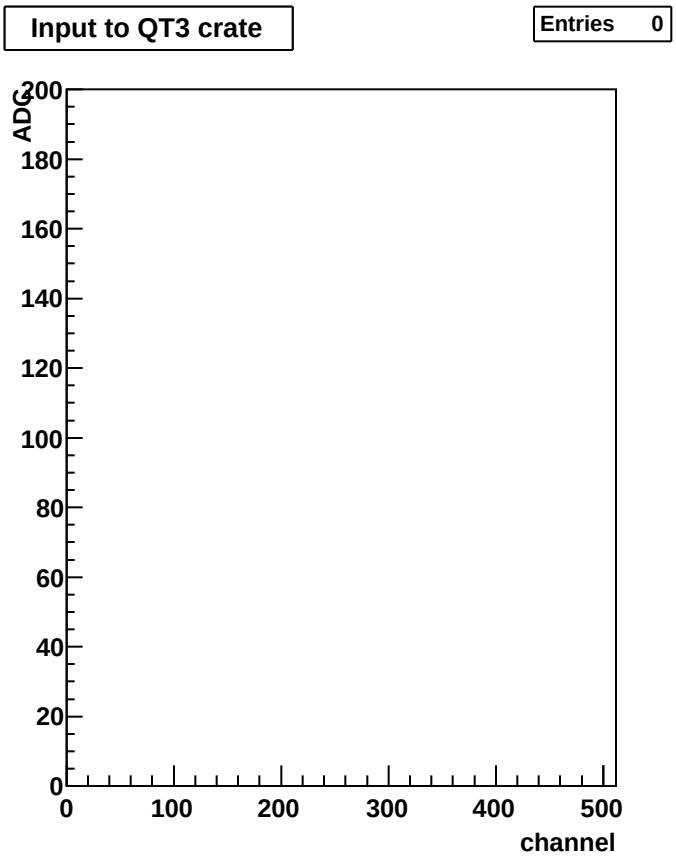

Entries 405724
BBC-S-East ADC Sum
BBC-S-West ADC Sum

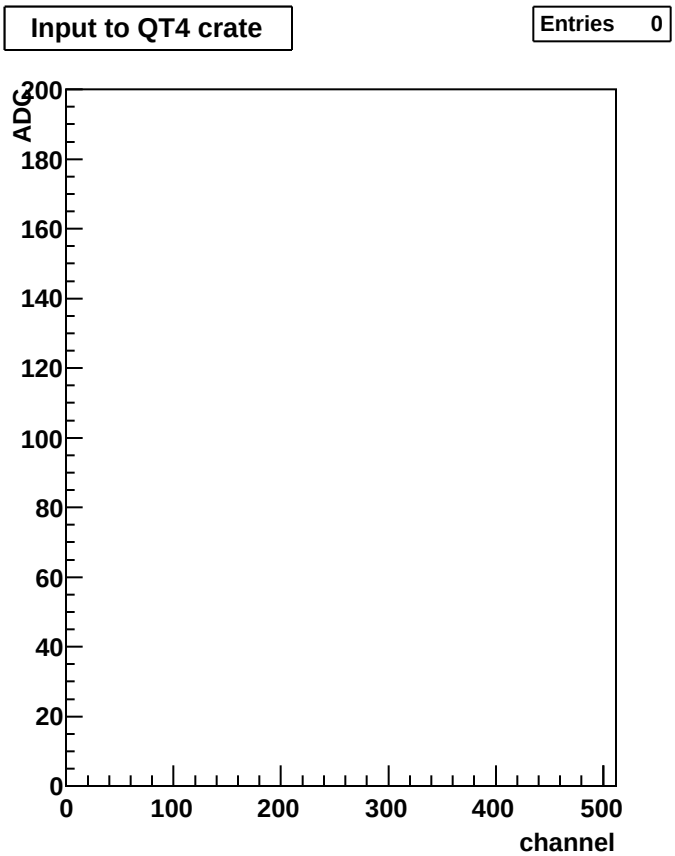

Entries 405724
ZDC East ADC Sum Att
ZDC West ADC Sum Att

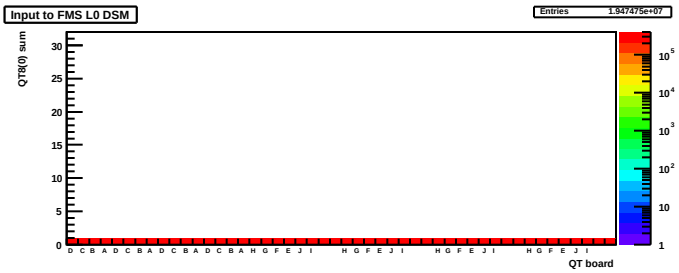

Entries 405724
BBC-L TAC Diff
BCC-S TAC Diff
Entries 405724
BBC-L TAC Diff
VPD TAC Diff
Entries 405724
BBC-L TAC Diff
ZDC TAC Diff
Entries 405724
BBC-S TAC Diff
VPD TAC Diff
Entries 405724
BBC-S TAC Diff
ZDC TAC Diff
Entries 405724
VPD TAC Diff
ZDC TAC Diff

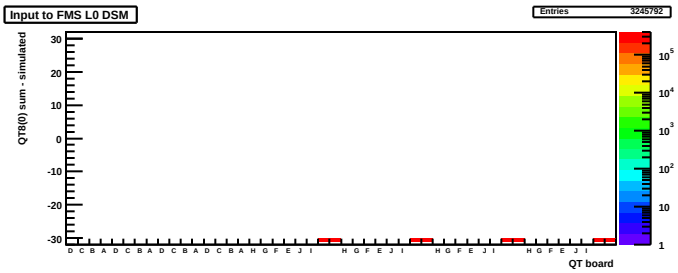

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

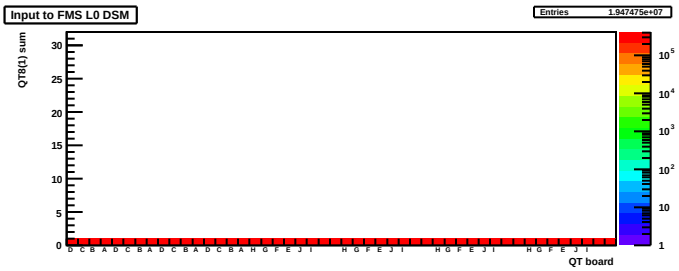

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

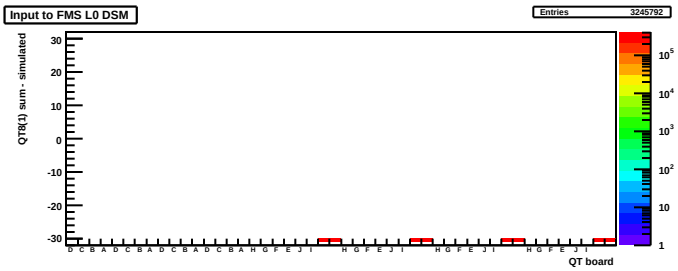

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

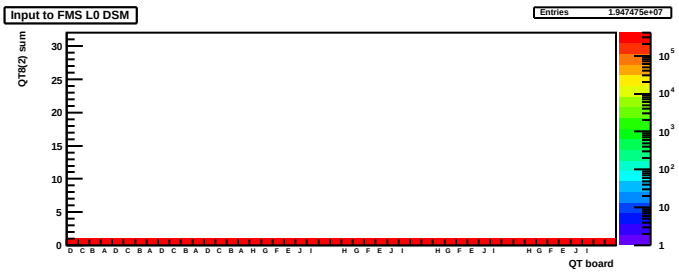

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

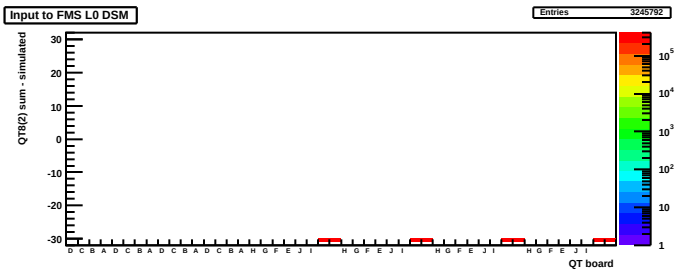

Input to FMS L0 DSM
Entries 1.947475e+07
QT8(0) sum
QT board

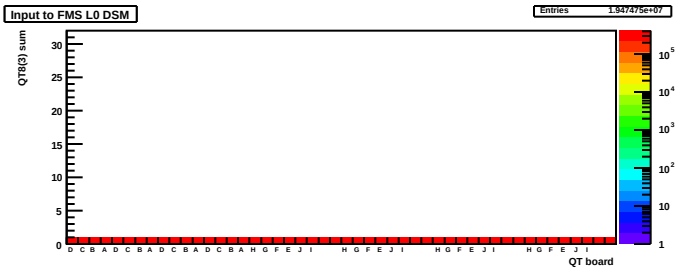

Input to FMS L0 DSM
Entries 3245792
QT8(0) sum - simulated
QT board

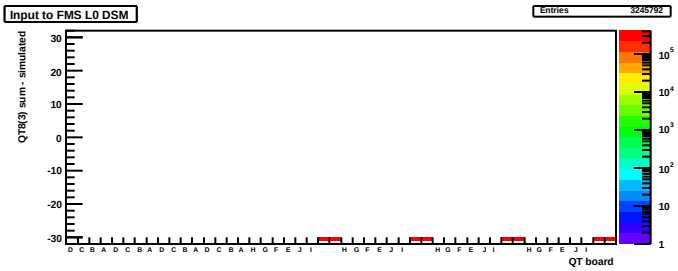

Input to FMS L0 DSM
Entries 1.947475e+07
QT8(1) sum
QT board

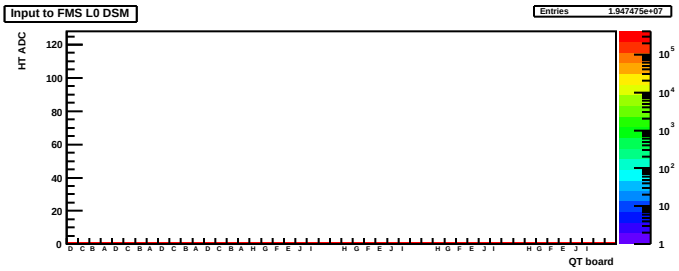

Input to FMS L0 DSM
Entries 3245792
QT8(1) sum - simulated
QT board

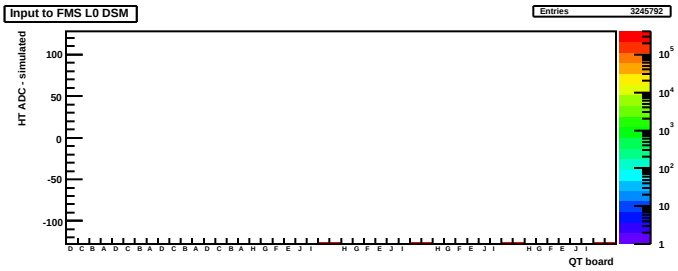

Input to FMS L0 DSM
Entries 1.947475e+07
QT8(2) sum
QT board

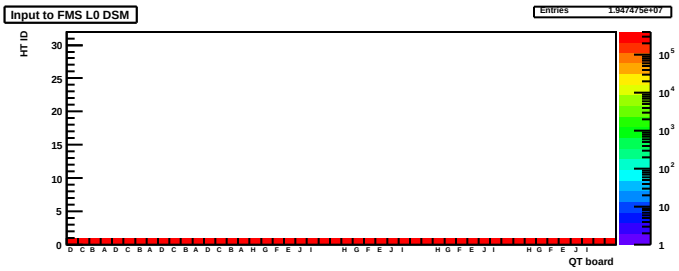

Input to FMS L0 DSM
Entries 3245792
QT8(2) sum - simulated
QT board

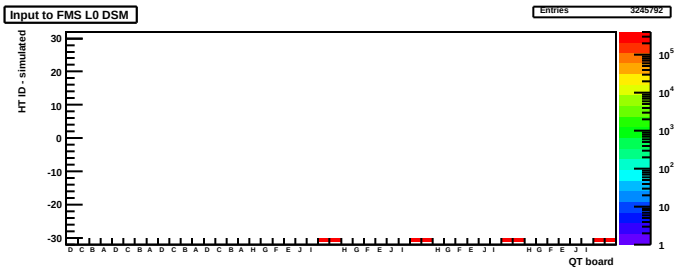

Input to FMS L0 DSM
Entries 1.947475e+07
QT8(3) sum
QT board

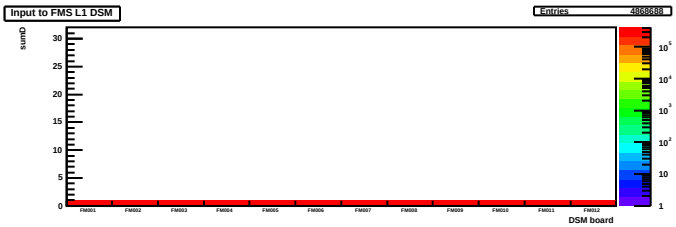

Input to FMS L0 DSM
Entries 3245792
QT8(3) sum - simulated
QT board

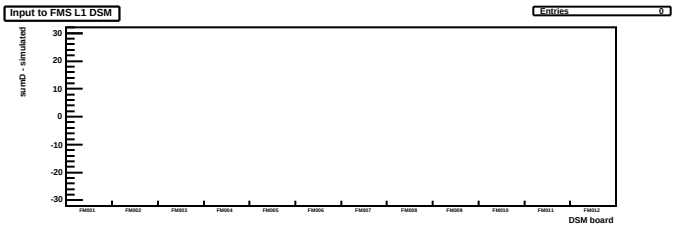

Input to FMS L0 DSM
Entries 1.947475e+07
HT ADC
QT board

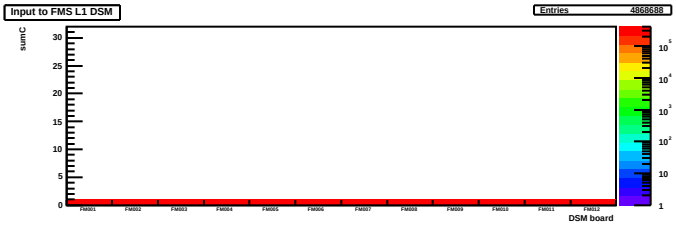

Input to FMS L0 DSM
Entries 3245792
HT ADC - simulated
QT board

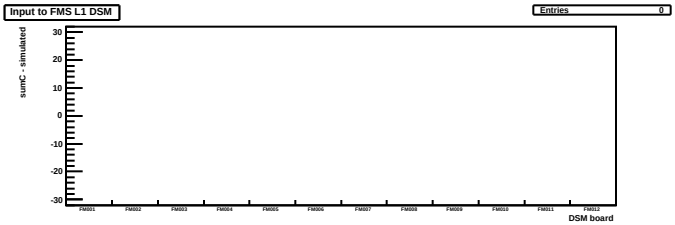

Input to FMS L0 DSM
Entries 1.947475e+07
HT ID
QT board

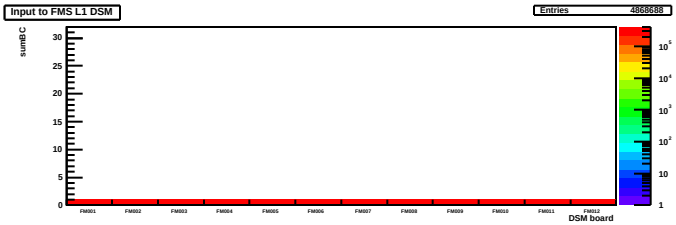

Input to FMS L0 DSM
Entries 3245792
HT ID - simulated
QT board

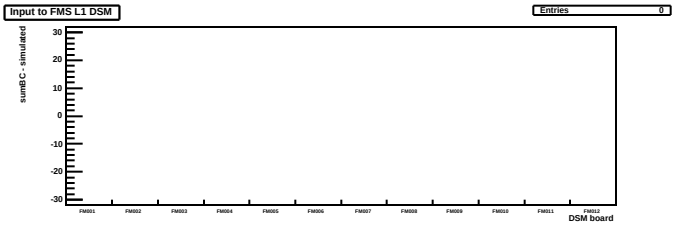

Input to FMS L1 DSM
Entries 4868688
sumD
DSM board

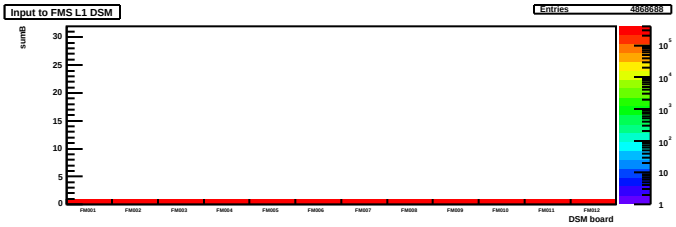

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

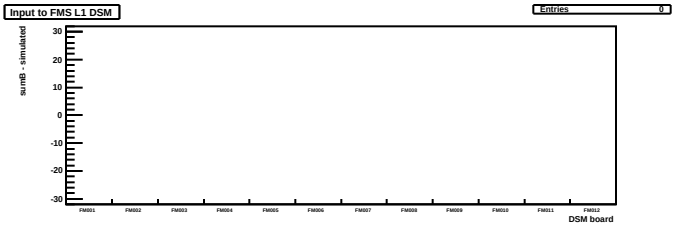

Input to FMS L1 DSM
Entries 4868688
sumC
DSM board

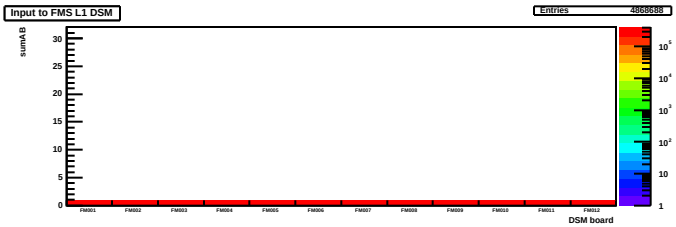

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

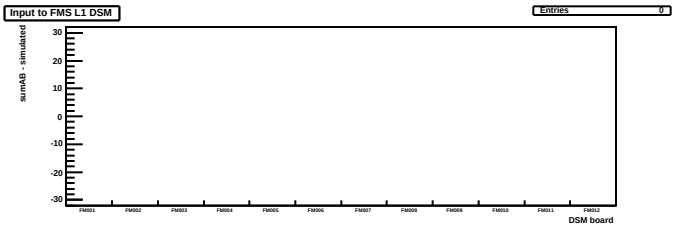

Input to FMS L1 DSM
Entries 4868688
sumBC
DSM board

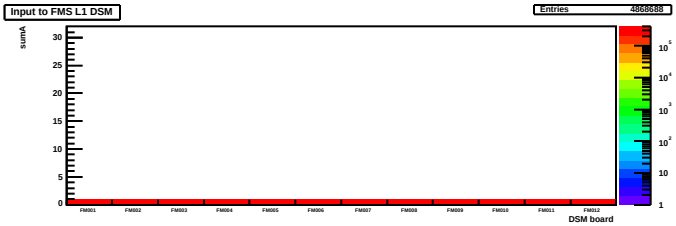

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

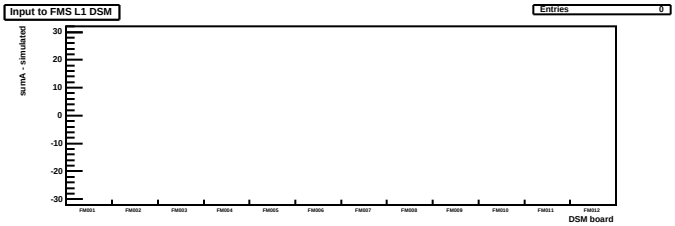

Input to FMS L1 DSM
Entries 4868688
sumB
DSM board

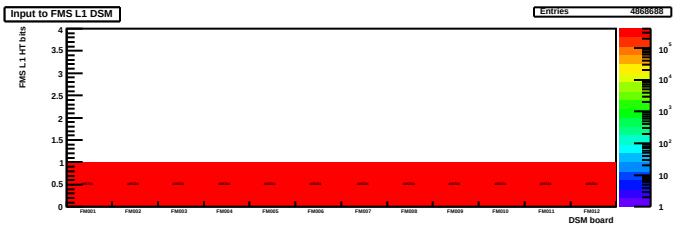

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

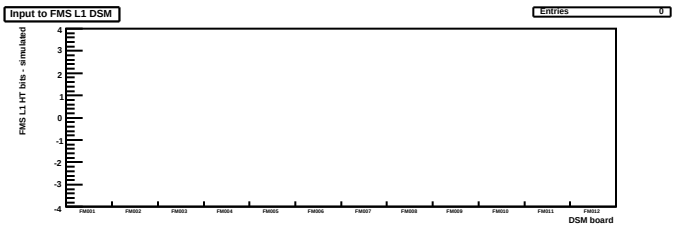

Input to FMS L1 DSM
Entries 4868688
sumAB
DSM board

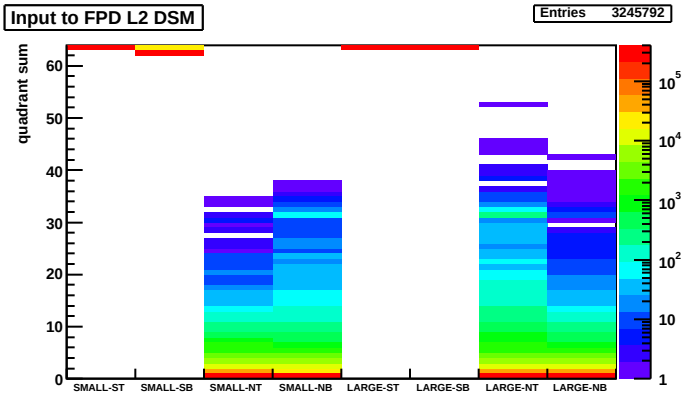

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

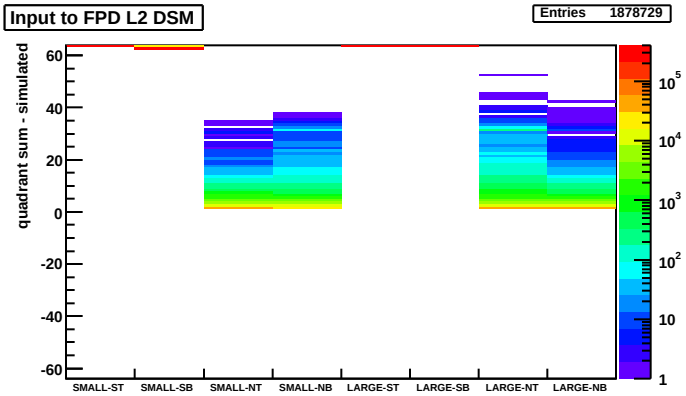

Input to FMS L1 DSM
Entries 4868688
sumA
DSM board

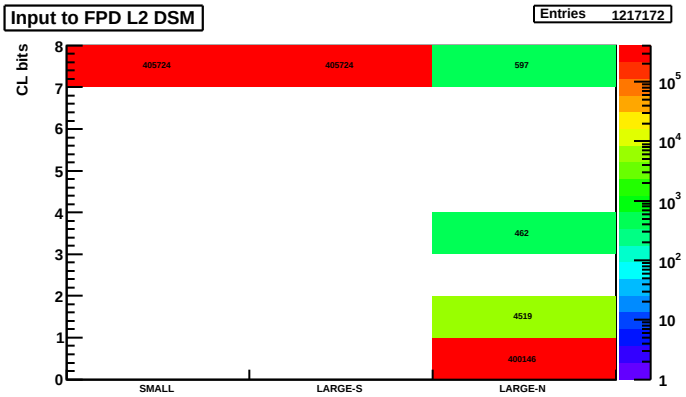

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

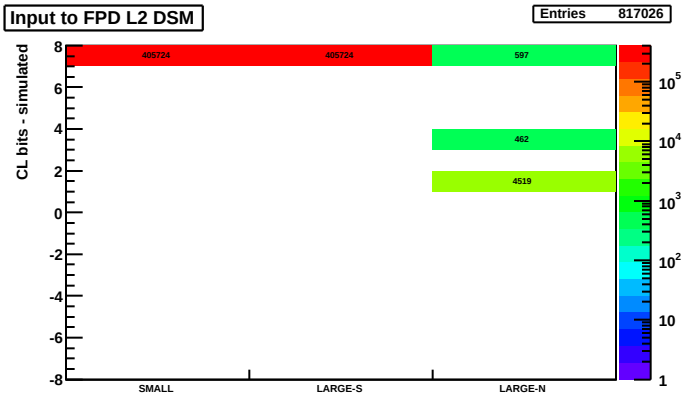

Input to FMS L1 DSM
Entries 4868688
FMS L1 HT bits
DSM board

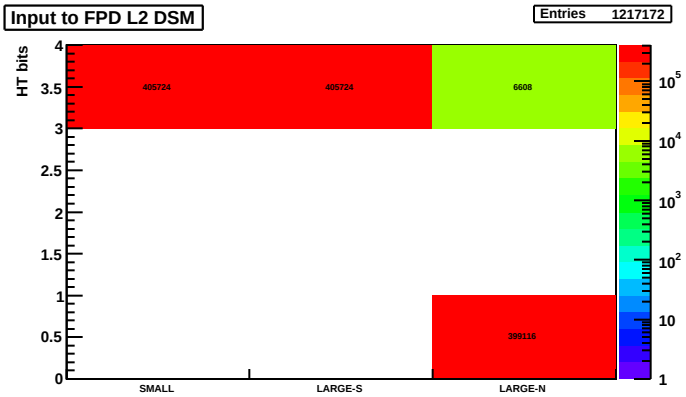

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

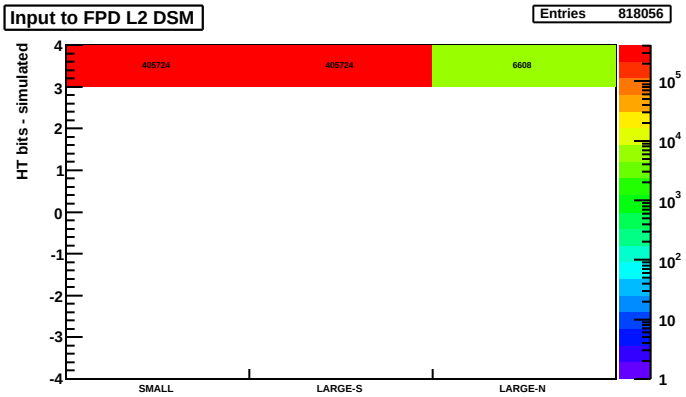

Input to FPD L2 DSM
Entries 3245792
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

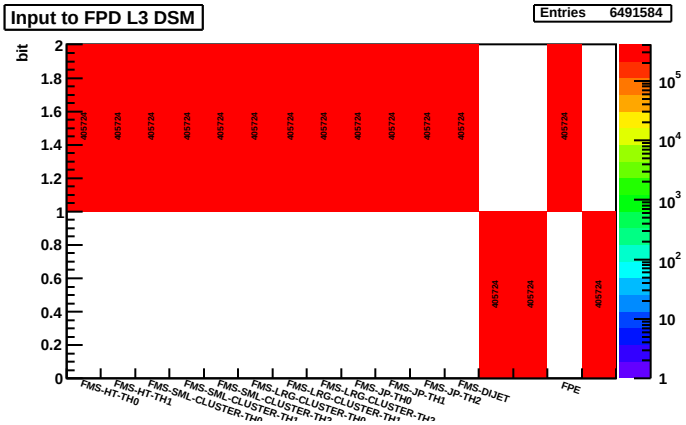

Input to FPD L2 DSM
Entries 1878729
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

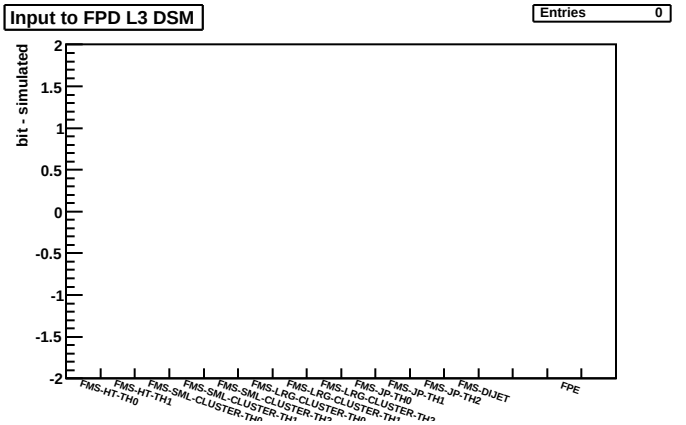

Input to FPD L2 DSM
Entries 1217172
CL bits
405724
405724
597
462
4519
400146
SMALL
LARGE-S
LARGE-N

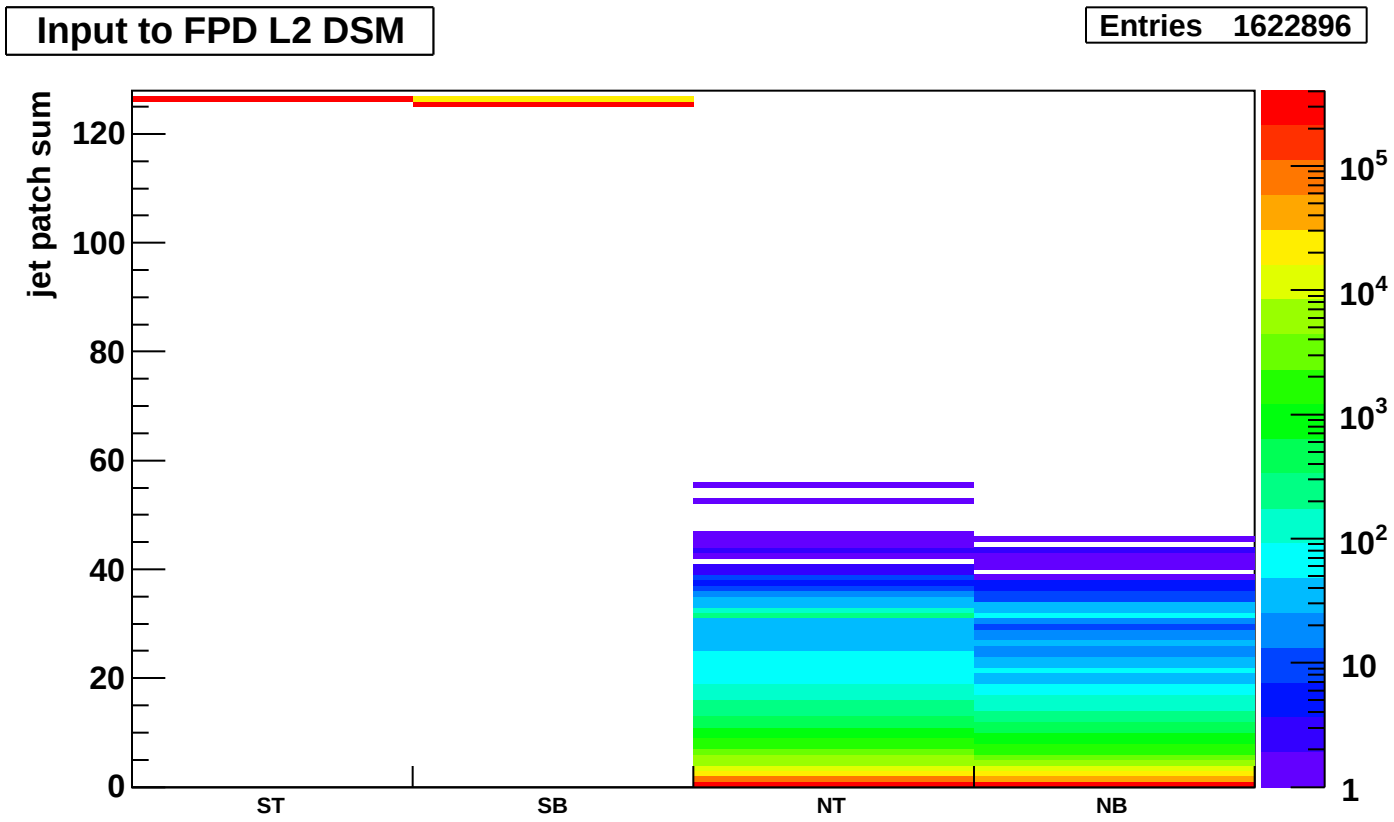

Input to FPD L2 DSM
Entries 817026
CL bits - simulated
405724
405724
597
462
4519
SMALL
LARGE-S
LARGE-N

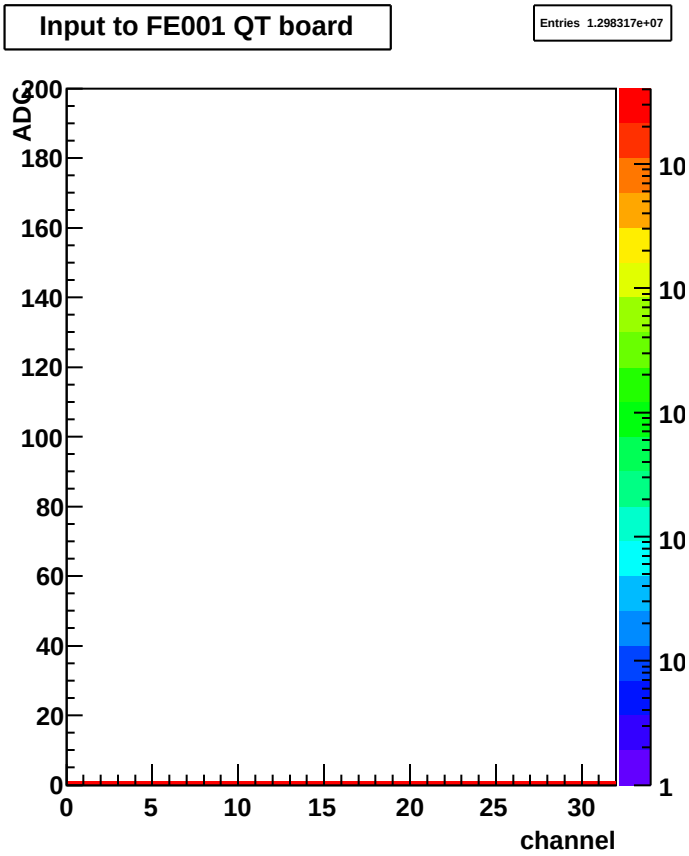

Input to FPD L2 DSM
Entries 1217172
HT bits
405724
405724
6608
399116
SMALL
LARGE-S
LARGE-N

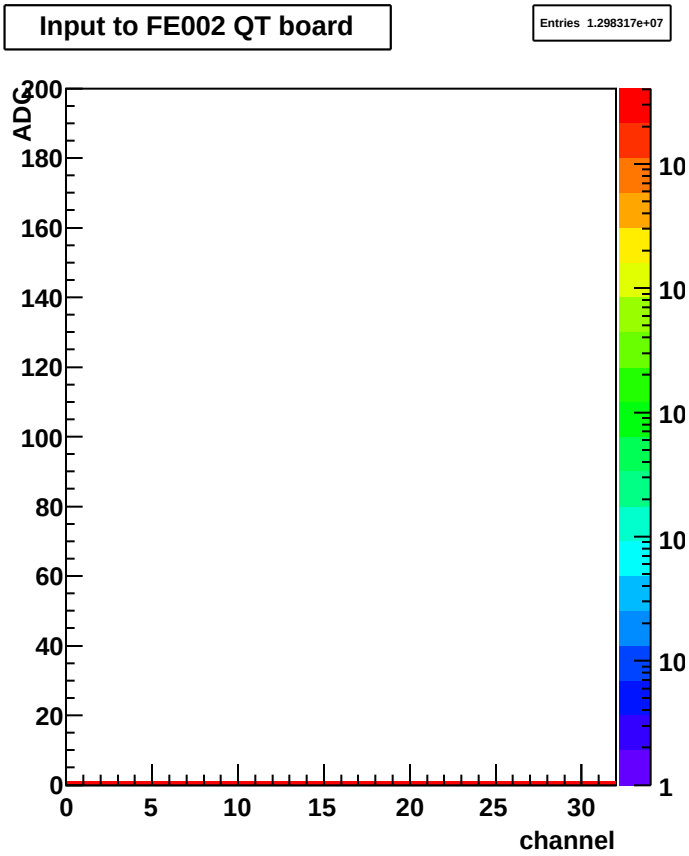

Input to FPD L2 DSM
Entries 818056
HT bits - simulated
405724
405724
6608
SMALL
LARGE-S
LARGE-N

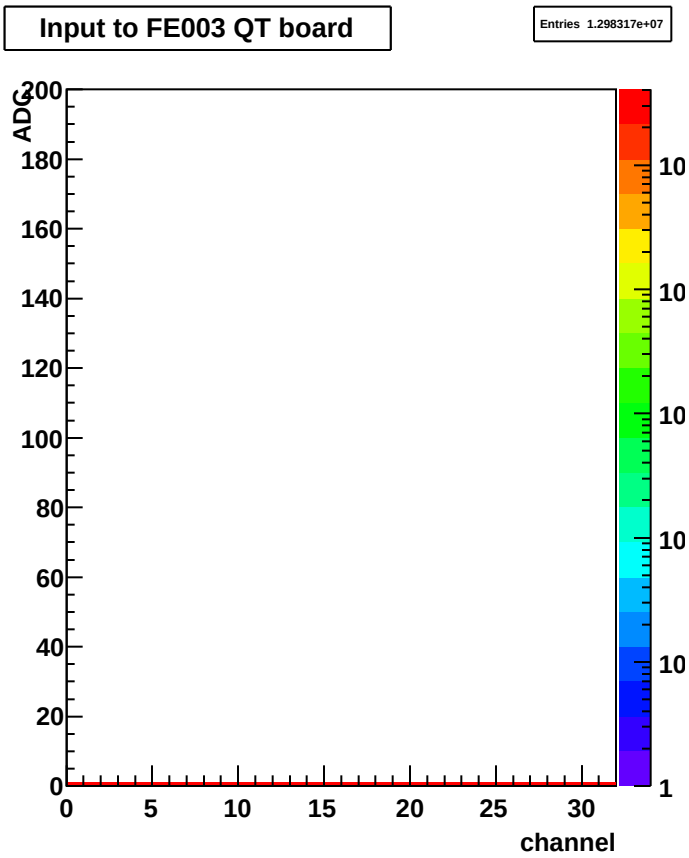

Input to FPD L3 DSM
Entries 6491584
bit
405724
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

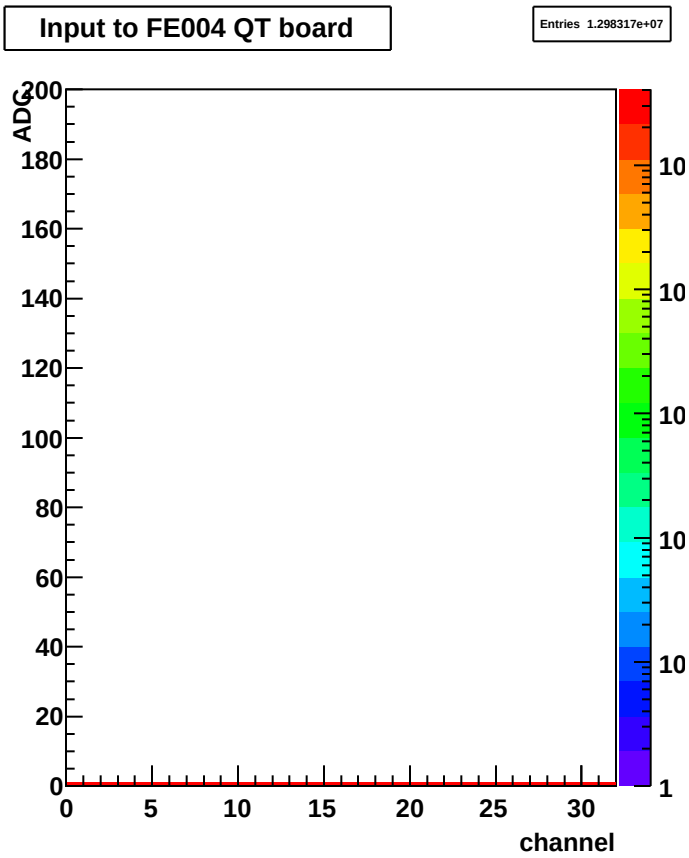

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

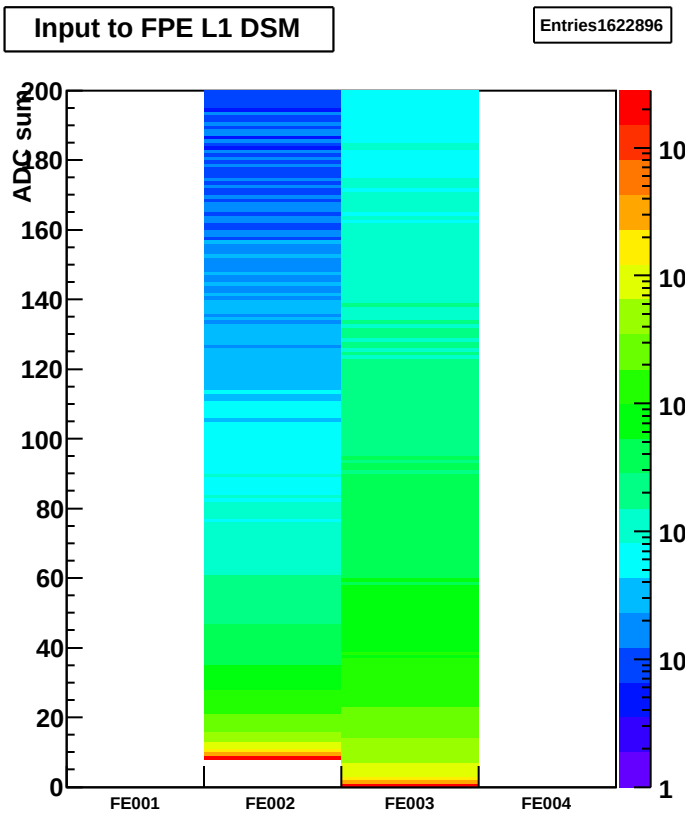

Input to FPD L2 DSM
Entries 1622896
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

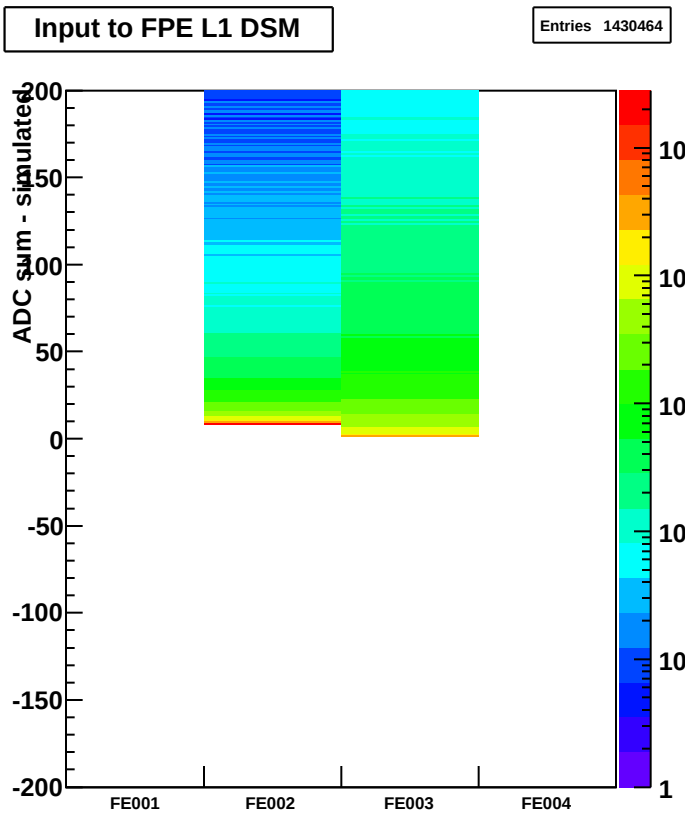

Input to FE001 QT board
Entries 1.298317e+07
ADC
channel

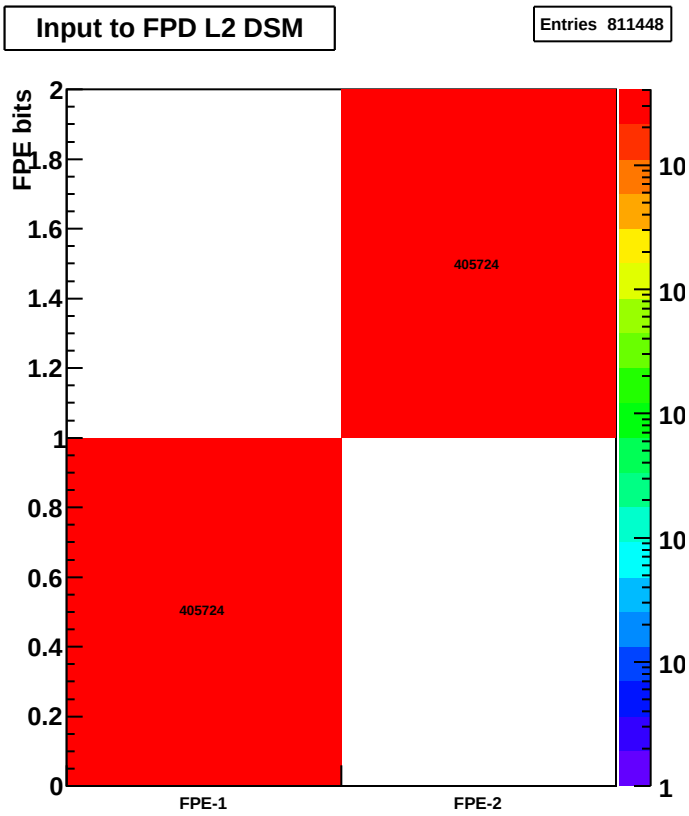

Input to FE002 QT board
Entries 1.298317e+07
ADC
channel

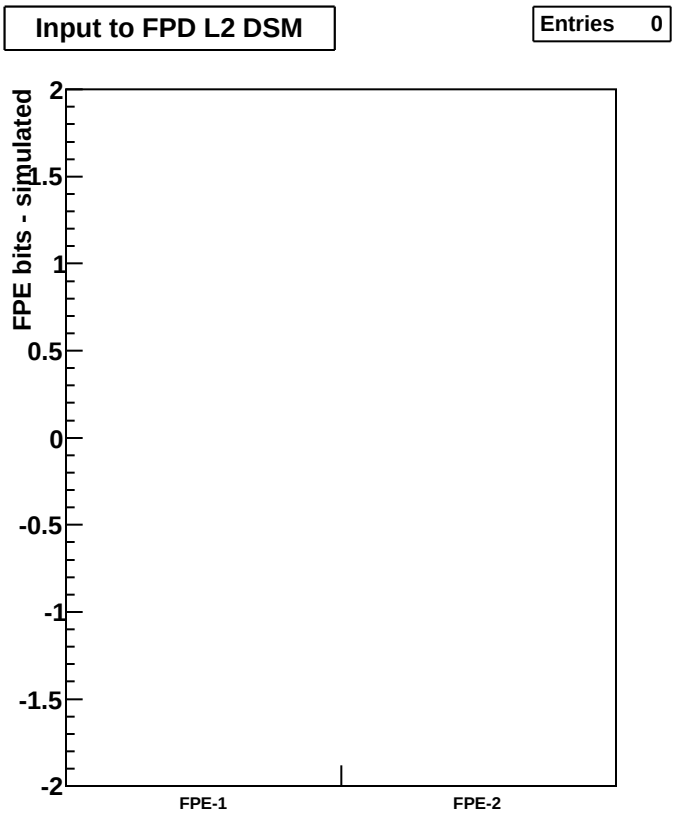

Input to FE003 QT board
Entries 1.298317e+07
ADC
channel


Input to FE004 QT board
Entries 1.298317e+07
ADC
channel


Input to FPE L1 DSM
Entries1622896
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 1430464
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 811448
FPE bits
405724
405724
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2