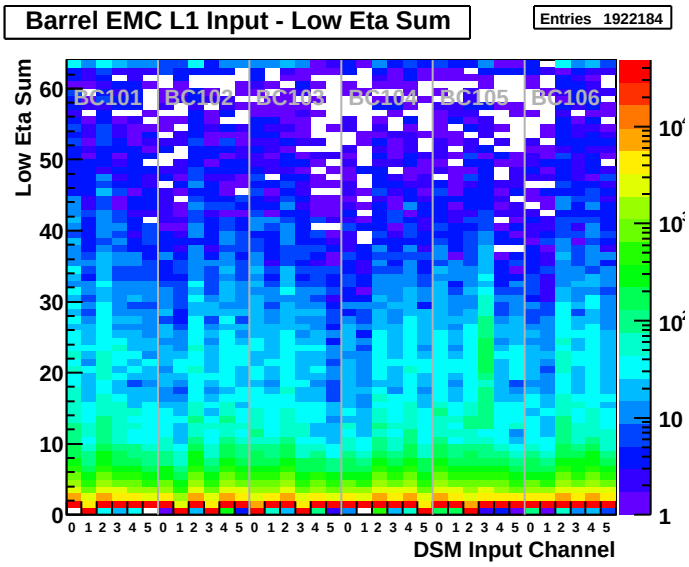
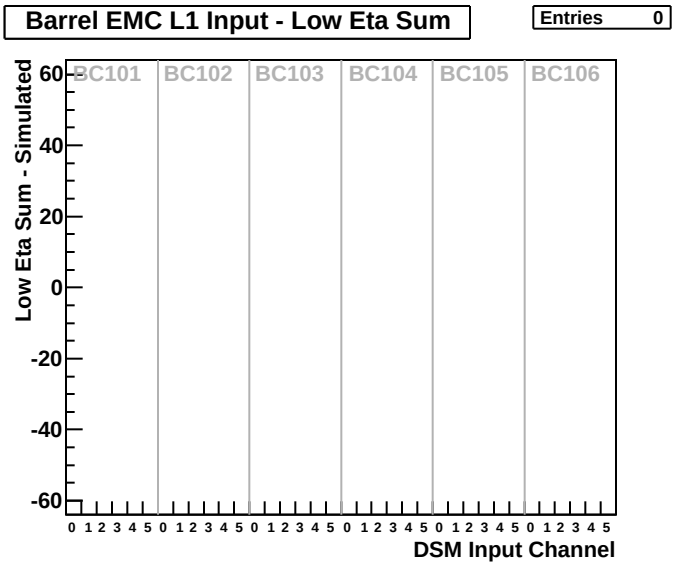
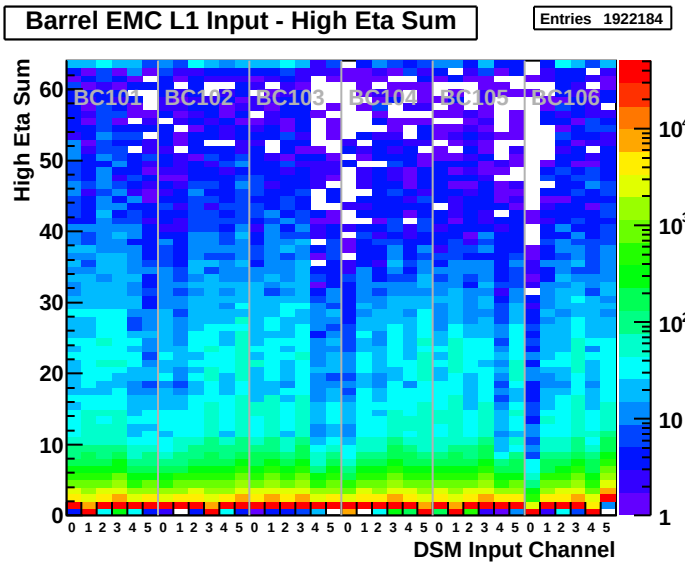
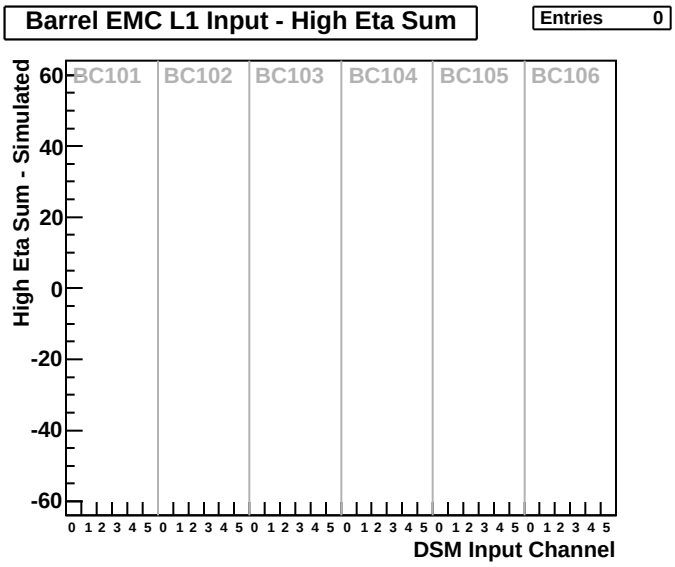
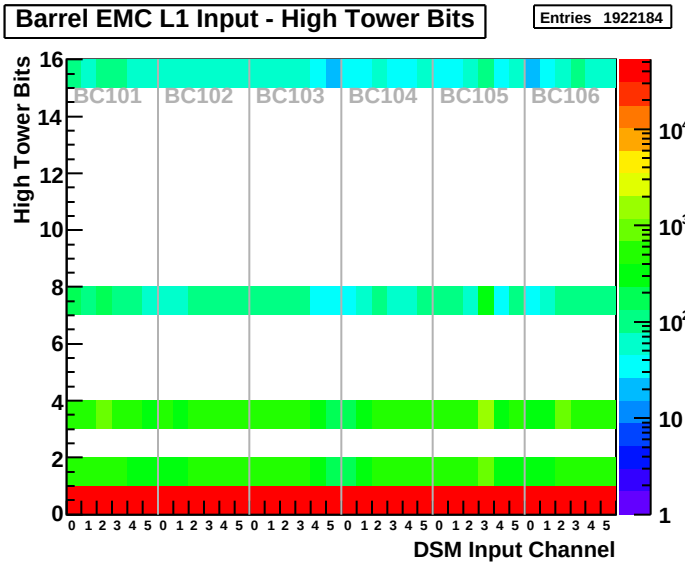
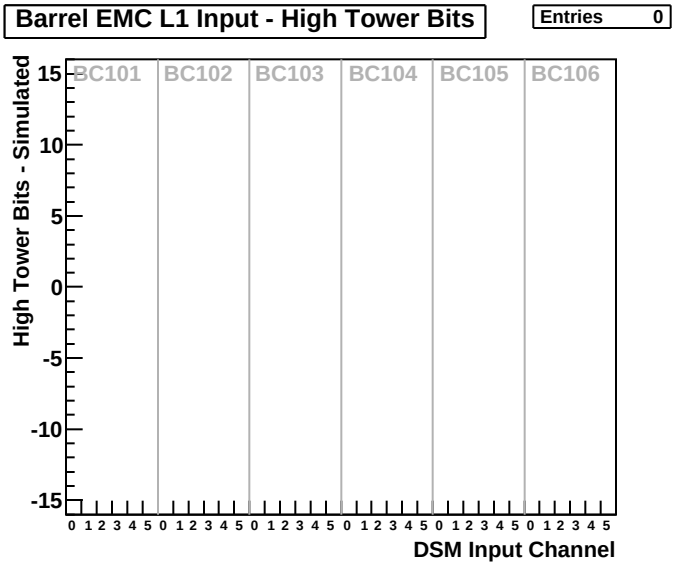

Barrel EMC L1 Input - Low Eta Sum
Entries 1922184
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

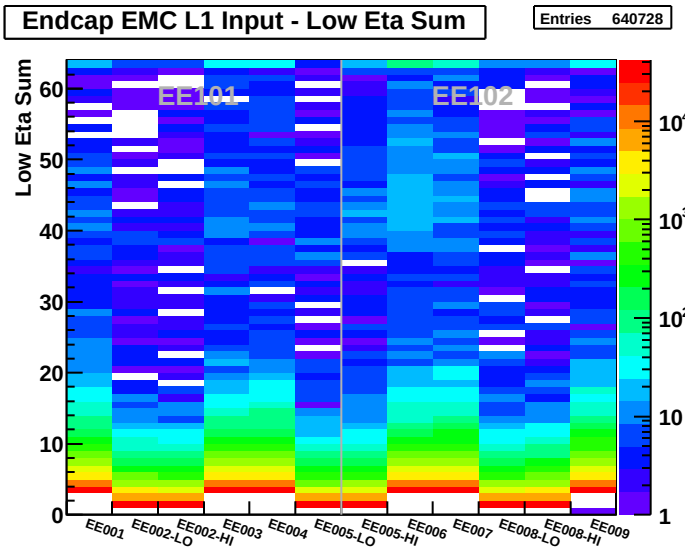

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

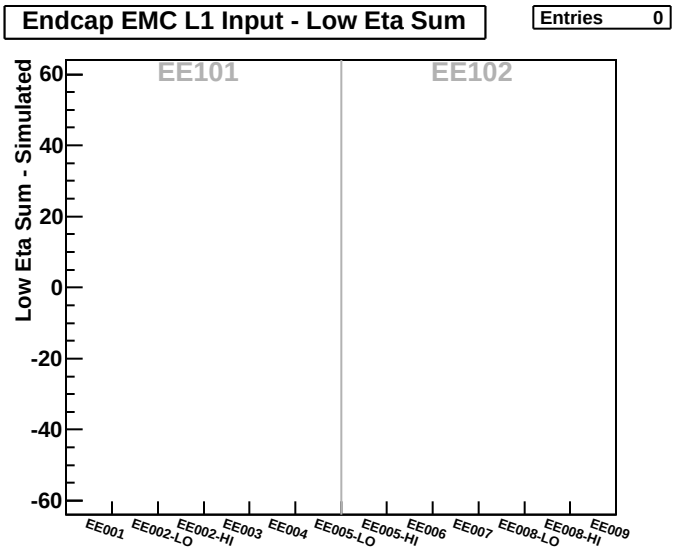

Barrel EMC L1 Input - High Eta Sum
Entries 1922184
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

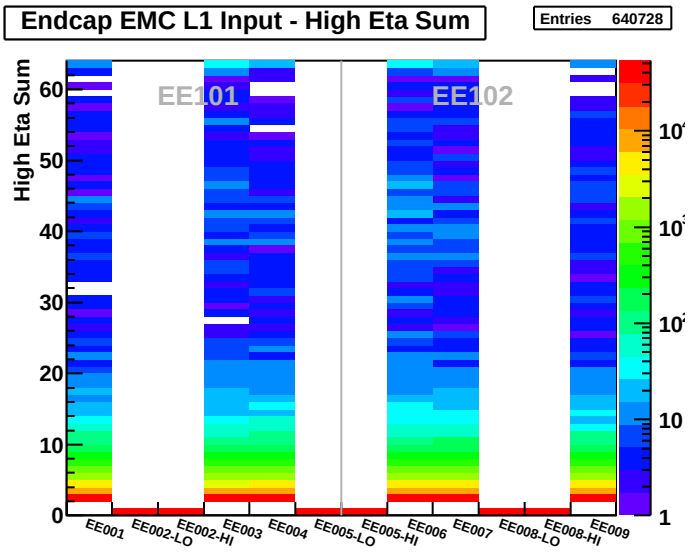

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

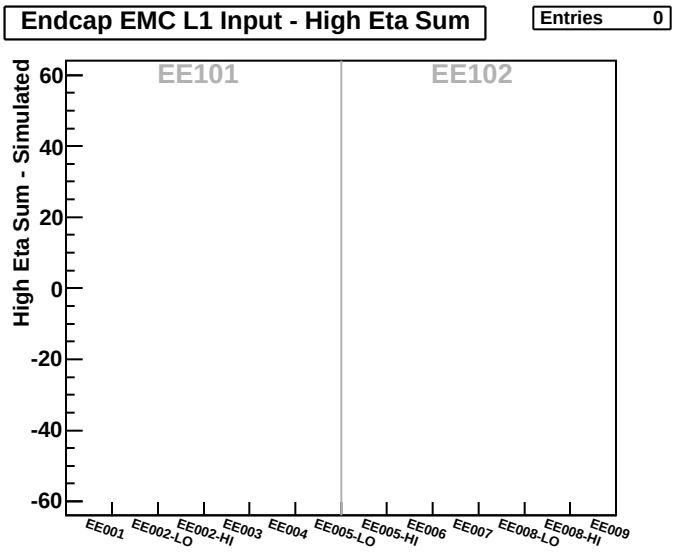

Barrel EMC L1 Input - High Tower Bits
Entries 1922184
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

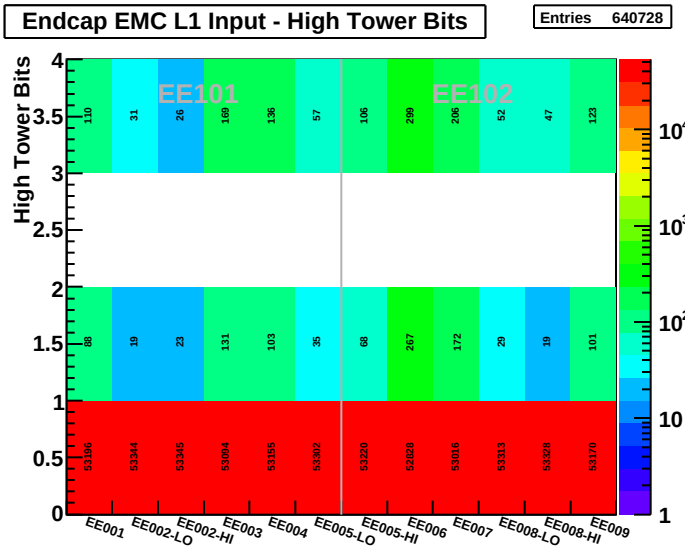

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

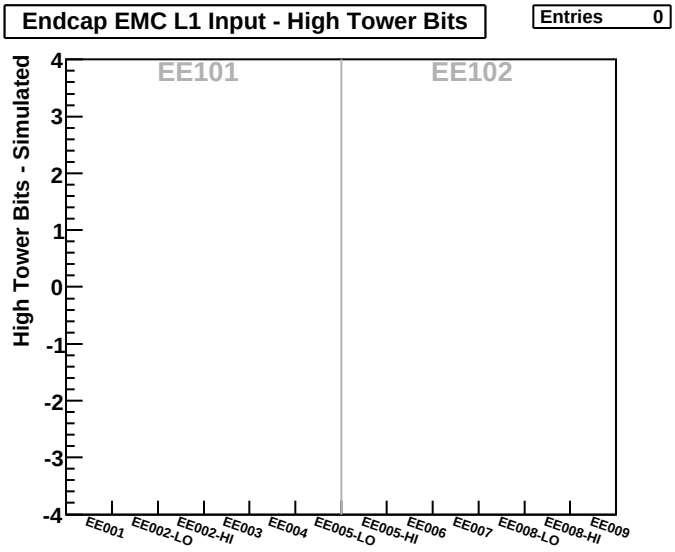

Endcap EMC L1 Input - Low Eta Sum
Entries 640728
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

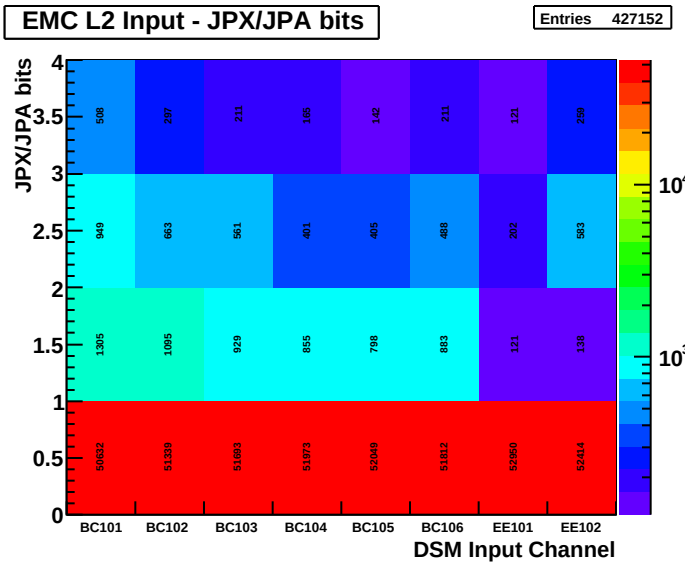

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

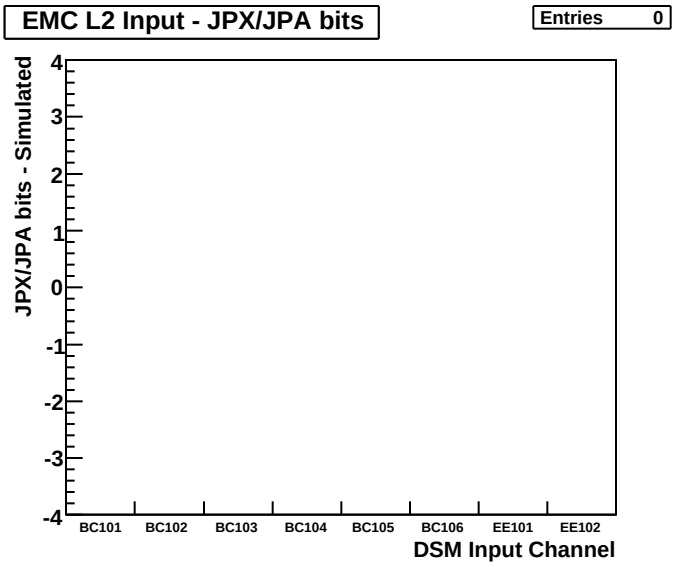

Endcap EMC L1 Input - High Eta Sum
Entries 640728
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

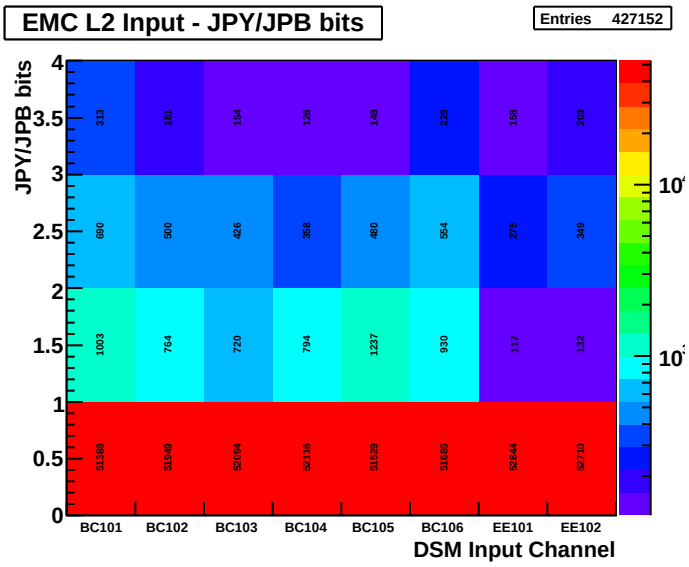

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

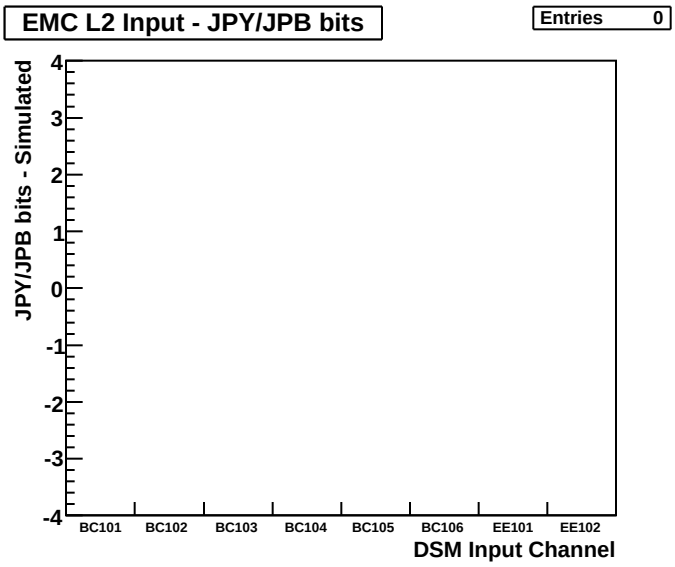

Endcap EMC L1 Input - High Tower Bits
Entries 640728
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

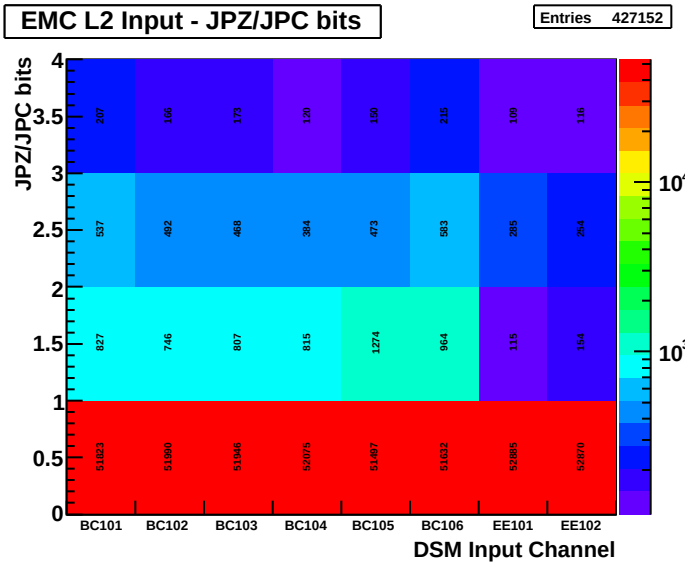

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

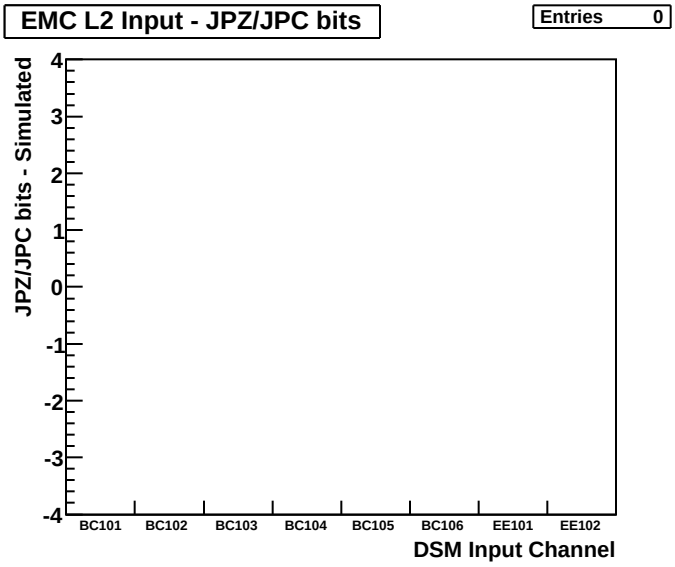

EMC L2 Input - JPX/JPA bits
Entries 427152
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
508 297 211 165 142 211 121 259
949 663 561 401 405 488 202 583
1305 1095 929 855 798 883 121 138
50632 51339 51693 51973 52049 51812 52950 52414

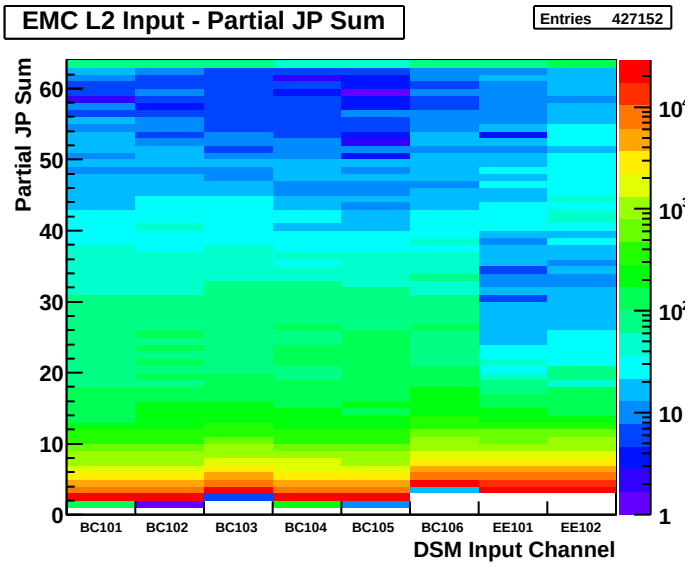

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

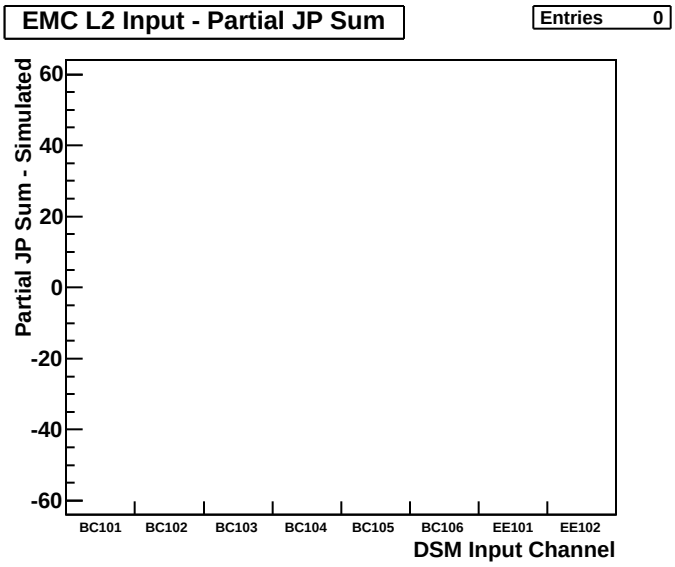

EMC L2 Input - JPY/JPB bits
Entries 427152
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
313 181 154 126 149 225 158 203
690 500 426 358 480 554 275 349
1003 764 720 794 1237 930 117 132
51388 51949 52094 52116 51529 51685 52844 52710

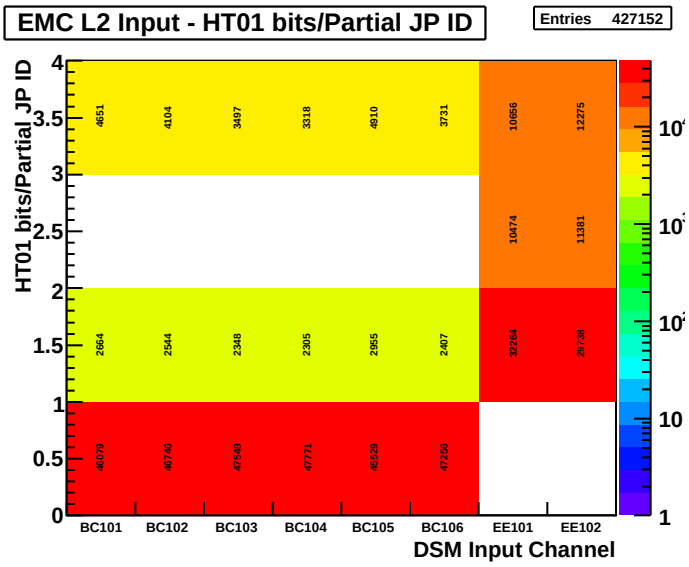

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

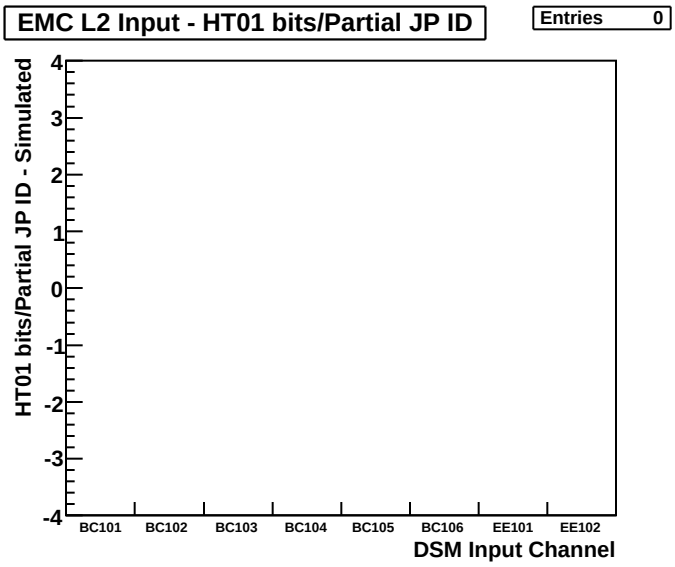

EMC L2 Input - JPZ/JPC bits
Entries 427152
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
207 166 173 120 150 215 109 116
537 492 468 384 473 583 285 254
827 746 807 815 1274 964 115 154
51623 51980 51946 52075 51497 51632 52885 52870

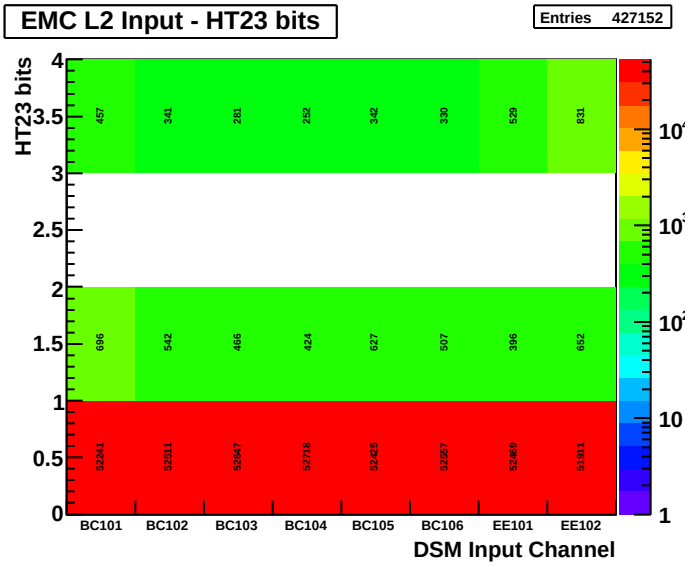

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

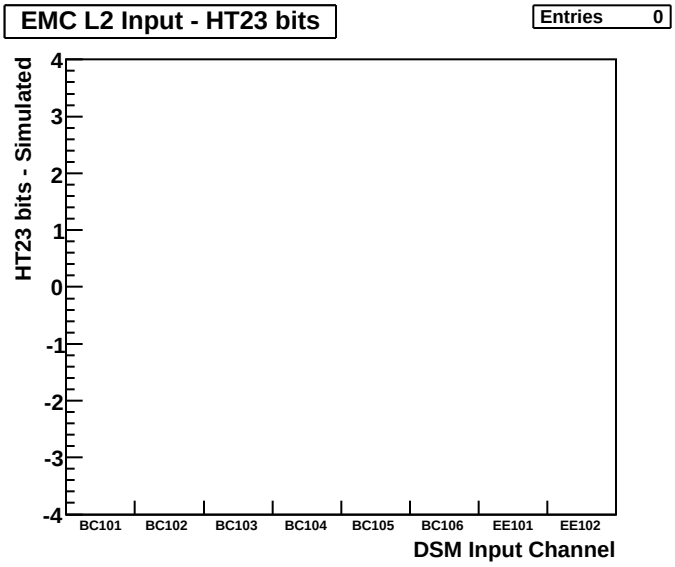

EMC L2 Input - Partial JP Sum
Entries 427152
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

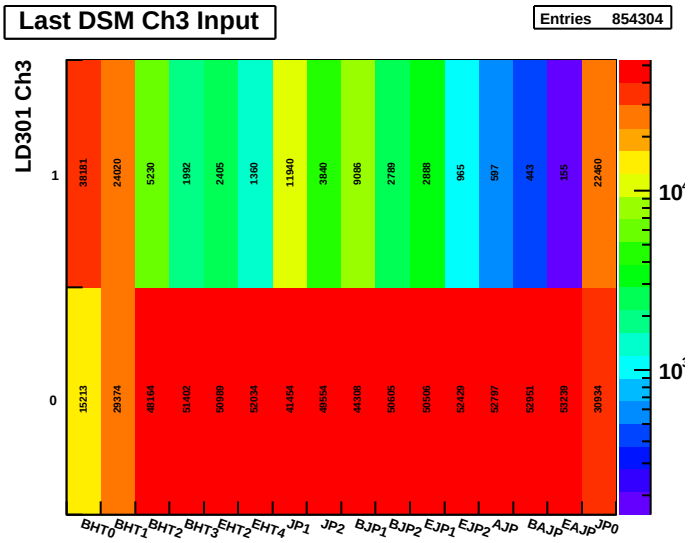

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

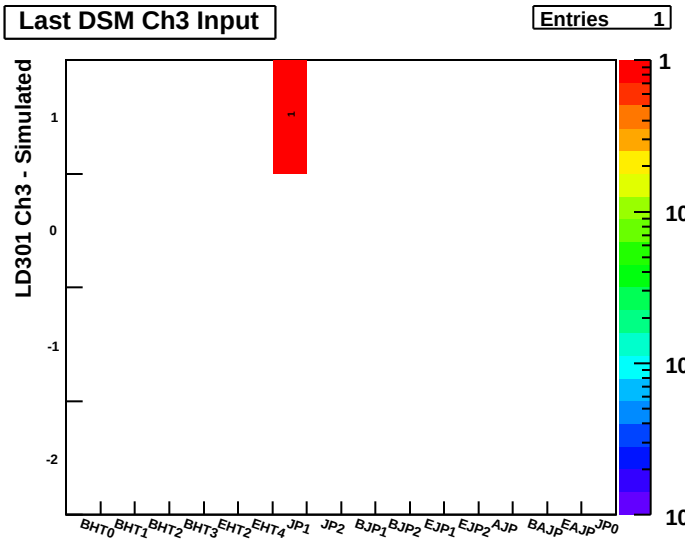

EMC L2 Input - HT01 bits/Partial JP ID
Entries 427152
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

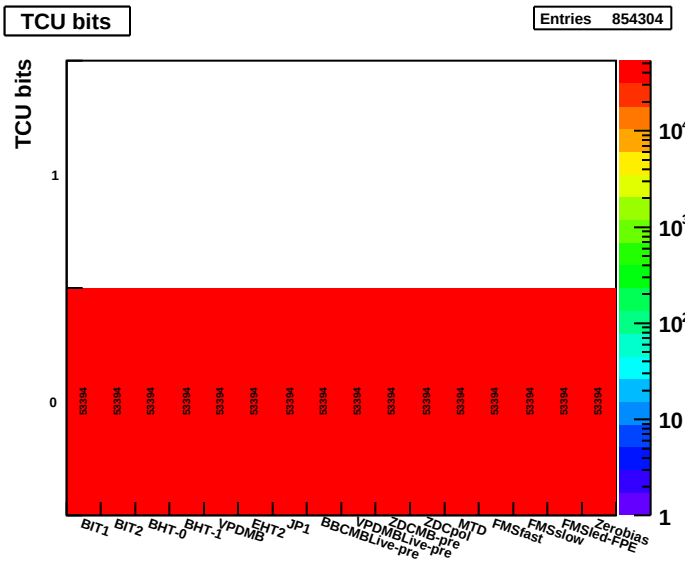

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

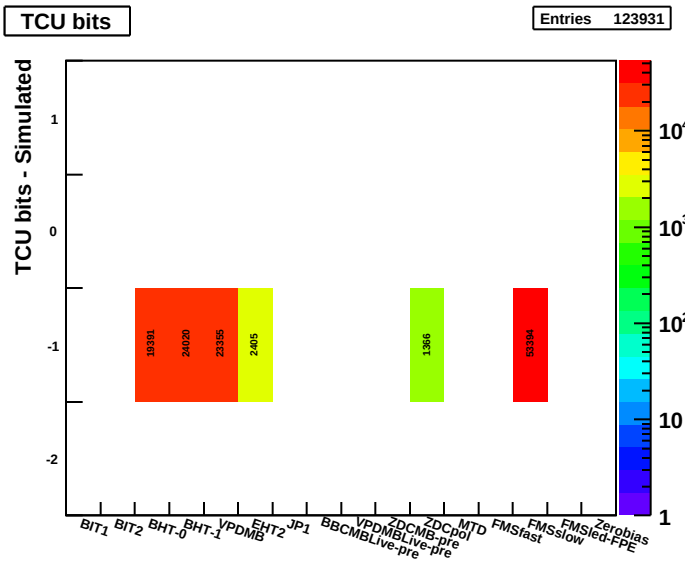

EMC L2 Input - HT23 bits
Entries 427152
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

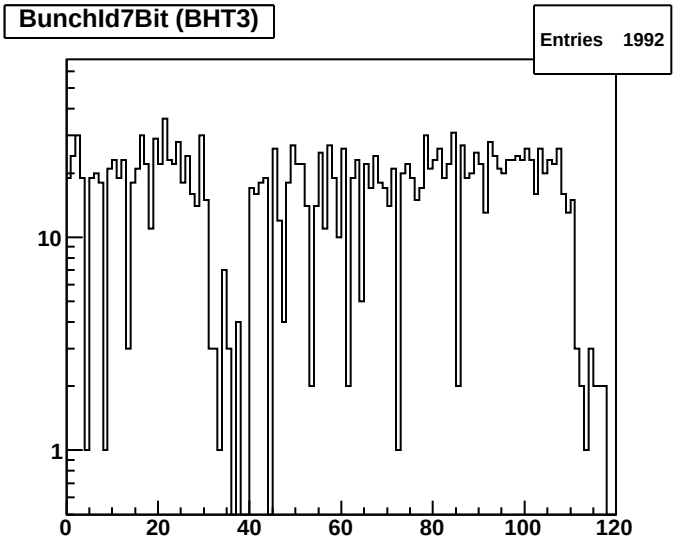

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

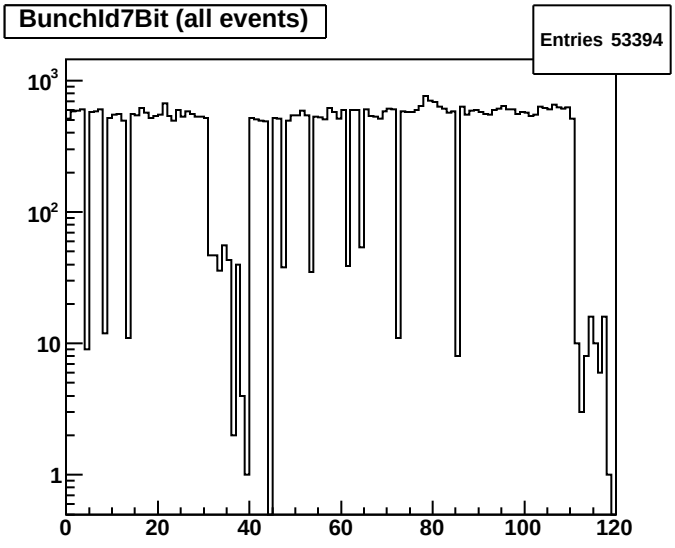

Last DSM Ch3 Input
Entries 854304
LD301 Ch3
38181 24020 5230 1992 2405 1360 11940 3840 9086 2789 2888 965 597 443 155 22460
15213 29374 48164 51402 50989 52034 41454 49554 44308 50605 50506 52429 52797 52951 53239 30934
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

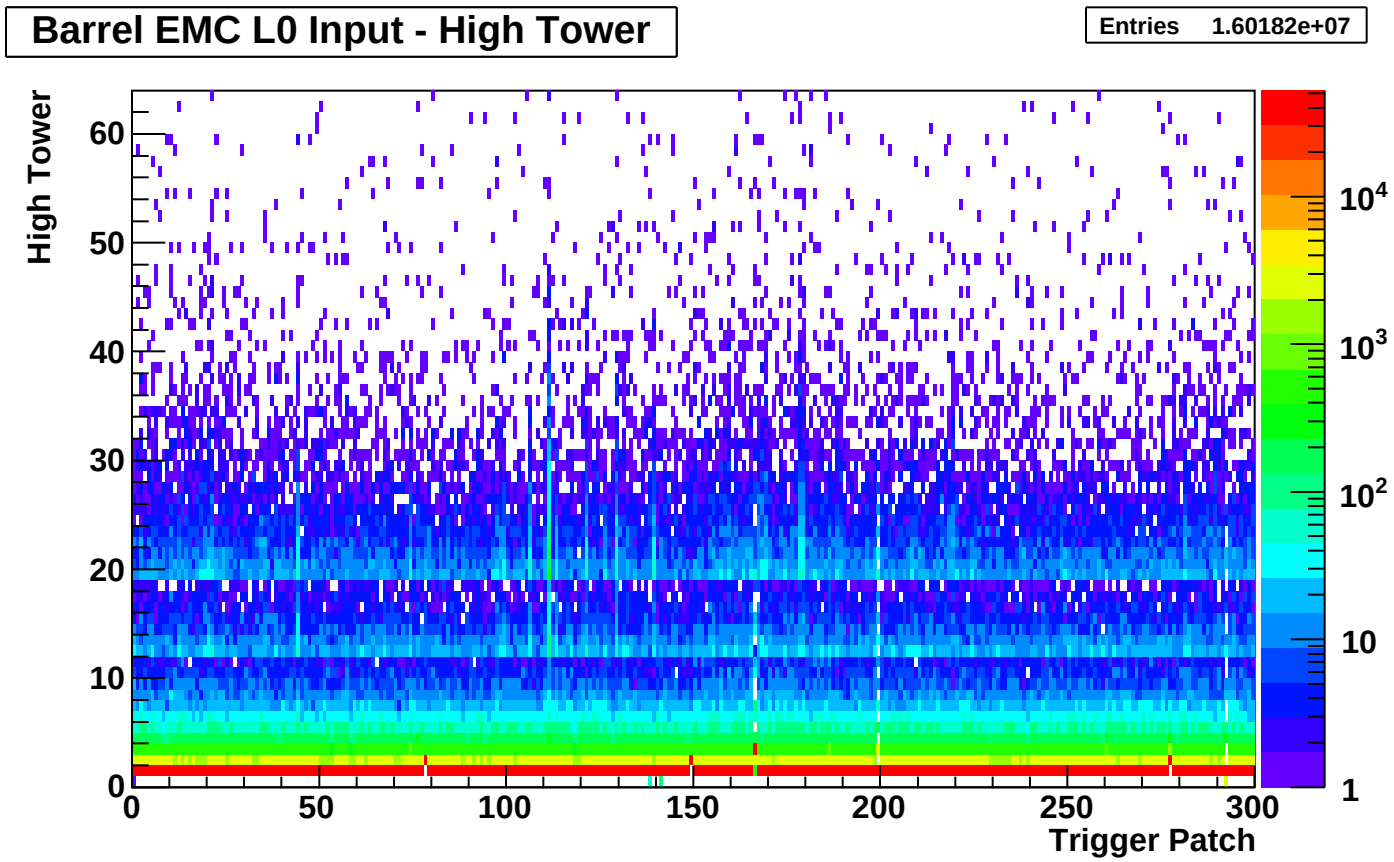

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

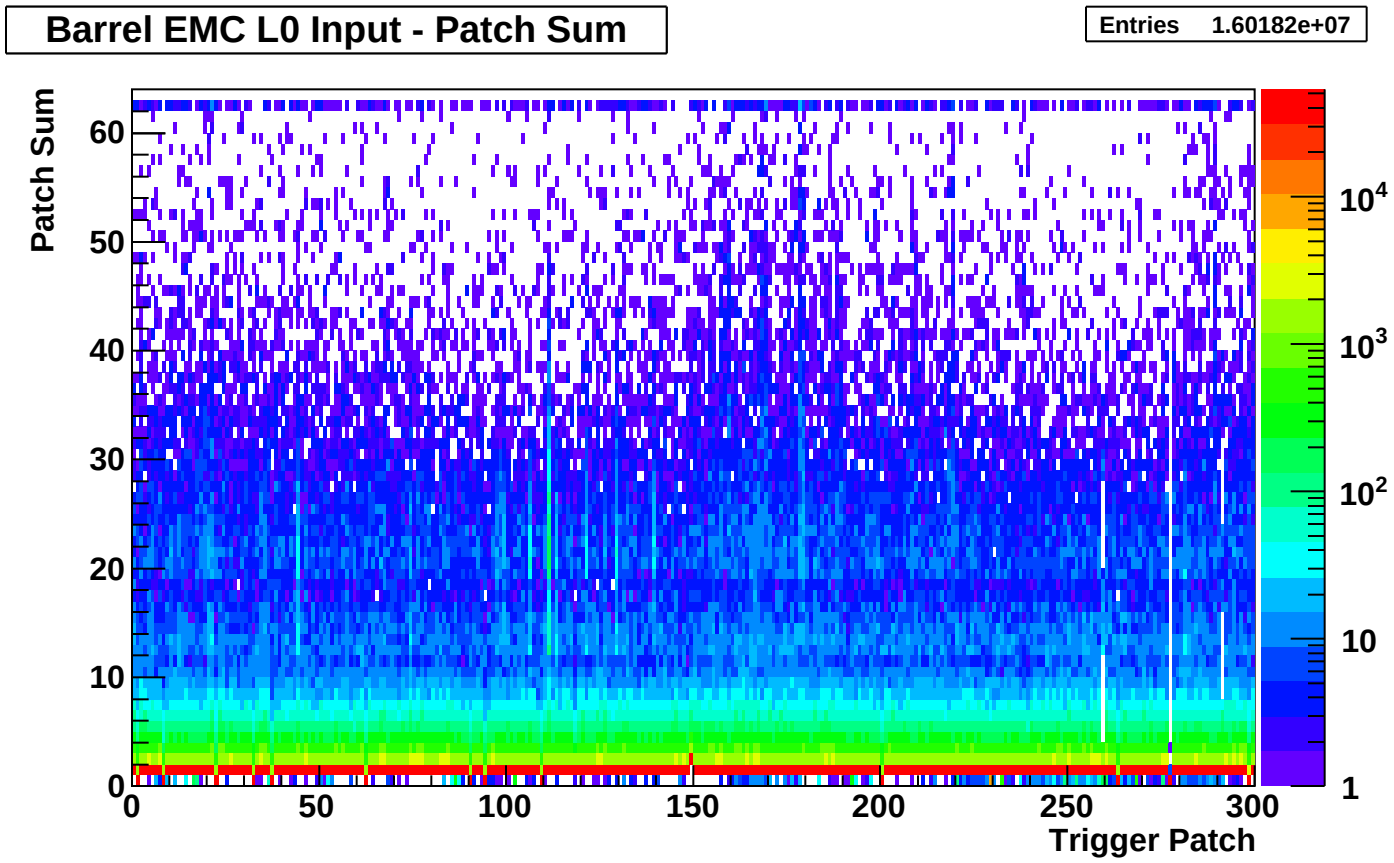

TCU bits
Entries 854304
TCU bits
53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394 53394
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

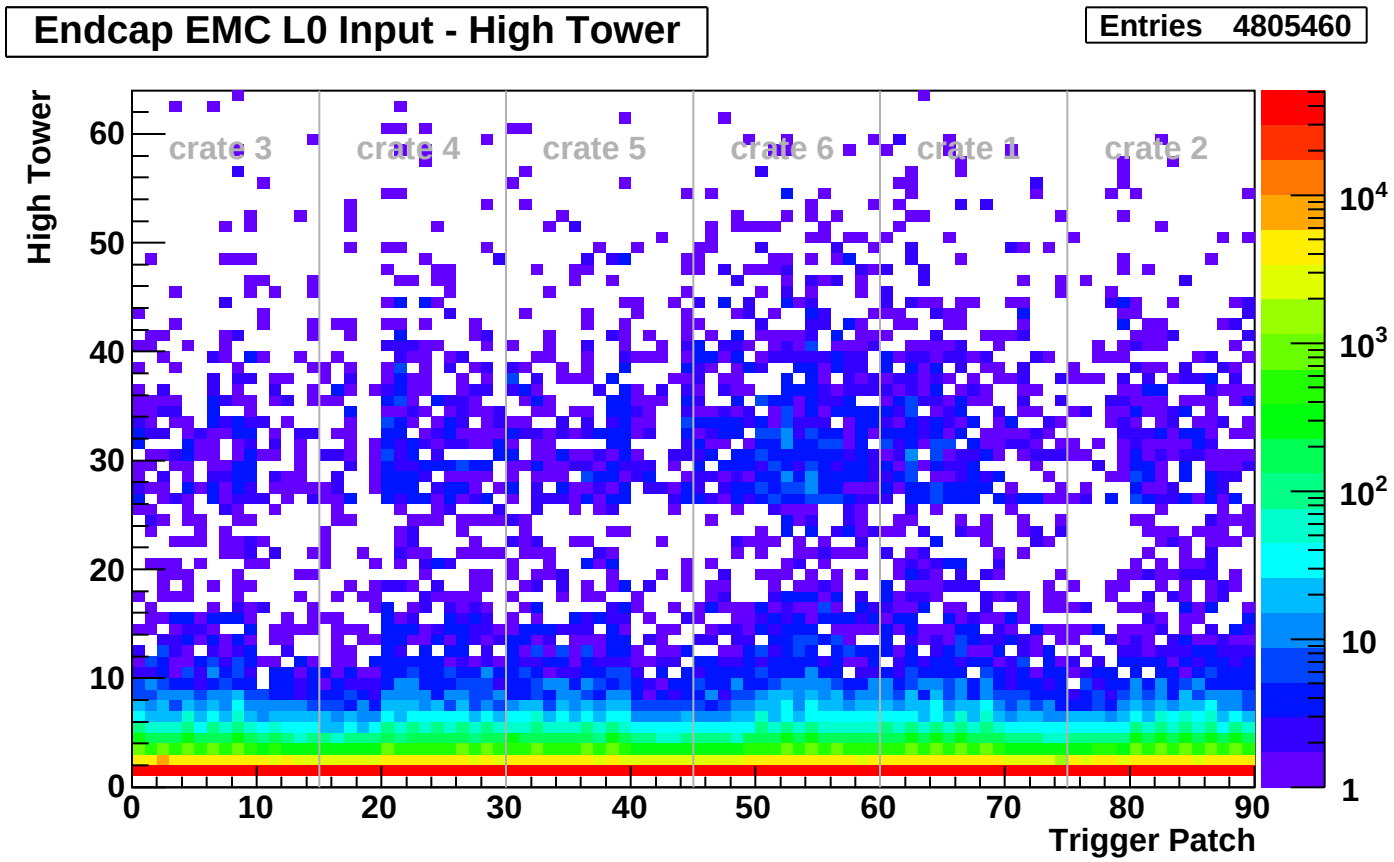

TCU bits
Entries 123931
TCU bits - Simulated
19391 24020 23355 2405 1366 53394
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

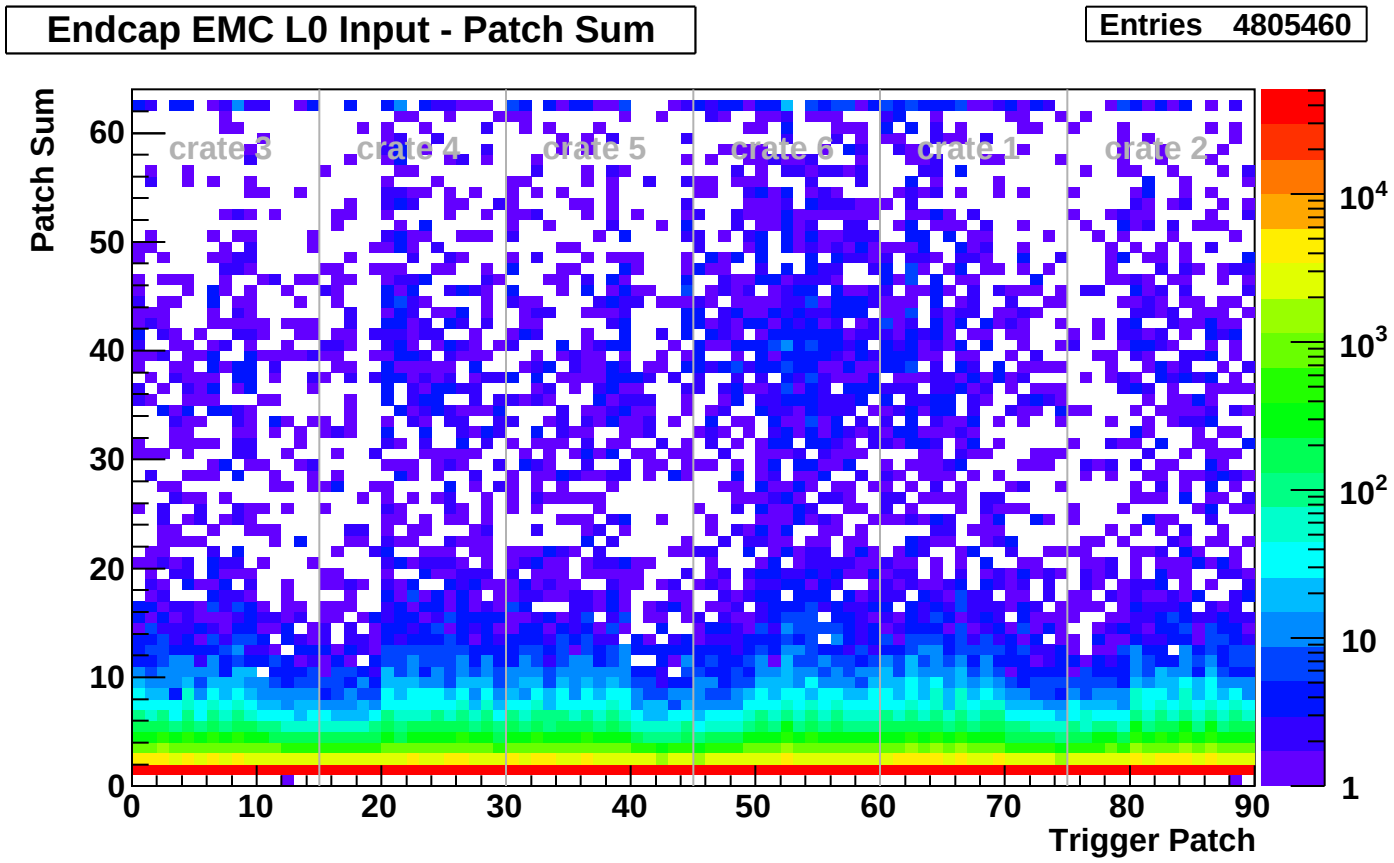

BunchId7Bit (BHT3)
Entries 1992

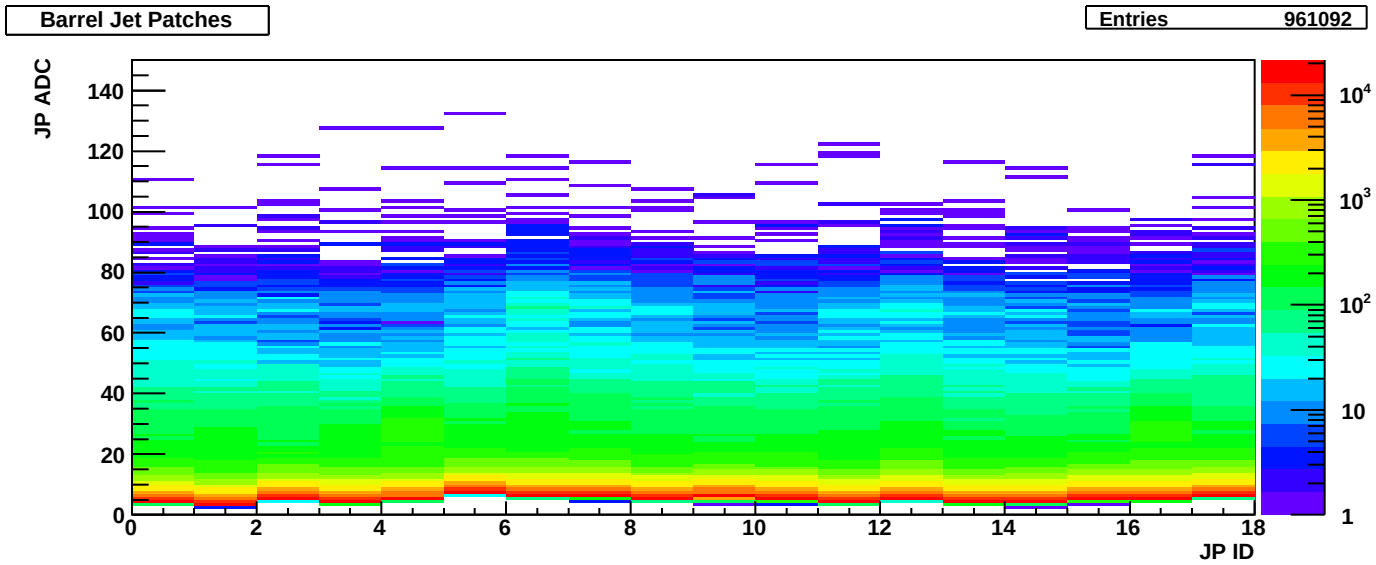

BunchId7Bit (all events)
Entries 53394

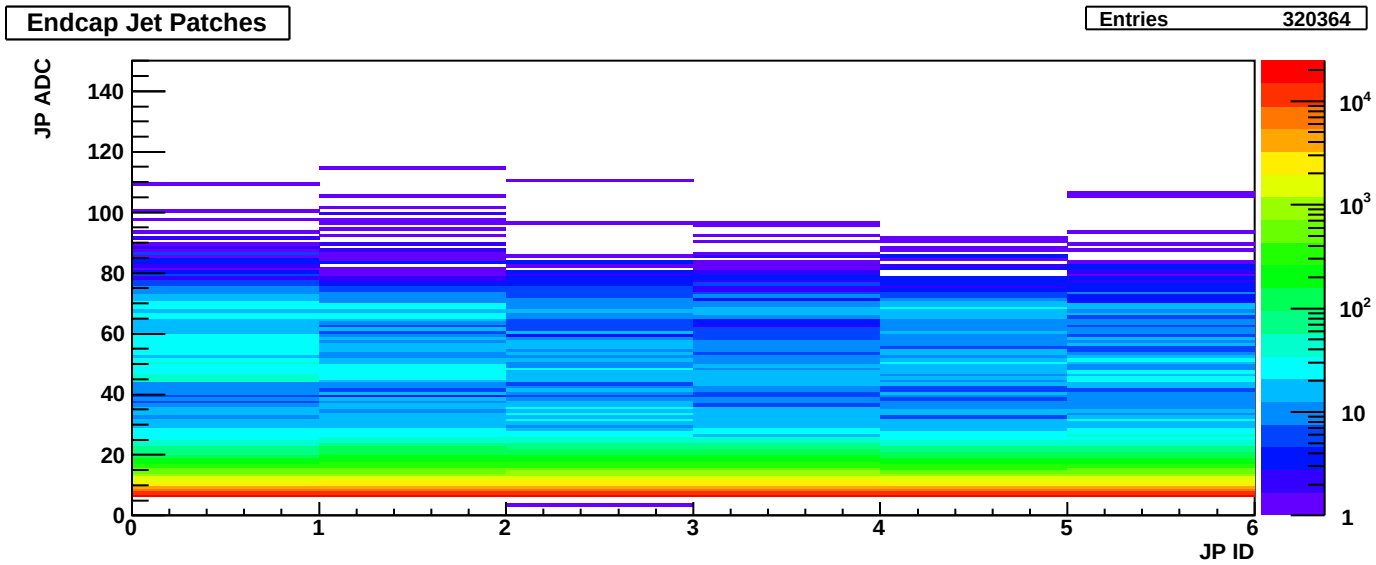

Barrel EMC L0 Input - High Tower
Entries 1.60182e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

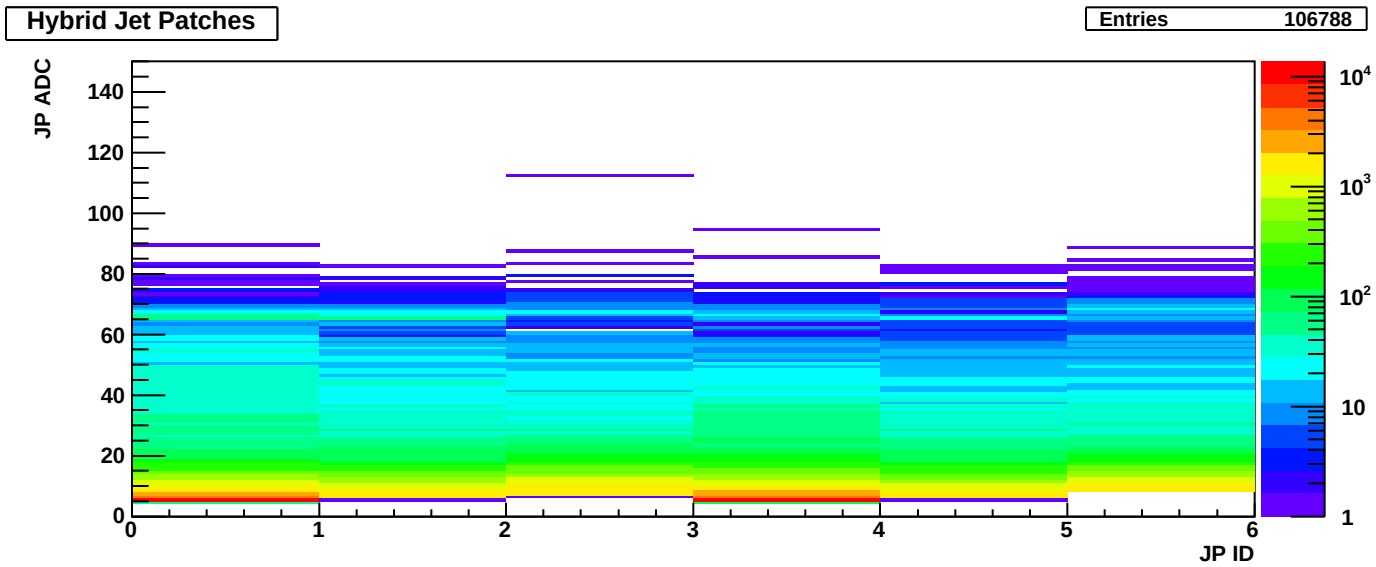

Barrel EMC L0 Input - Patch Sum
Entries 1.60182e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

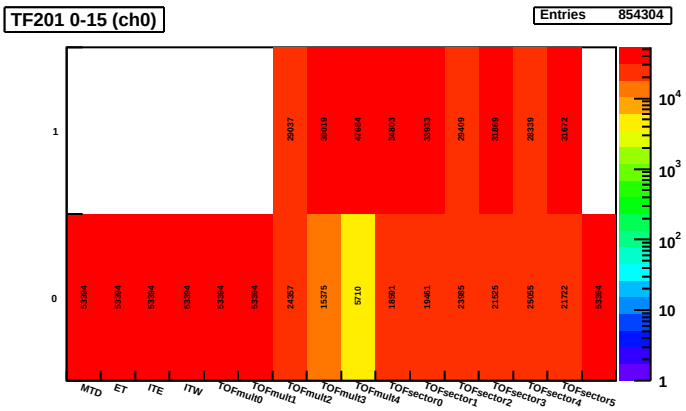

Endcap EMC L0 Input - High Tower
Entries 4805460
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

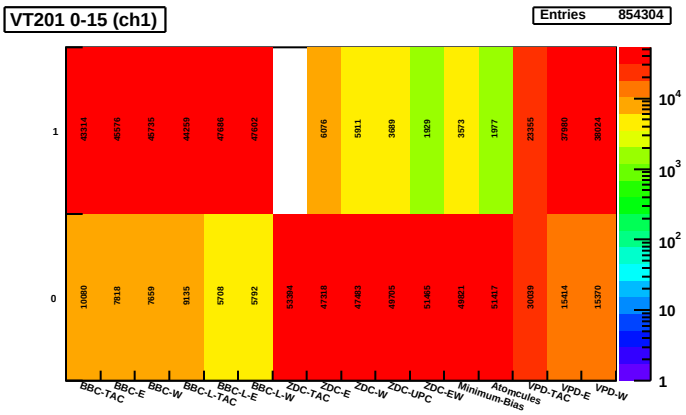

Endcap EMC L0 Input - Patch Sum
Entries 4805460
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

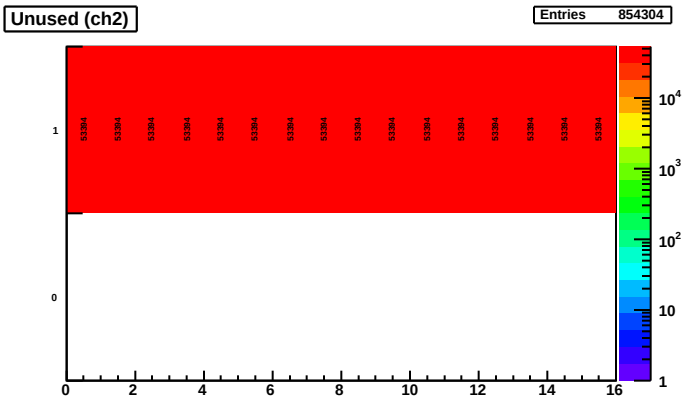

Barrel Jet Patches
Entries 961092
JP ADC
JP ID

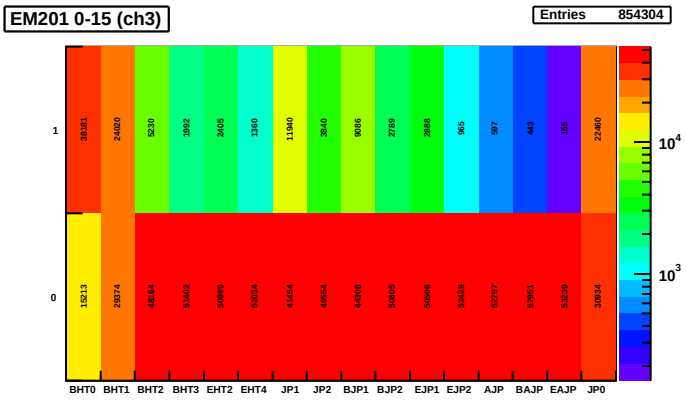

Endcap Jet Patches
Entries 320364
JP ADC
JP ID

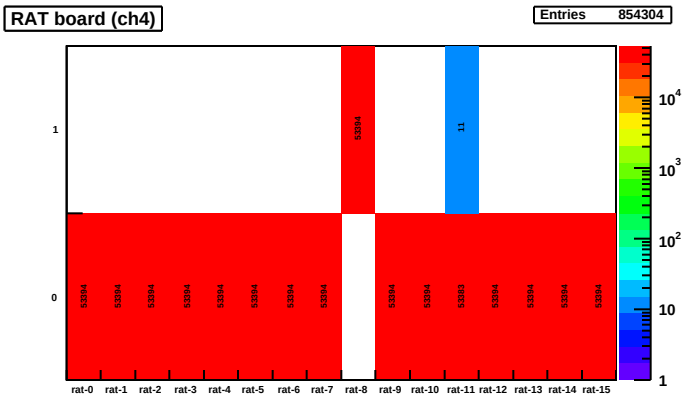

Hybrid Jet Patches
Entries 106788
JP ADC
JP ID

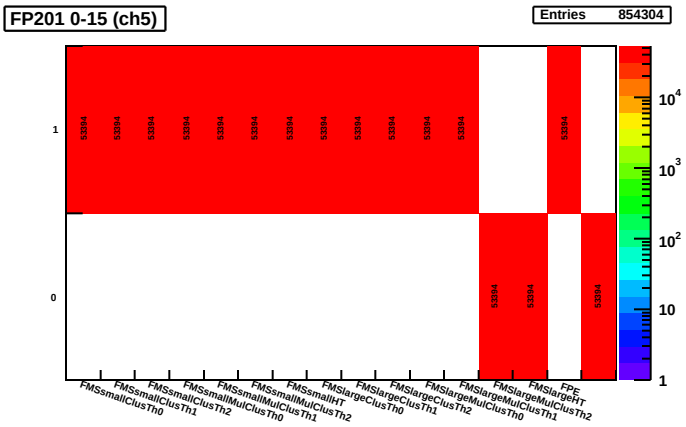

TF201 0-15 (ch0)
Entries 854304
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

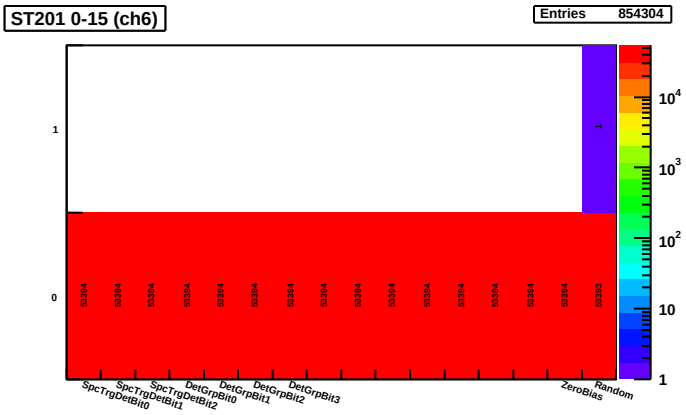

VT201 0-15 (ch1)
Entries 854304
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

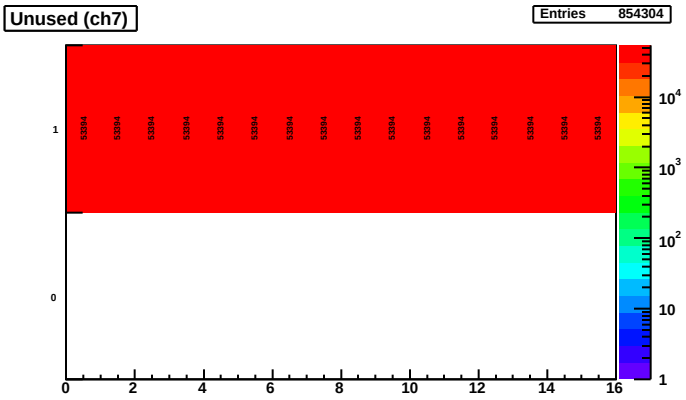

Unused (ch2)
Entries 854304

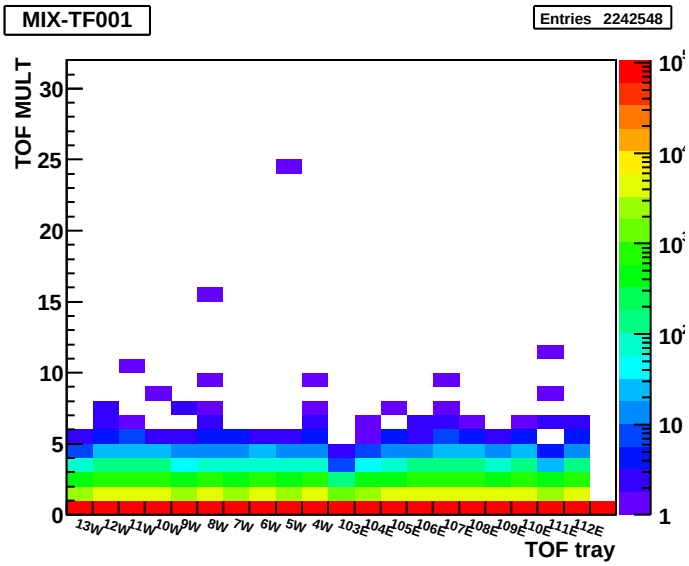

EM201 0-15 (ch3)
Entries 854304
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

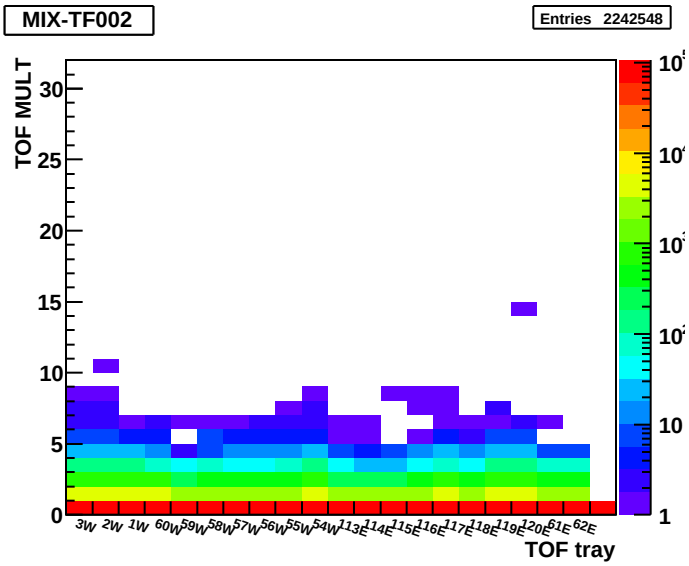

RAT board (ch4)
Entries 854304
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

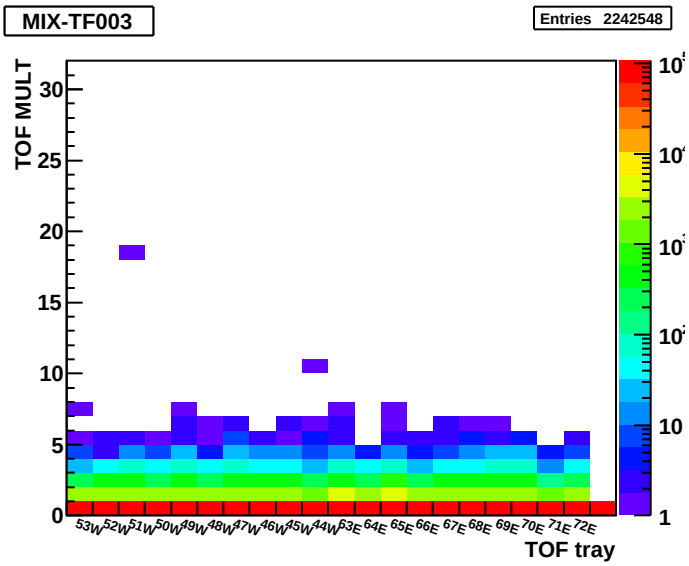

FP201 0-15 (ch5)
Entries 854304
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

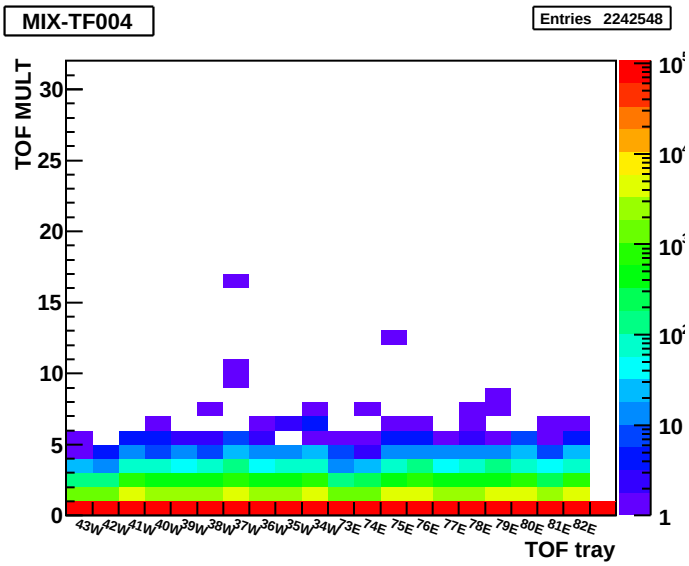

ST201 0-15 (ch6)
Entries 854304
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

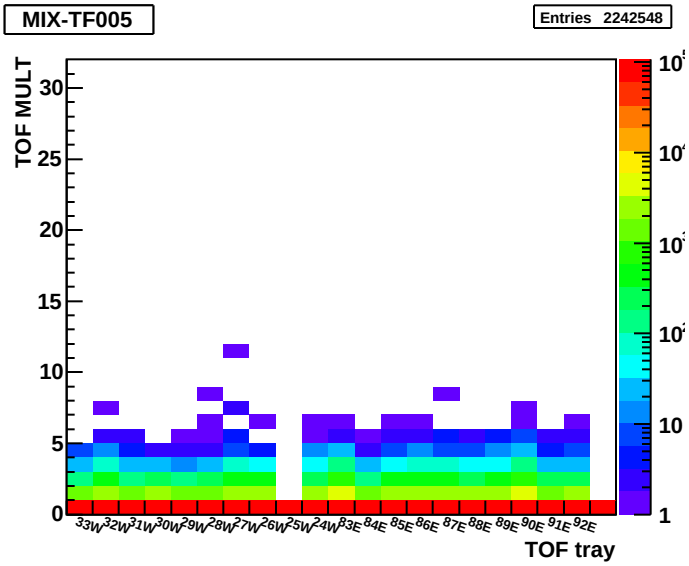

Unused (ch7)
Entries 854304

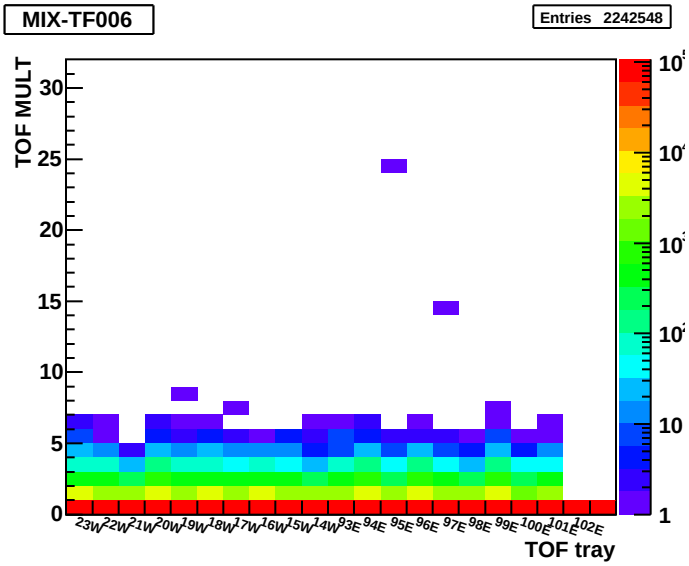

MIX-TF001
Entries 2242548
TOF MULT
TOF tray

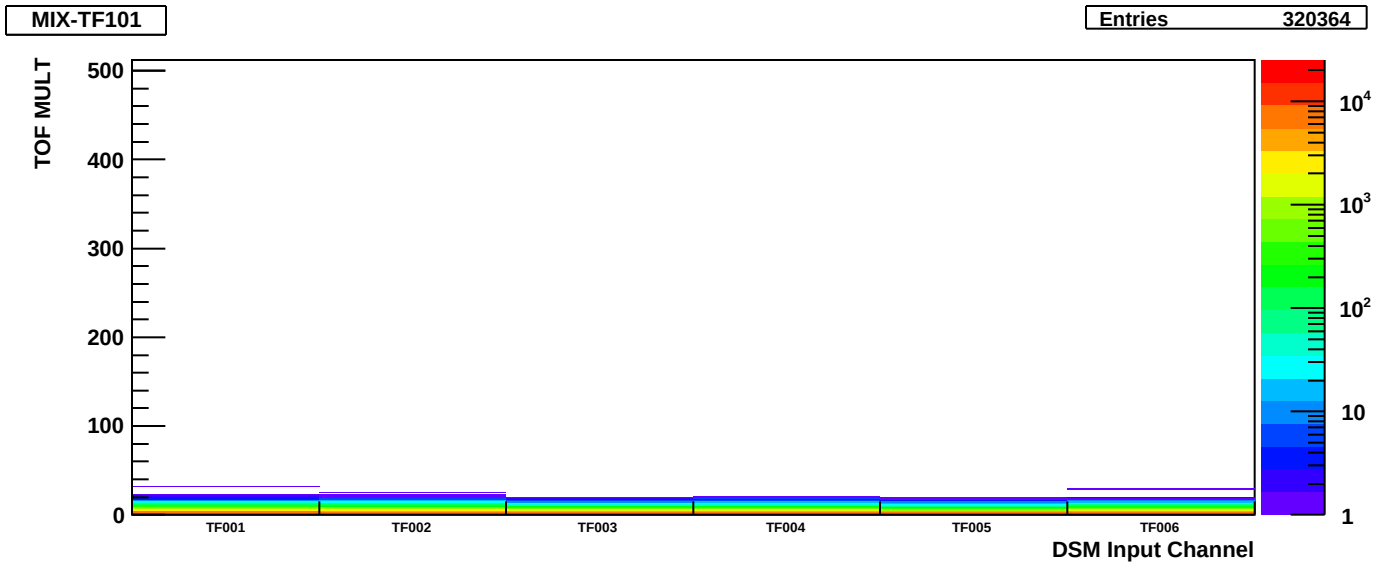

MIX-TF002
Entries 2242548
TOF MULT
TOF tray

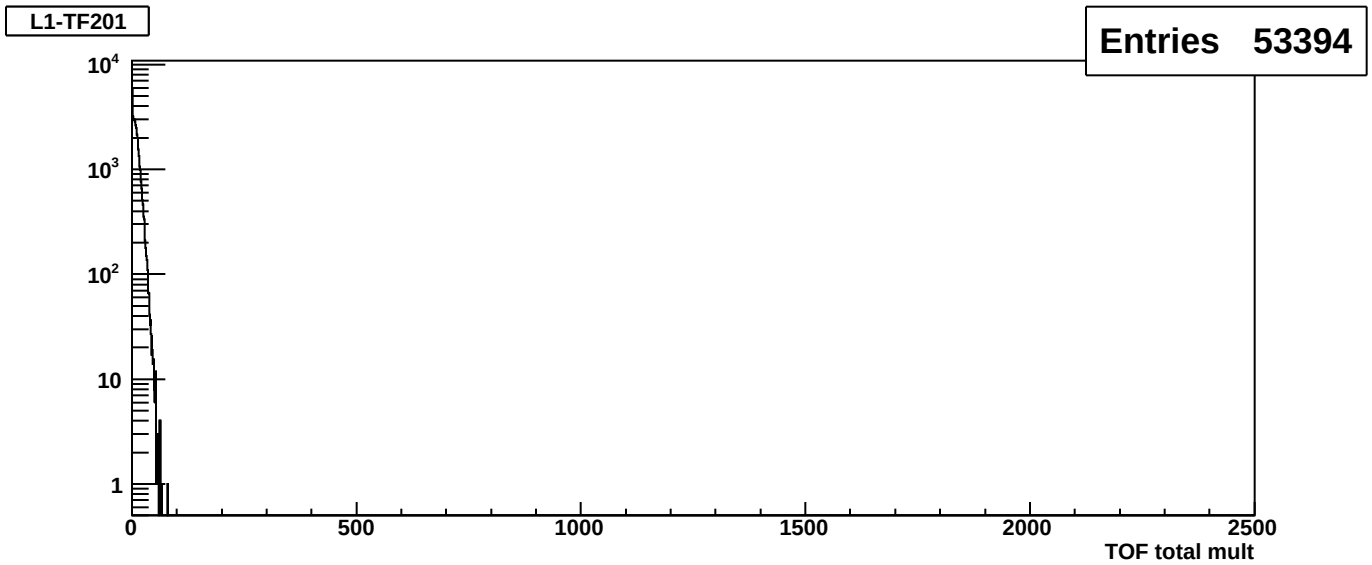

MIX-TF003
Entries 2242548
TOF MULT
TOF tray

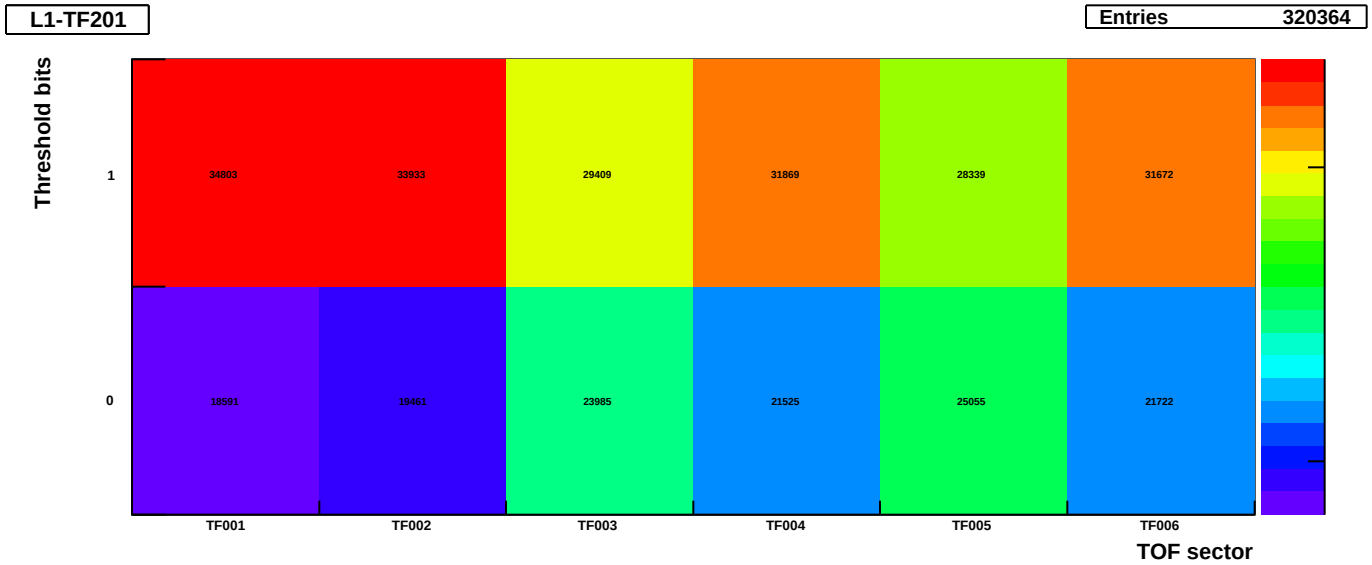

MIX-TF004
Entries 2242548
TOF MULT
TOF tray

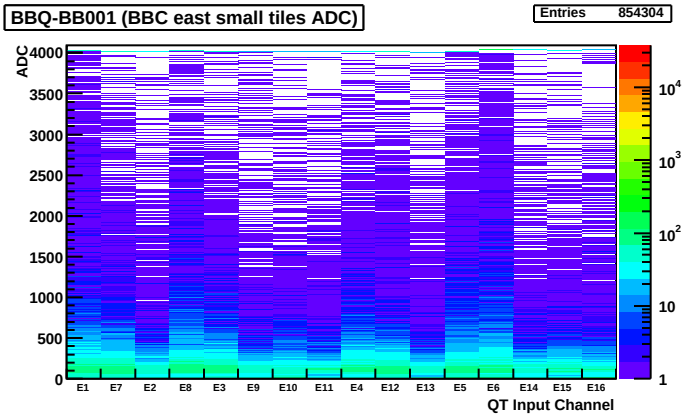

MIX-TF005
Entries 2242548
TOF MULT
TOF tray

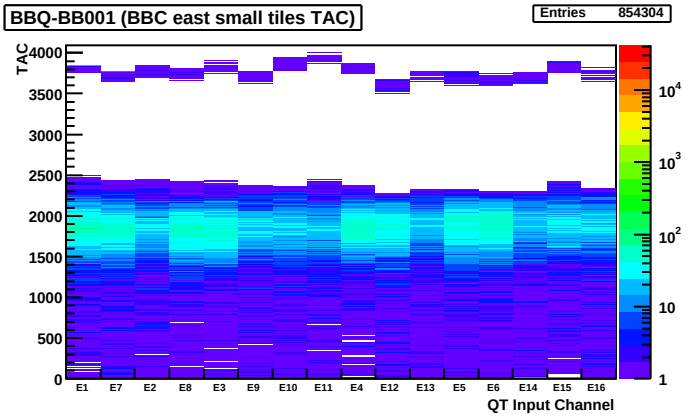

MIX-TF006
Entries 2242548
TOF MULT
TOF tray

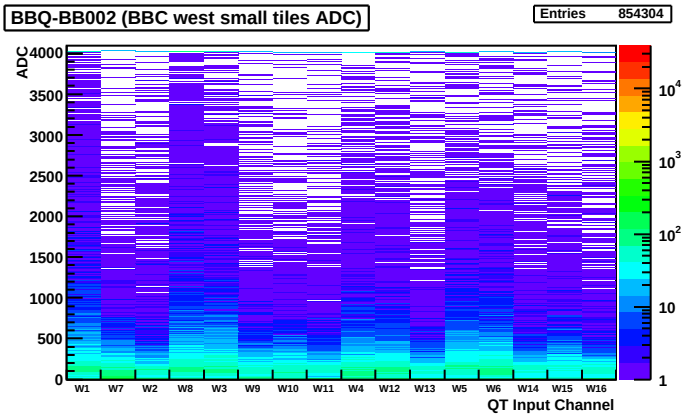

MIX-TF101
Entries 320364
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

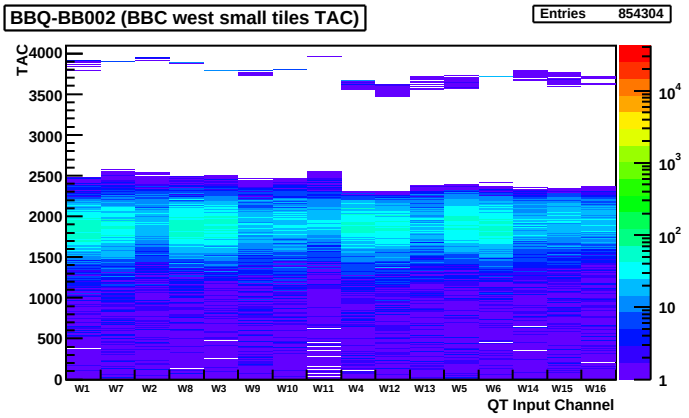

L1-TF201
Entries 53394
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

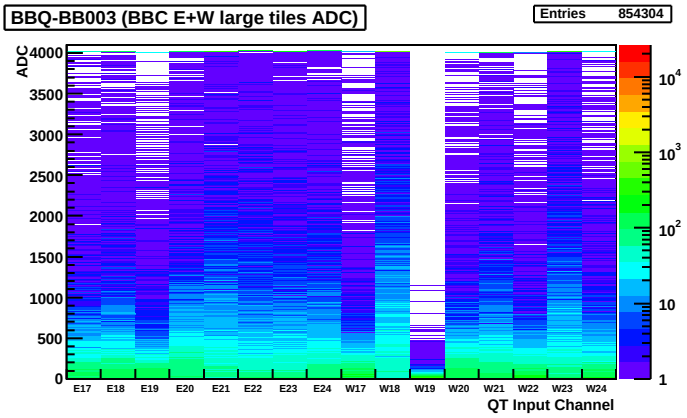

L1-TF201
Entries 320364
Threshold bits
1
0
34803
33933
29409
31869
28339
31672
18591
19461
23985
21525
25055
21722
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

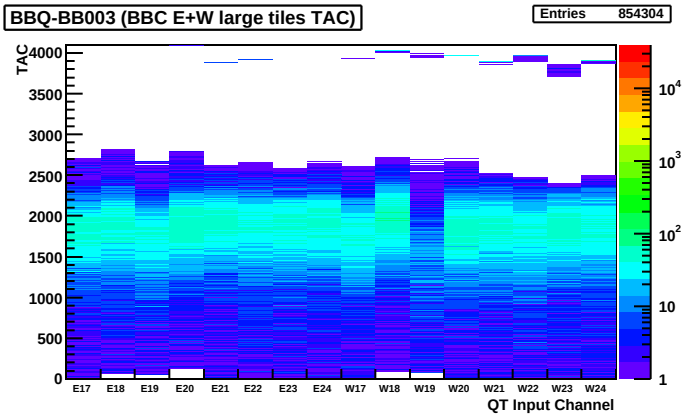

BBQ-BB001 (BBC east small tiles ADC)
Entries 854304
ADC
QT Input Channel

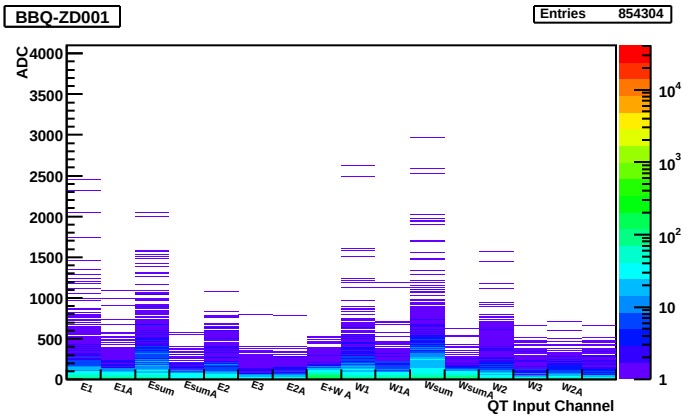

BBQ-BB001 (BBC east small tiles TAC)
Entries 854304
TAC
QT Input Channel

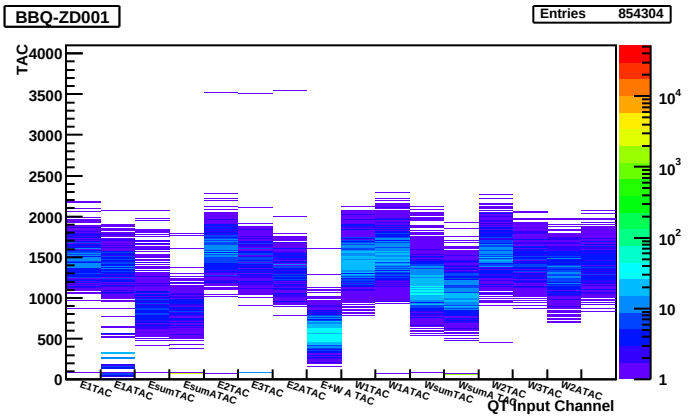

BBQ-BB002 (BBC west small tiles ADC)
Entries 854304
ADC
QT Input Channel

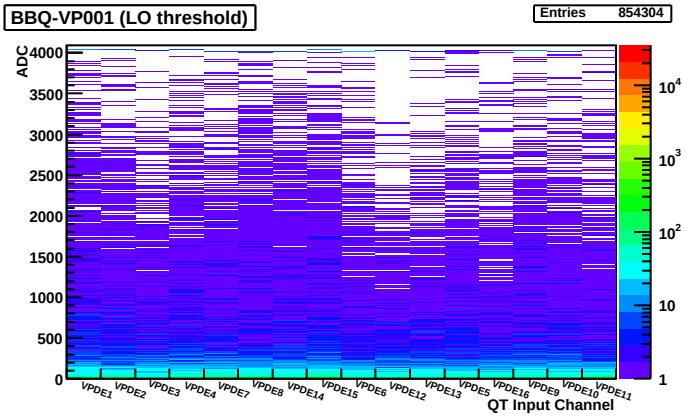

BBQ-BB002 (BBC west small tiles TAC)
Entries 854304
TAC
QT Input Channel

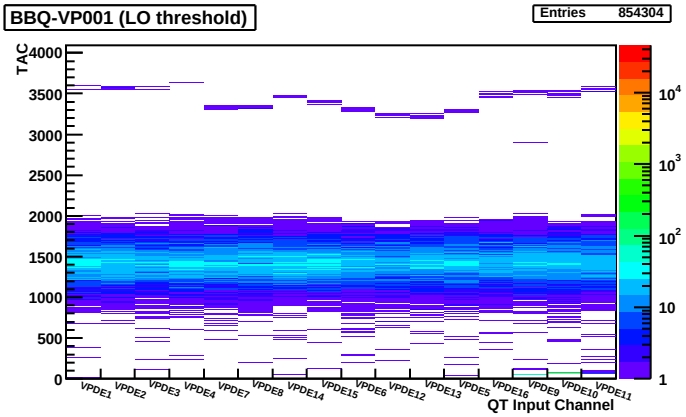

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 854304
ADC
QT Input Channel

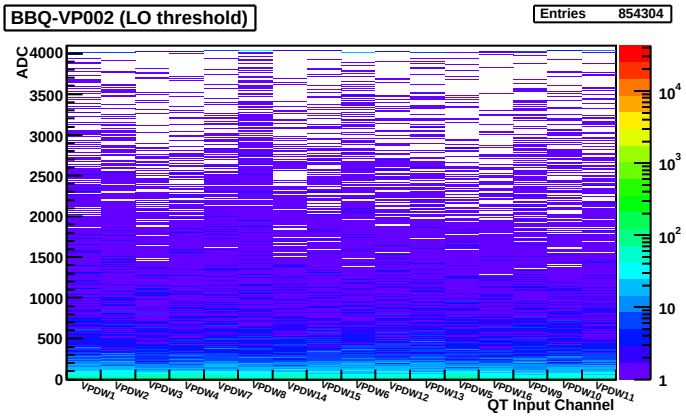

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 854304
TAC
QT Input Channel

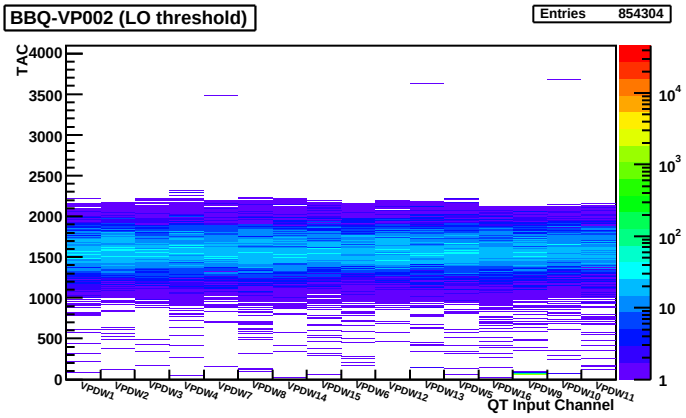

BBQ-ZD001
Entries 854304
ADC
QT Input Channel

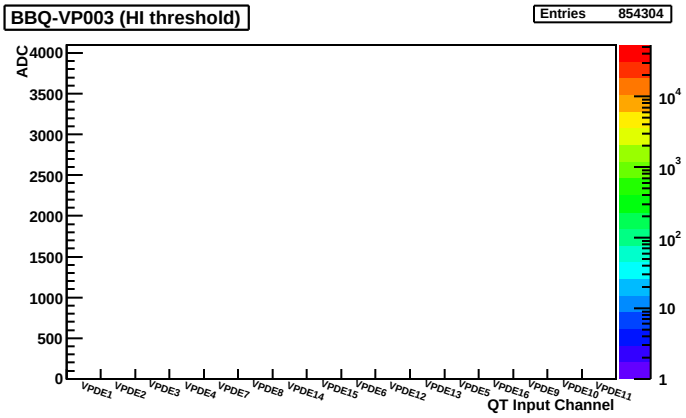

BBQ-ZD001
Entries 854304
TAC
QT Input Channel

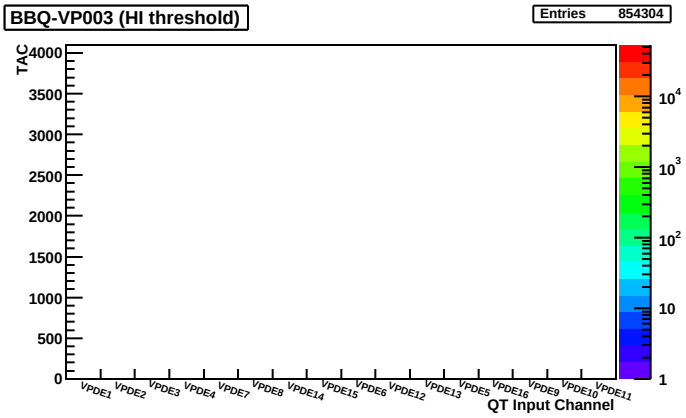

BBQ-VP001 (LO threshold)
Entries 854304
ADC
QT Input Channel

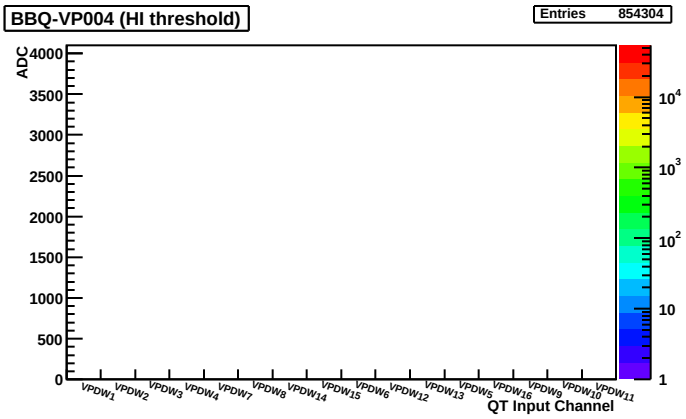

BBQ-VP001 (LO threshold)
Entries 854304
TAC
QT Input Channel

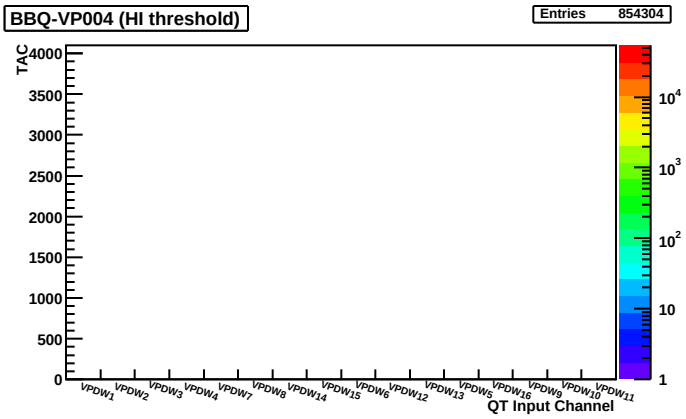

BBQ-VP002 (LO threshold)
Entries 854304
ADC
QT Input Channel

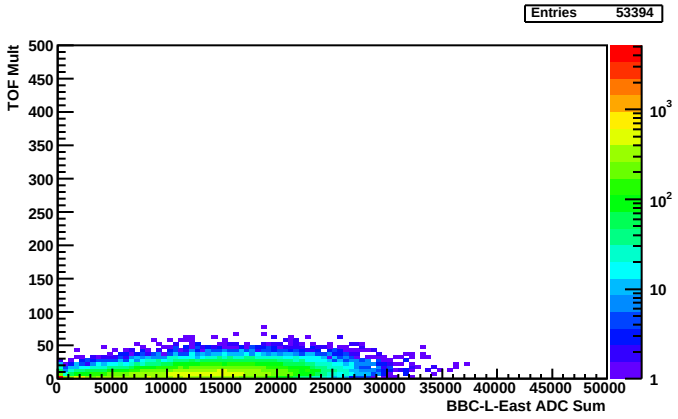

BBQ-VP002 (LO threshold)
Entries 854304
TAC
QT Input Channel

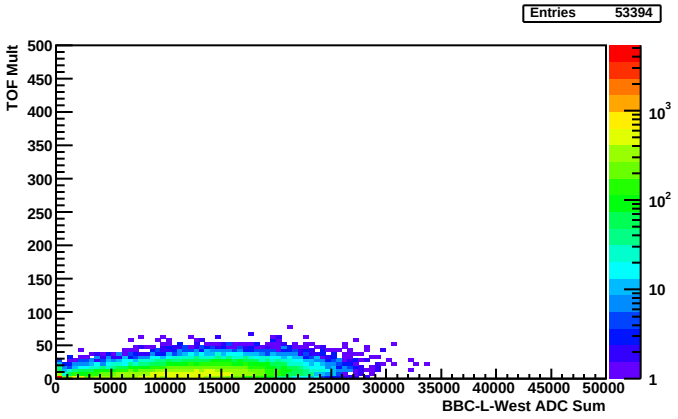

BBQ-VP003 (HI threshold)
Entries 854304
ADC
QT Input Channel

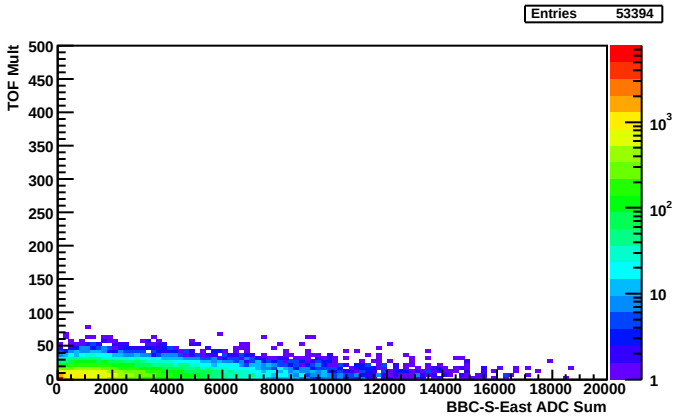

BBQ-VP003 (HI threshold)
Entries 854304
TAC
QT Input Channel

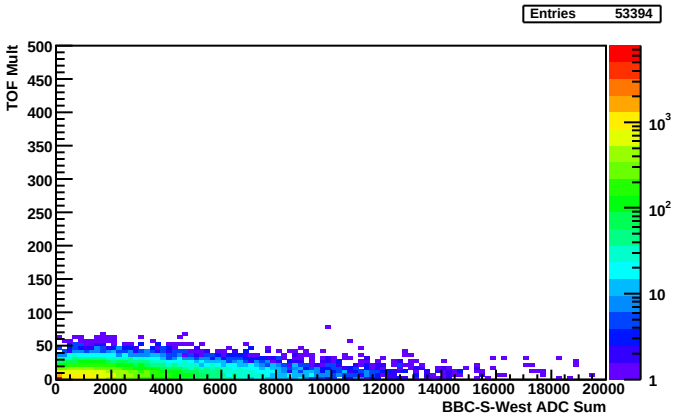

BBQ-VP004 (HI threshold)
Entries 854304
ADC
QT Input Channel

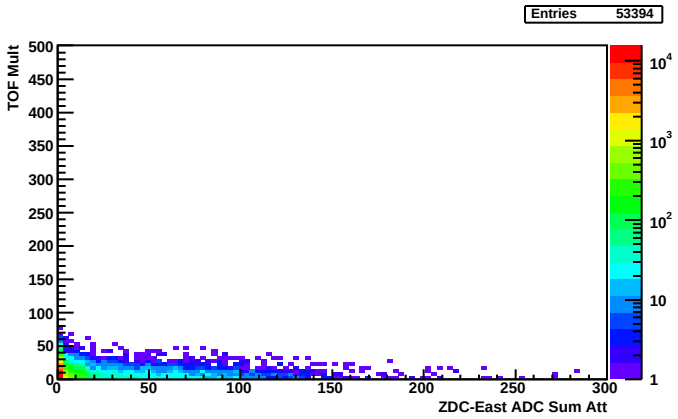

BBQ-VP004 (HI threshold)
Entries 854304
TAC
QT Input Channel

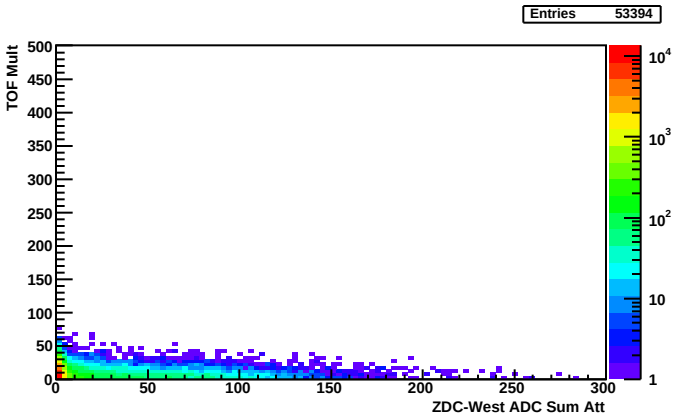

Entries 53394
TOF Mult
BBC-L-East ADC Sum

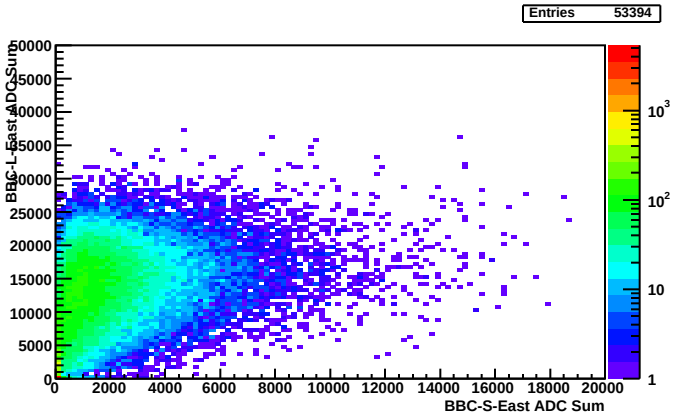

Entries 53394
TOF Mult
BBC-L-West ADC Sum

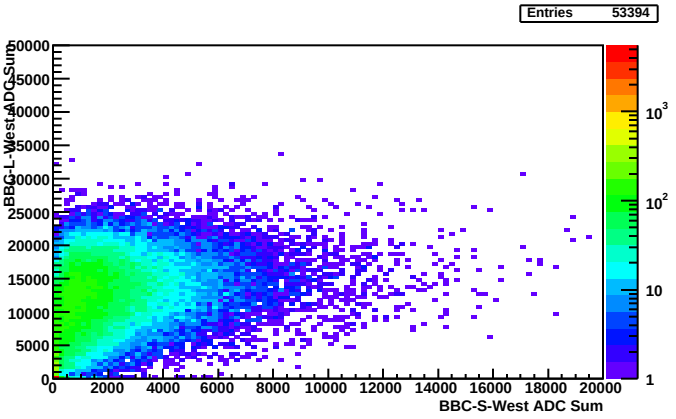

Entries 53394
TOF Mult
BBC-S-East ADC Sum

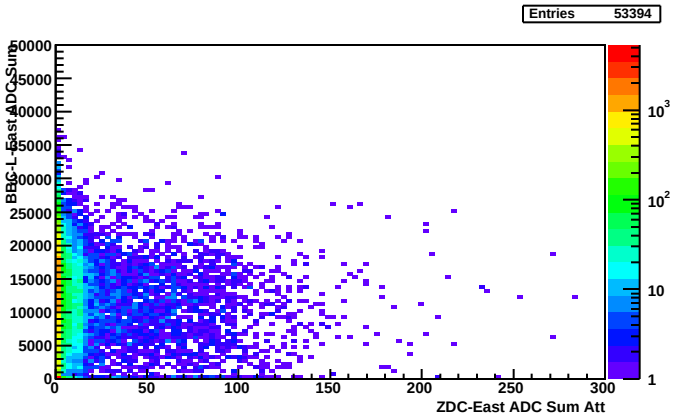

Entries 53394
TOF Mult
BBC-S-West ADC Sum

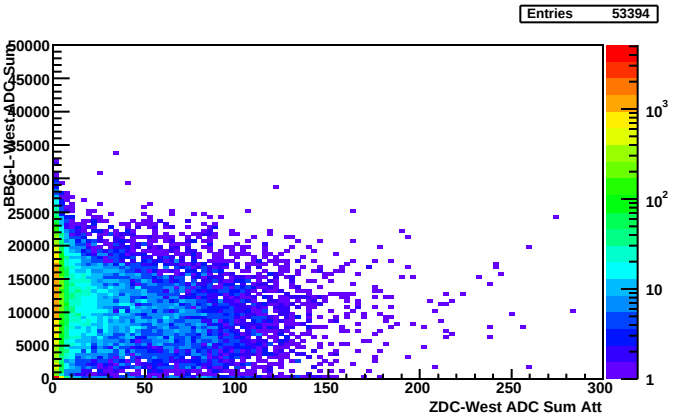

Entries 53394
TOF Mult
ZDC-East ADC Sum Att

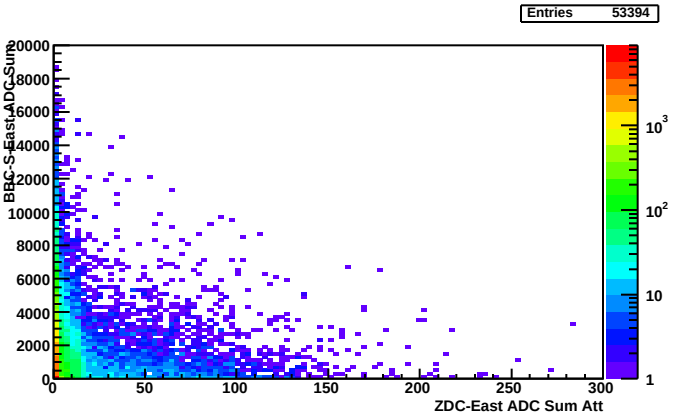

Entries 53394
TOF Mult
ZDC-West ADC Sum Att

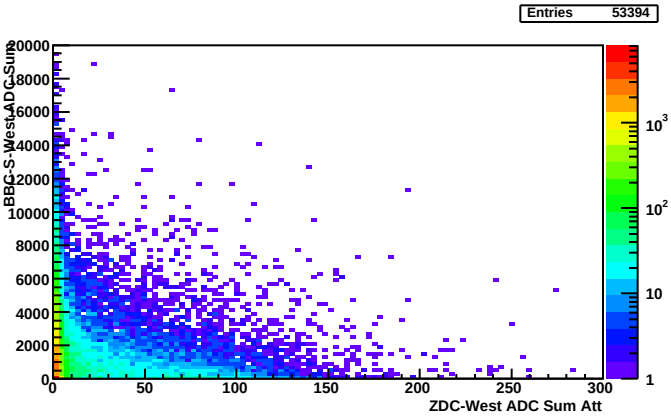

Entries 53394
BBC-L-East ADC Sum
BBC-S-East ADC Sum

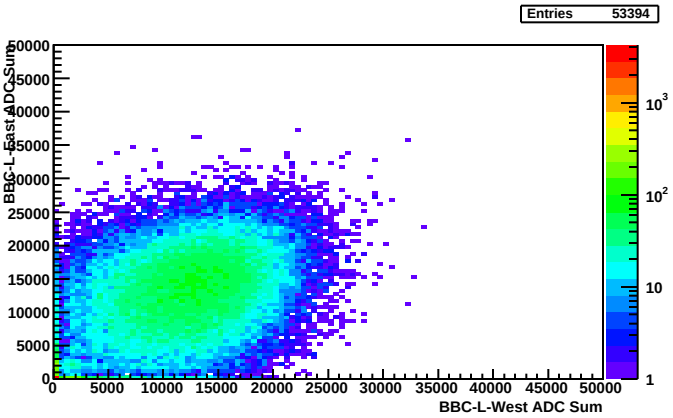

Entries 53394
BBC-L-West ADC Sum
BBC-S-West ADC Sum

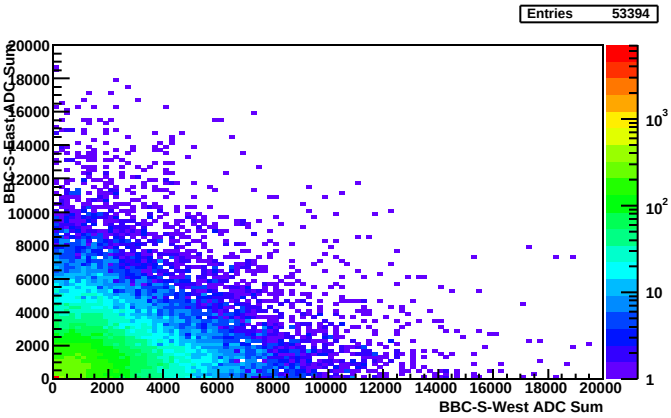

Entries 53394
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

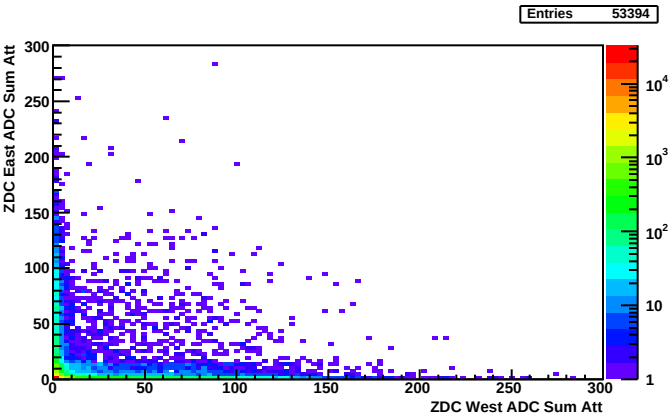

Entries 53394
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

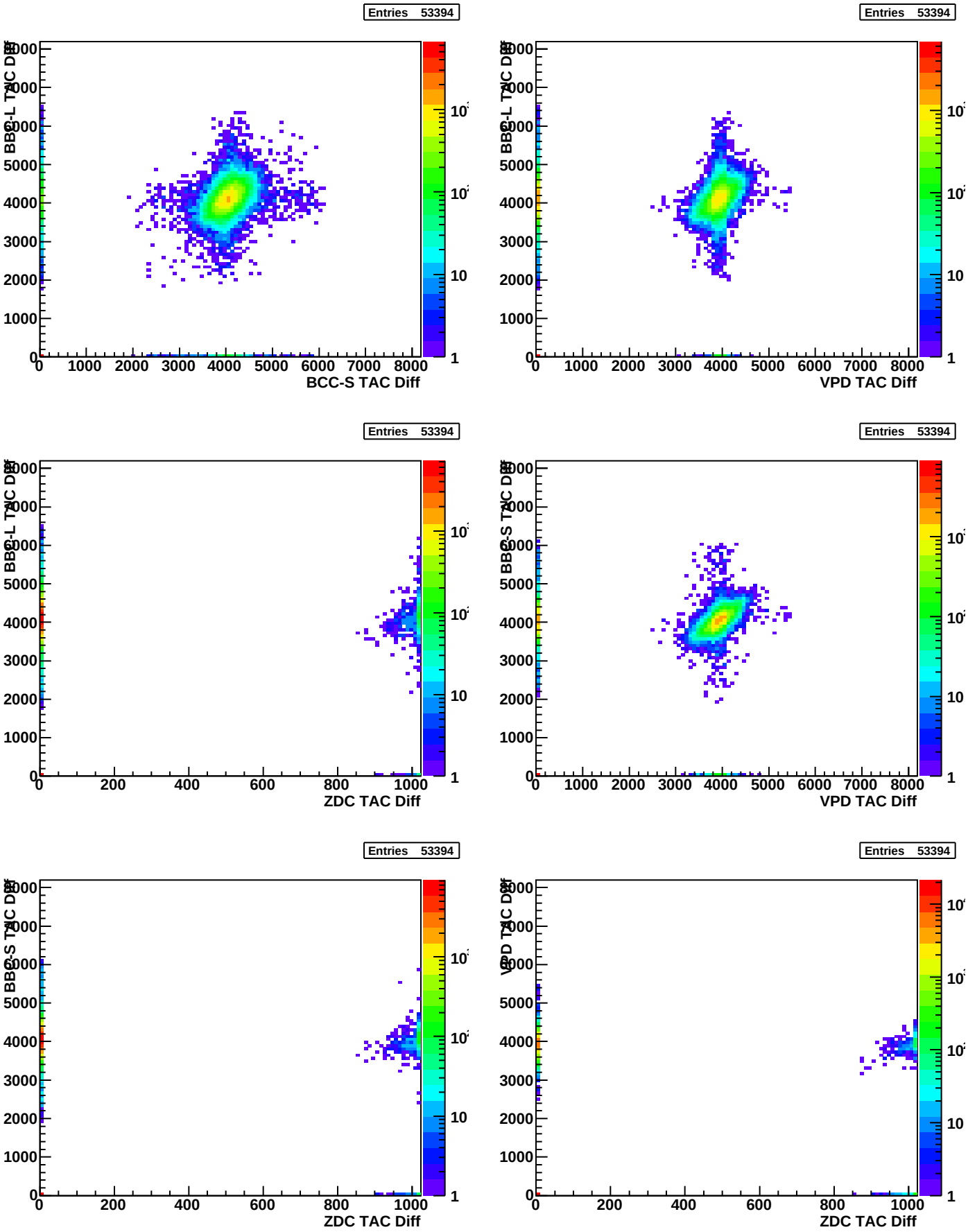

Entries 53394
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

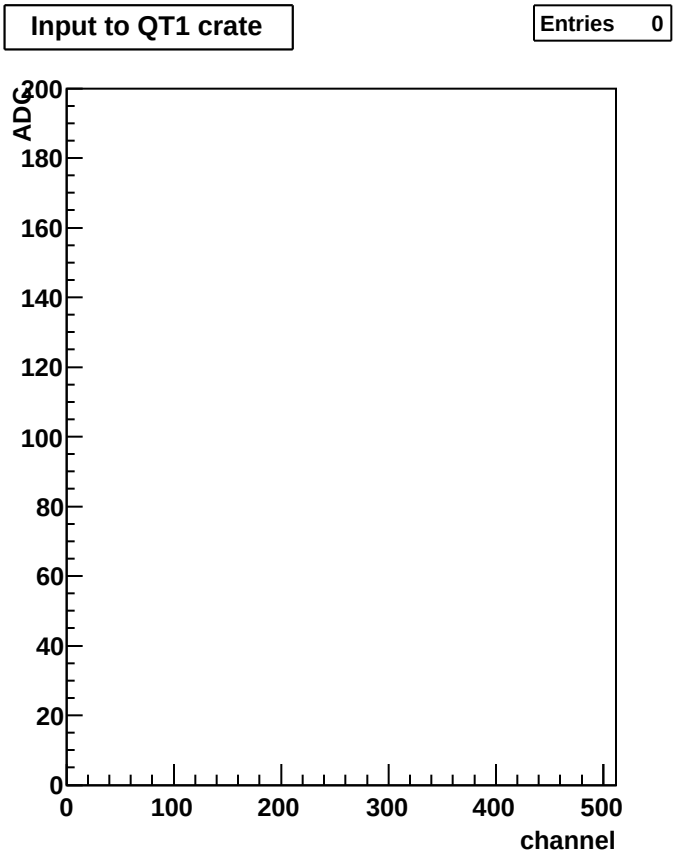

Entries 53394
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

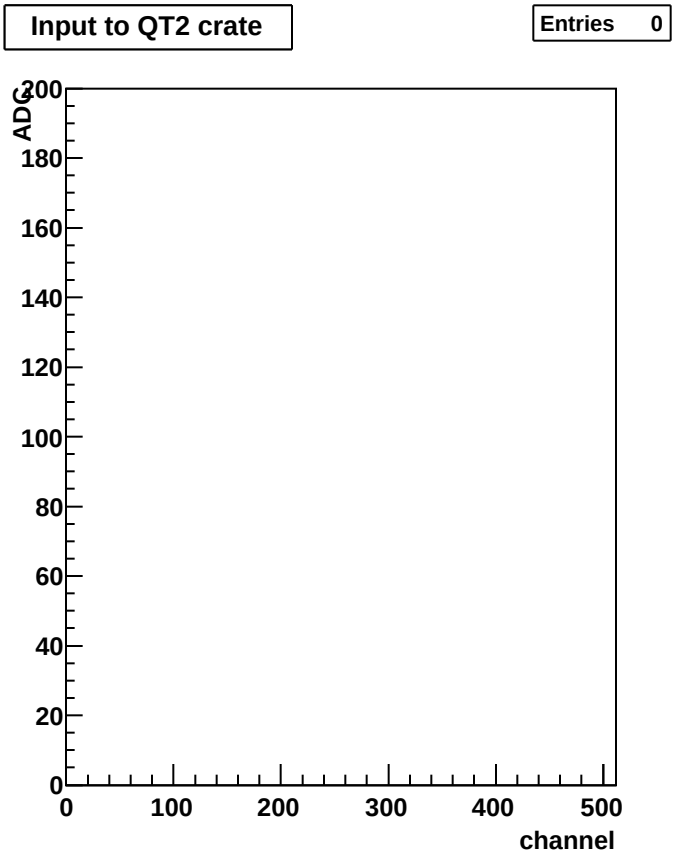

Entries 53394
BBC-L-East ADC Sum
BBC-L-West ADC Sum

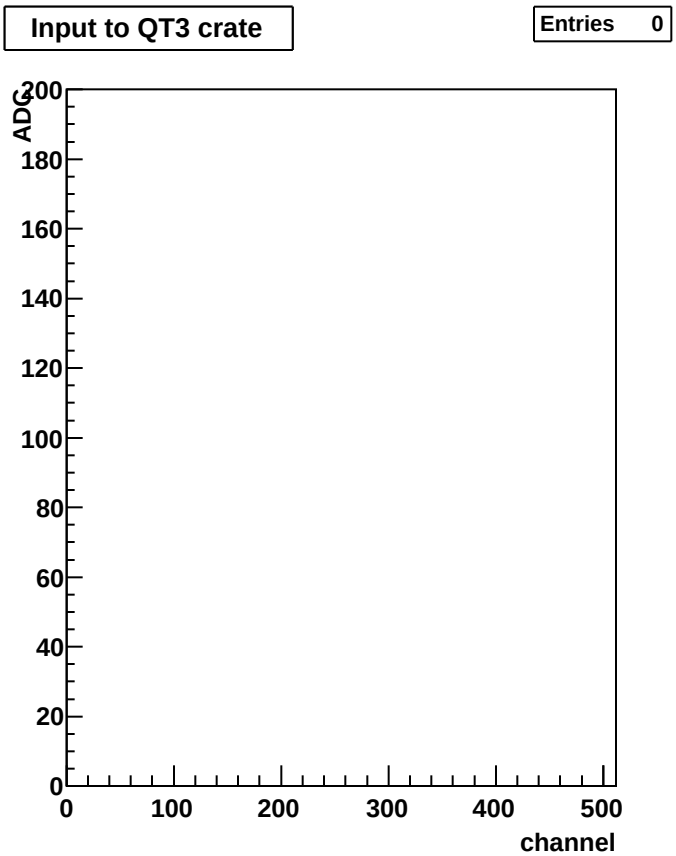

Entries 53394
BBC-S-East ADC Sum
BBC-S-West ADC Sum

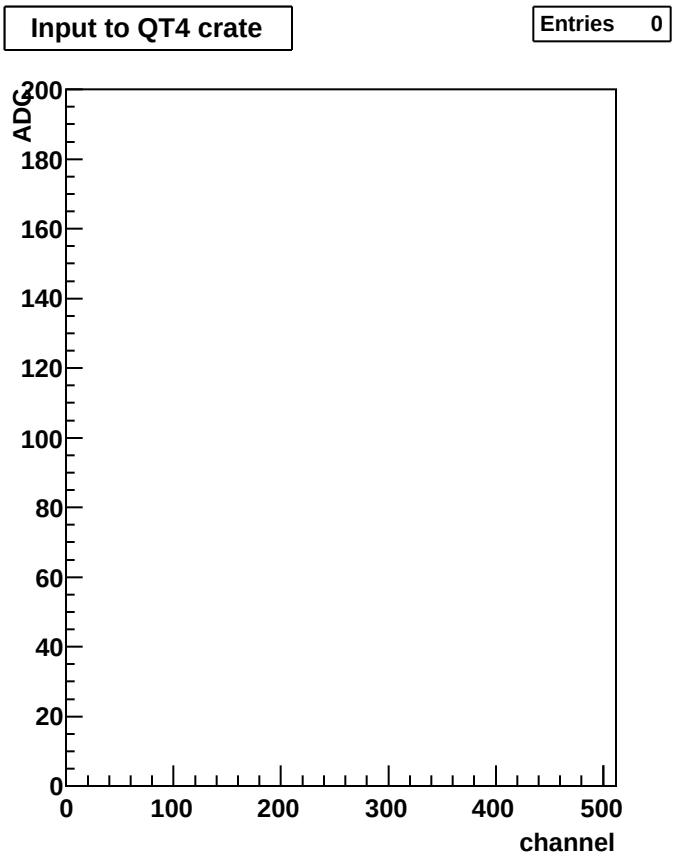

Entries 53394
ZDC East ADC Sum Att
ZDC West ADC Sum Att

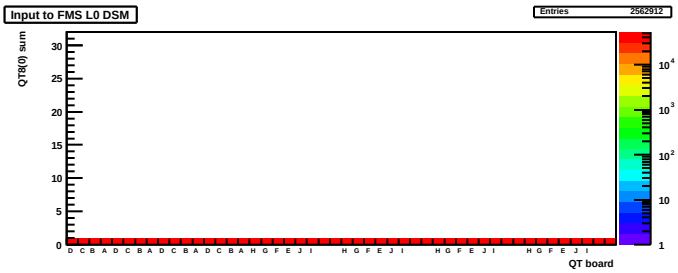

Entries 53394
BBC-L TAC Diff
BCC-S TAC Diff
Entries 53394
BBC-L TAC Diff
VPD TAC Diff
Entries 53394
BBC-L TAC Diff
ZDC TAC Diff
Entries 53394
BBC-S TAC Diff
VPD TAC Diff
Entries 53394
BBC-S TAC Diff
ZDC TAC Diff
Entries 53394
VPD TAC Diff
ZDC TAC Diff

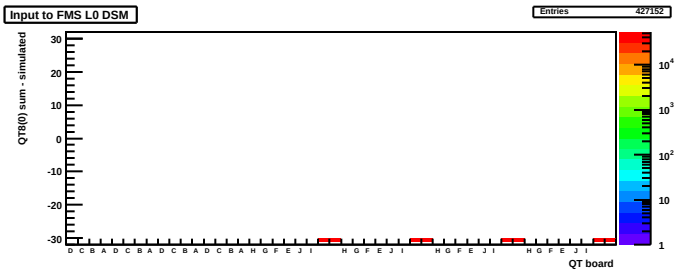

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

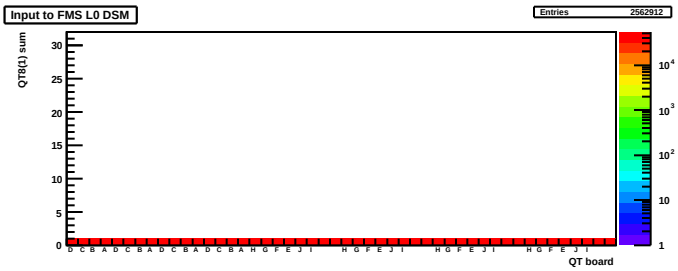

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

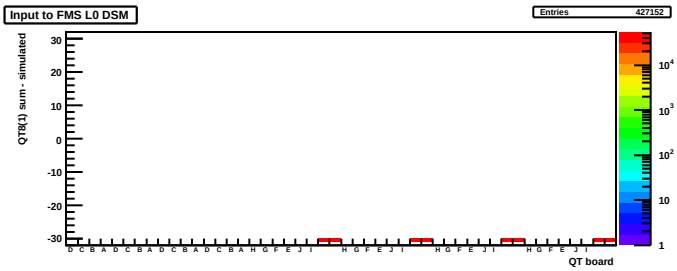

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

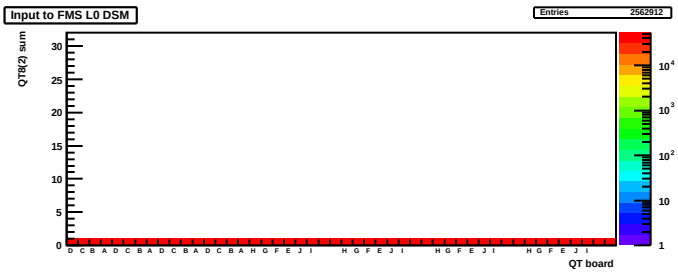

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

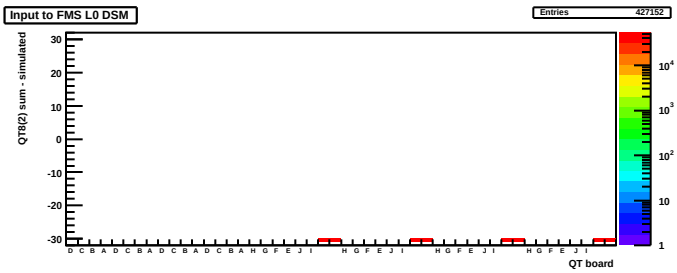

Input to FMS L0 DSM
Entries 2562912
QT8(0) sum
QT board

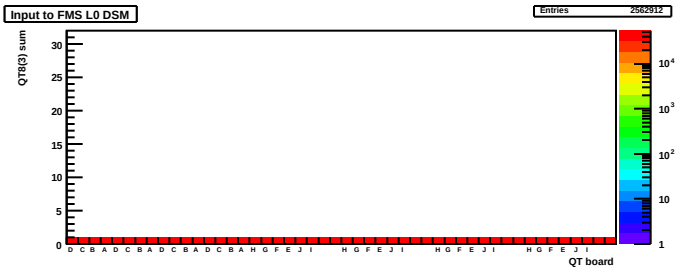

Input to FMS L0 DSM
Entries 427152
QT8(0) sum - simulated
QT board

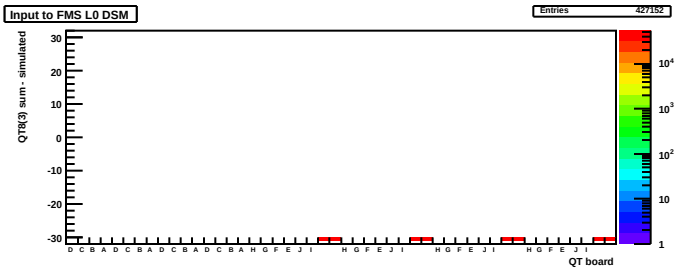

Input to FMS L0 DSM
Entries 2562912
QT8(1) sum
QT board

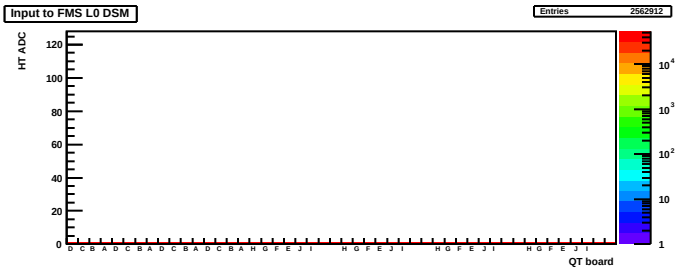

Input to FMS L0 DSM
Entries 427152
QT8(1) sum - simulated
QT board

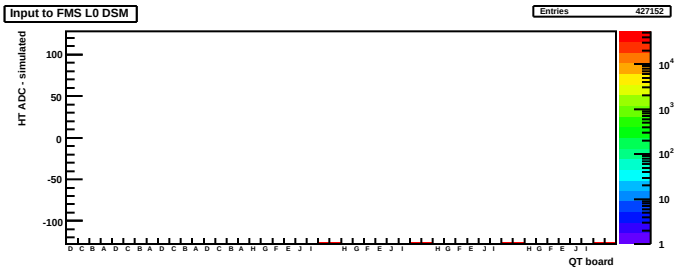

Input to FMS L0 DSM
Entries 2562912
QT8(2) sum
QT board

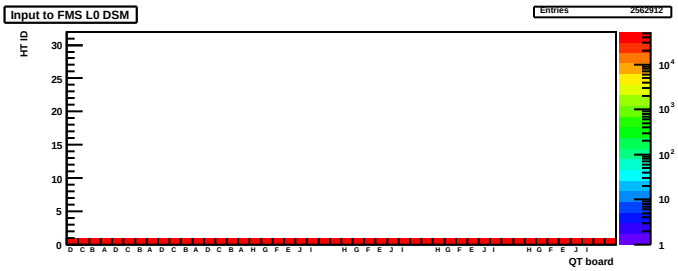

Input to FMS L0 DSM
Entries 427152
QT8(2) sum - simulated
QT board

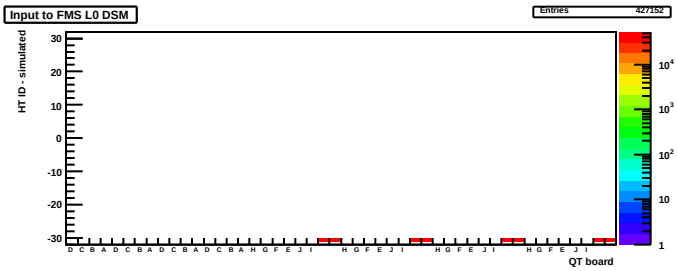

Input to FMS L0 DSM
Entries 2562912
QT8(3) sum
QT board

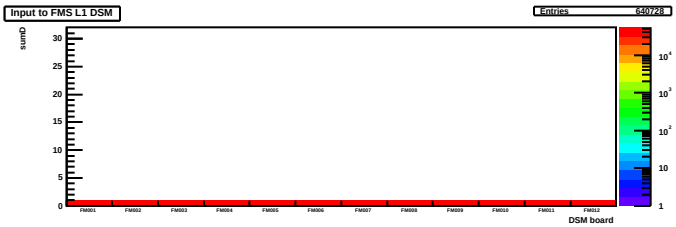

Input to FMS L0 DSM
Entries 427152
QT8(3) sum - simulated
QT board

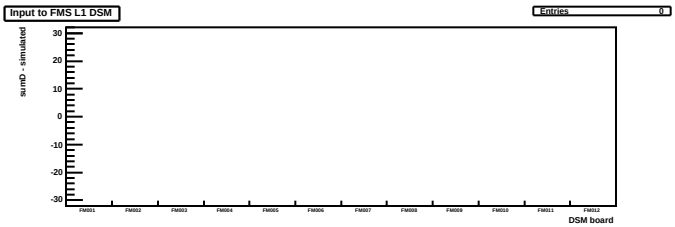

Input to FMS L0 DSM
Entries 2562912
HT ADC
QT board

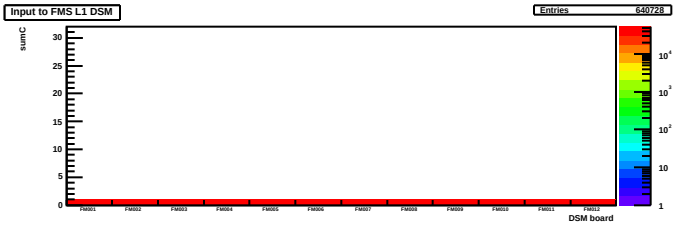

Input to FMS L0 DSM
Entries 427152
HT ADC - simulated
QT board

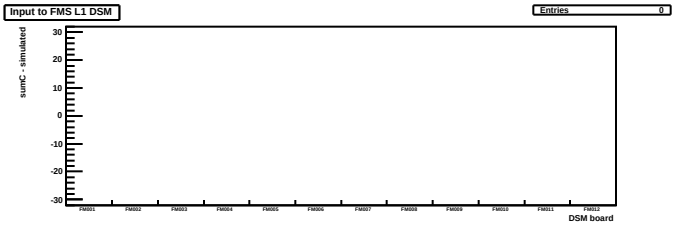

Input to FMS L0 DSM
Entries 2562912
HT ID
QT board

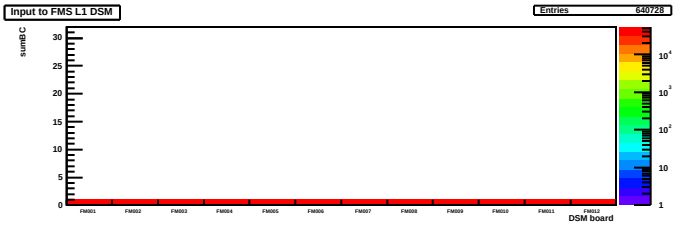

Input to FMS L0 DSM
Entries 427152
HT ID - simulated
QT board

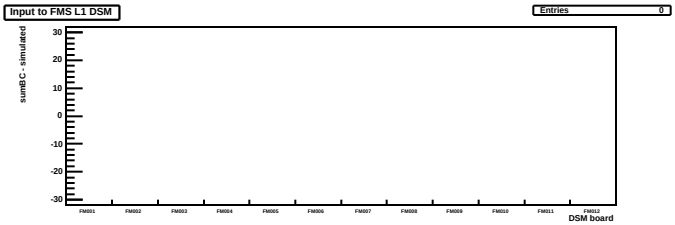

Input to FMS L1 DSM
Entries 640728
sumD
DSM board

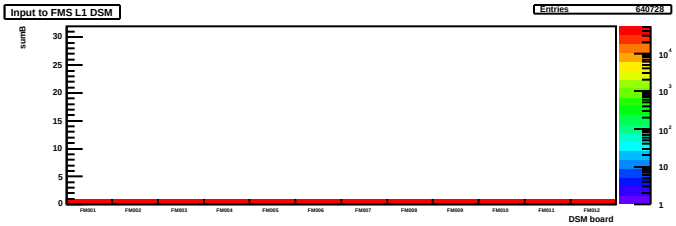

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

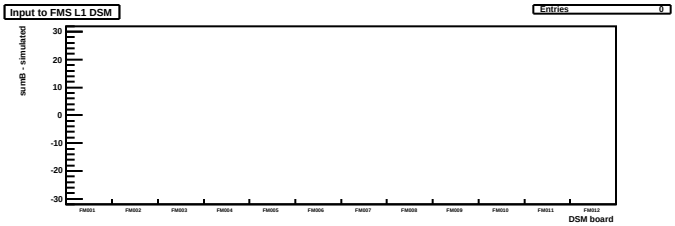

Input to FMS L1 DSM
Entries 640728
sumC
DSM board

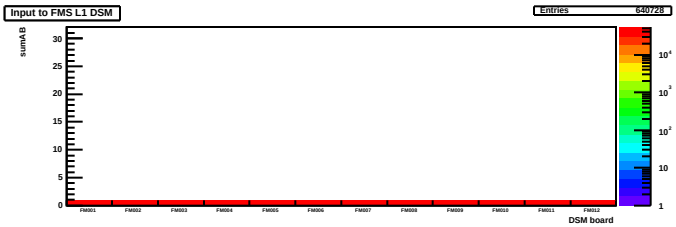

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

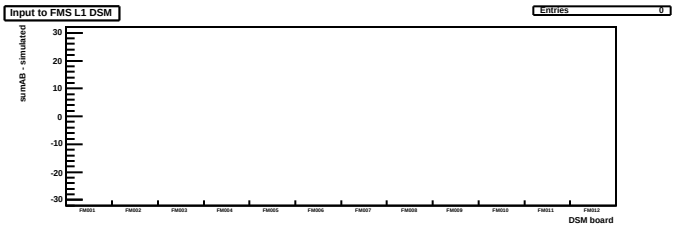

Input to FMS L1 DSM
Entries 640728
sumBC
DSM board

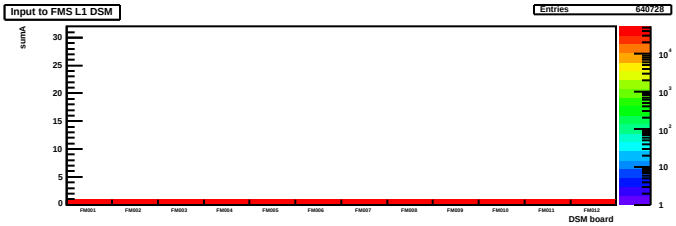

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

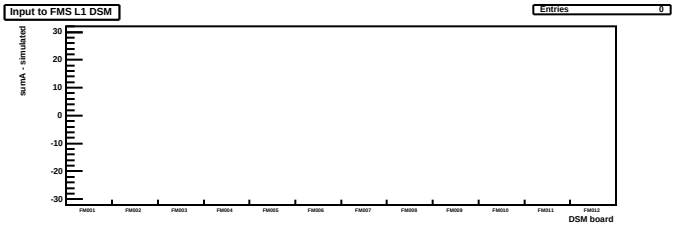

Input to FMS L1 DSM
Entries 640728
sumB
DSM board

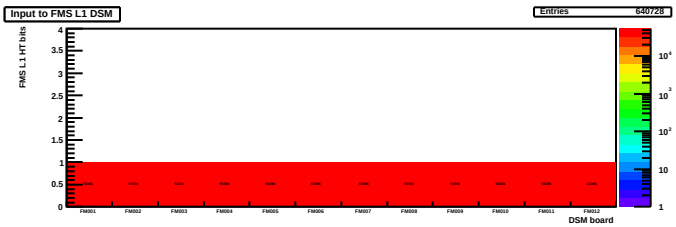

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

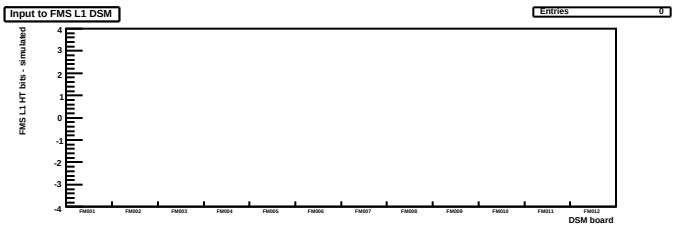

Input to FMS L1 DSM
Entries 640728
sumAB
DSM board

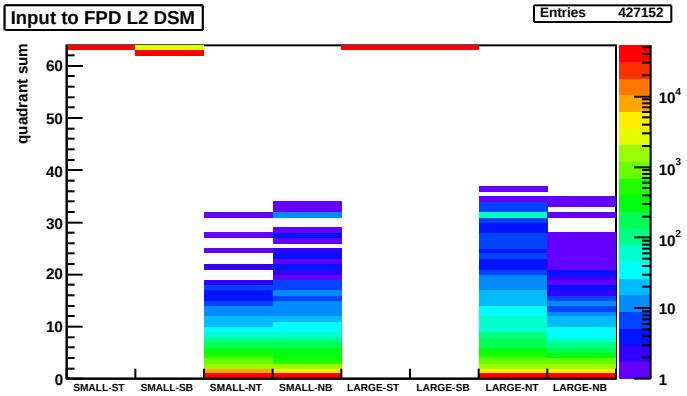

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

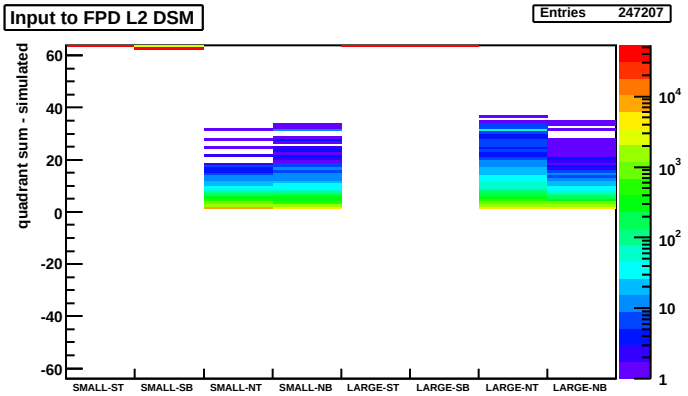

Input to FMS L1 DSM
Entries 640728
sumA
DSM board

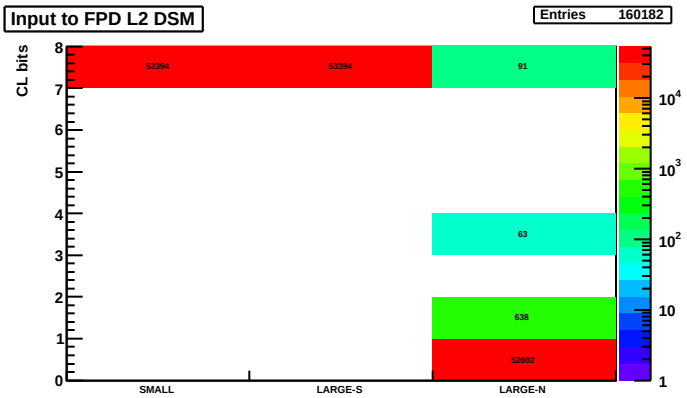

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

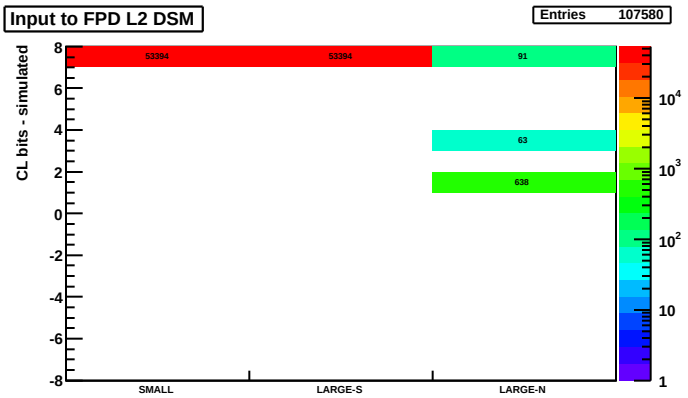

Input to FMS L1 DSM
Entries 640728
FMS L1 HT bits
DSM board

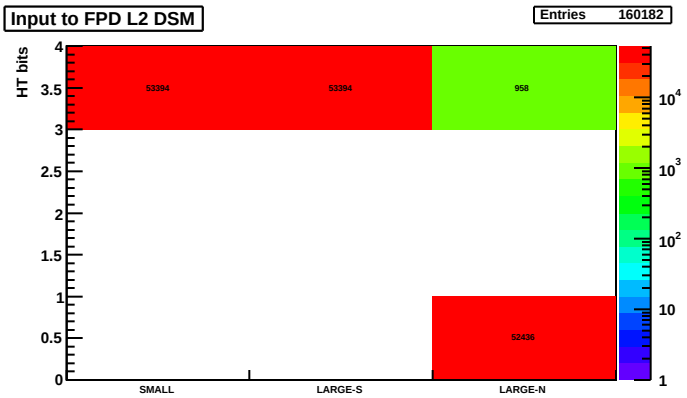

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

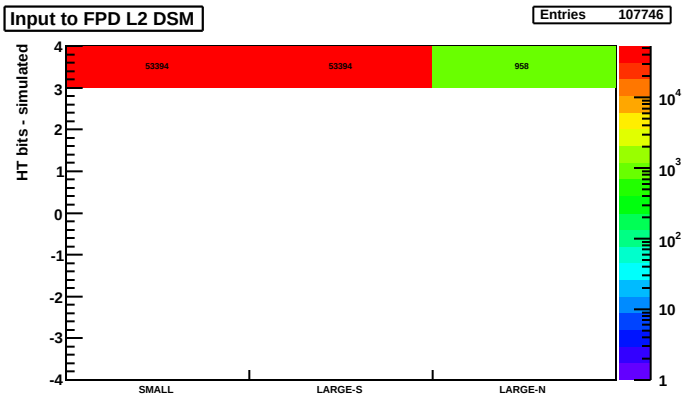

Input to FPD L2 DSM
Entries 427152
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

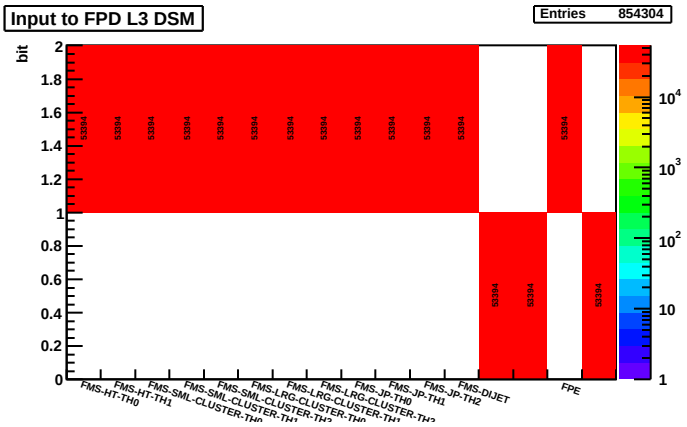

Input to FPD L2 DSM
Entries 247207
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

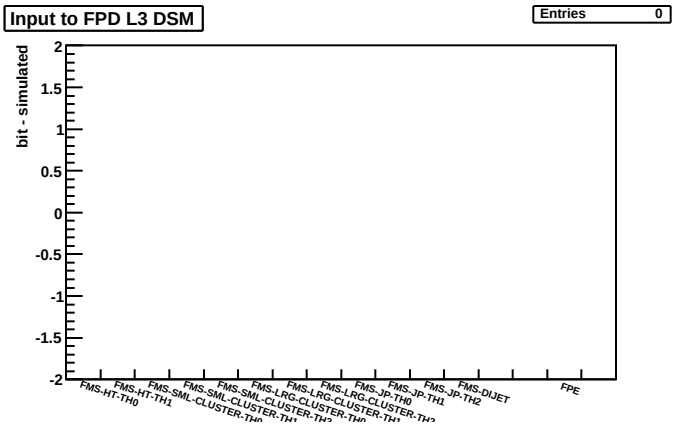

Input to FPD L2 DSM
Entries 160182
CL bits
53394
53394
91
63
638
52602
SMALL
LARGE-S
LARGE-N

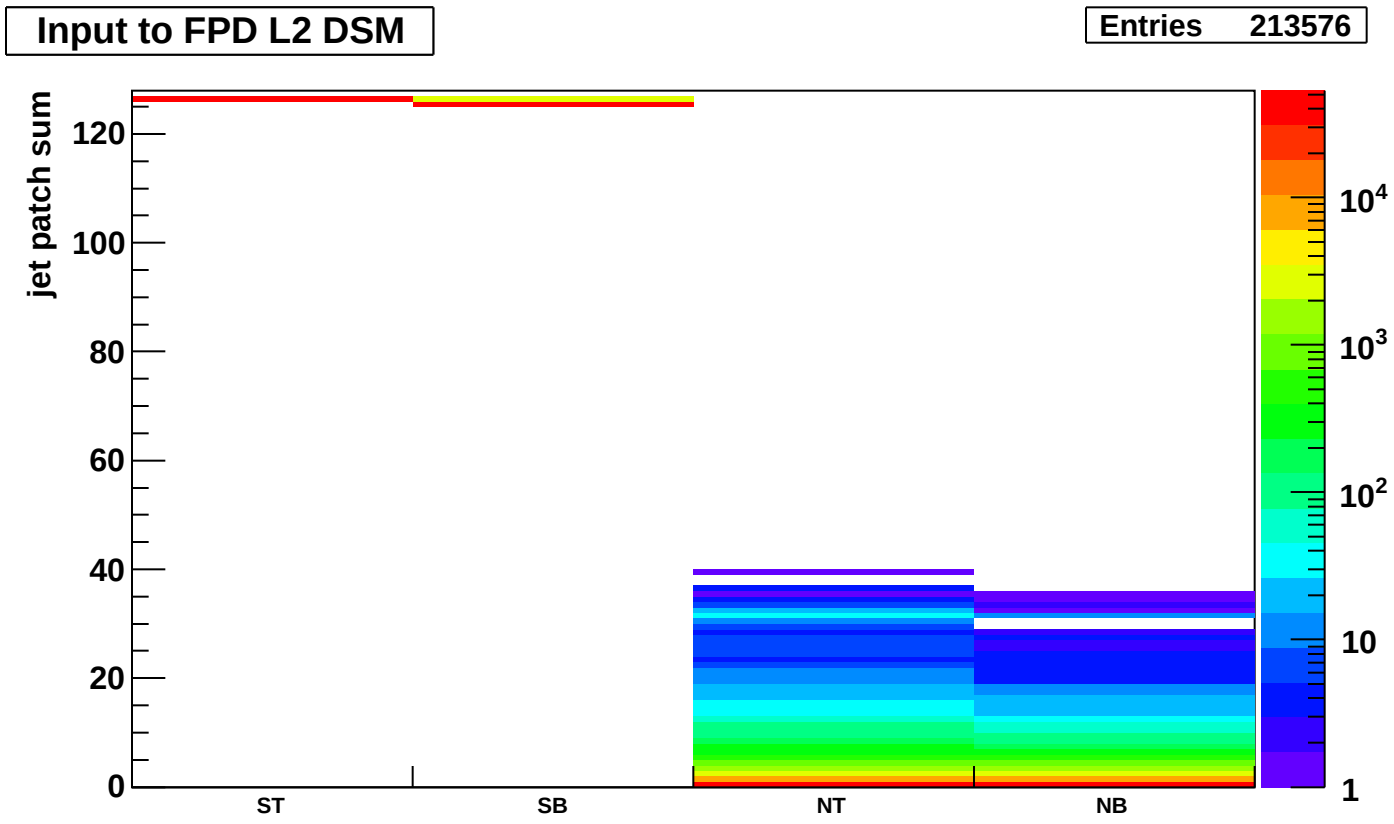

Input to FPD L2 DSM
Entries 107580
CL bits - simulated
53394
53394
91
63
638
SMALL
LARGE-S
LARGE-N

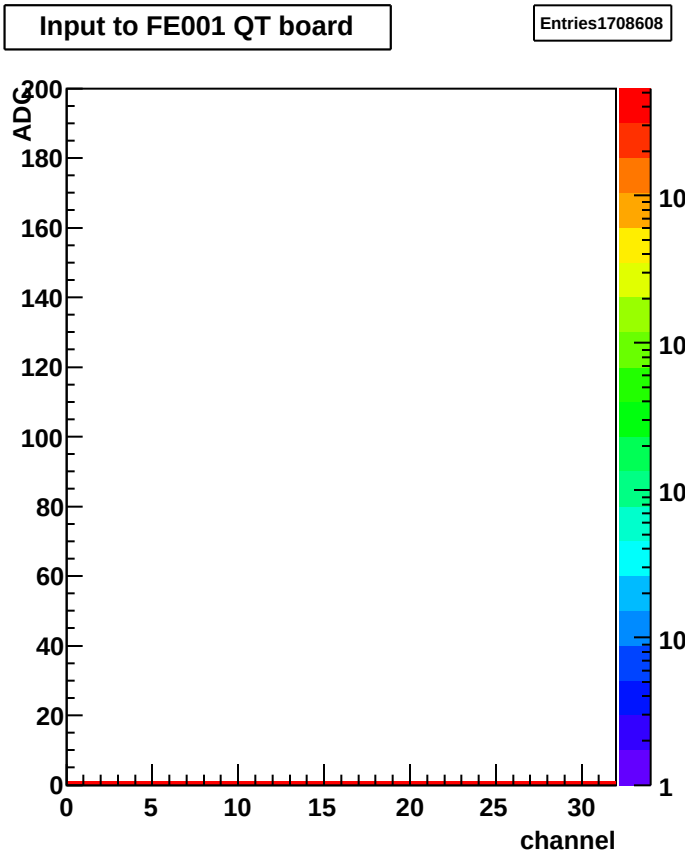

Input to FPD L2 DSM
Entries 160182
HT bits
53394
53394
958
52436
SMALL
LARGE-S
LARGE-N

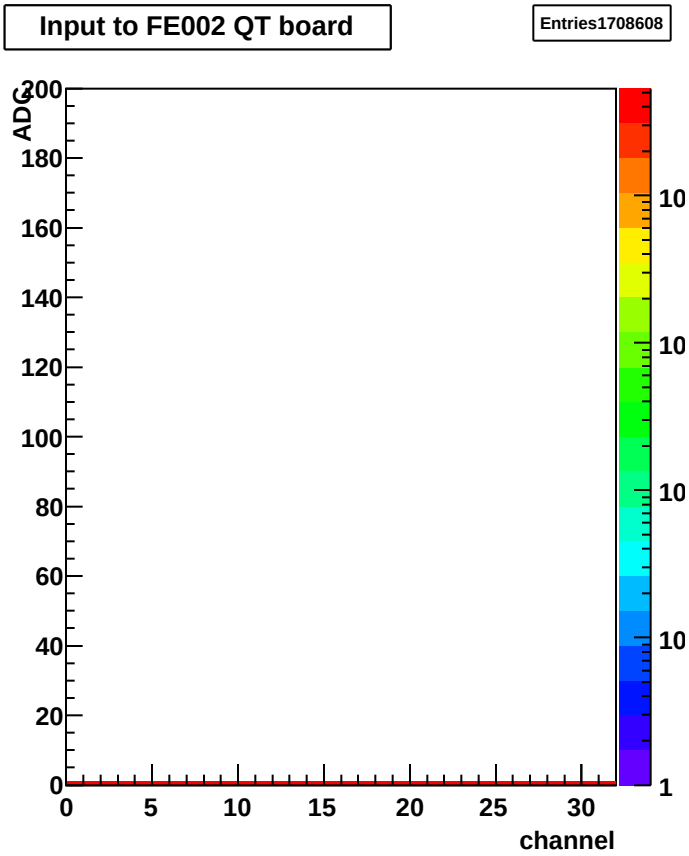

Input to FPD L2 DSM
Entries 107746
HT bits - simulated
53394
53394
958
SMALL
LARGE-S
LARGE-N

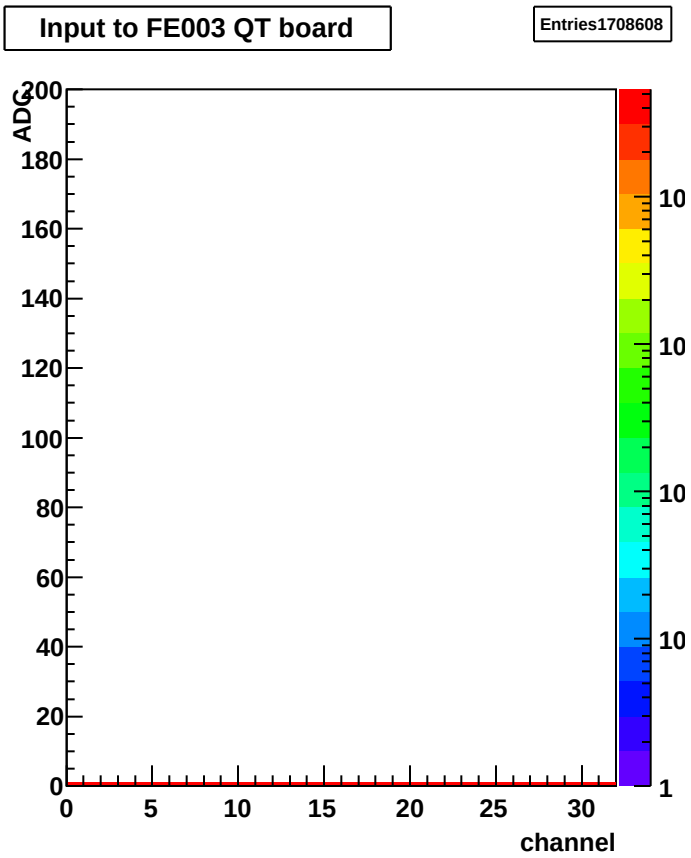

Input to FPD L3 DSM
Entries 854304
bit
53394
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

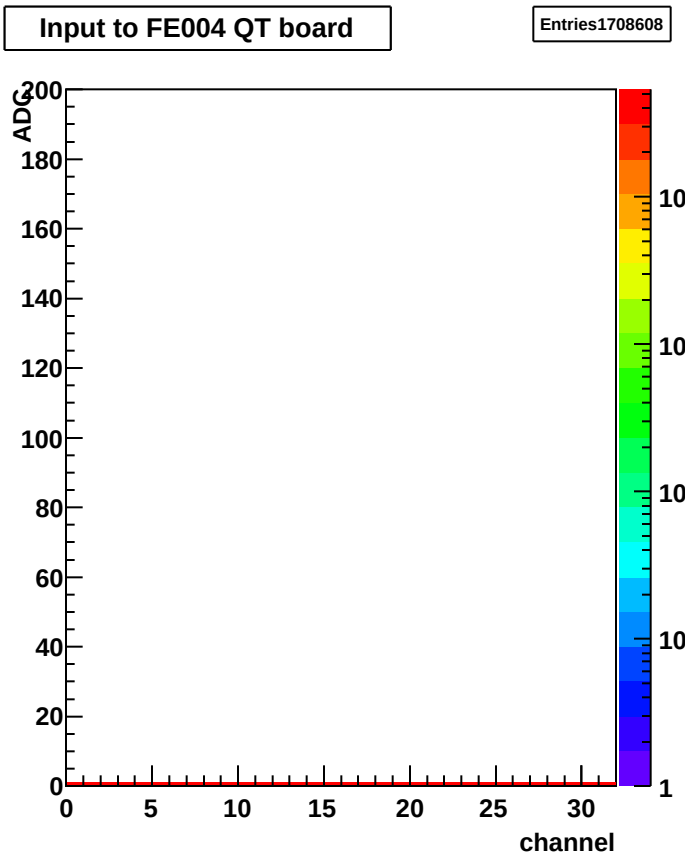

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

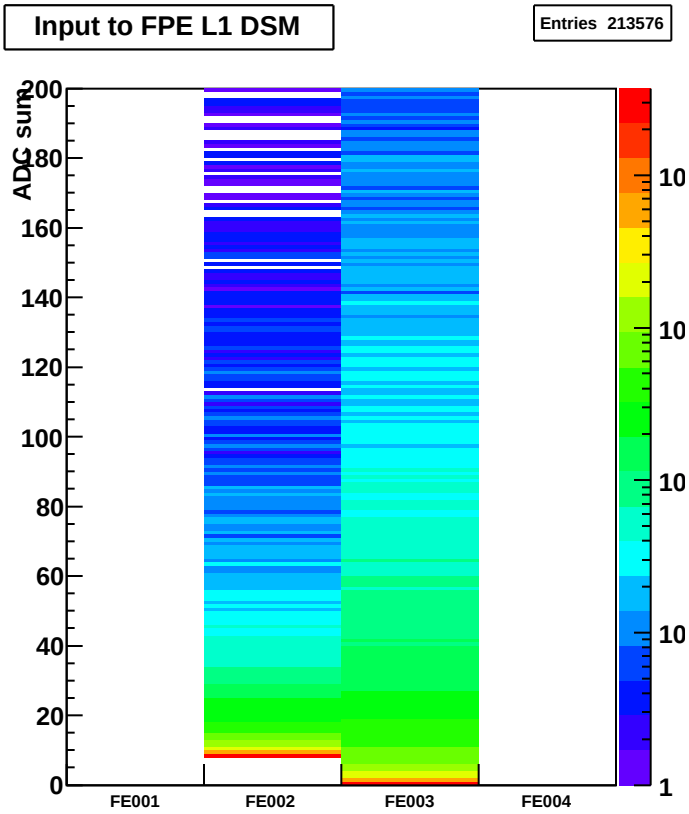

Input to FPD L2 DSM
Entries 213576
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

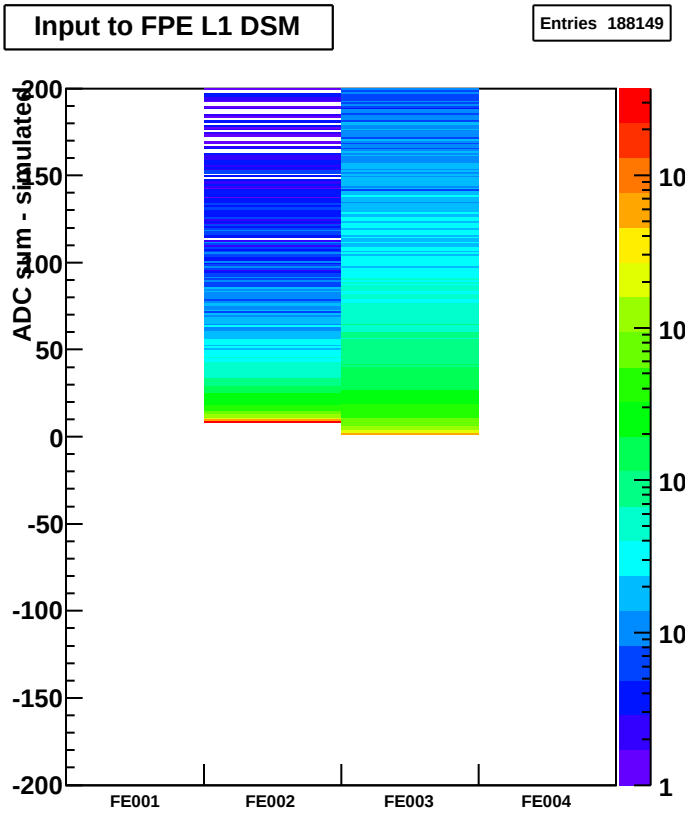

Input to FE001 QT board
Entries1708608
ADC
channel

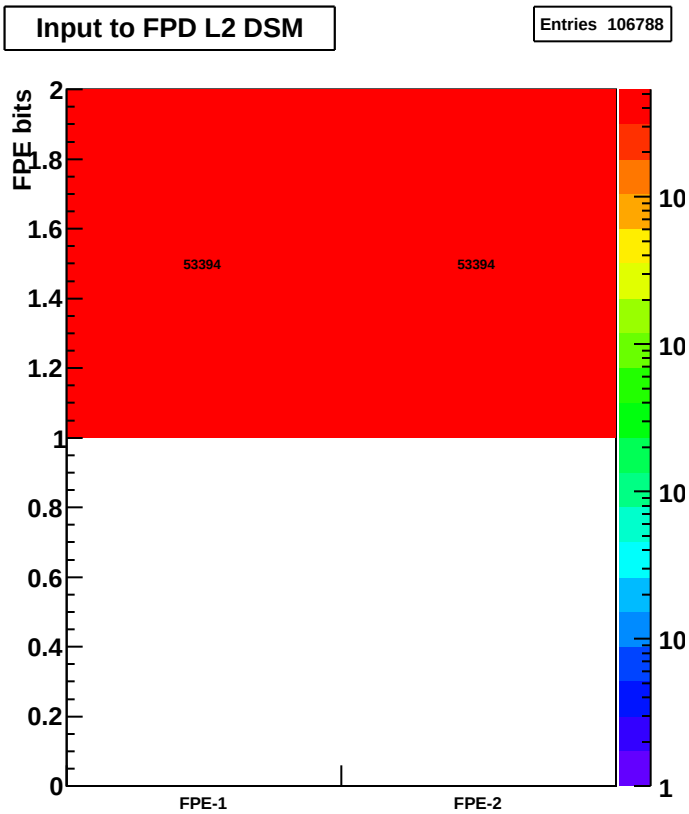

Input to FE002 QT board
Entries1708608
ADC
channel

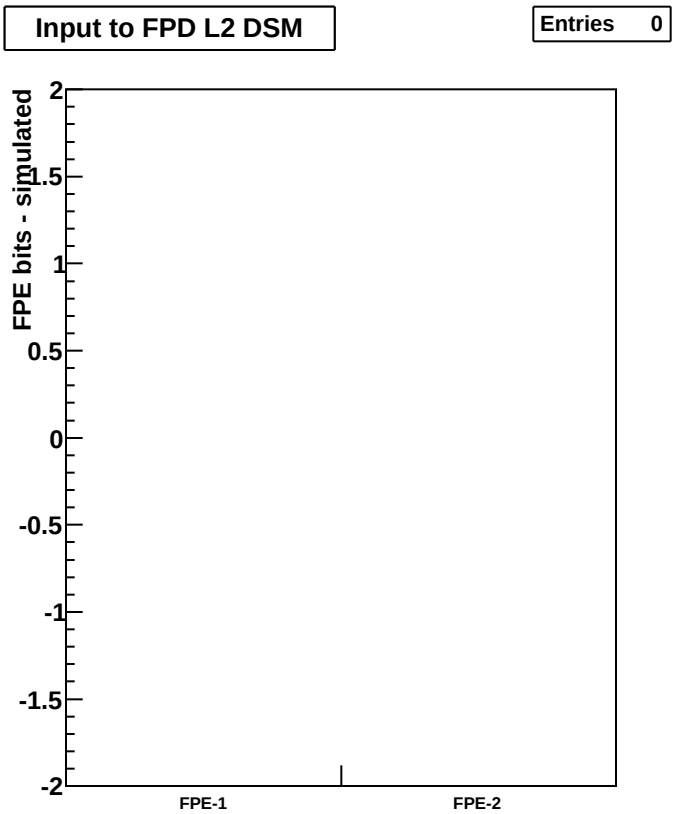

Input to FE003 QT board
Entries1708608
ADC
channel


Input to FE004 QT board
Entries1708608
ADC
channel


Input to FPE L1 DSM
Entries 213576
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 188149
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 106788
FPE bits
53394
53394
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2