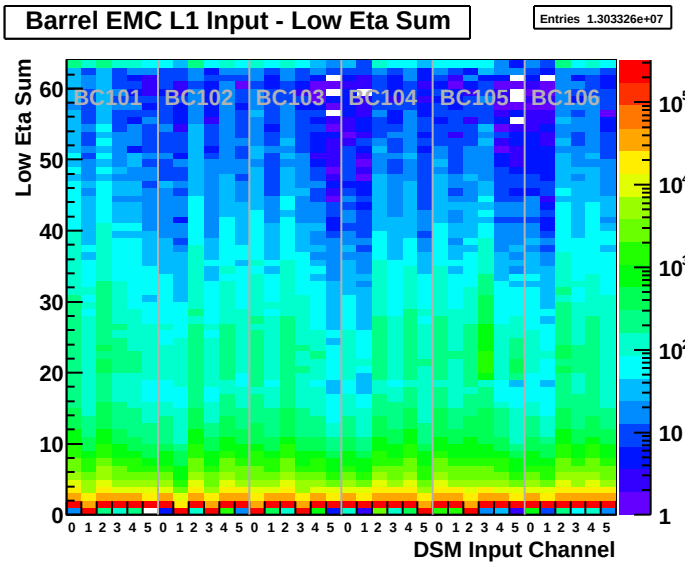
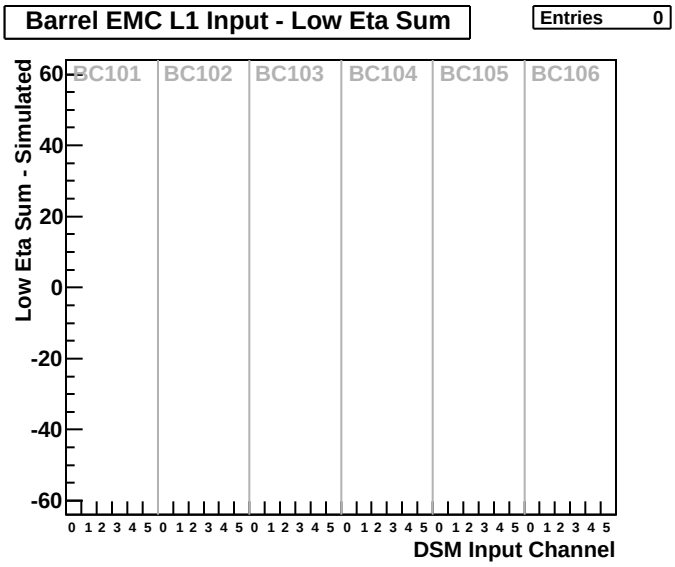
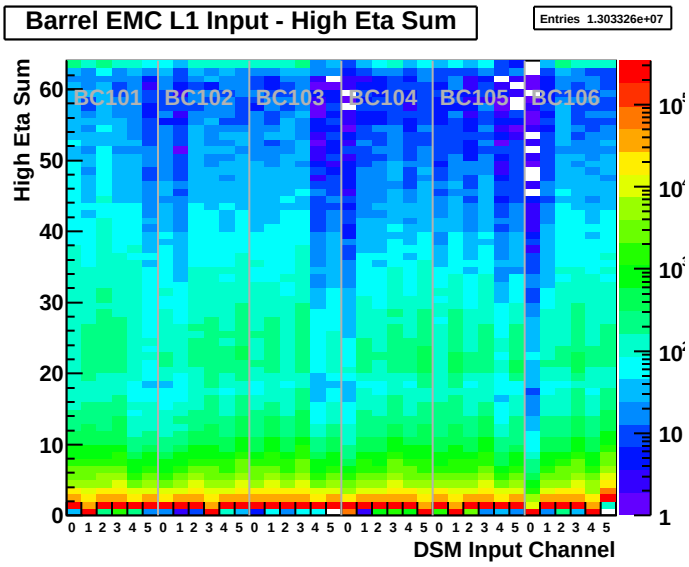
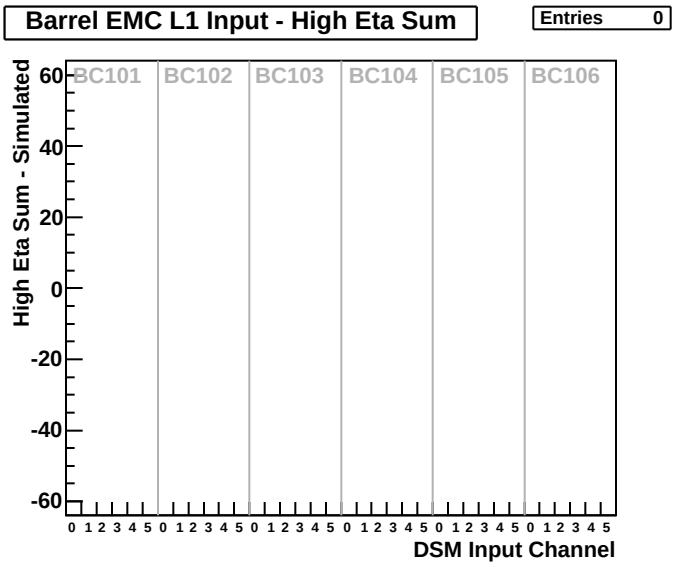
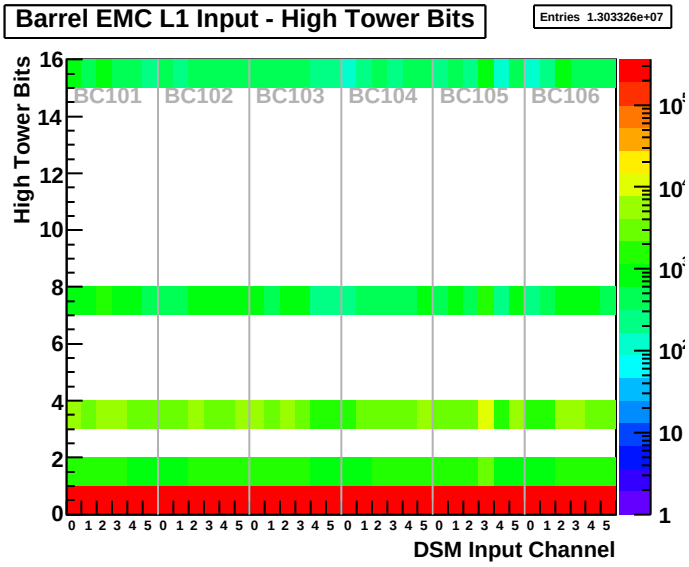
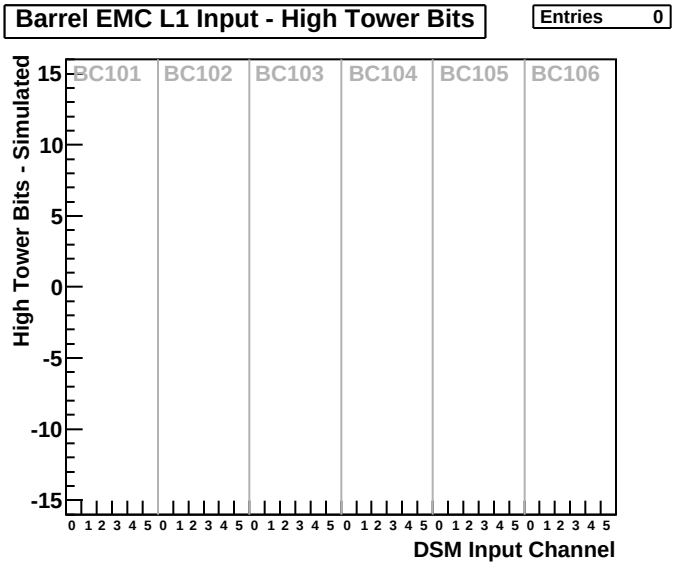

Barrel EMC L1 Input - Low Eta Sum
Entries 1.303326e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

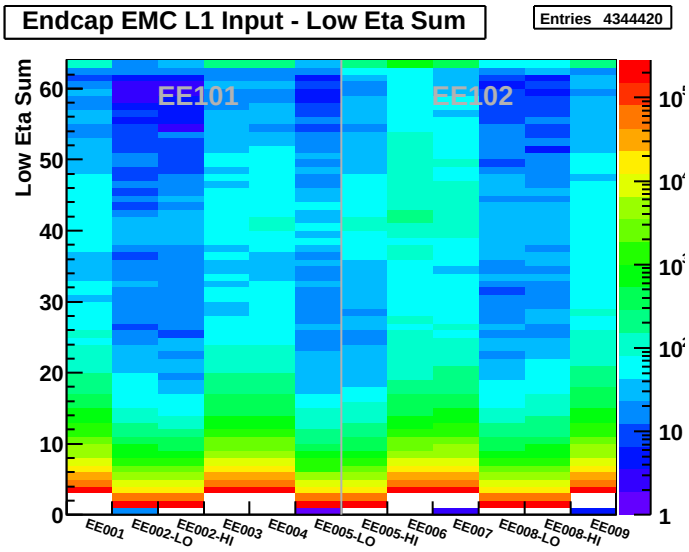

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

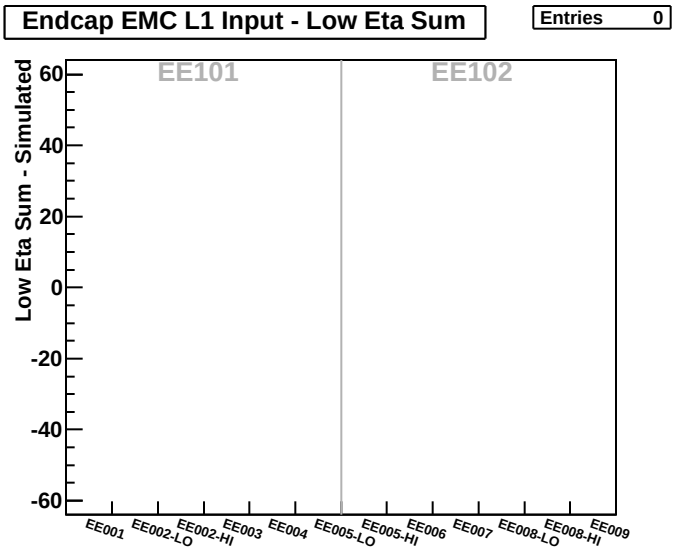

Barrel EMC L1 Input - High Eta Sum
Entries 1.303326e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

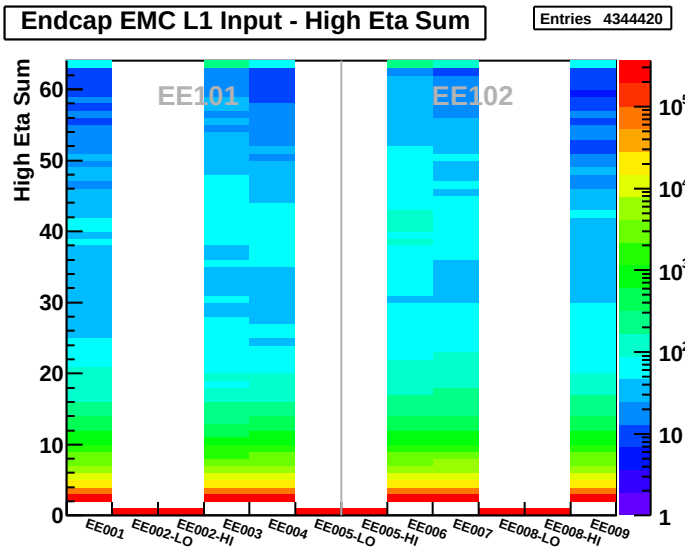

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

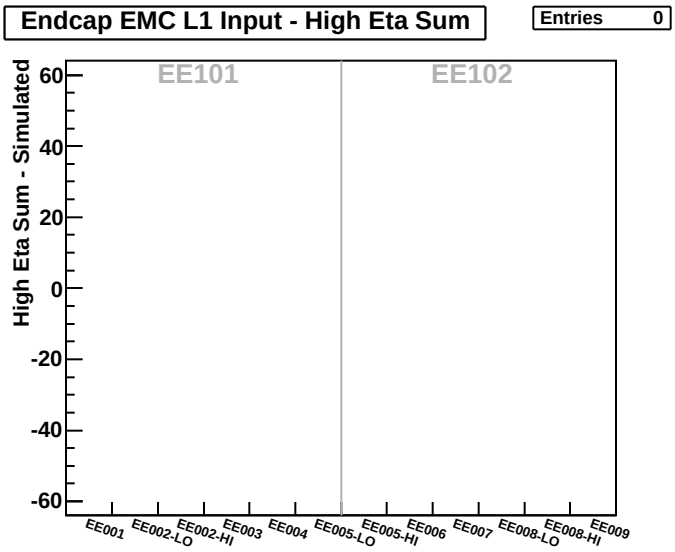

Barrel EMC L1 Input - High Tower Bits
Entries 1.303326e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

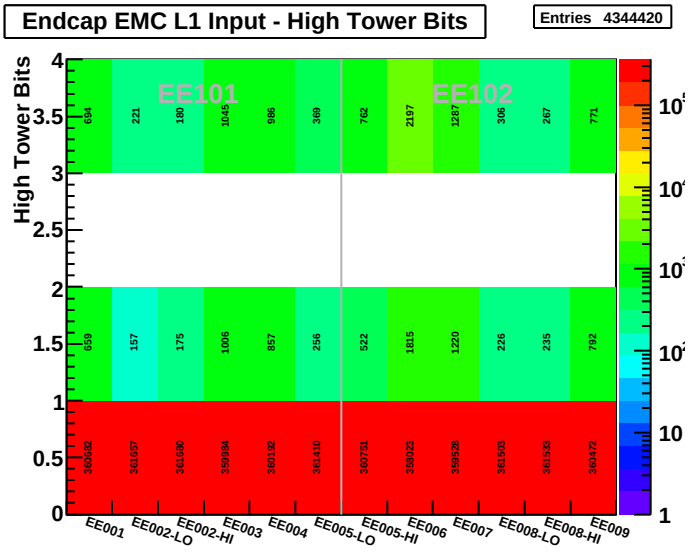

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

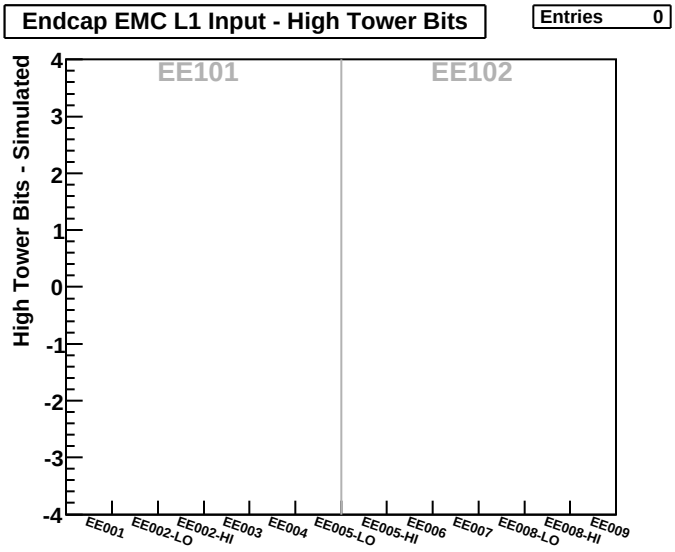

Endcap EMC L1 Input - Low Eta Sum
Entries 4344420
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

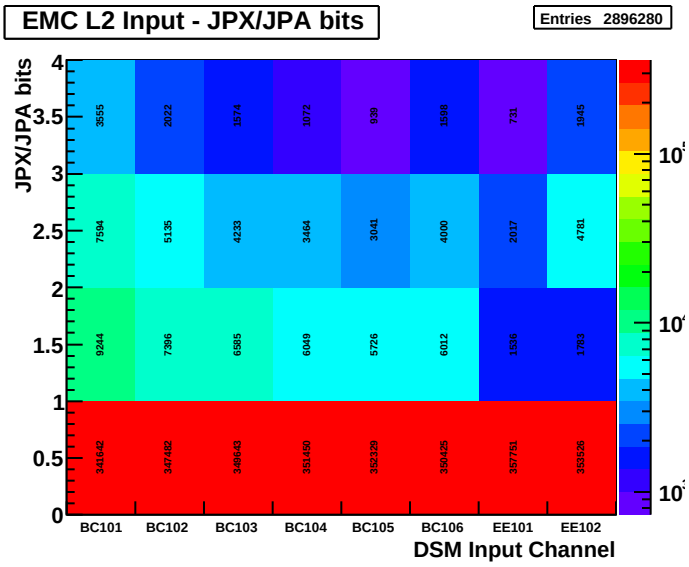

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

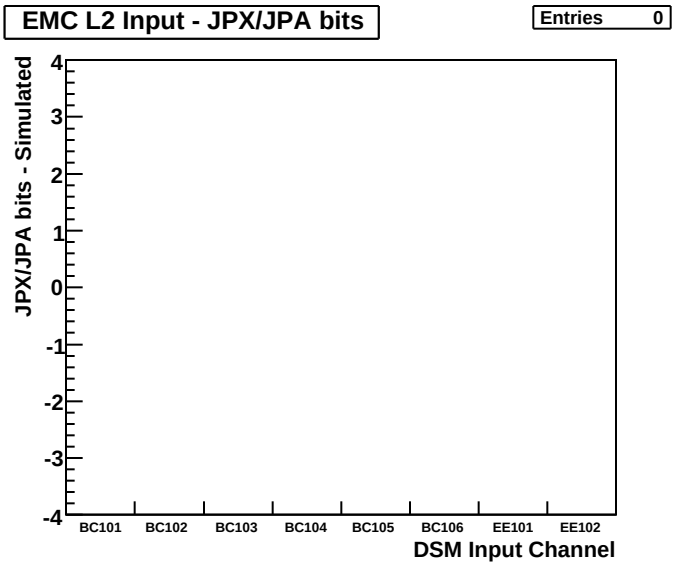

Endcap EMC L1 Input - High Eta Sum
Entries 4344420
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

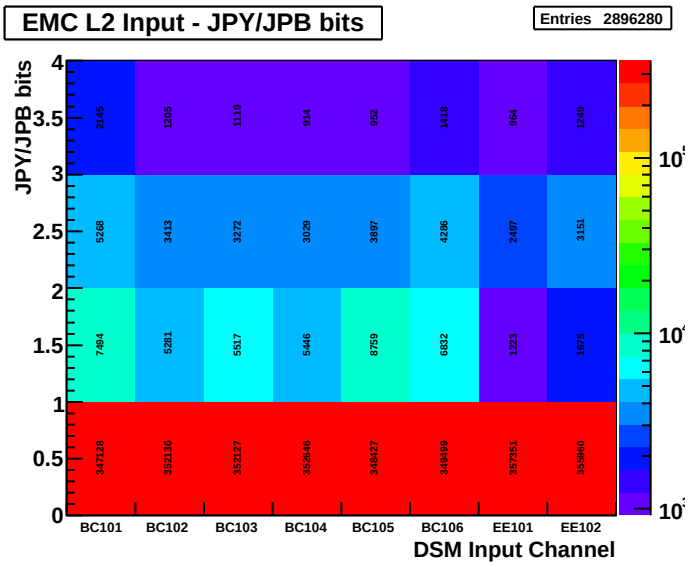

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

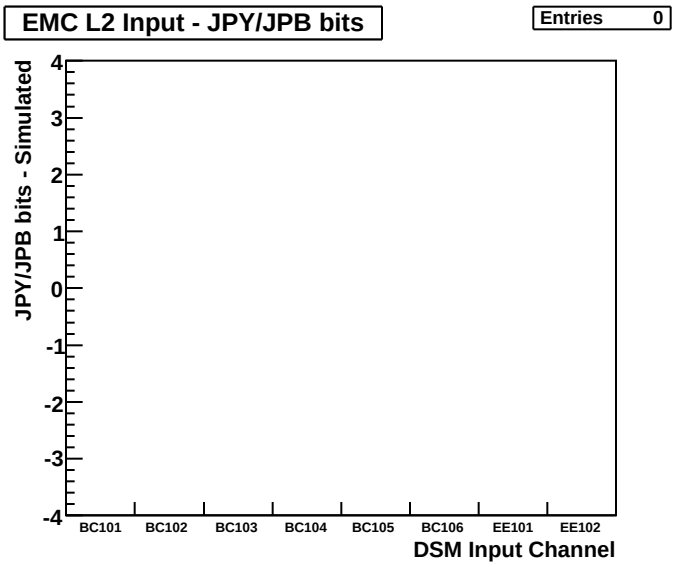

Endcap EMC L1 Input - High Tower Bits
Entries 4344420
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

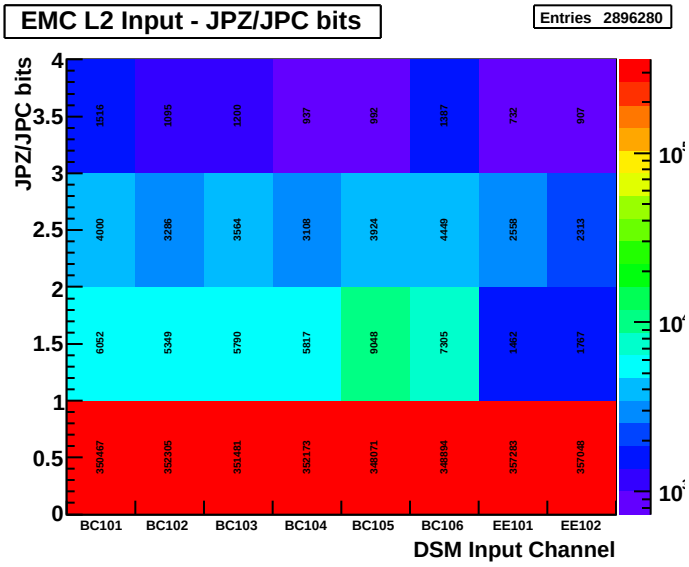

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

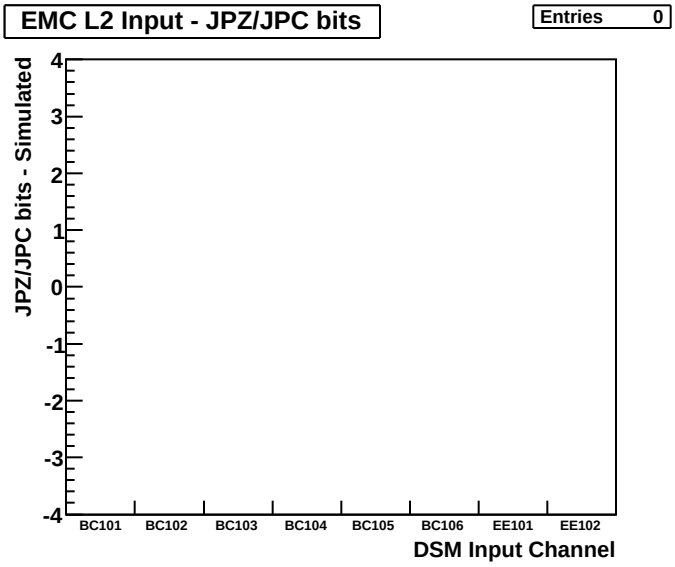

EMC L2 Input - JPX/JPA bits
Entries 2896280
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3555 2022 1574 1072 939 1598 731 1945
7594 5135 4233 3464 3041 4000 2017 4781
9244 7396 6585 6049 5726 6012 1586 1783
341642 347482 340643 351450 352329 350425 357751 353526

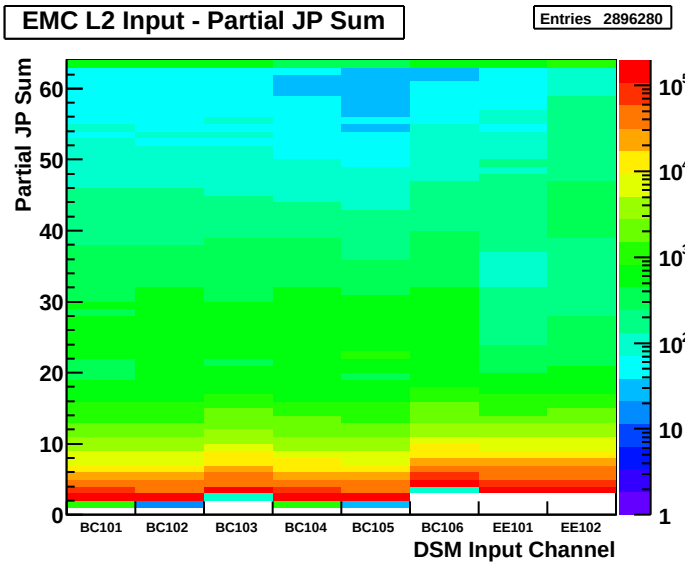

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

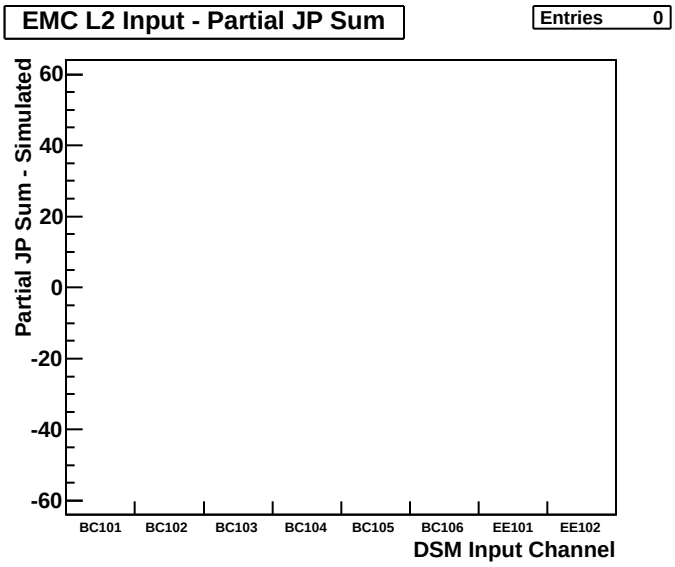

EMC L2 Input - JPY/JPB bits
Entries 2896280
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2145 1205 1119 914 952 1418 964 1249
5288 3413 3272 3029 3897 4286 2497 3151
7494 5281 5517 5446 8759 6832 1223 1675
347128 352136 352127 352646 348427 349499 357351 355960

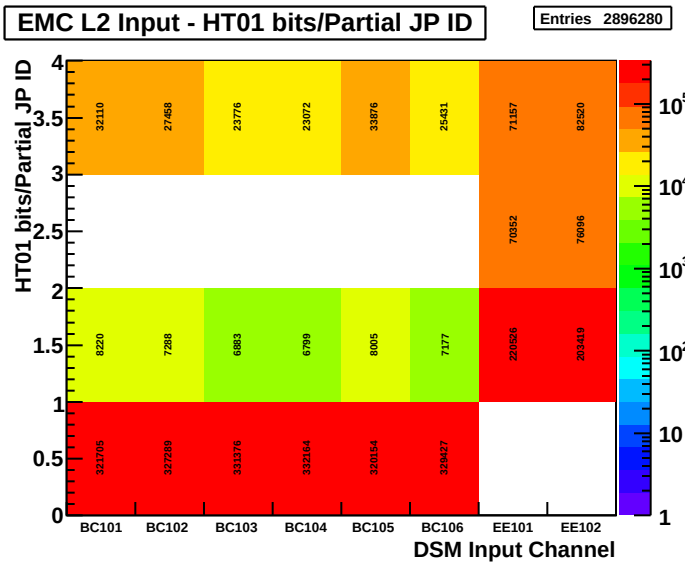

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

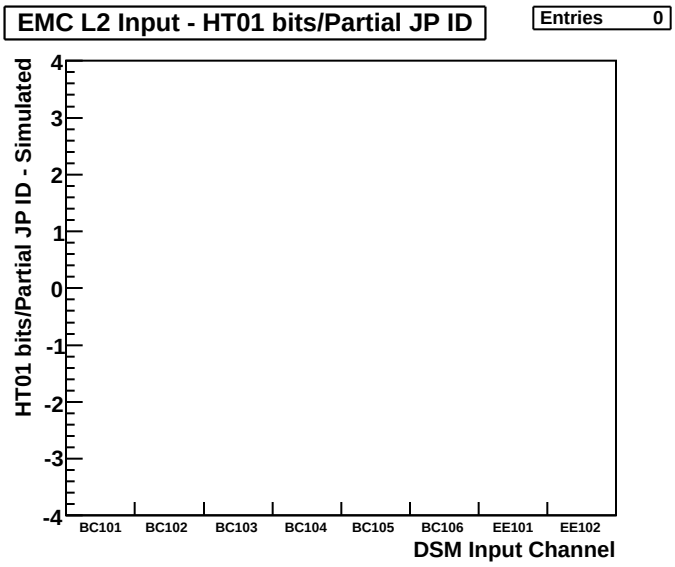

EMC L2 Input - JPZ/JPC bits
Entries 2896280
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1516 1095 1200 937 992 1837 732 907
4000 3286 3564 3108 3924 4449 2558 2313
6052 5349 5790 5817 9048 7305 1462 1737
350467 352305 351481 352173 348071 348894 357283 357048

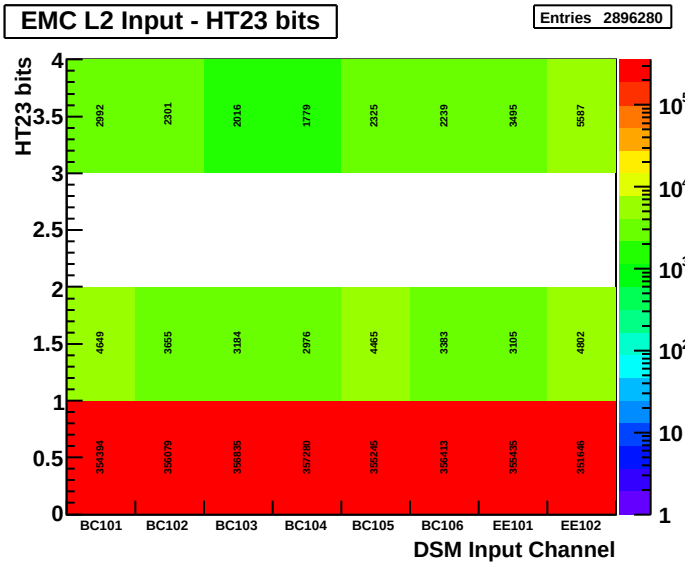

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

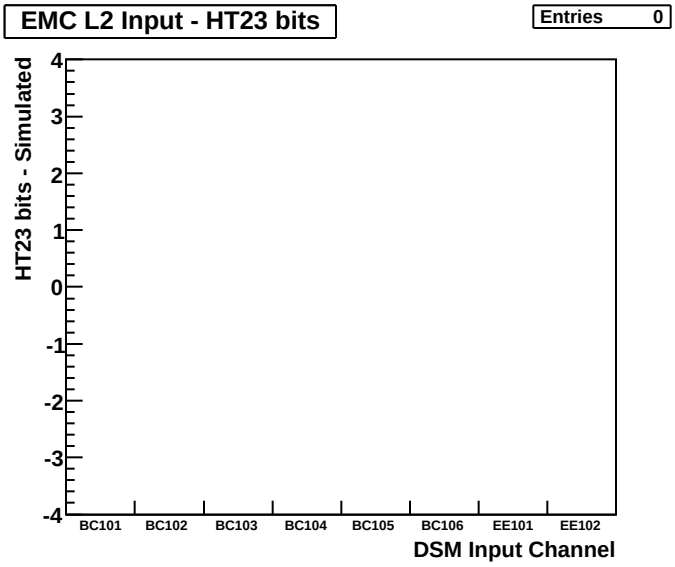

EMC L2 Input - Partial JP Sum
Entries 2896280
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

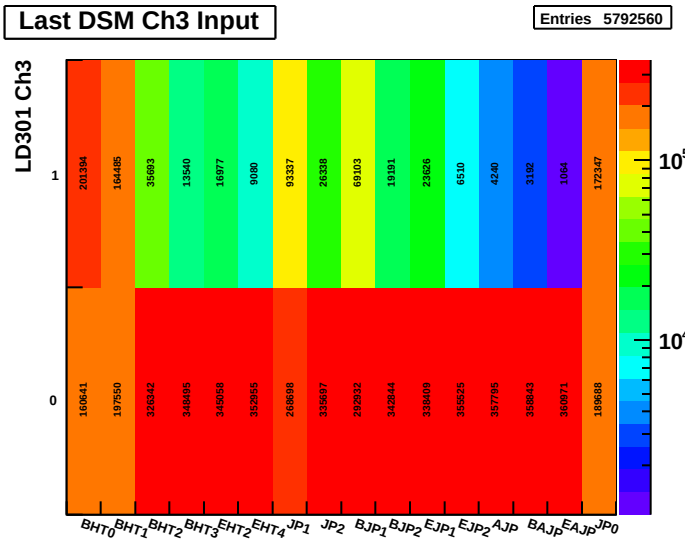

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

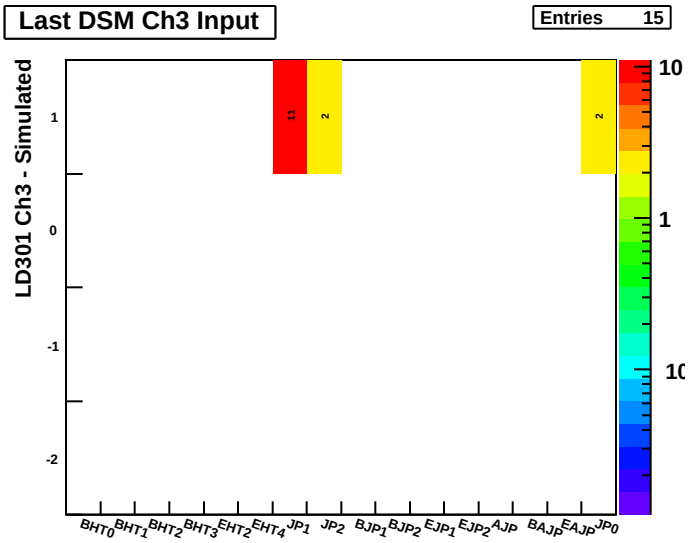

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2896280
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

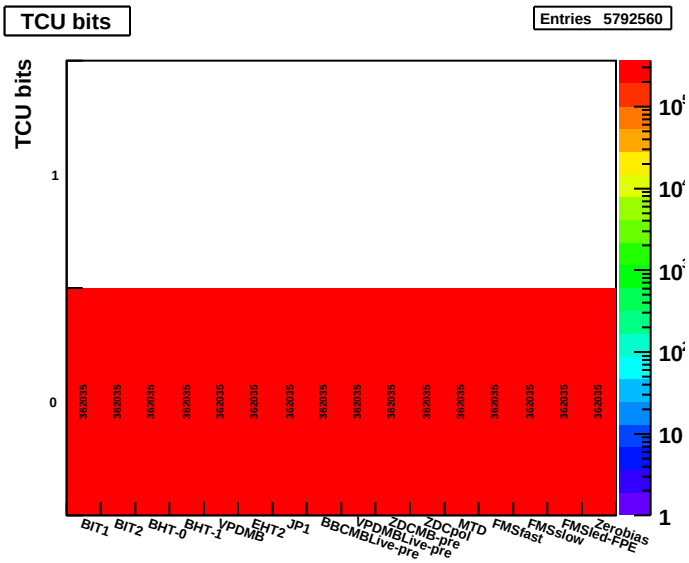

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

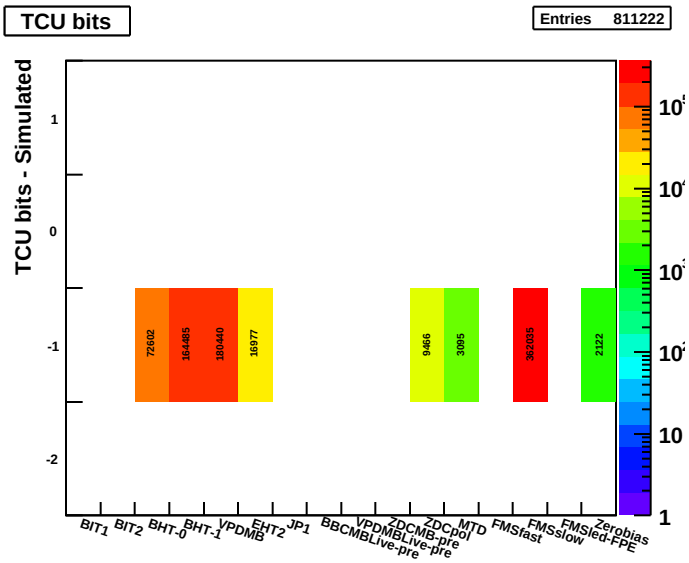

EMC L2 Input - HT23 bits
Entries 2896280
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

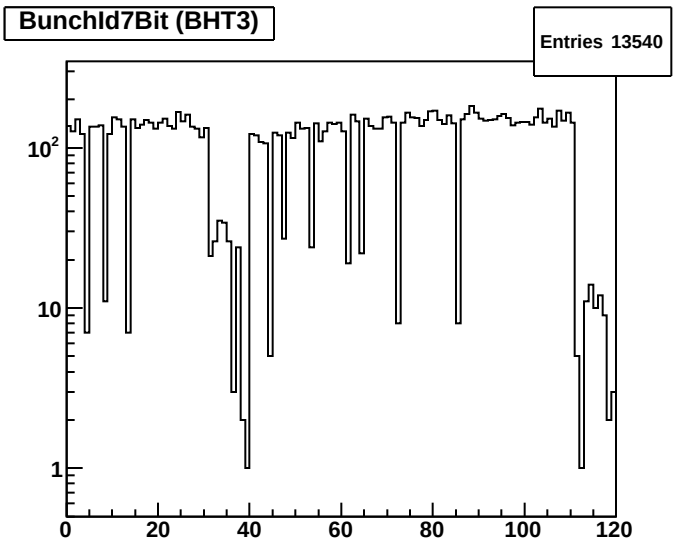

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

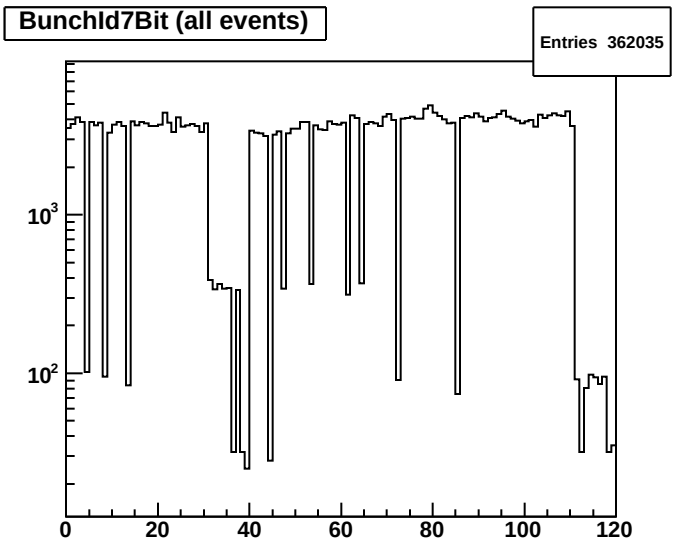

Last DSM Ch3 Input
Entries 5792560
LD301 Ch3
201394 164485 35693 13540 16977 9080 93337 26338 69103 19191 23626 6510 4240 3192 1064 172347
160641 197550 326342 348495 345058 352955 268698 335697 292932 342844 338409 355525 357795 358843 360971 189688
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

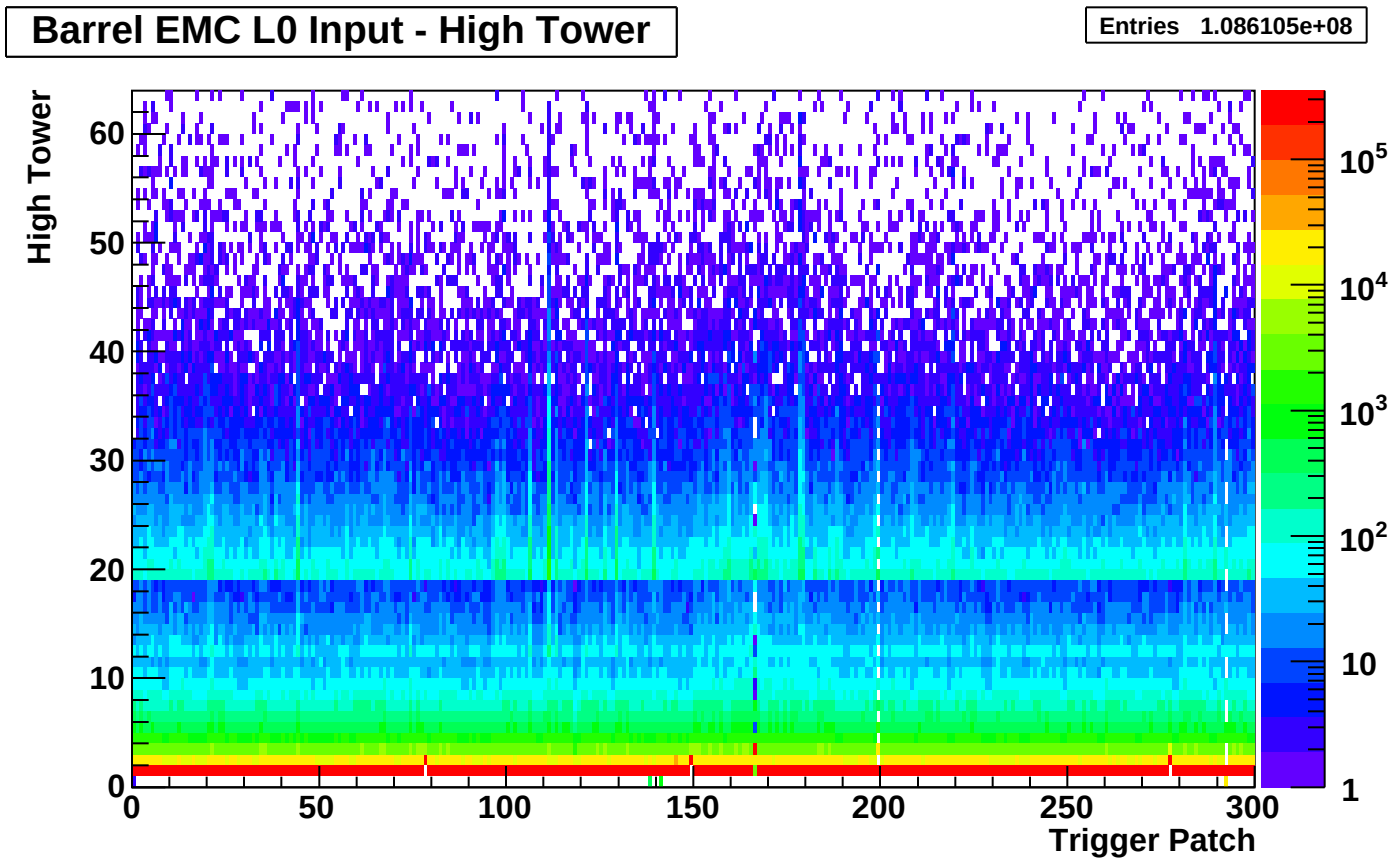

Last DSM Ch3 Input
Entries 15
LD301 Ch3 - Simulated
11 2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

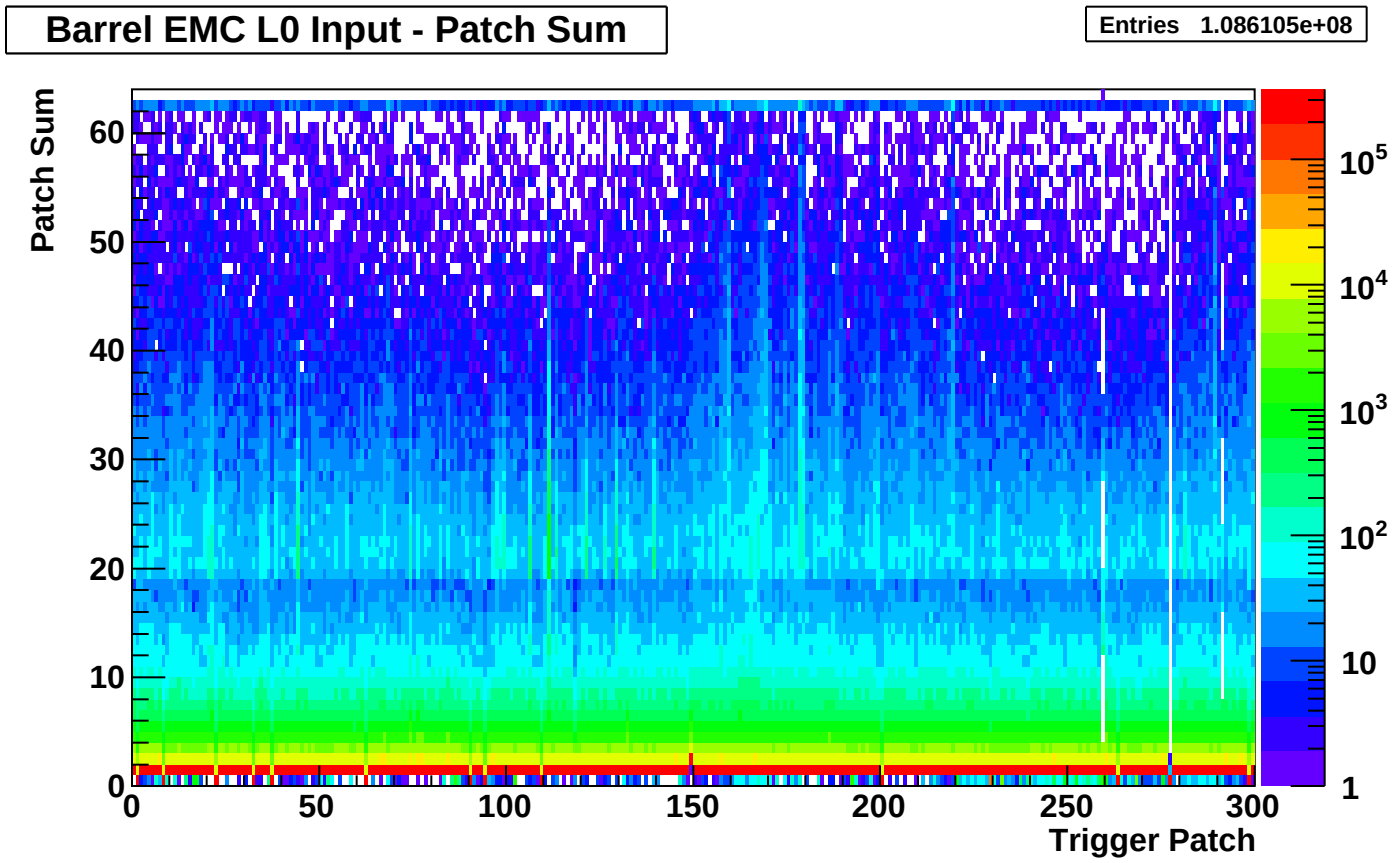

TCU bits
Entries 5792560
TCU bits
362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035 362035
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

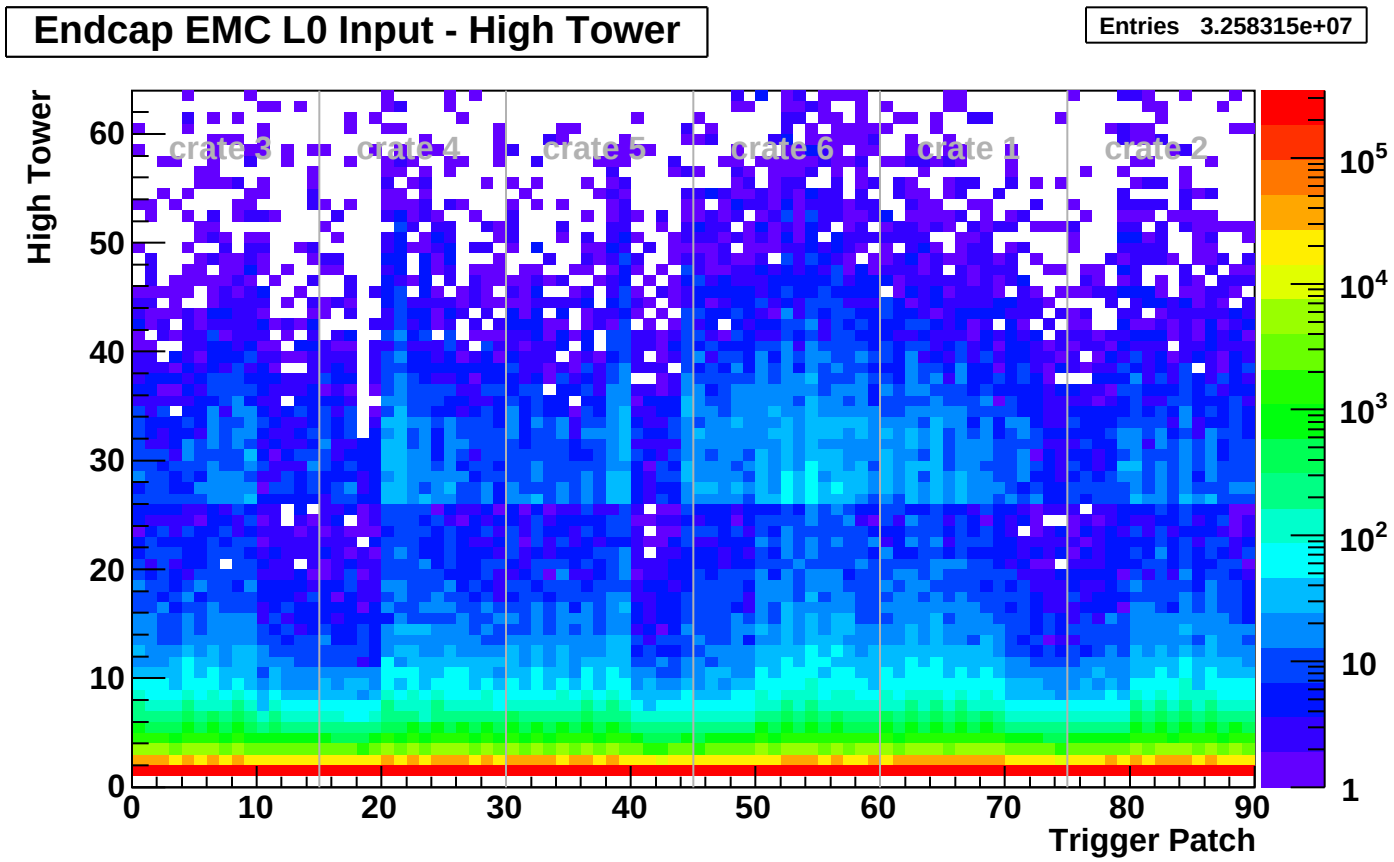

TCU bits
Entries 811222
TCU bits - Simulated
72602 164485 180440 16977 9466 3095 362035 2122
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

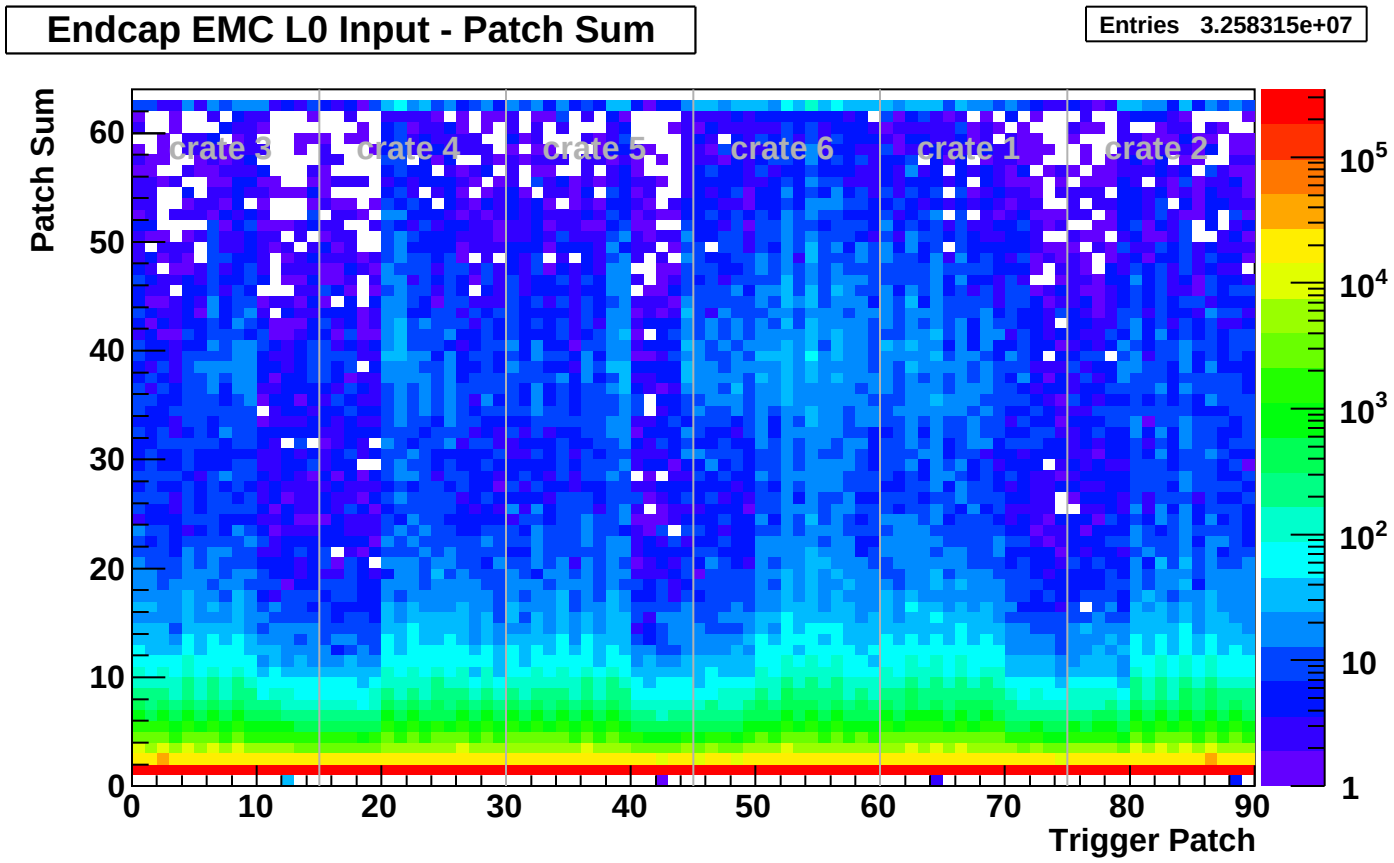

BunchId7Bit (BHT3)
Entries 13540

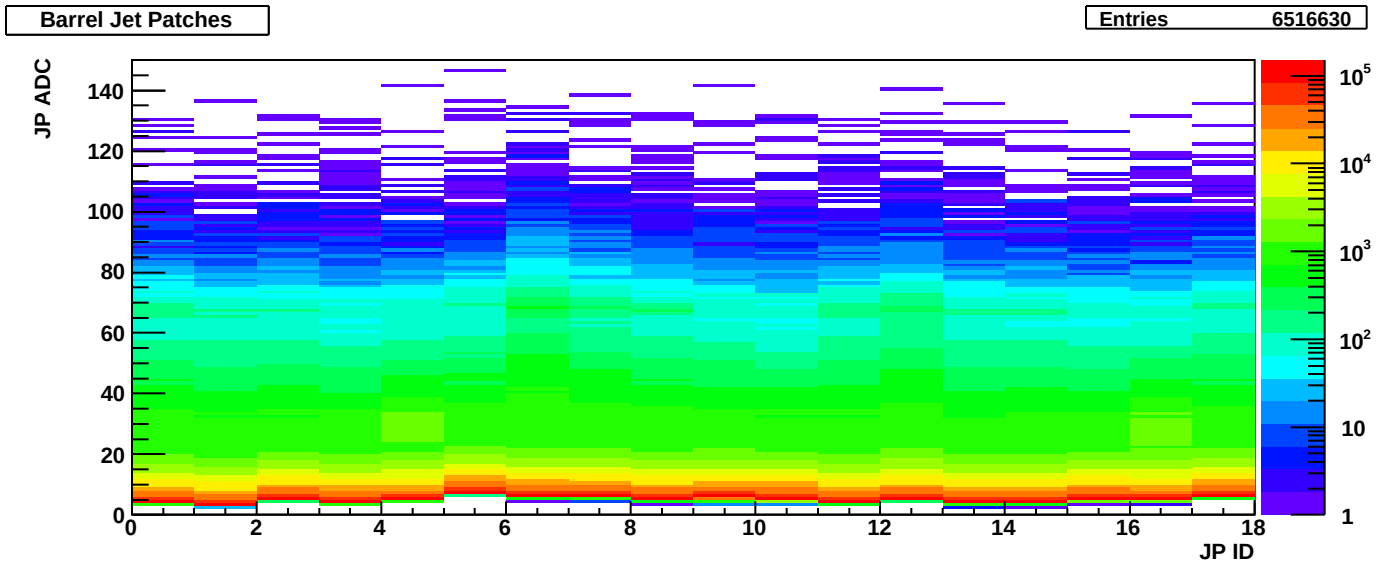

BunchId7Bit (all events)
Entries 362035

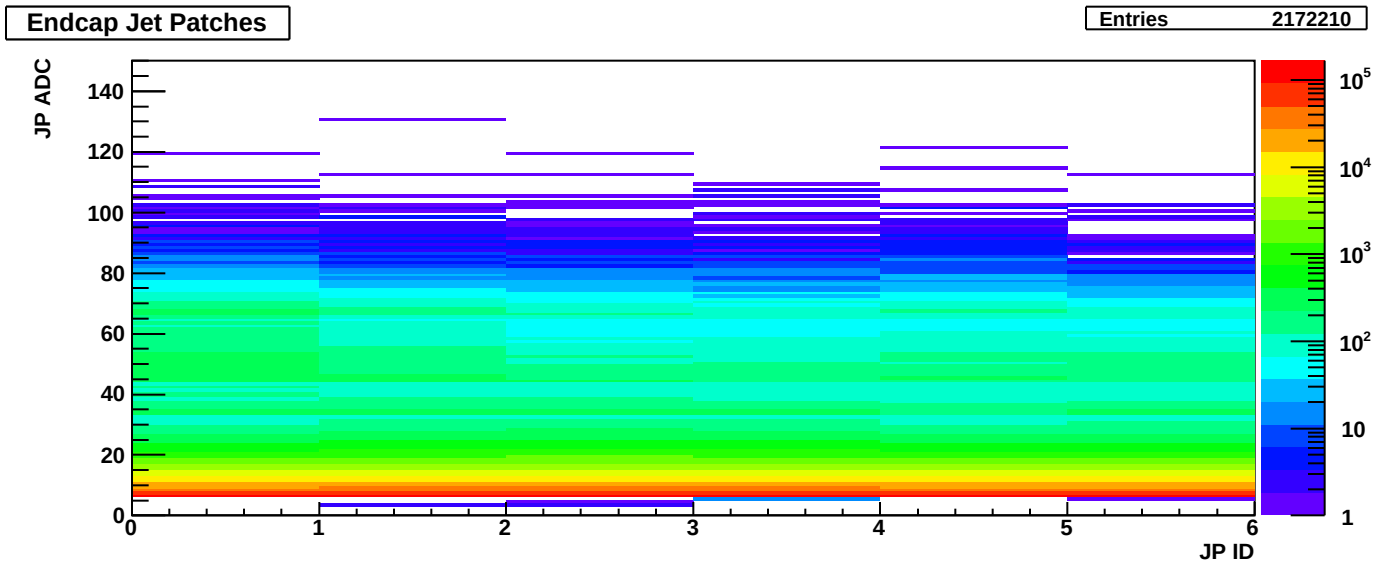

Barrel EMC L0 Input - High Tower
Entries 1.086105e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

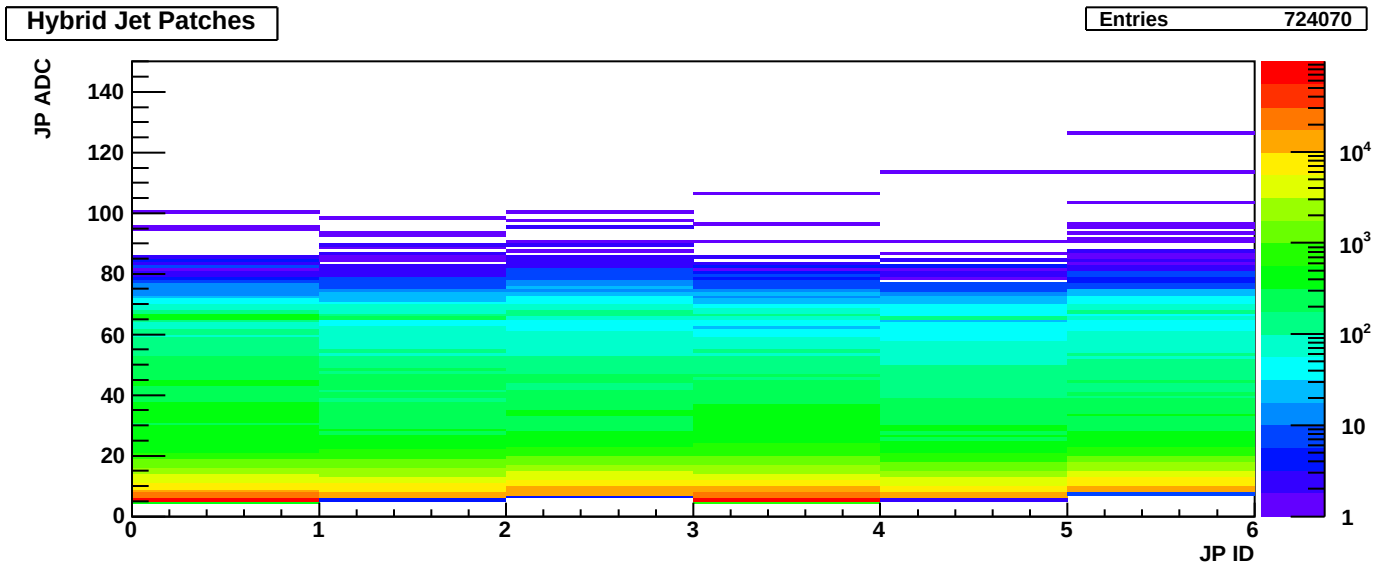

Barrel EMC L0 Input - Patch Sum
Entries 1.086105e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

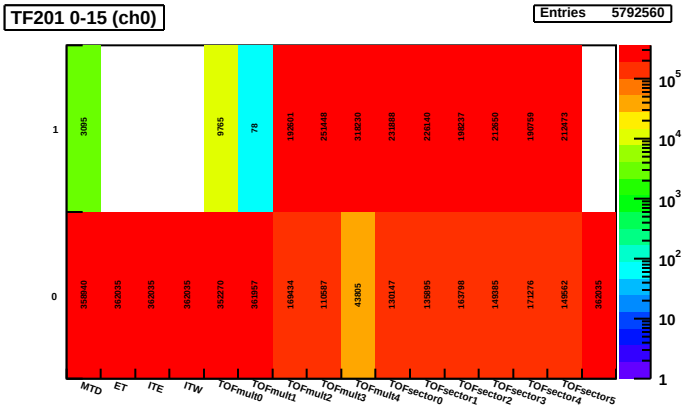

Endcap EMC L0 Input - High Tower
Entries 3.258315e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

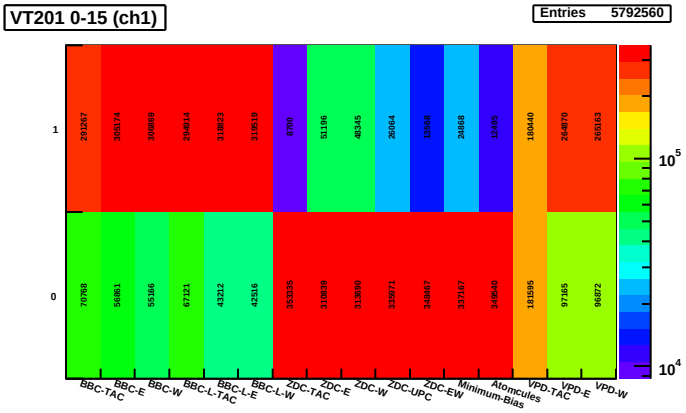

Endcap EMC L0 Input - Patch Sum
Entries 3.258315e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

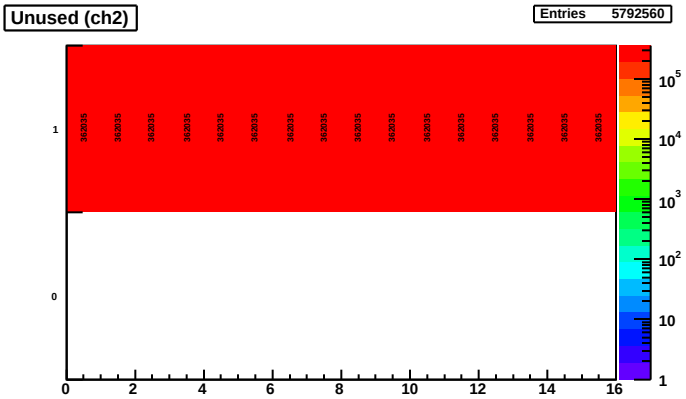

Barrel Jet Patches
Entries 6516630
JP ADC
JP ID

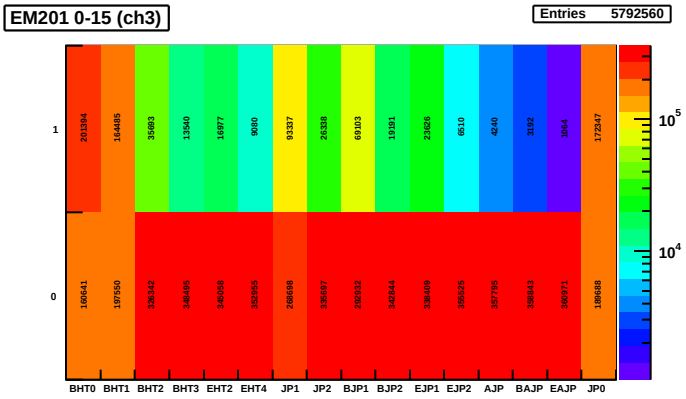

Endcap Jet Patches
Entries 2172210
JP ADC
JP ID

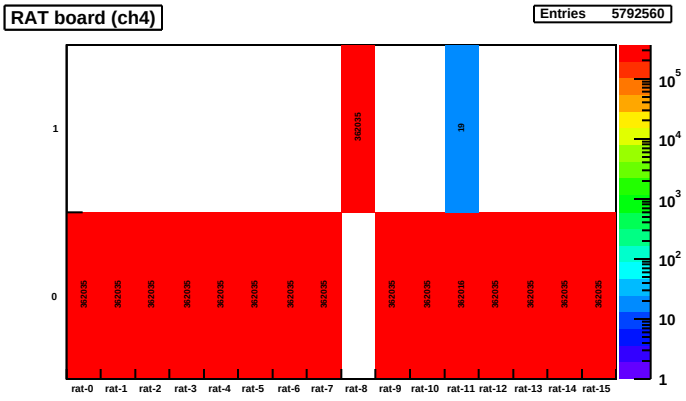

Hybrid Jet Patches
Entries 724070
JP ADC
JP ID

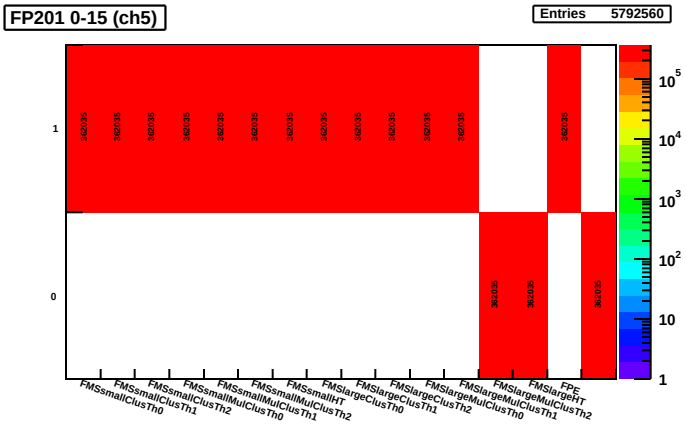

TF201 0-15 (ch0)
Entries 5792560
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

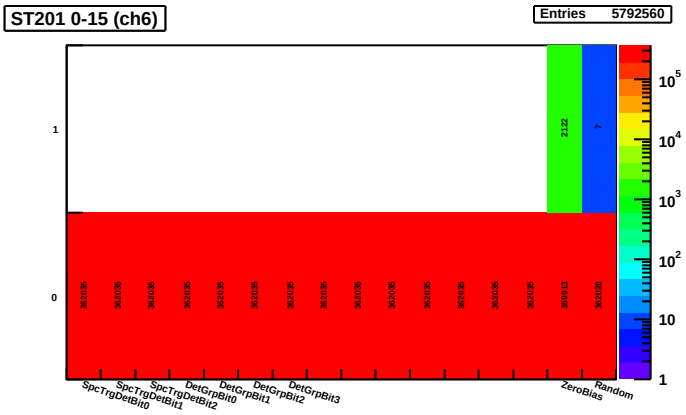

VT201 0-15 (ch1)
Entries 5792560
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

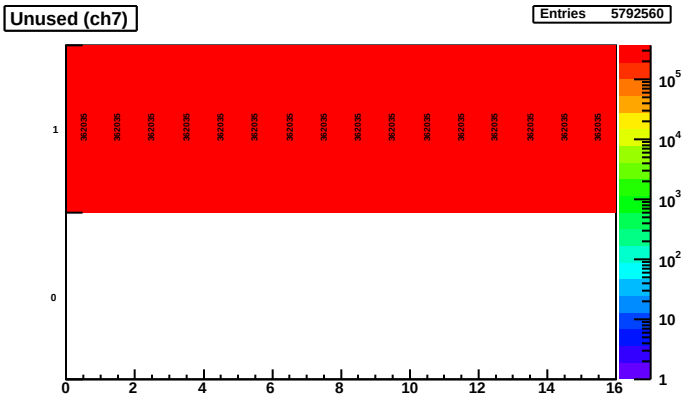

Unused (ch2)
Entries 5792560

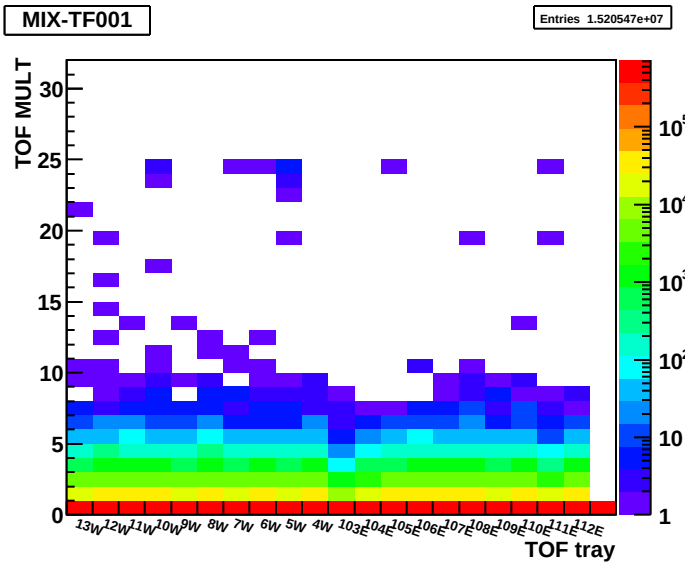

EM201 0-15 (ch3)
Entries 5792560
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

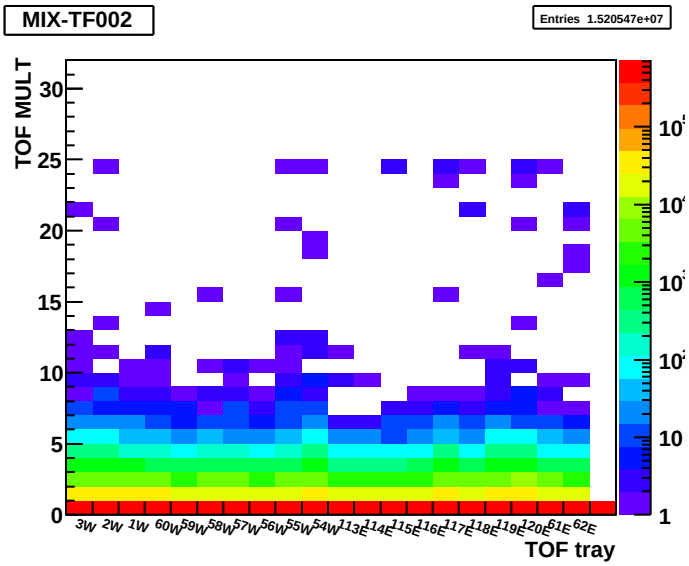

RAT board (ch4)
Entries 5792560
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

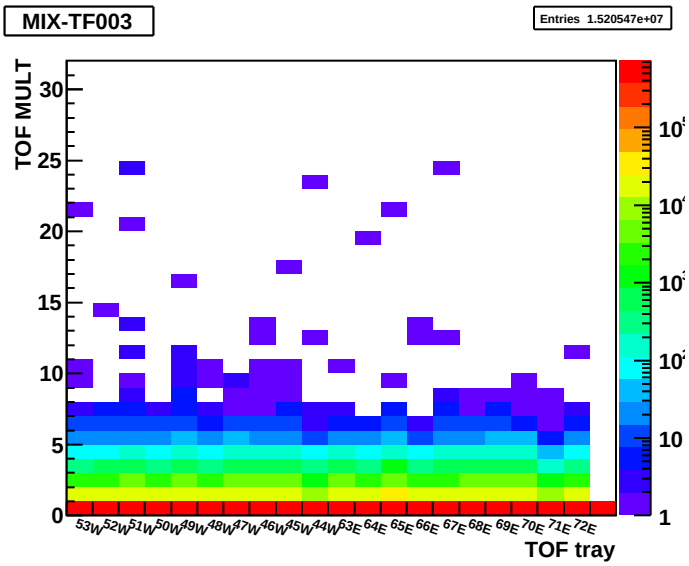

FP201 0-15 (ch5)
Entries 5792560
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

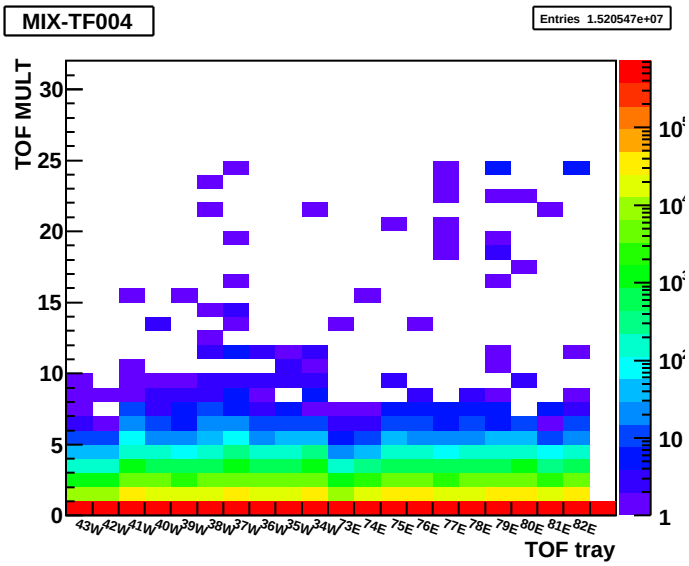

ST201 0-15 (ch6)
Entries 5792560
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

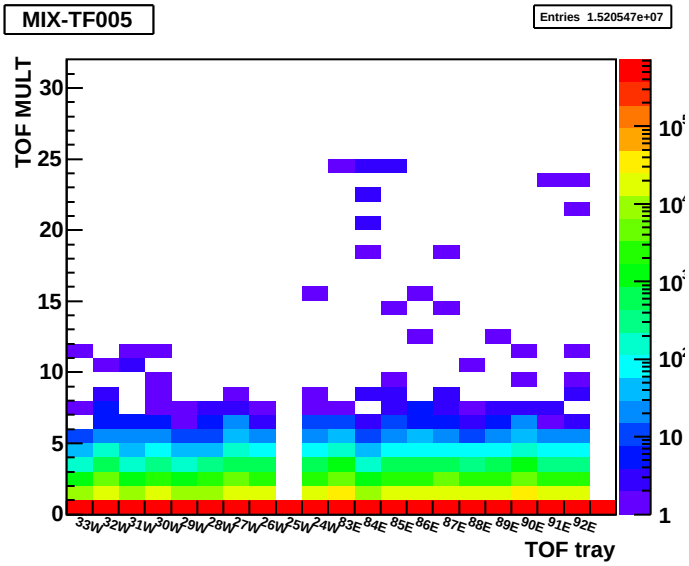

Unused (ch7)
Entries 5792560

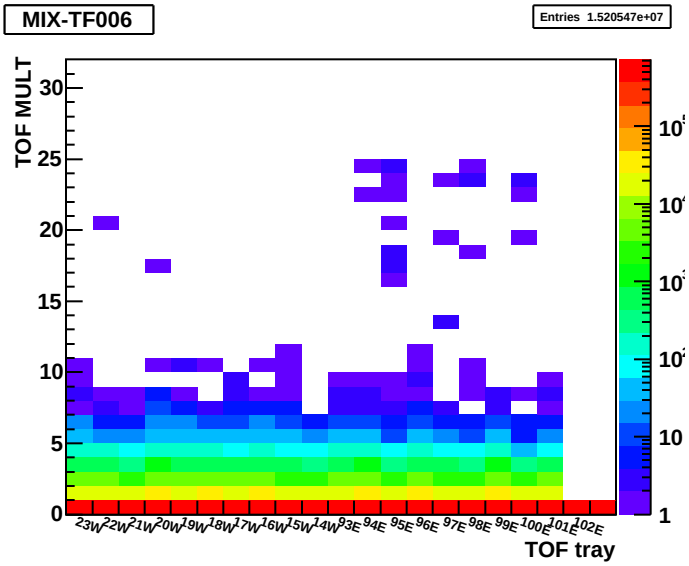

MIX-TF001
Entries 1.520547e+07
TOF MULT
TOF tray

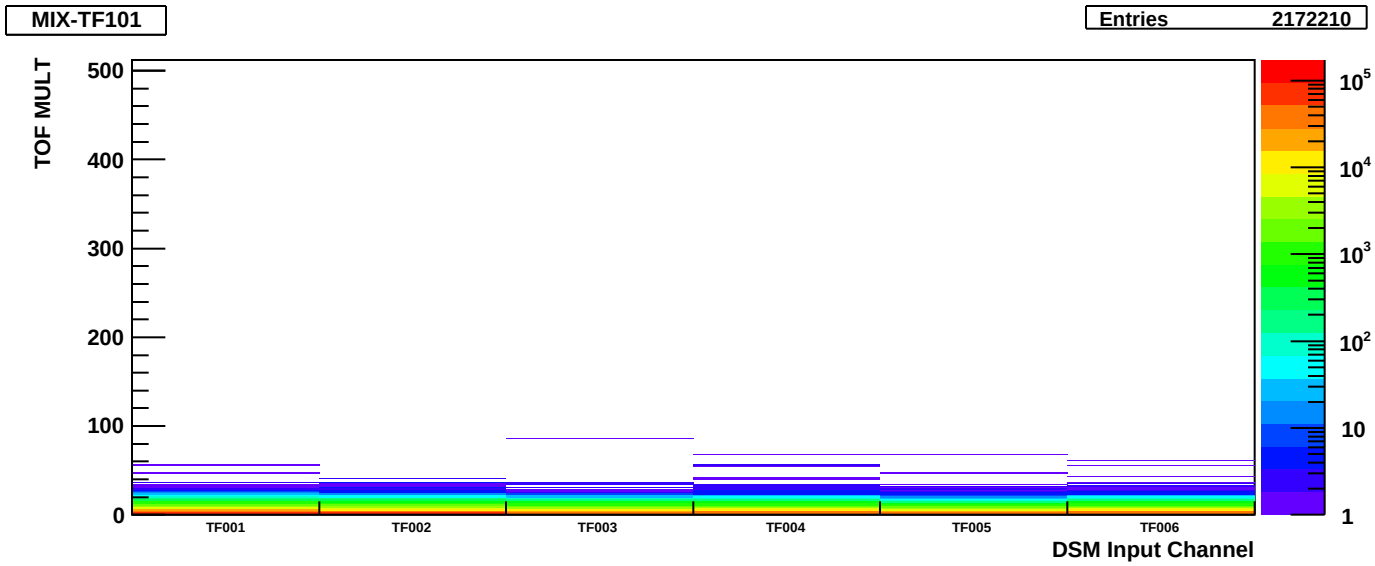

MIX-TF002
Entries 1.520547e+07
TOF MULT
TOF tray

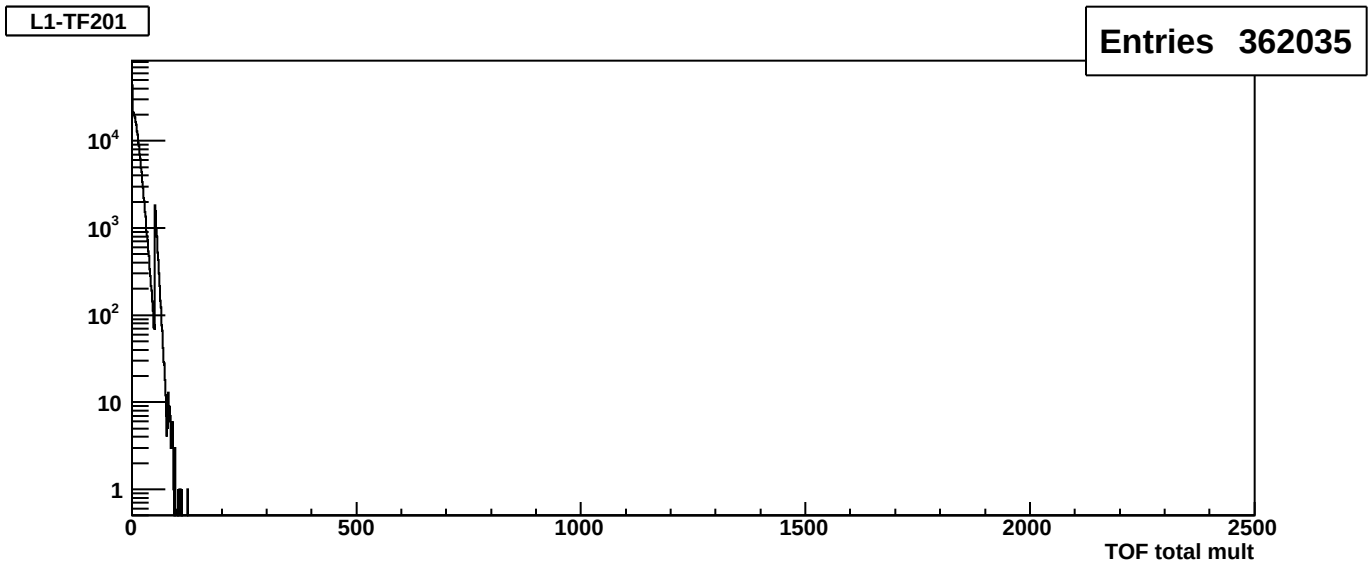

MIX-TF003
Entries 1.520547e+07
TOF MULT
TOF tray

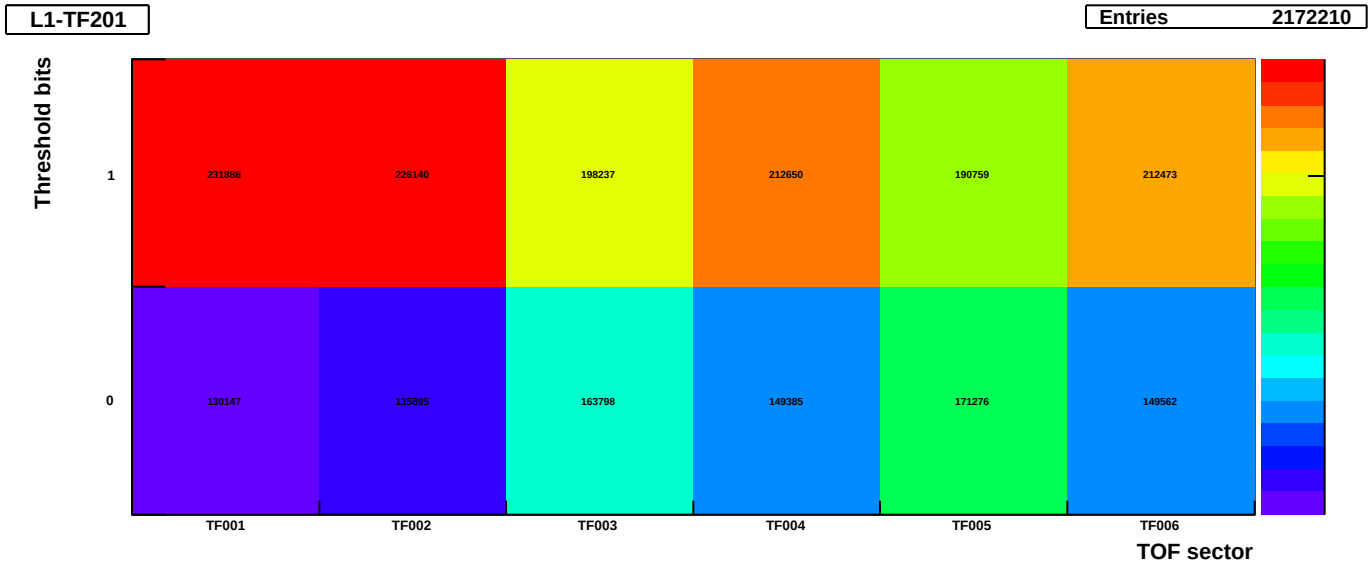

MIX-TF004
Entries 1.520547e+07
TOF MULT
TOF tray

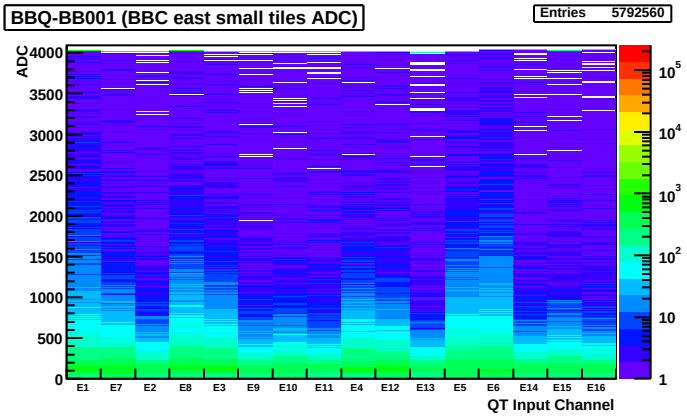

MIX-TF005
Entries 1.520547e+07
TOF MULT
TOF tray

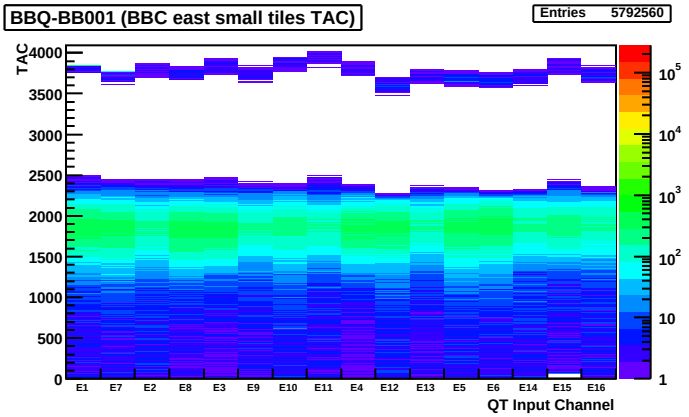

MIX-TF006
Entries 1.520547e+07
TOF MULT
TOF tray

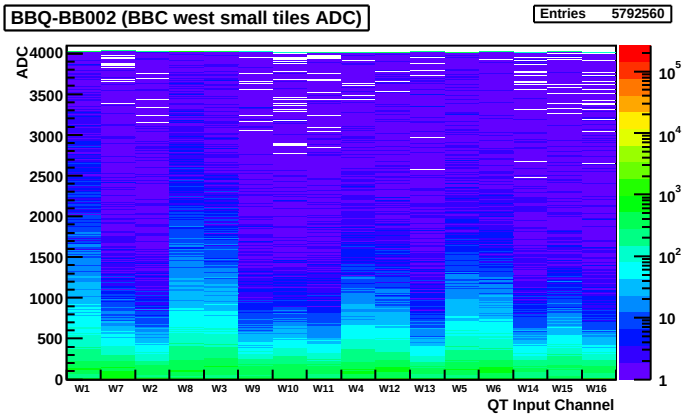

MIX-TF101
Entries 2172210
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

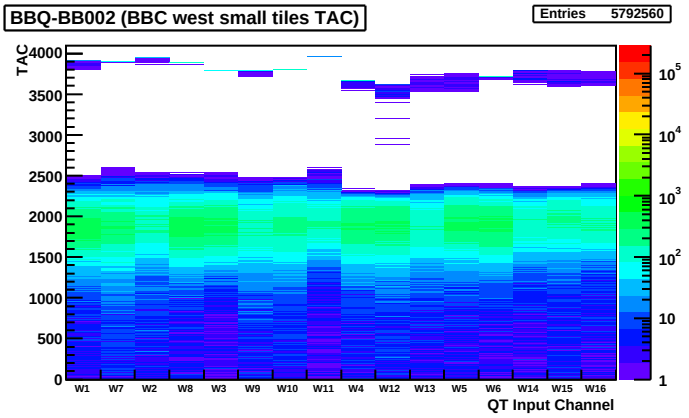

L1-TF201
Entries 362035
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

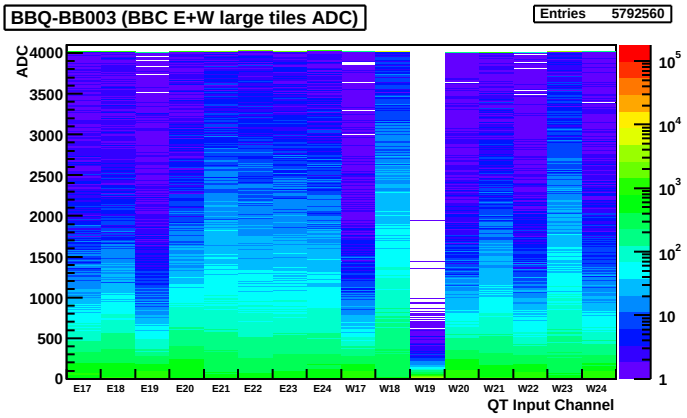

L1-TF201
Entries 2172210
Threshold bits
1
0
231888
226140
198237
212650
190759
212473
130147
135895
163798
149385
171276
149562
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

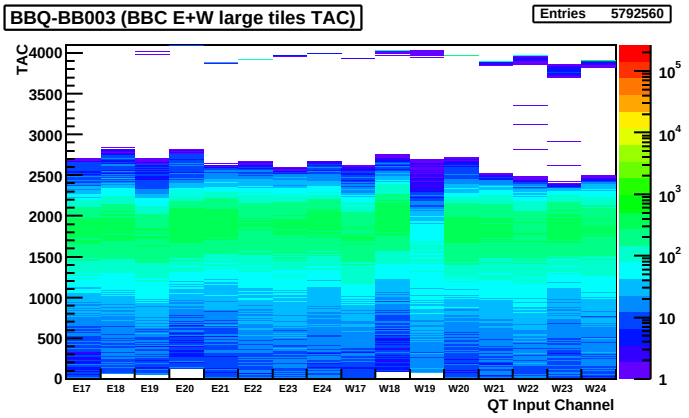

BBQ-BB001 (BBC east small tiles ADC)
Entries 5792560
ADC
QT Input Channel

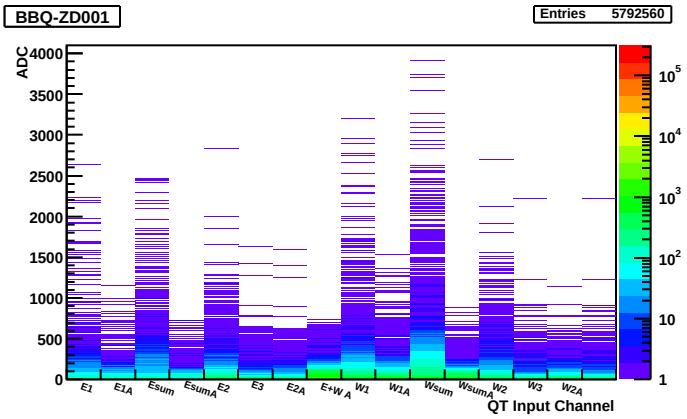

BBQ-BB001 (BBC east small tiles TAC)
Entries 5792560
TAC
QT Input Channel

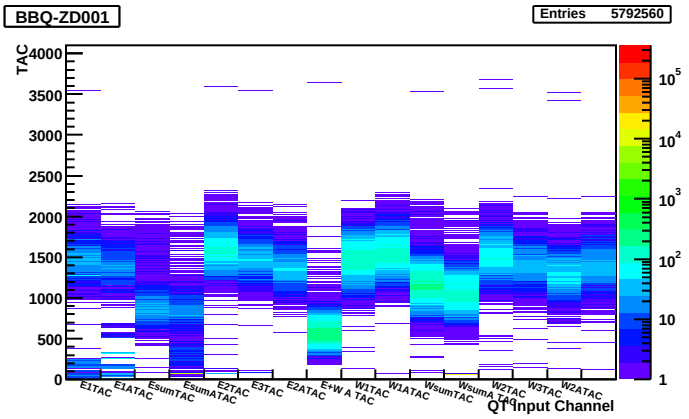

BBQ-BB002 (BBC west small tiles ADC)
Entries 5792560
ADC
QT Input Channel

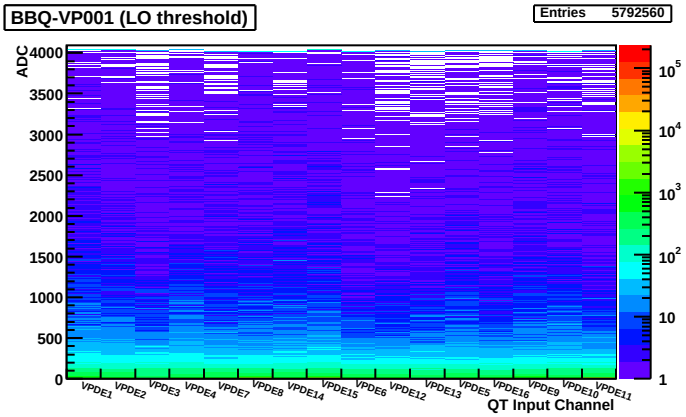

BBQ-BB002 (BBC west small tiles TAC)
Entries 5792560
TAC
QT Input Channel

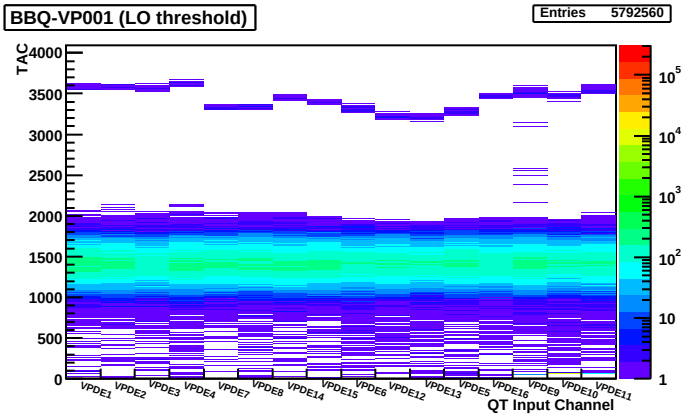

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5792560
ADC
QT Input Channel

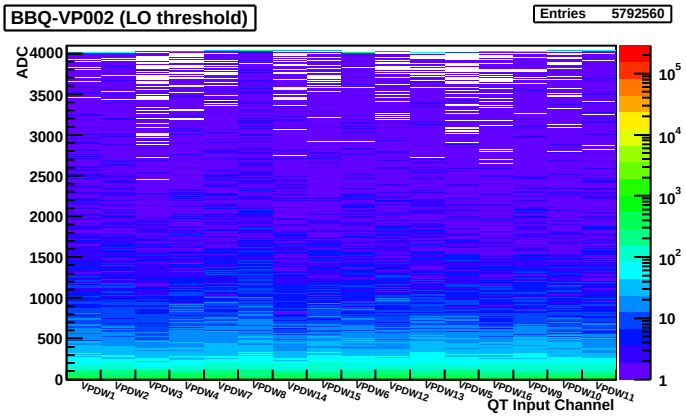

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5792560
TAC
QT Input Channel

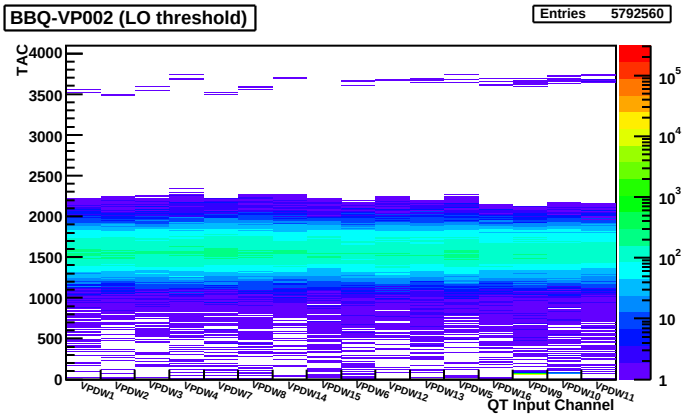

BBQ-ZD001
Entries 5792560
ADC
QT Input Channel

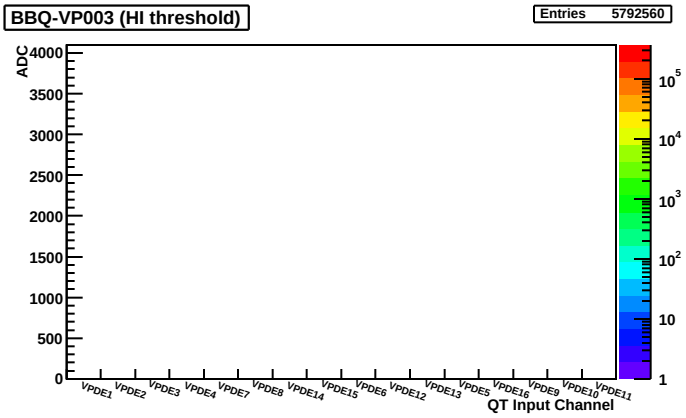

BBQ-ZD001
Entries 5792560
TAC
QT Input Channel

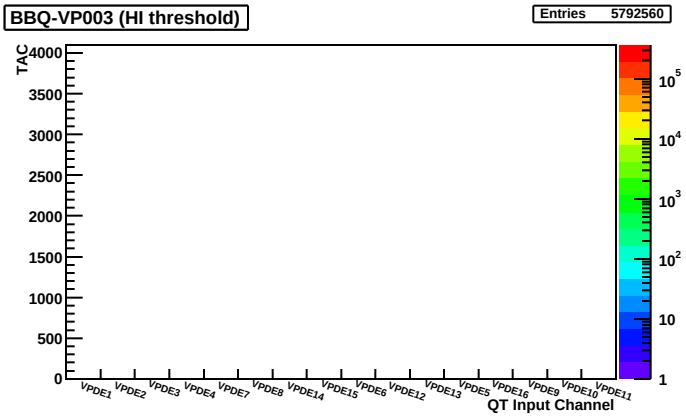

BBQ-VP001 (LO threshold)
Entries 5792560
ADC
QT Input Channel

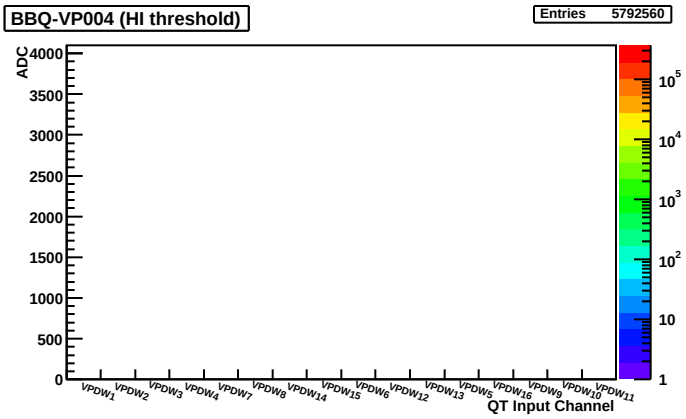

BBQ-VP001 (LO threshold)
Entries 5792560
TAC
QT Input Channel

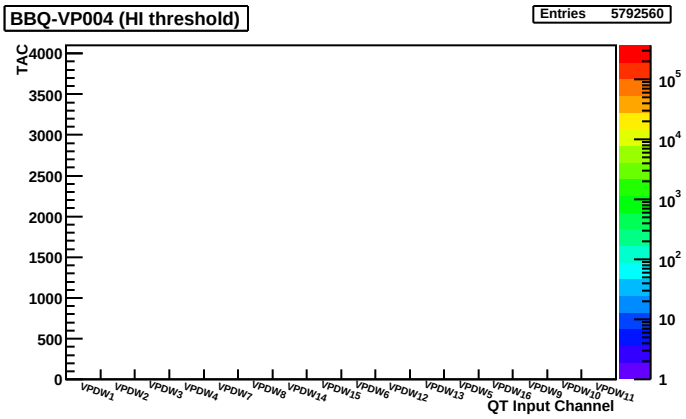

BBQ-VP002 (LO threshold)
Entries 5792560
ADC
QT Input Channel

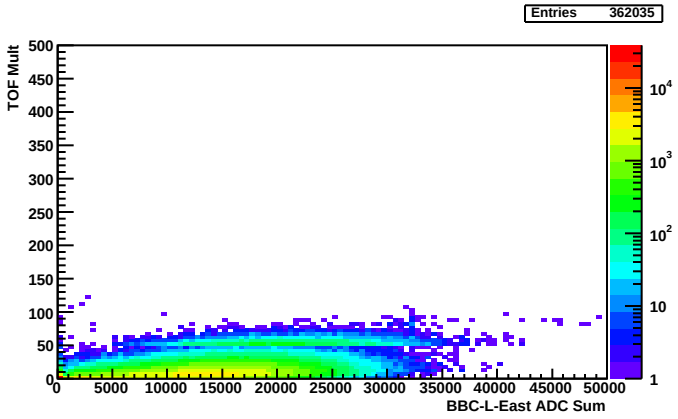

BBQ-VP002 (LO threshold)
Entries 5792560
TAC
QT Input Channel

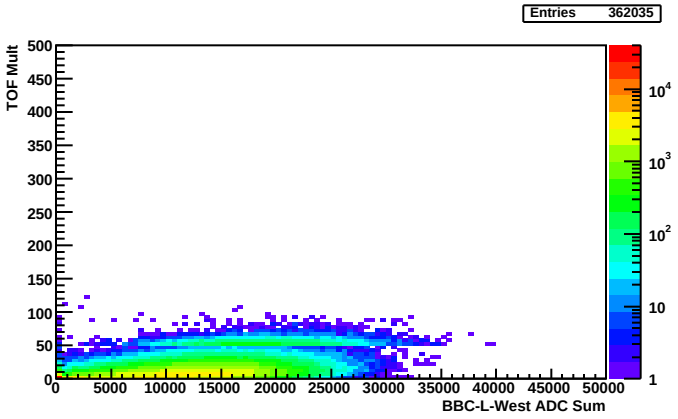

BBQ-VP003 (HI threshold)
Entries 5792560
ADC
QT Input Channel

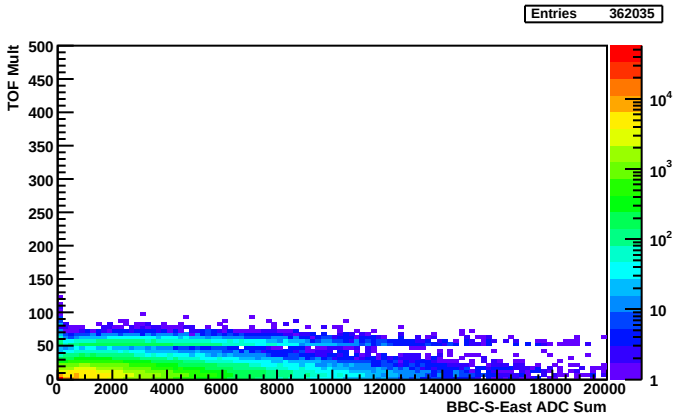

BBQ-VP003 (HI threshold)
Entries 5792560
TAC
QT Input Channel

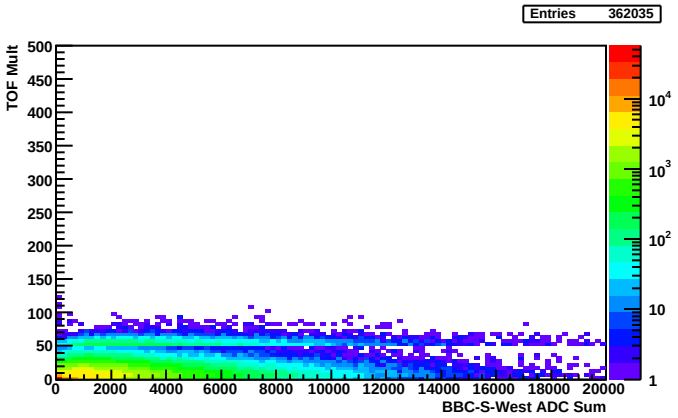

BBQ-VP004 (HI threshold)
Entries 5792560
ADC
QT Input Channel

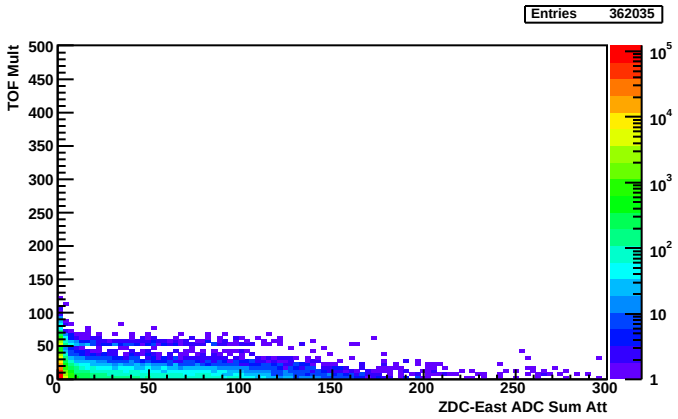

BBQ-VP004 (HI threshold)
Entries 5792560
TAC
QT Input Channel

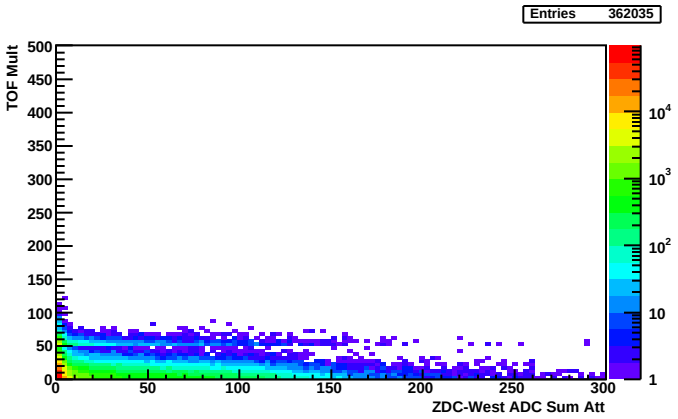

Entries 362035
TOF Mult
BBC-L-East ADC Sum

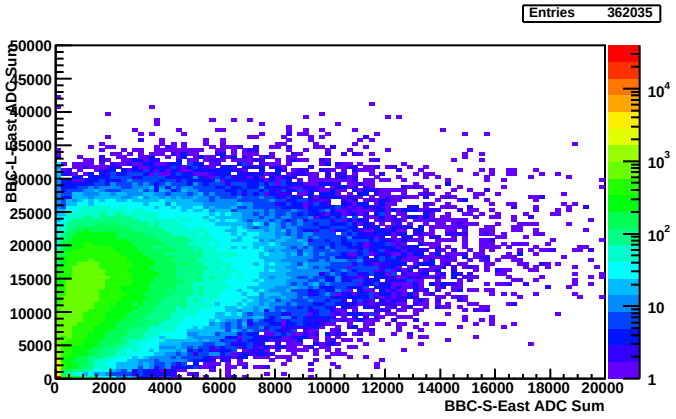

Entries 362035
TOF Mult
BBC-L-West ADC Sum

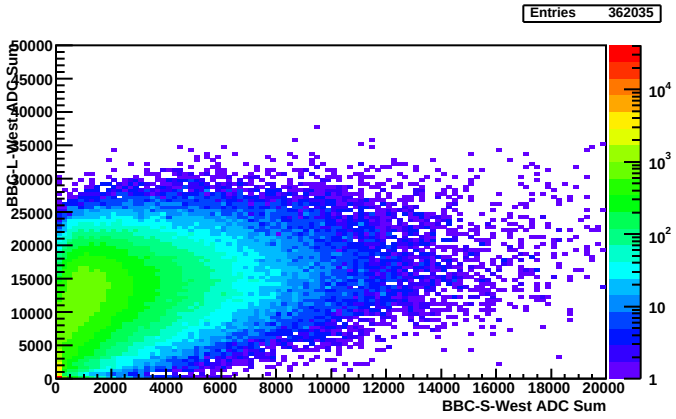

Entries 362035
TOF Mult
BBC-S-East ADC Sum

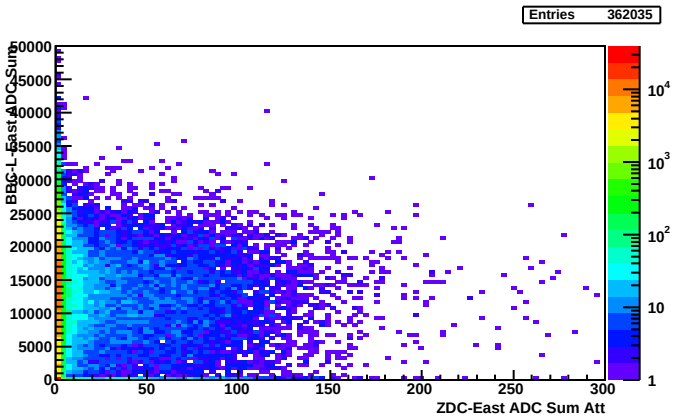

Entries 362035
TOF Mult
BBC-S-West ADC Sum

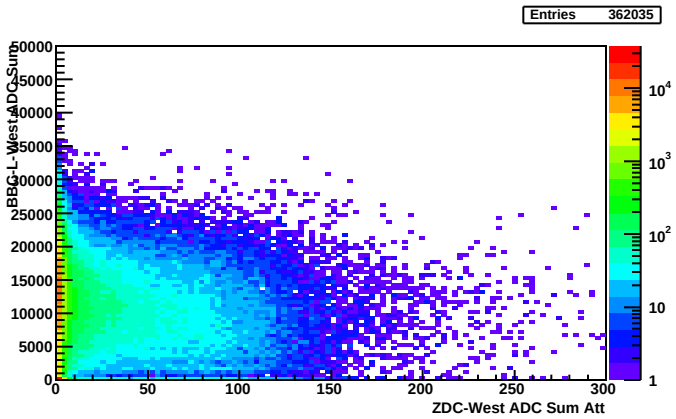

Entries 362035
TOF Mult
ZDC-East ADC Sum Att

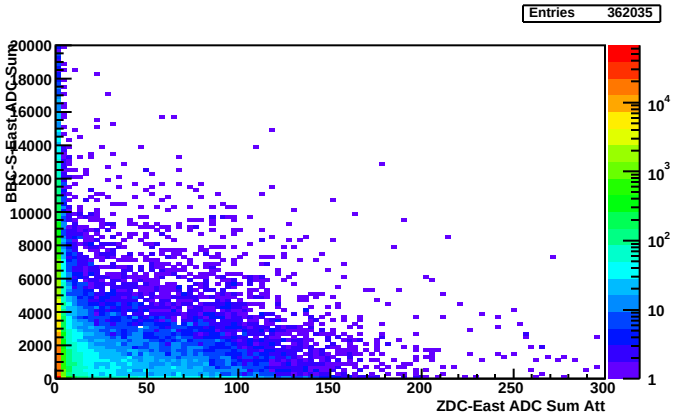

Entries 362035
TOF Mult
ZDC-West ADC Sum Att

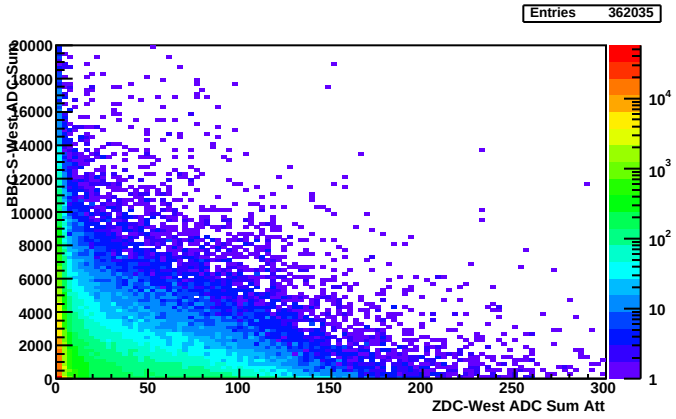

Entries 362035
BBC-L-East ADC Sum
BBC-S-East ADC Sum

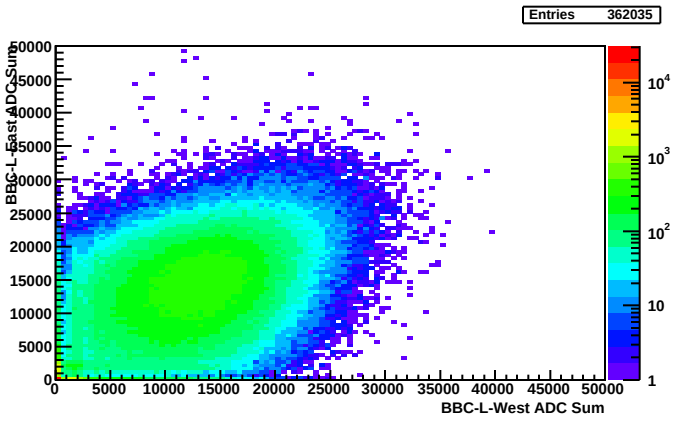

Entries 362035
BBC-L-West ADC Sum
BBC-S-West ADC Sum

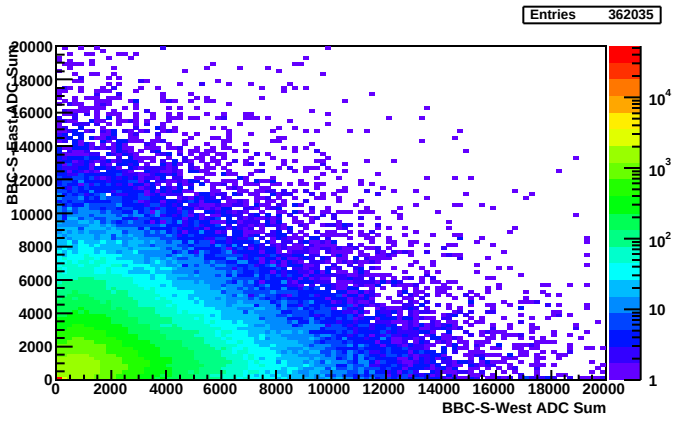

Entries 362035
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

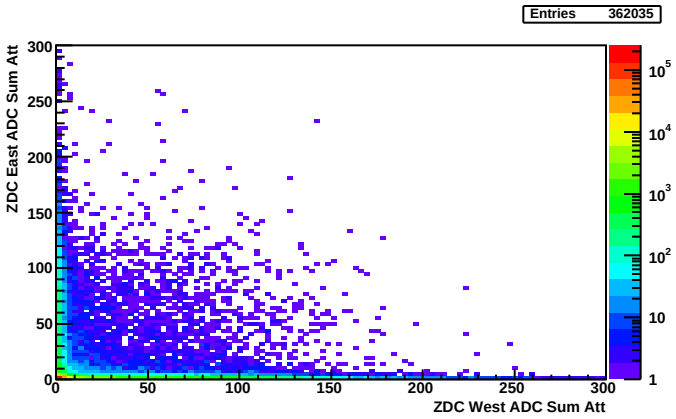

Entries 362035
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

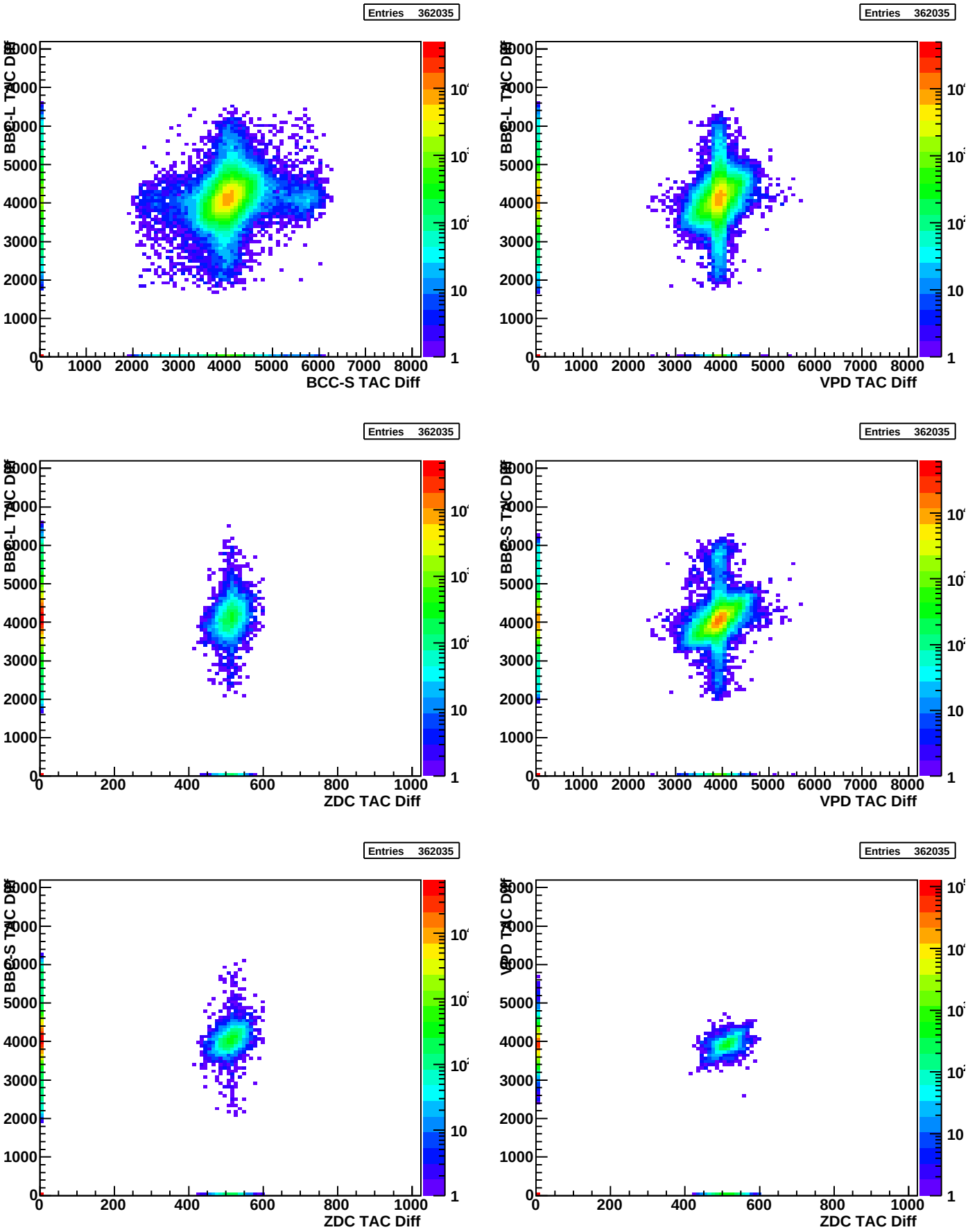

Entries 362035
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

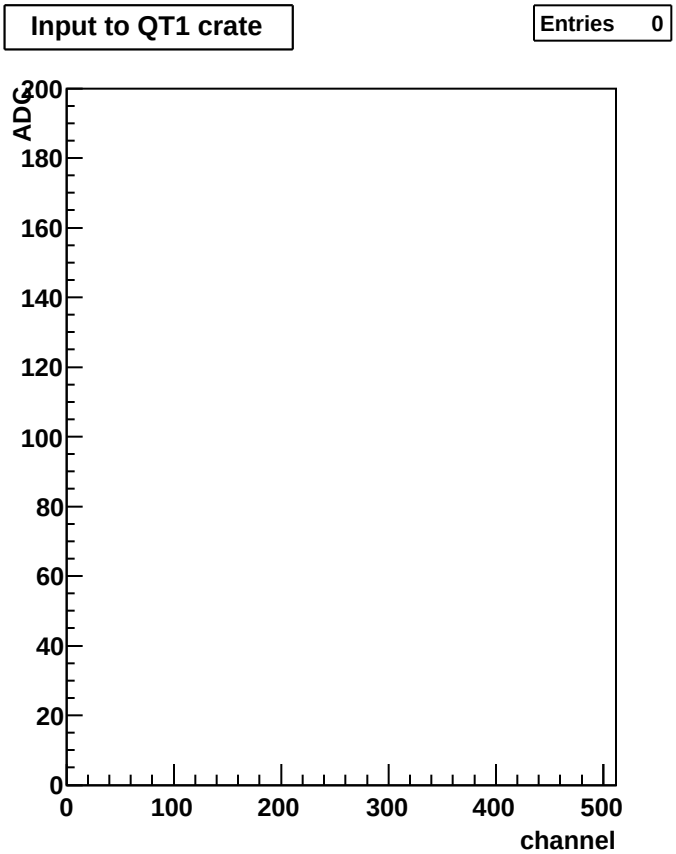

Entries 362035
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

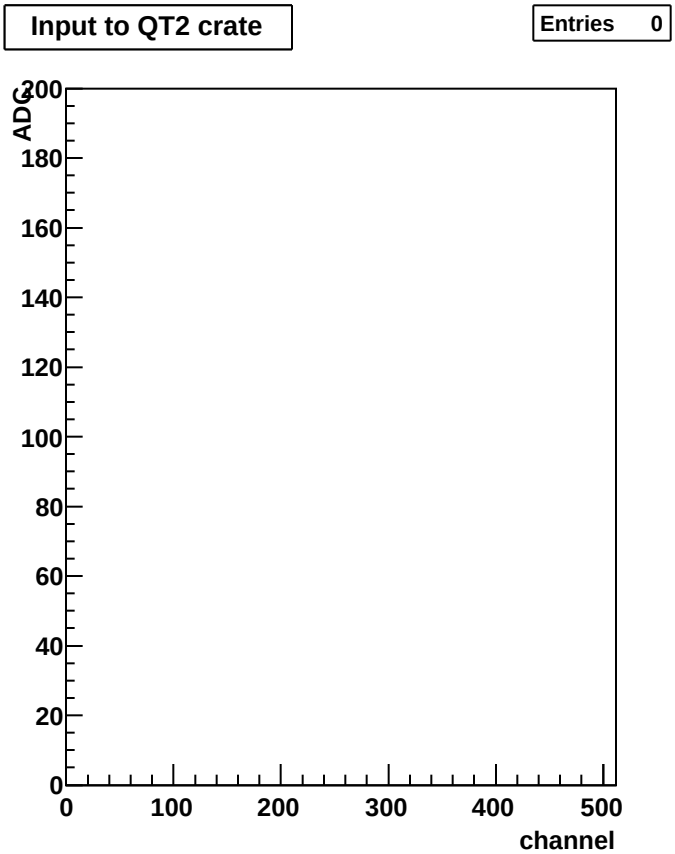

Entries 362035
BBC-L-East ADC Sum
BBC-L-West ADC Sum

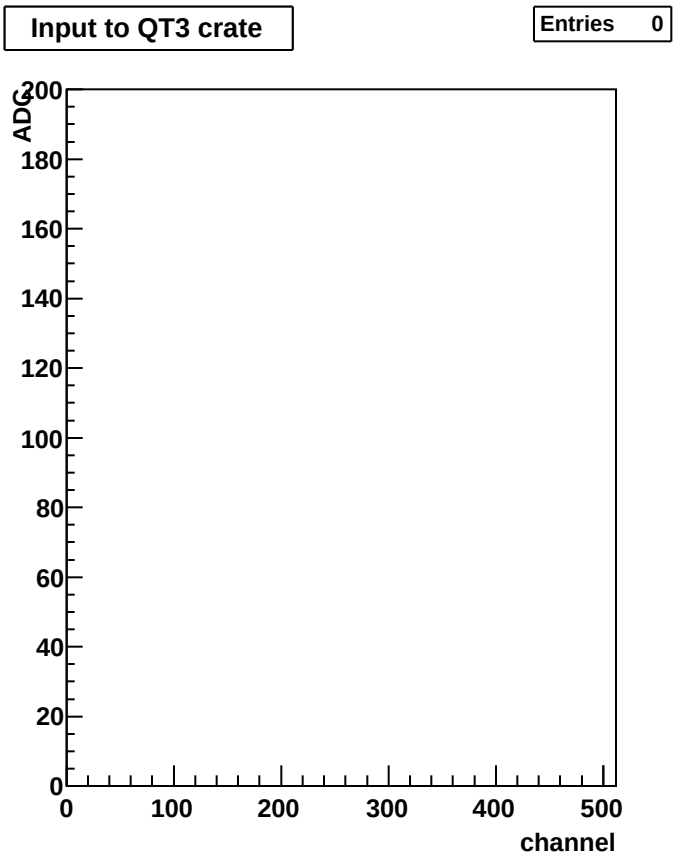

Entries 362035
BBC-S-East ADC Sum
BBC-S-West ADC Sum

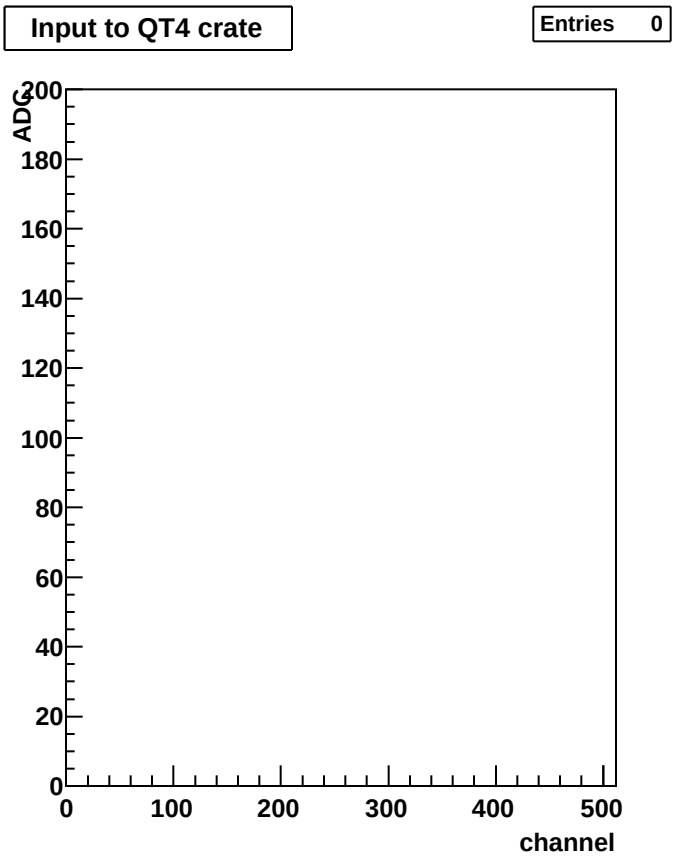

Entries 362035
ZDC East ADC Sum Att
ZDC West ADC Sum Att

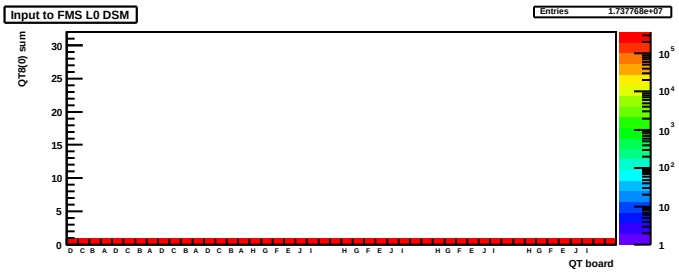

Entries 362035
BBC-L TAC Diff
BCC-S TAC Diff
Entries 362035
BBC-L TAC Diff
VPD TAC Diff
Entries 362035
BBC-L TAC Diff
ZDC TAC Diff
Entries 362035
BBC-S TAC Diff
VPD TAC Diff
Entries 362035
BBC-S TAC Diff
ZDC TAC Diff
Entries 362035
VPD TAC Diff
ZDC TAC Diff

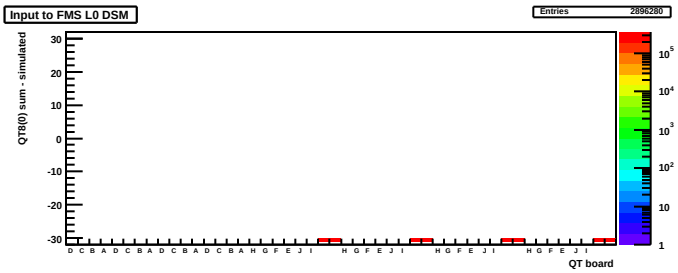

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

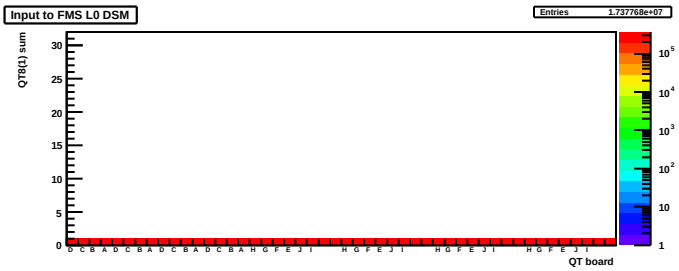

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

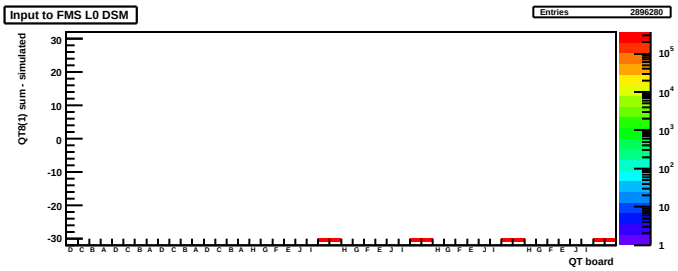

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

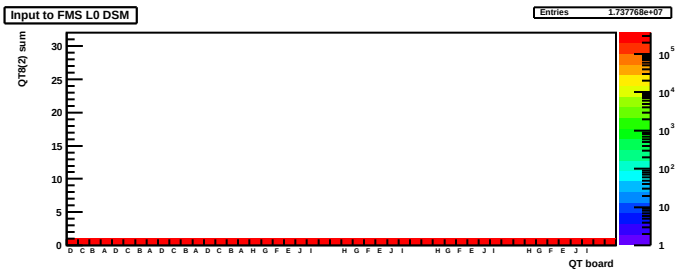

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

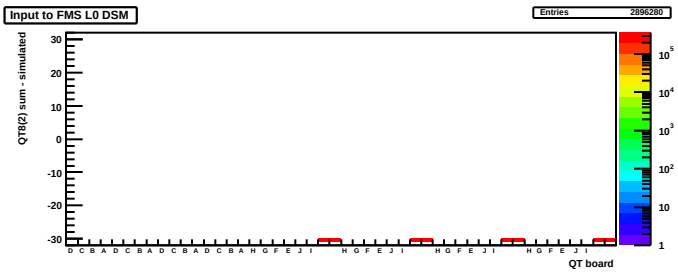

Input to FMS L0 DSM
Entries 1.737768e+07
QT8(0) sum
QT board

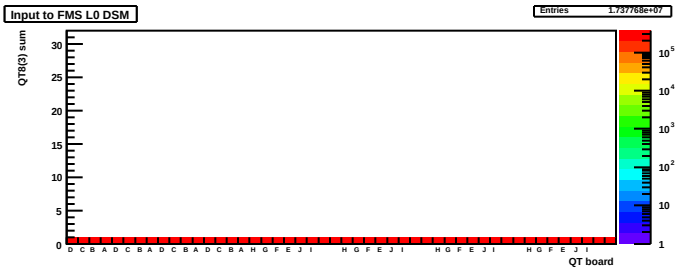

Input to FMS L0 DSM
Entries 2896280
QT8(0) sum - simulated
QT board

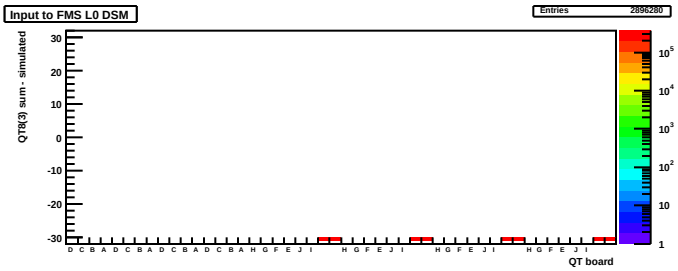

Input to FMS L0 DSM
Entries 1.737768e+07
QT8(1) sum
QT board

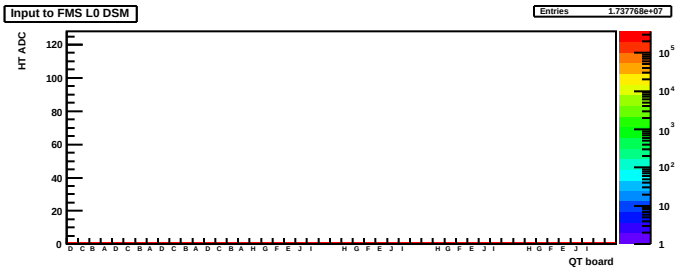

Input to FMS L0 DSM
Entries 2896280
QT8(1) sum - simulated
QT board

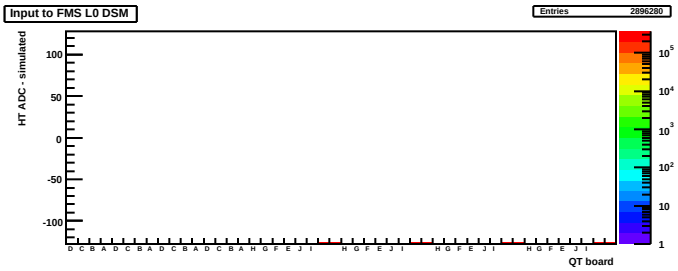

Input to FMS L0 DSM
Entries 1.737768e+07
QT8(2) sum
QT board

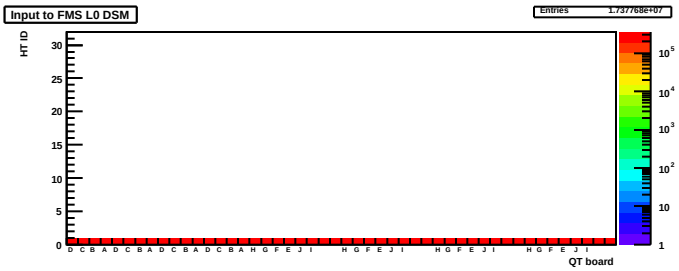

Input to FMS L0 DSM
Entries 2896280
QT8(2) sum - simulated
QT board

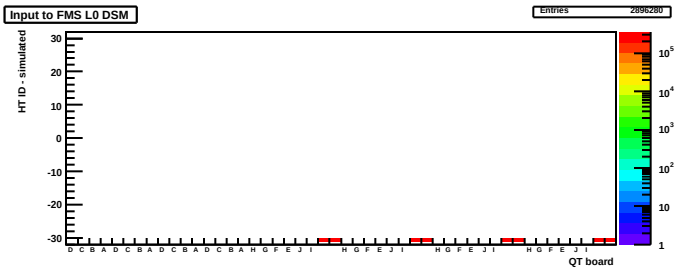

Input to FMS L0 DSM
Entries 1.737768e+07
QT8(3) sum
QT board

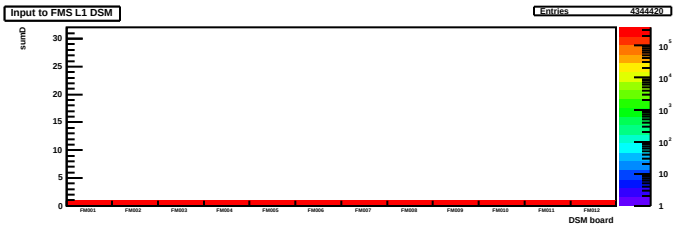

Input to FMS L0 DSM
Entries 2896280
QT8(3) sum - simulated
QT board

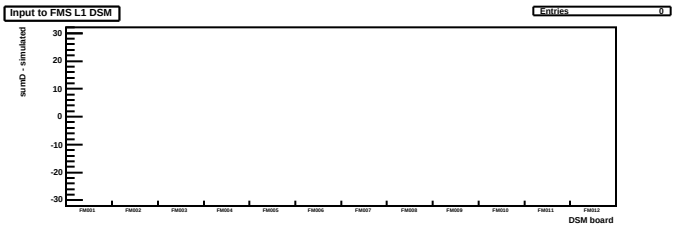

Input to FMS L0 DSM
Entries 1.737768e+07
HT ADC
QT board

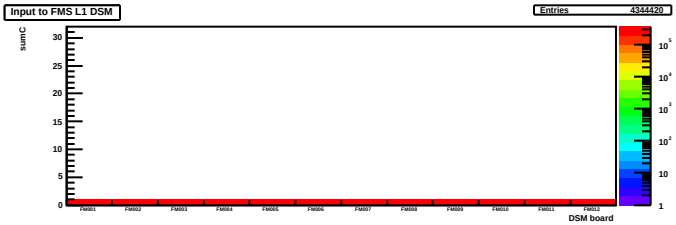

Input to FMS L0 DSM
Entries 2896280
HT ADC - simulated
QT board

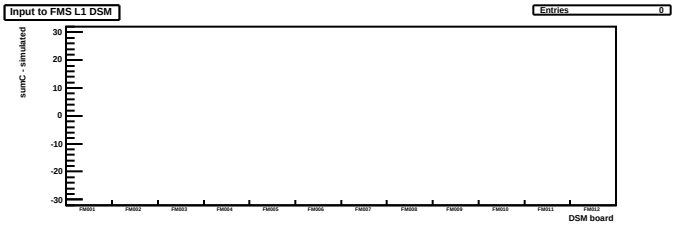

Input to FMS L0 DSM
Entries 1.737768e+07
HT ID
QT board

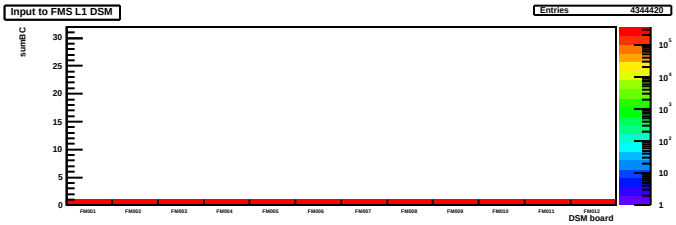

Input to FMS L0 DSM
Entries 2896280
HT ID - simulated
QT board

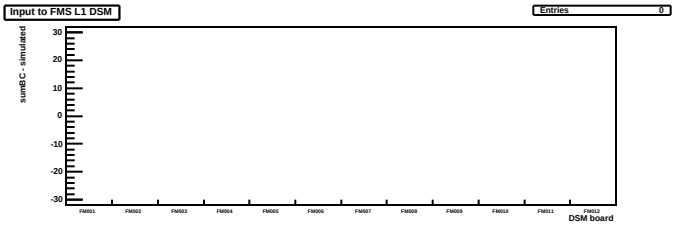

Input to FMS L1 DSM
Entries 4344420
sumD
DSM board

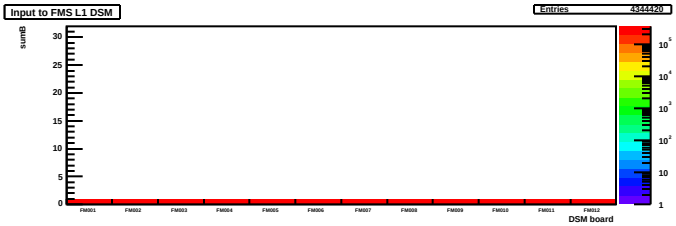

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

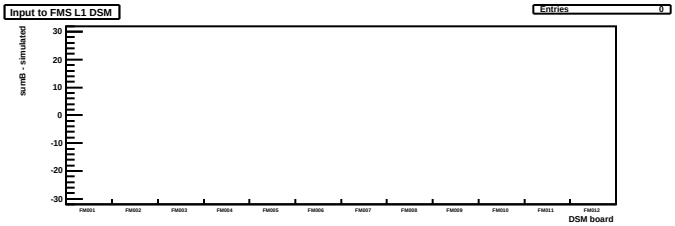

Input to FMS L1 DSM
Entries 4344420
sumC
DSM board

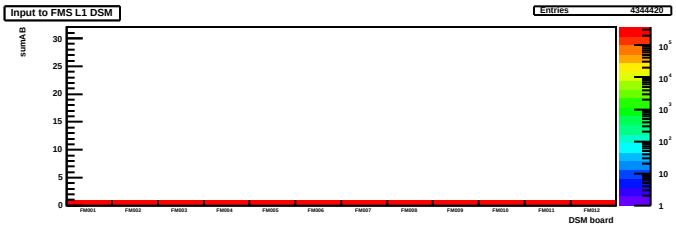

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

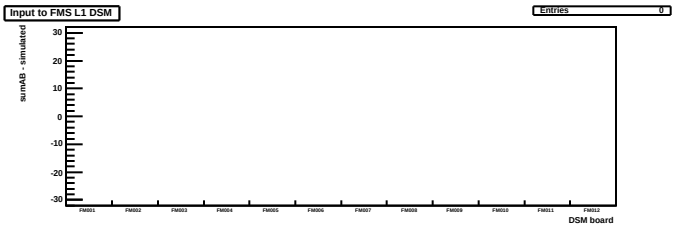

Input to FMS L1 DSM
Entries 4344420
sumBC
DSM board

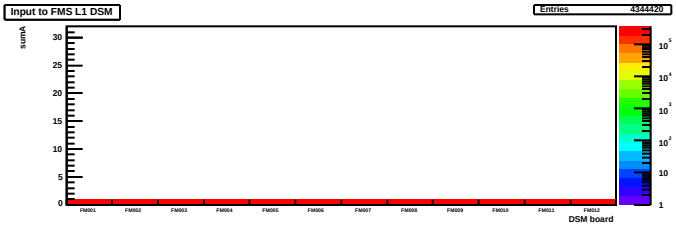

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

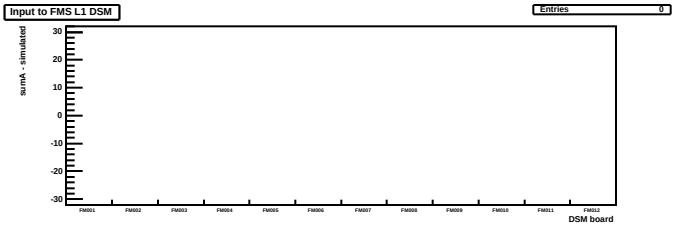

Input to FMS L1 DSM
Entries 4344420
sumB
DSM board

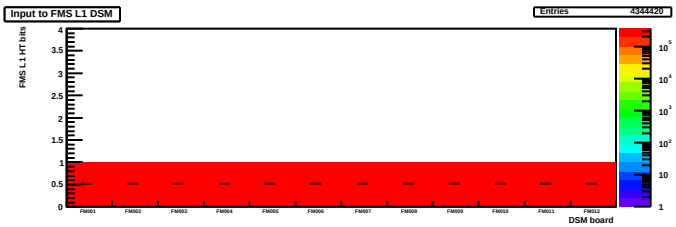

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

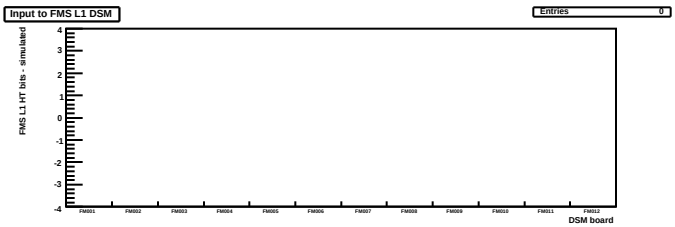

Input to FMS L1 DSM
Entries 4344420
sumAB
DSM board

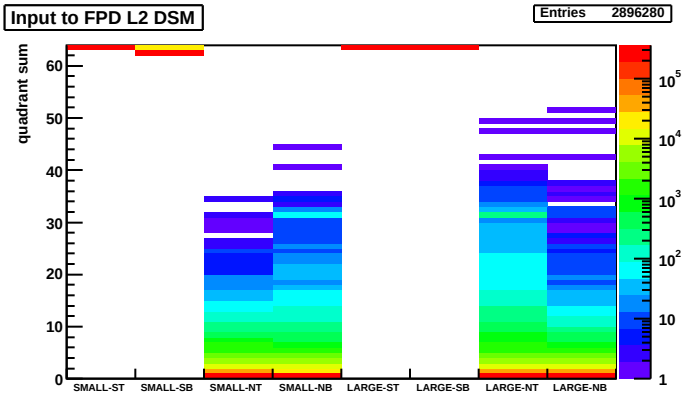

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

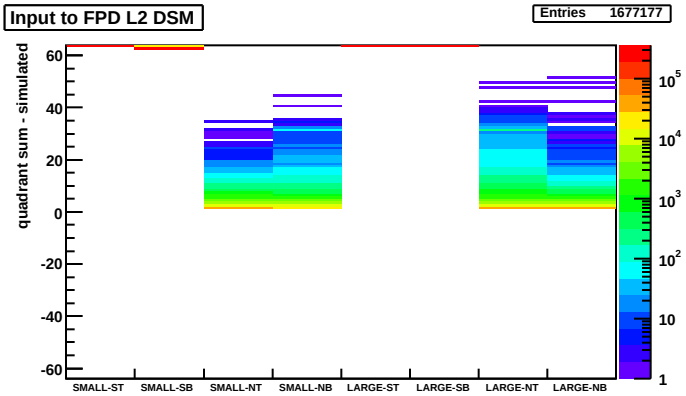

Input to FMS L1 DSM
Entries 4344420
sumA
DSM board

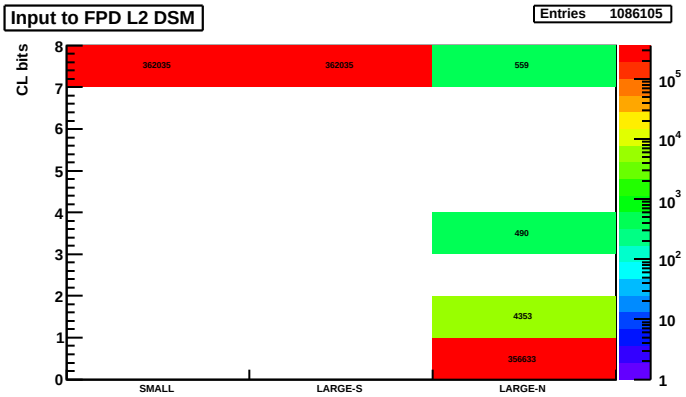

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

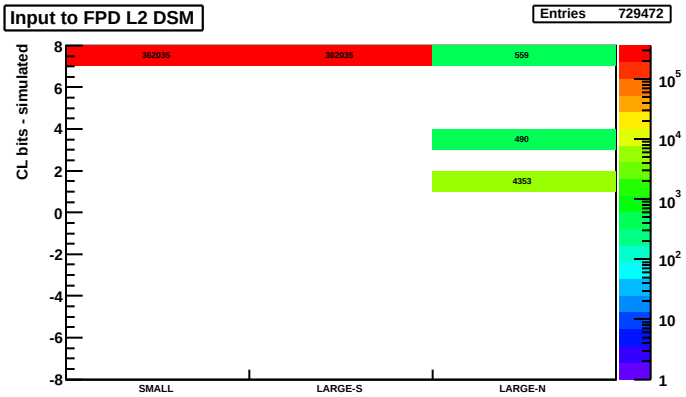

Input to FMS L1 DSM
Entries 4344420
FMS L1 HT bits
DSM board

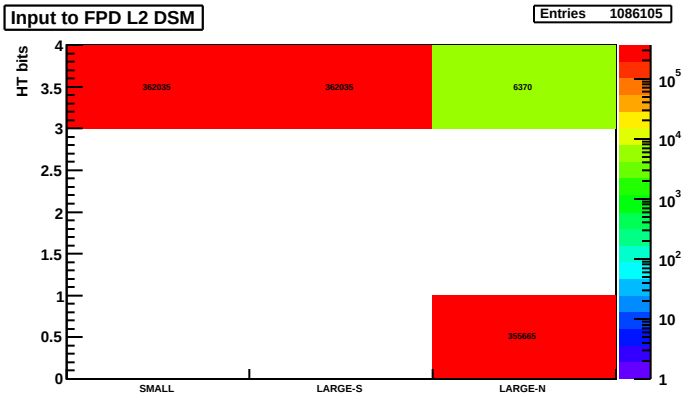

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

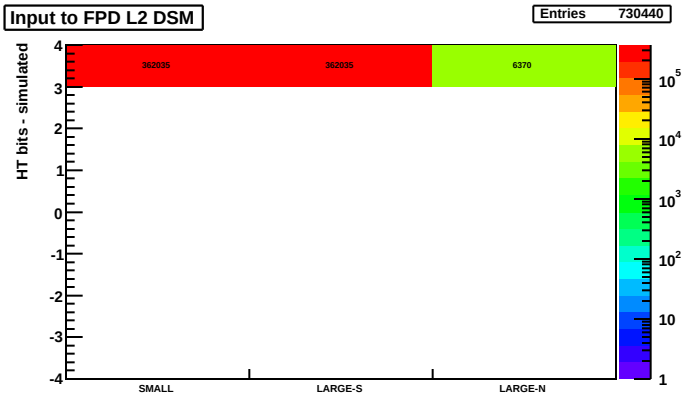

Input to FPD L2 DSM
Entries 2896280
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

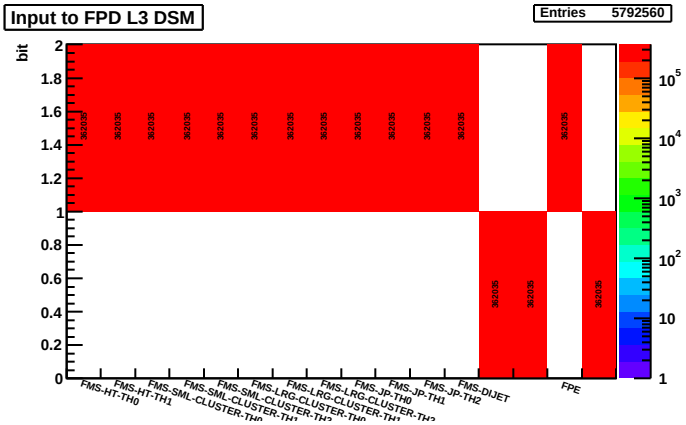

Input to FPD L2 DSM
Entries 1677177
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

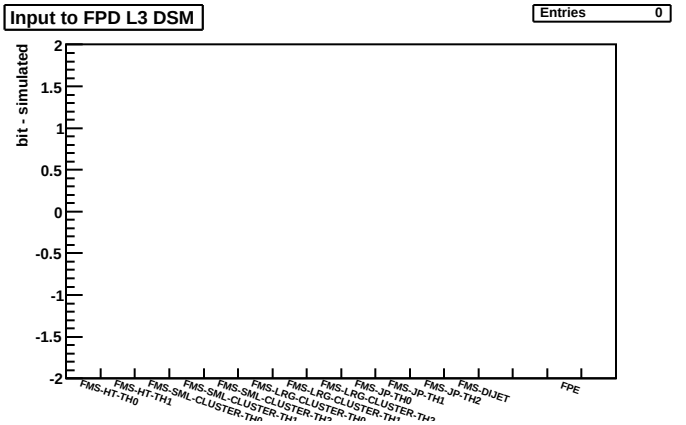

Input to FPD L2 DSM
Entries 1086105
CL bits
362035
362035
559
490
4353
356633
SMALL
LARGE-S
LARGE-N

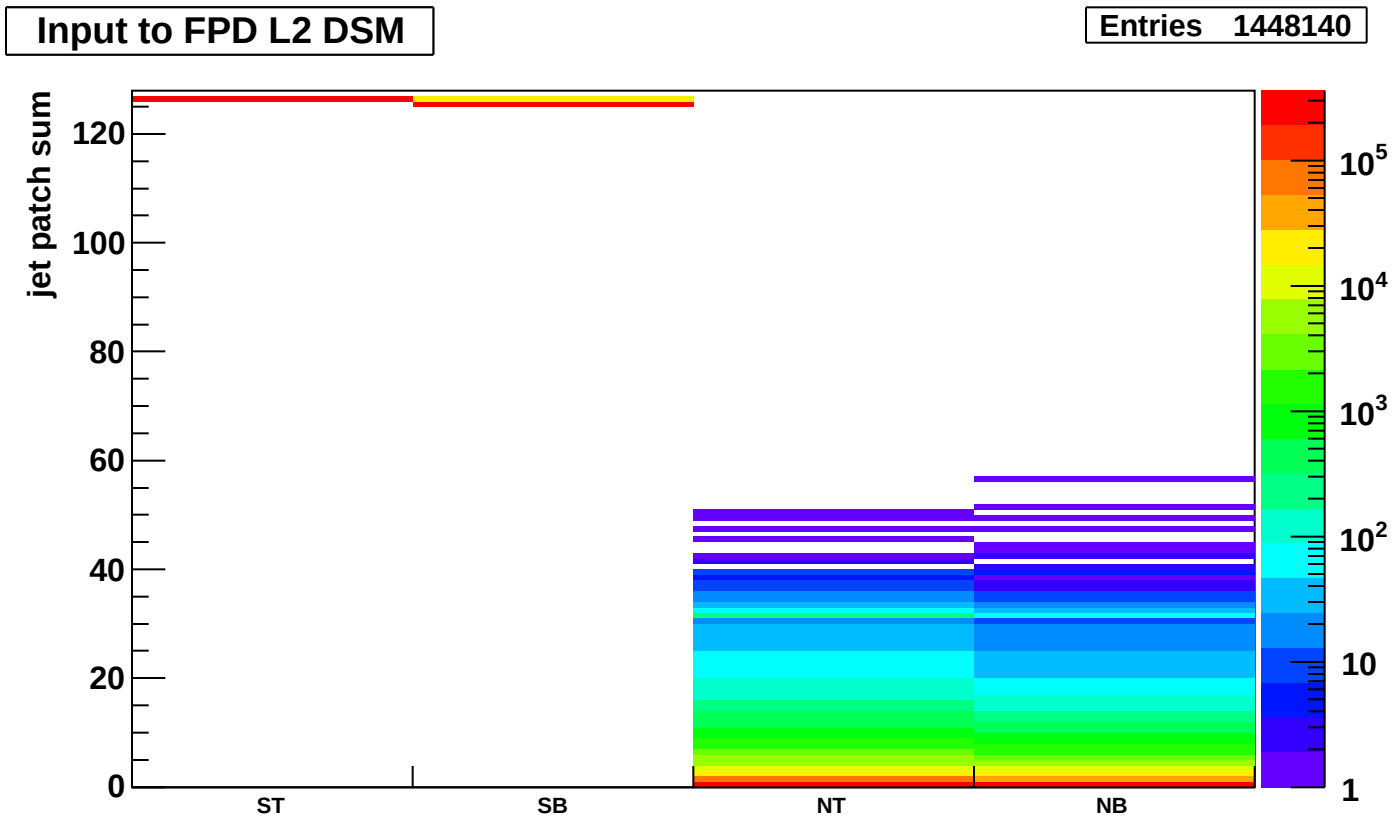

Input to FPD L2 DSM
Entries 729472
CL bits - simulated
362035
362035
559
490
4353
SMALL
LARGE-S
LARGE-N

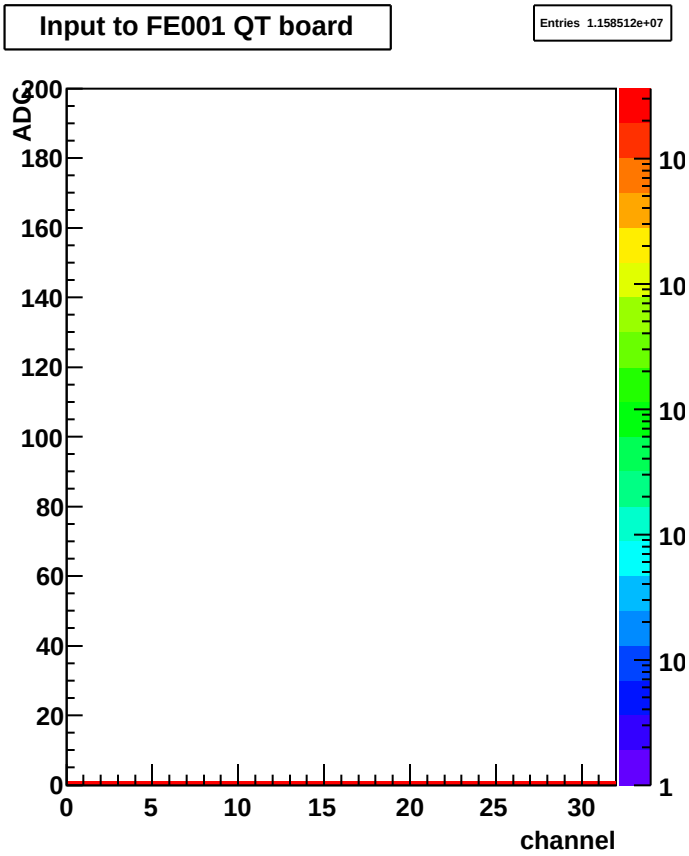

Input to FPD L2 DSM
Entries 1086105
HT bits
362035
362035
6370
355665
SMALL
LARGE-S
LARGE-N

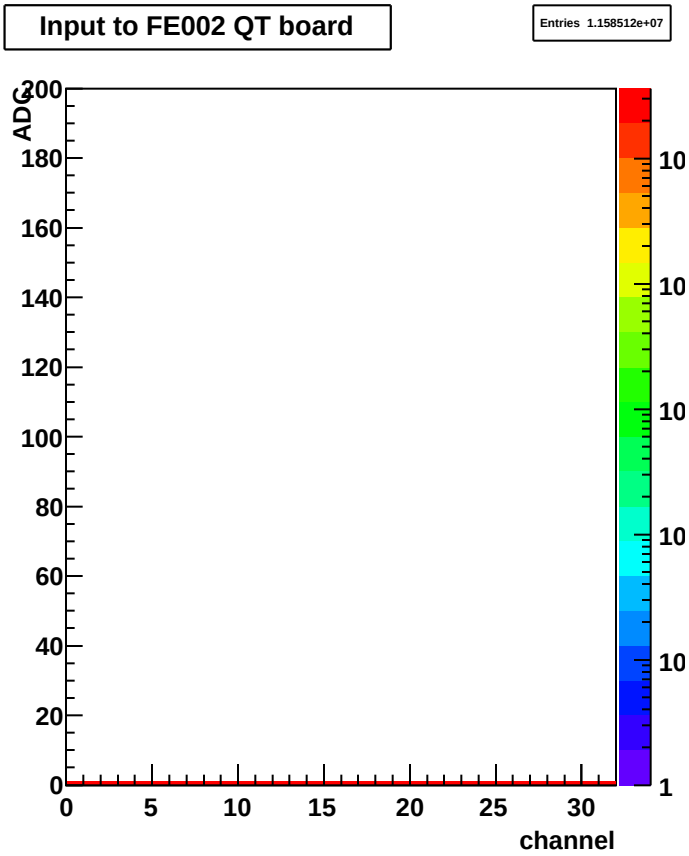

Input to FPD L2 DSM
Entries 730440
HT bits - simulated
362035
362035
6370
SMALL
LARGE-S
LARGE-N

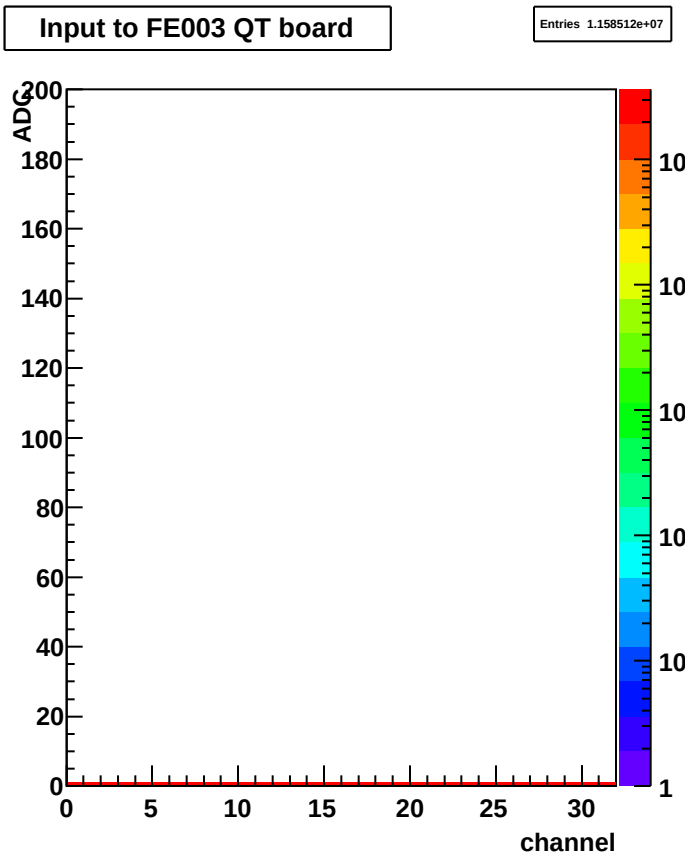

Input to FPD L3 DSM
Entries 5792560
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

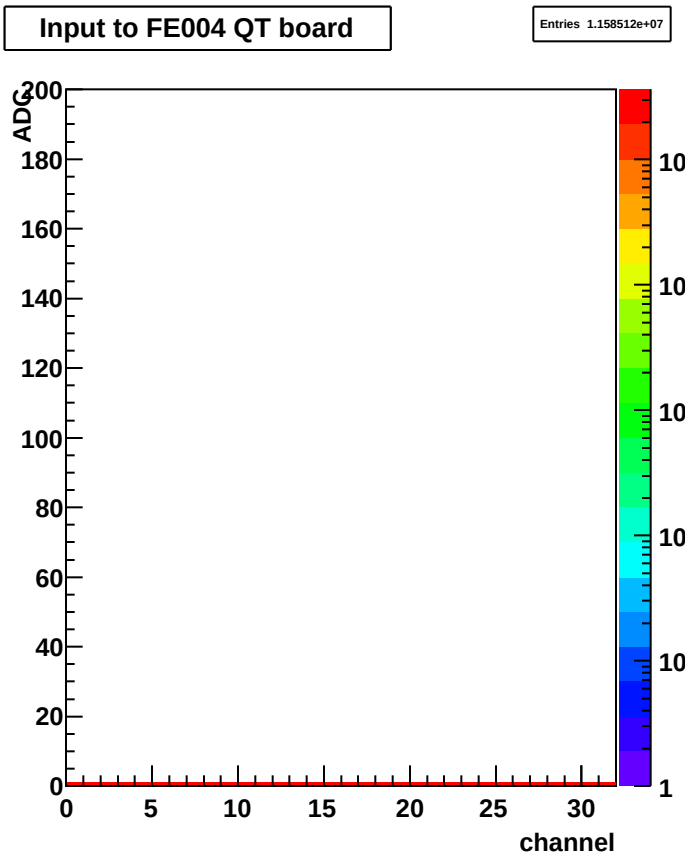

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

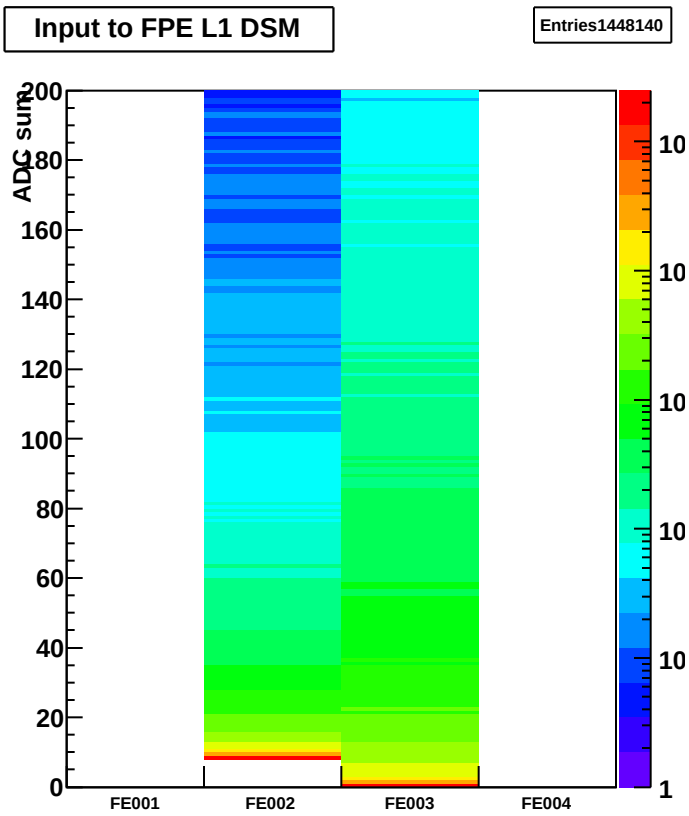

Input to FPD L2 DSM
Entries 1448140
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

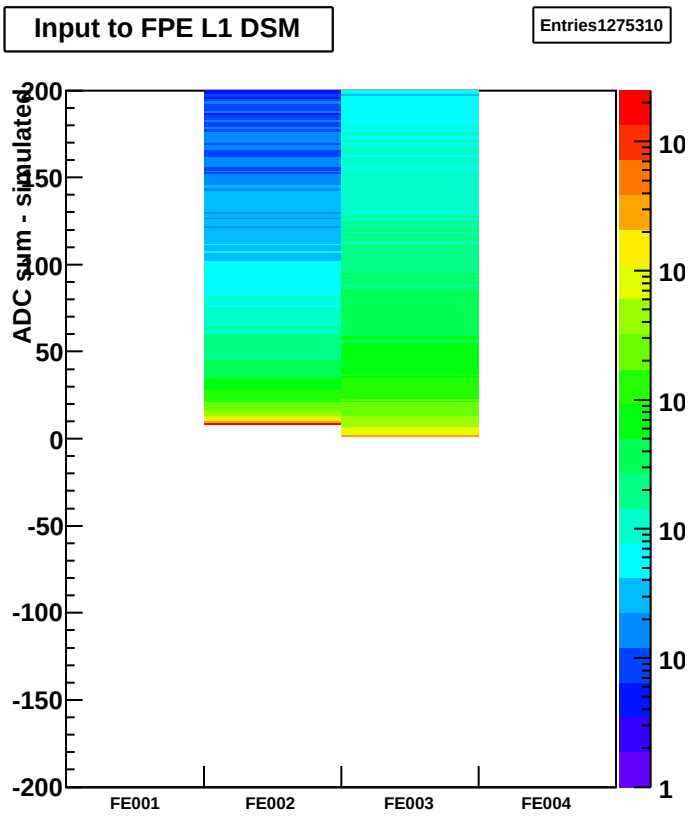

Input to FE001 QT board
Entries 1.158512e+07
ADC
channel

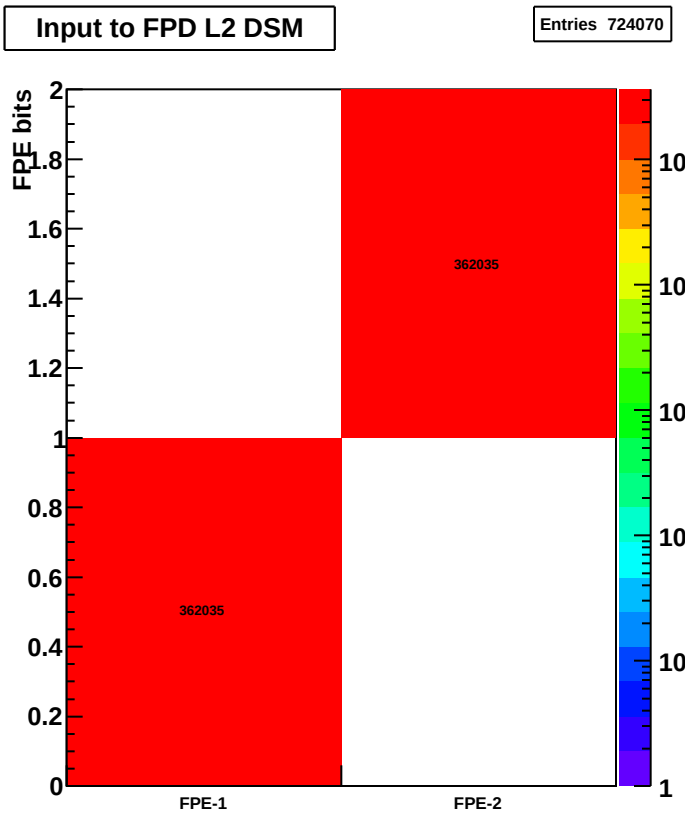

Input to FE002 QT board
Entries 1.158512e+07
ADC
channel

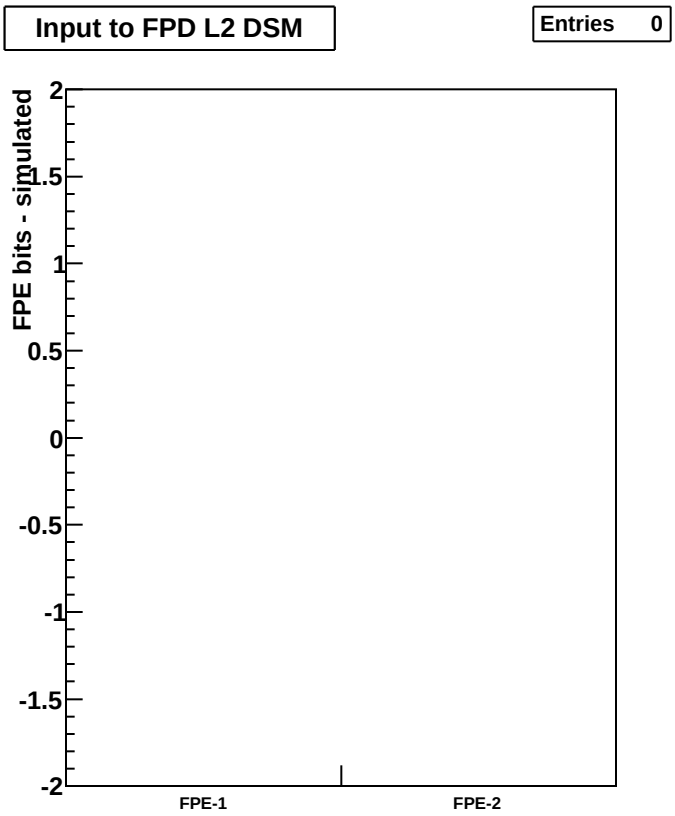

Input to FE003 QT board
Entries 1.158512e+07
ADC
channel


Input to FE004 QT board
Entries 1.158512e+07
ADC
channel


Input to FPE L1 DSM
Entries1448140
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1275310
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 724070
FPE bits
362035
362035
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2