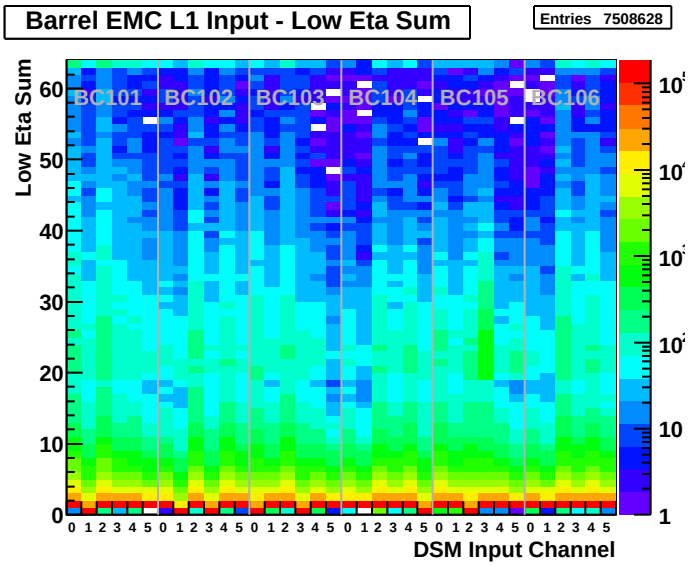
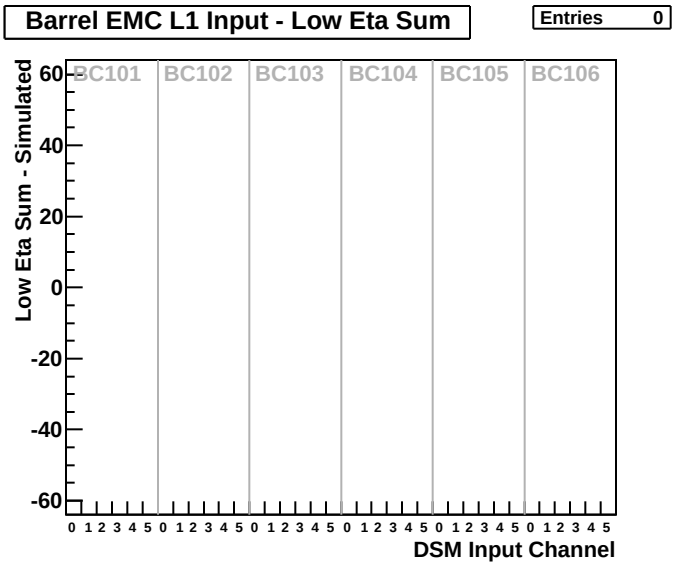
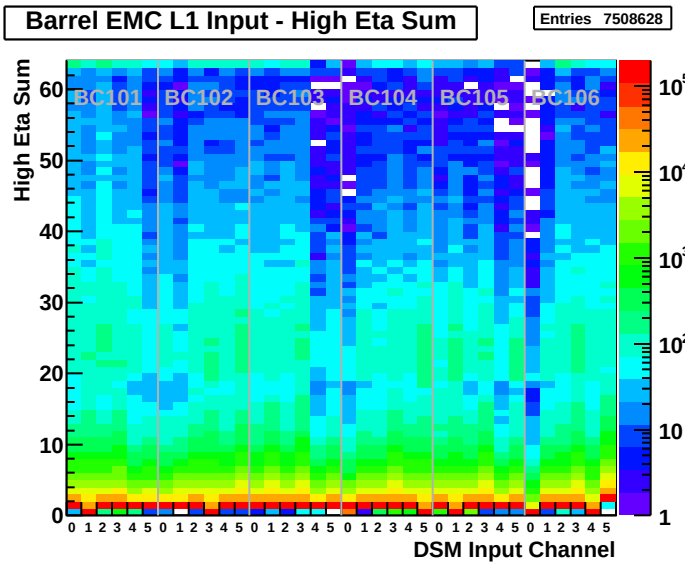
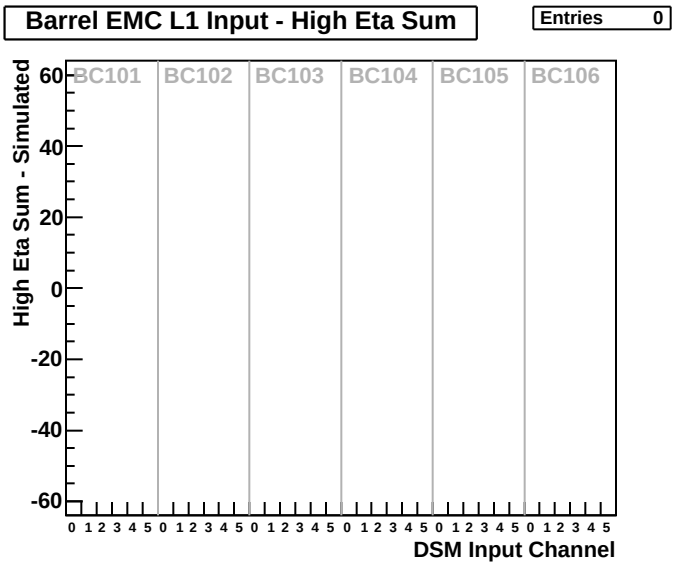
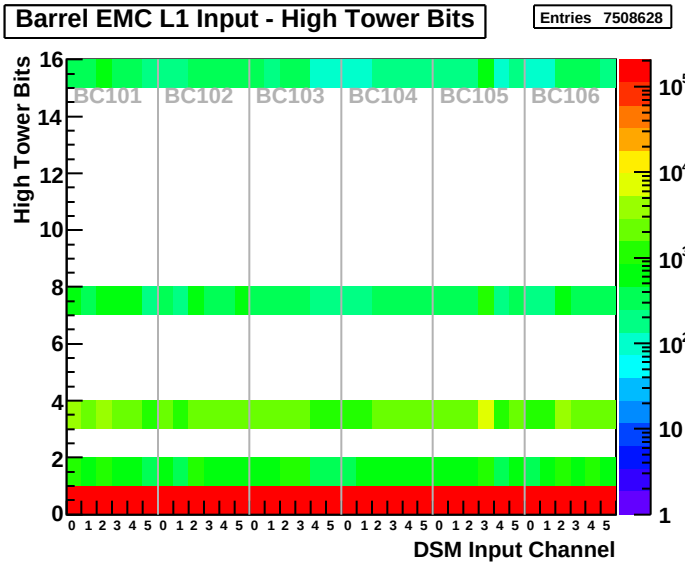
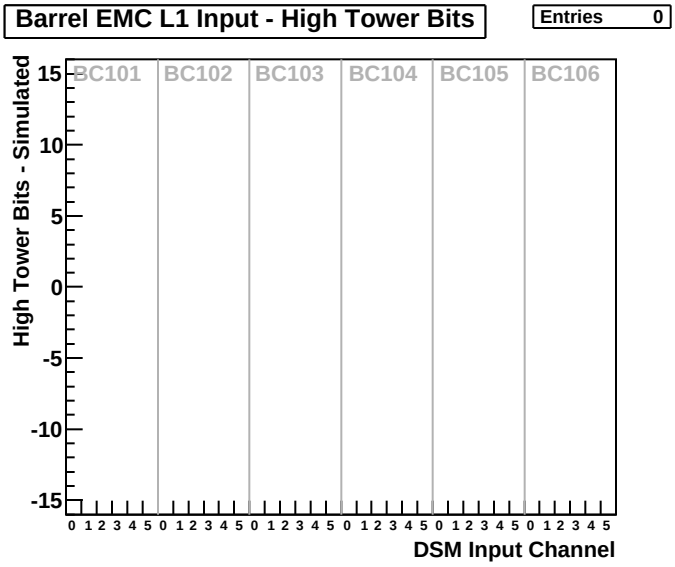

Barrel EMC L1 Input - Low Eta Sum
Entries 7508628
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

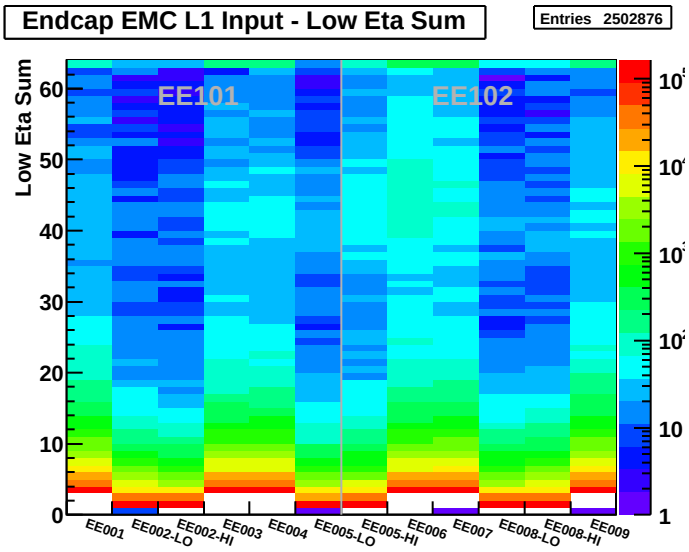

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

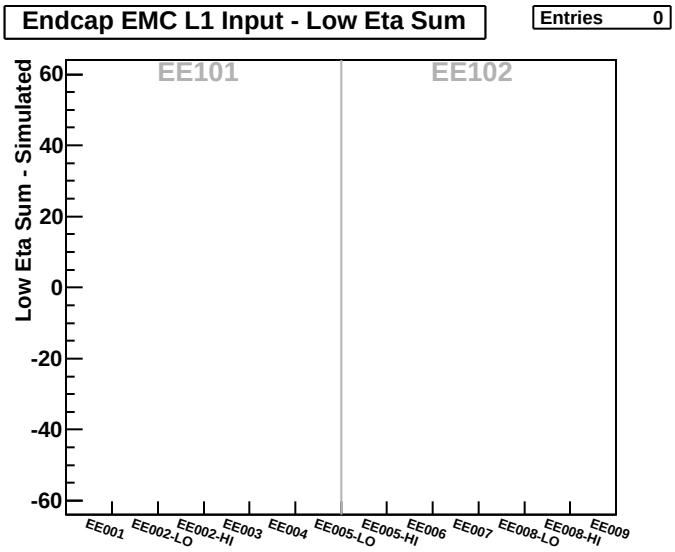

Barrel EMC L1 Input - High Eta Sum
Entries 7508628
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

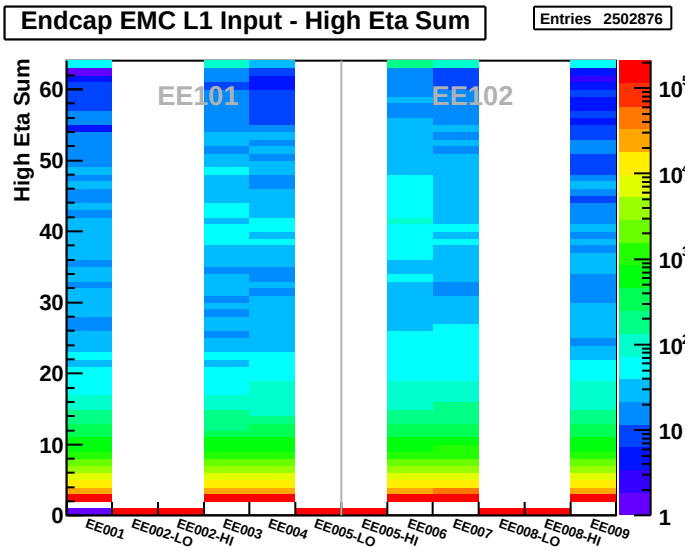

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

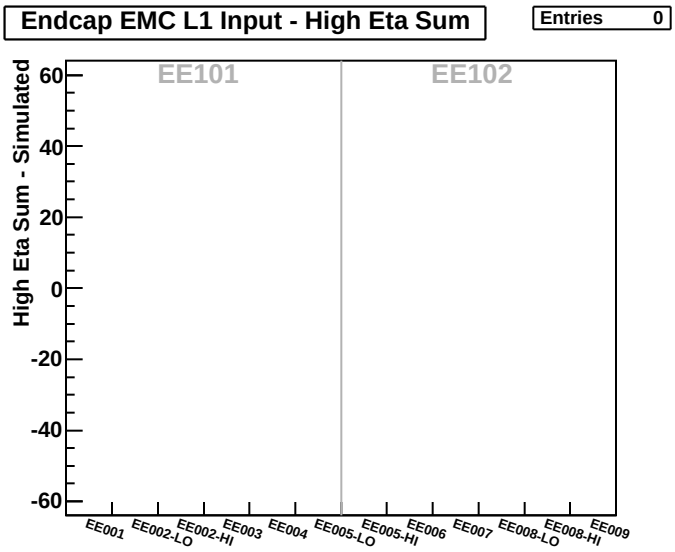

Barrel EMC L1 Input - High Tower Bits
Entries 7508628
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

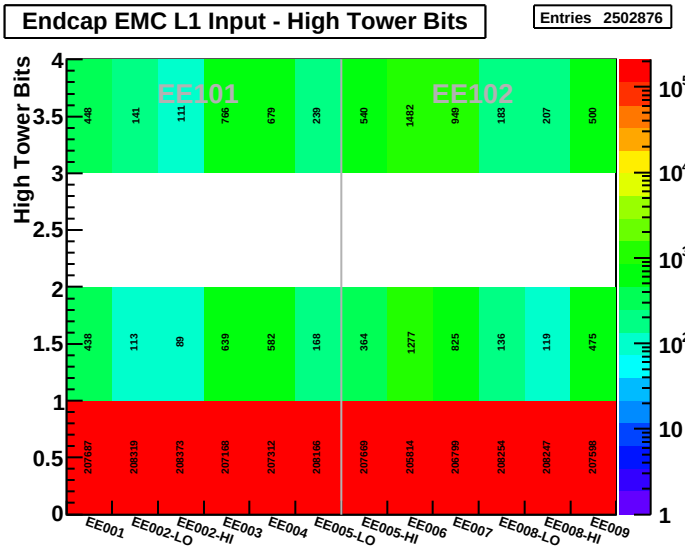

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

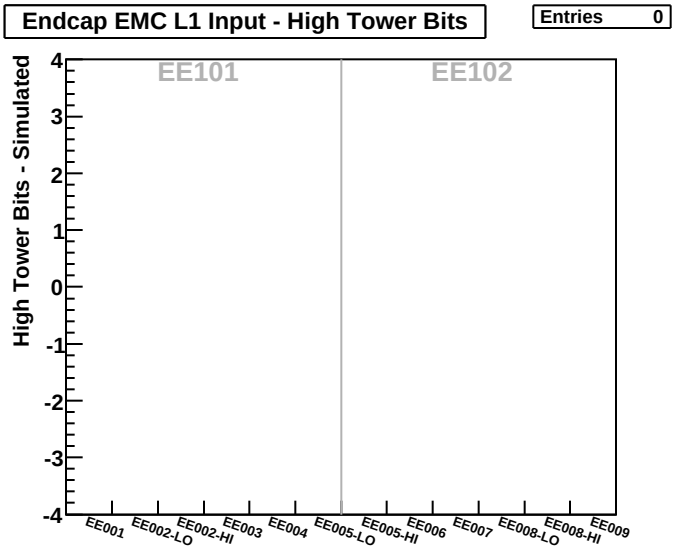

Endcap EMC L1 Input - Low Eta Sum
Entries 2502876
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

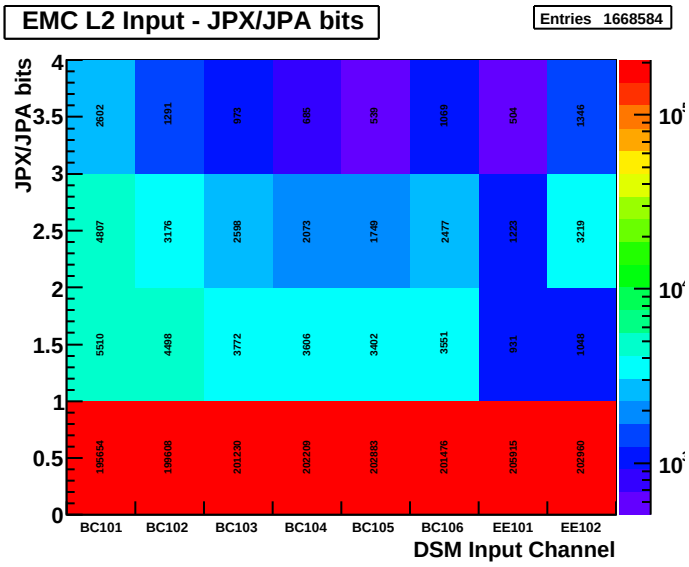

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

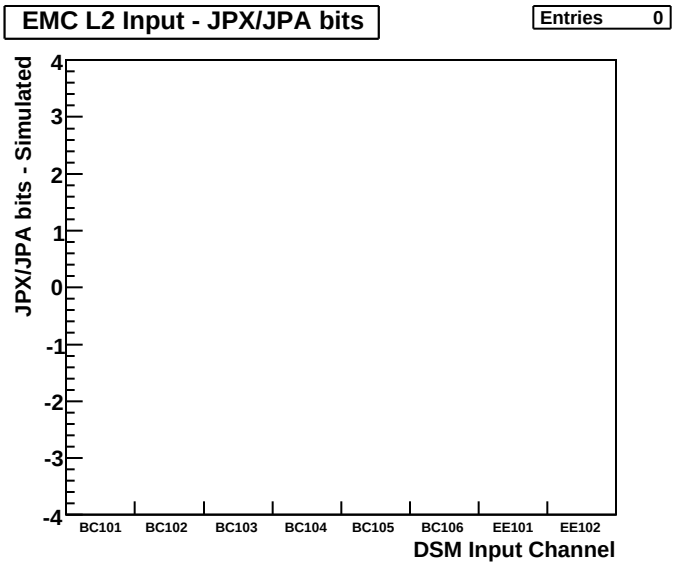

Endcap EMC L1 Input - High Eta Sum
Entries 2502876
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

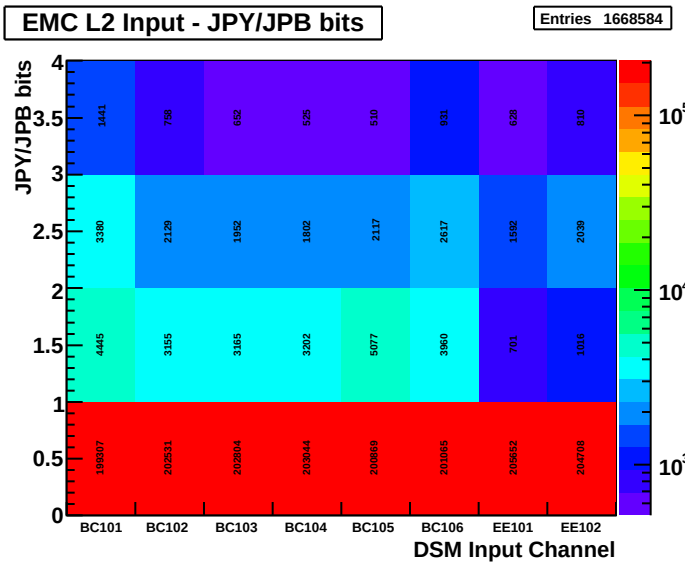

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

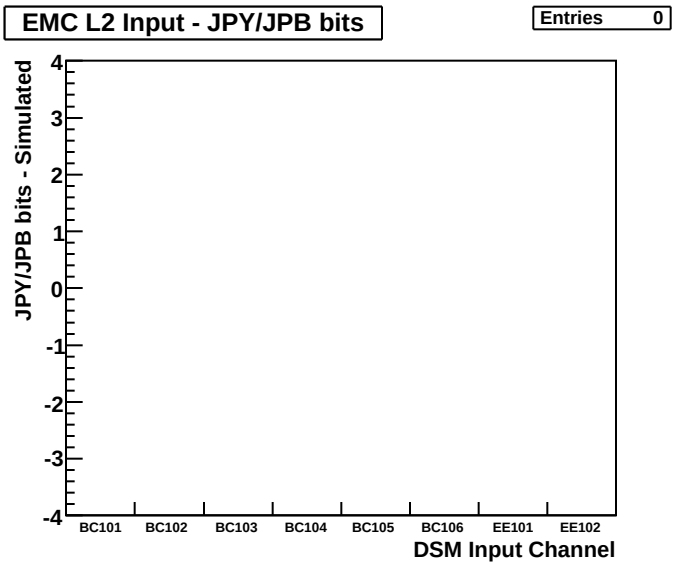

Endcap EMC L1 Input - High Tower Bits
Entries 2502876
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

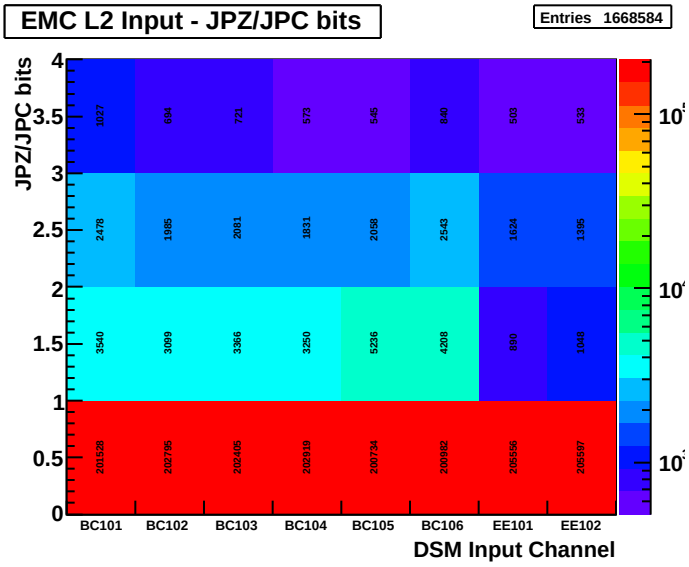

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

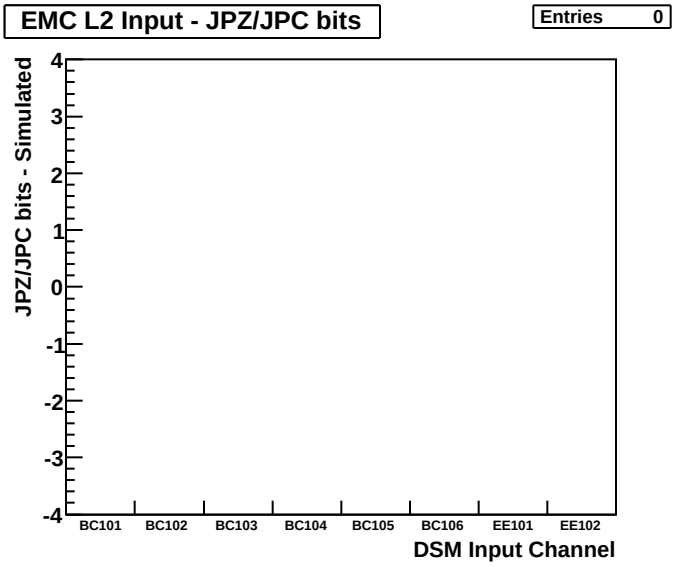

EMC L2 Input - JPX/JPA bits
Entries 1668584
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2602 1291 973 685 539 1069 504 1346
4807 3176 2598 2073 1749 2477 1223 3219
5510 4498 3772 3606 3402 3551 931 1048
195654 199608 201230 202209 202893 201476 205915 202960

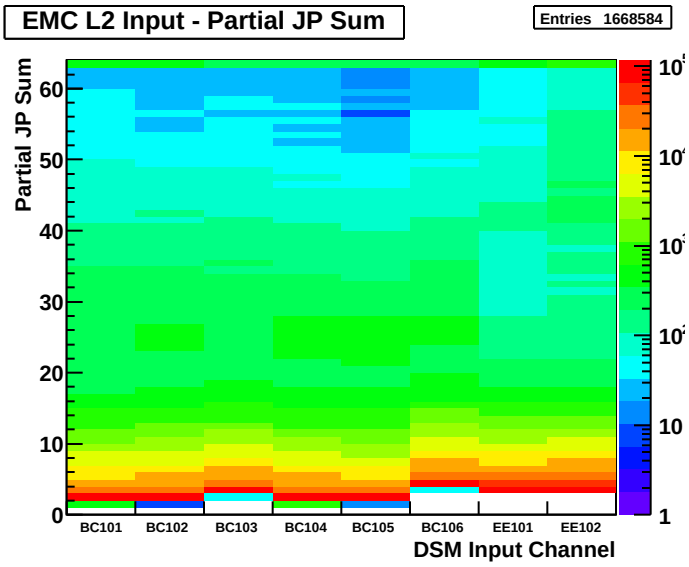

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

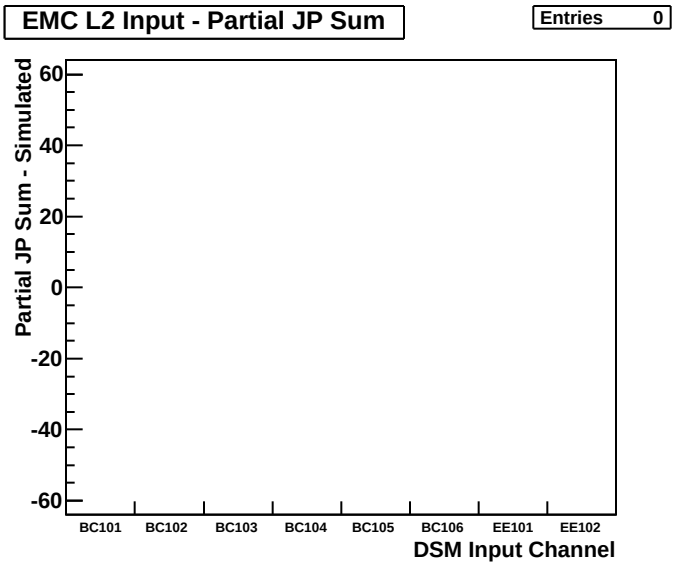

EMC L2 Input - JPY/JPB bits
Entries 1668584
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1441 758 652 525 510 931 628 810
3380 2129 1952 1802 2117 2617 1582 2039
4445 3155 3165 3202 5077 3960 701 1016
199307 202531 202804 203044 200869 201065 205652 204708

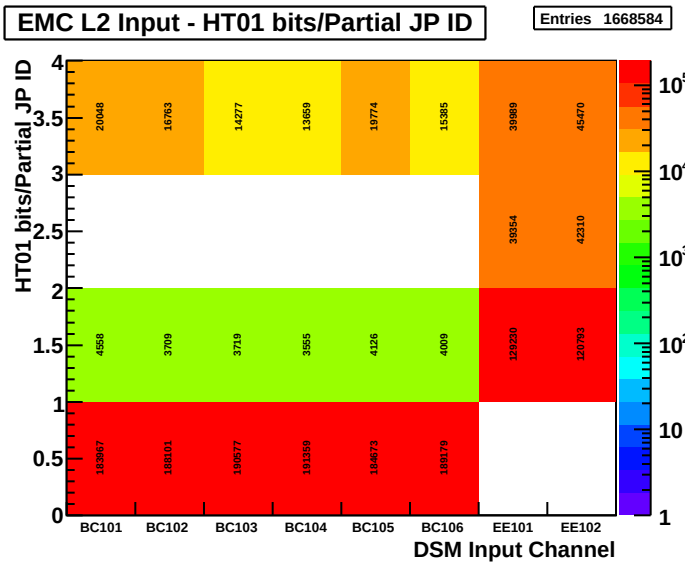

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

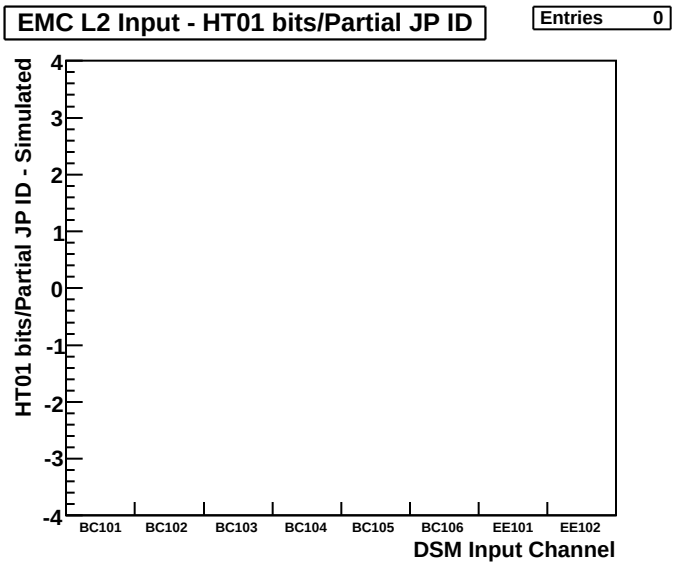

EMC L2 Input - JPZ/JPC bits
Entries 1668584
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1027 694 721 573 545 840 503 533
2478 1985 2081 1831 2058 2543 1624 1395
3540 3099 3366 3250 5236 4208 890 1048
201528 202795 202405 202919 200734 200982 205558 205597

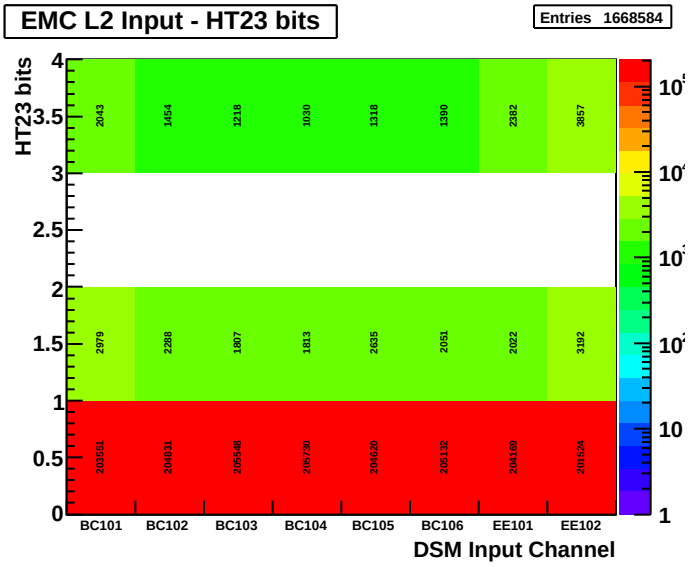

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

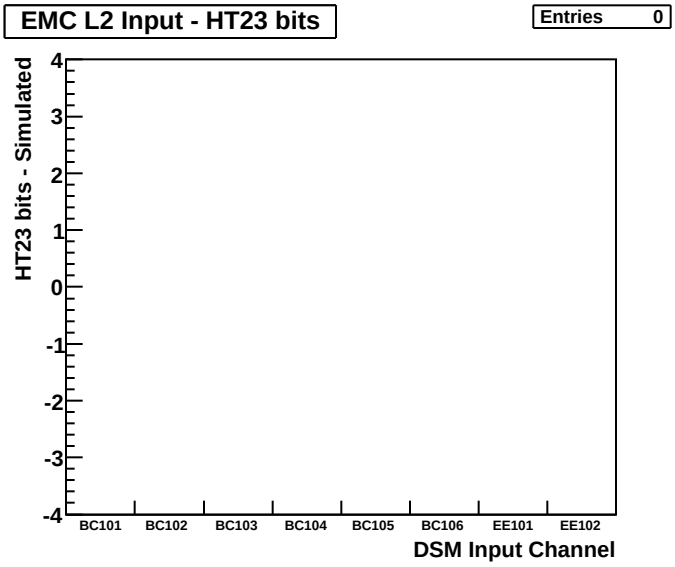

EMC L2 Input - Partial JP Sum
Entries 1668584
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

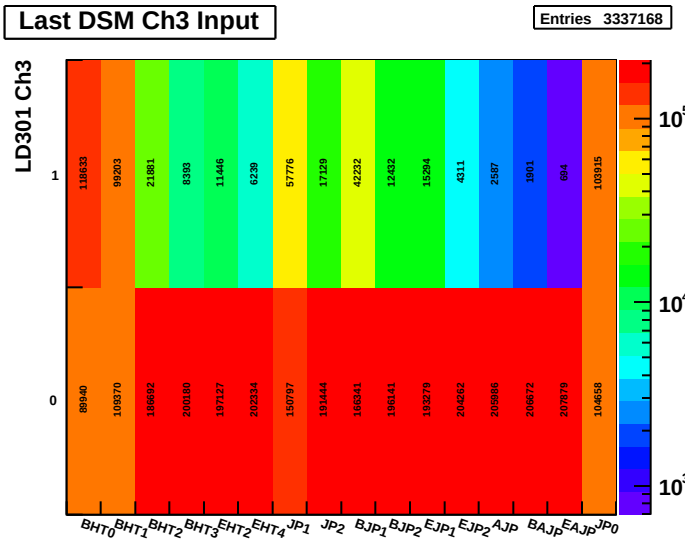

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

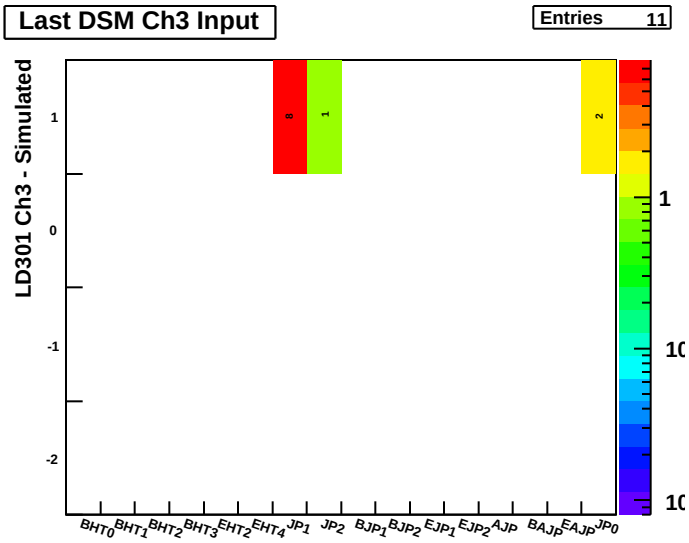

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1668584
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

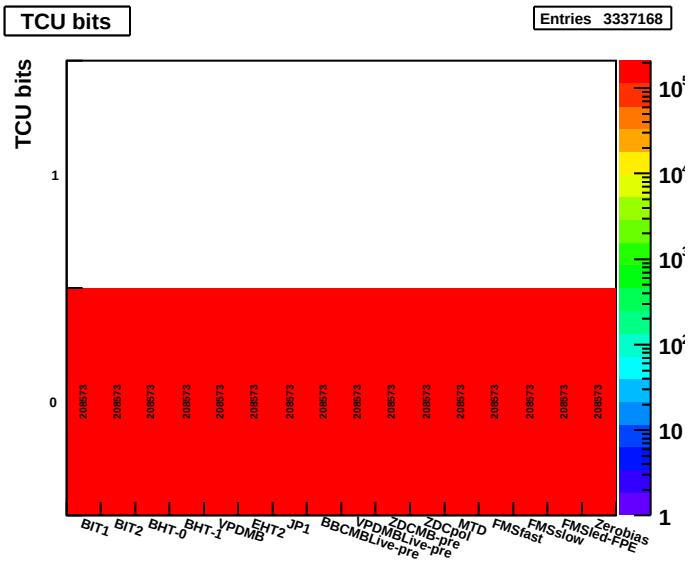

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

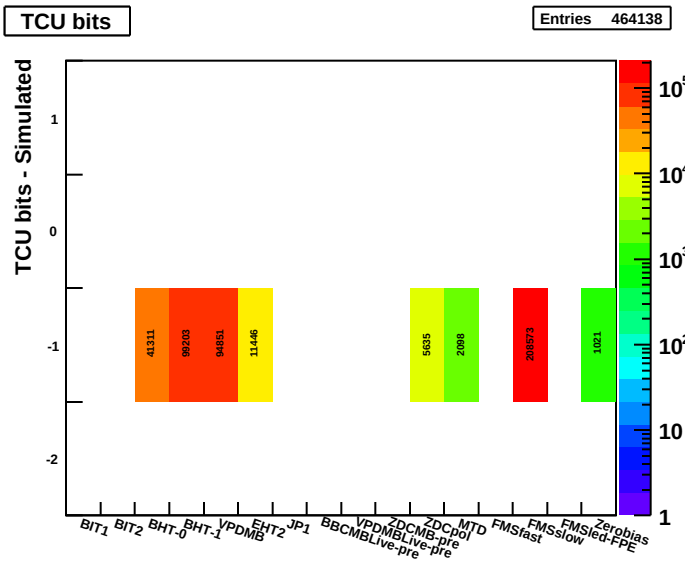

EMC L2 Input - HT23 bits
Entries 1668584
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

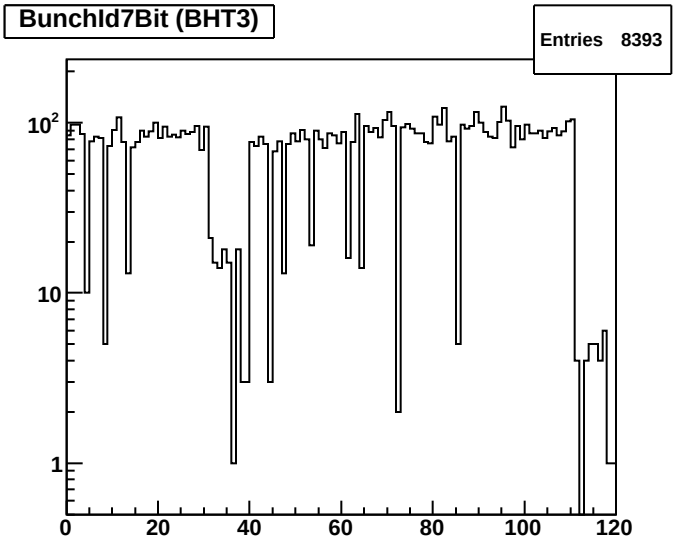

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

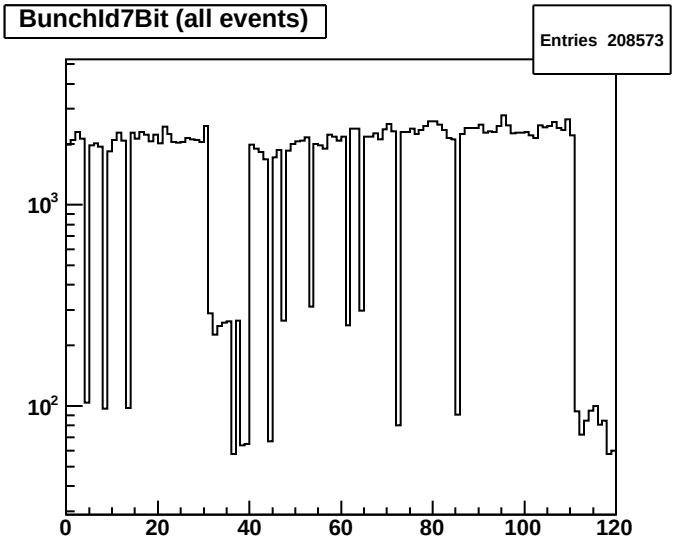

Last DSM Ch3 Input
Entries 3337168
LD301 Ch3
1
0
118633 99203 21881 8393 11446 6239 57776 17129 42232 12432 15294 4311 2587 1901 694 103915
89940 109370 186692 200180 197127 202334 150797 191444 166341 196141 193279 204262 205986 206672 207879 104658
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

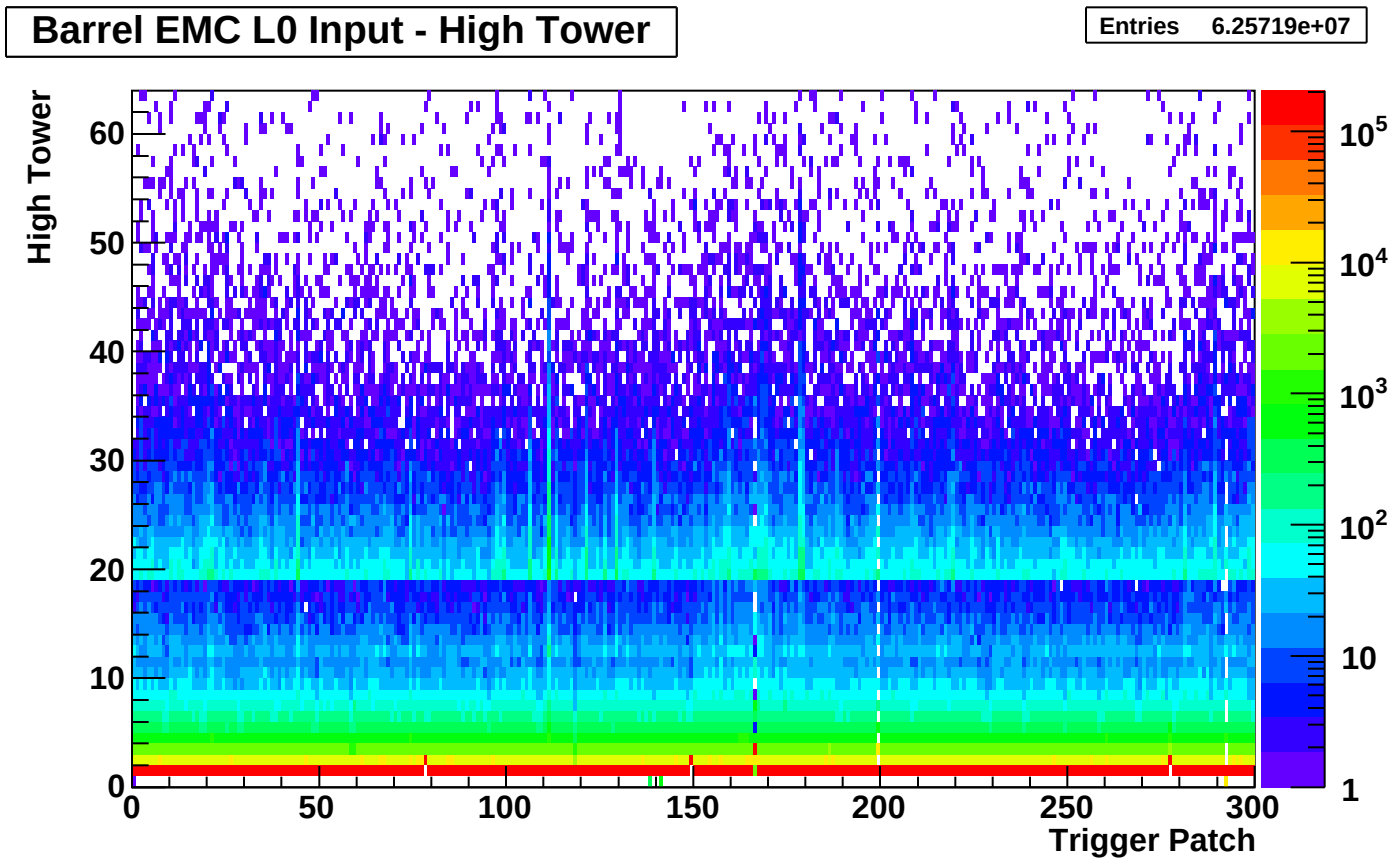

Last DSM Ch3 Input
Entries 11
LD301 Ch3 - Simulated
1
0
-1
-2
8 1 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

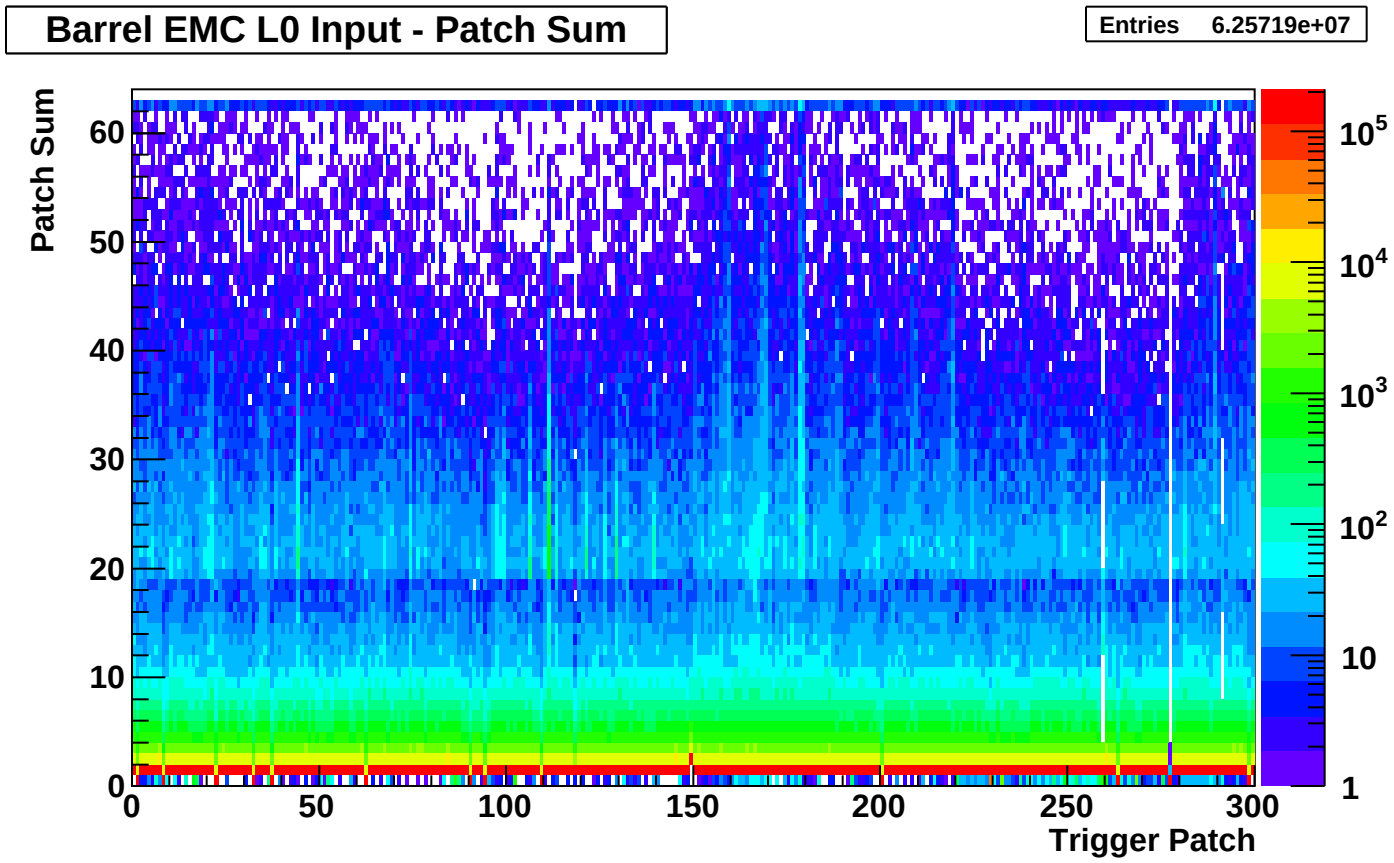

TCU bits
Entries 3337168
TCU bits
1
0
208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573 208573
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

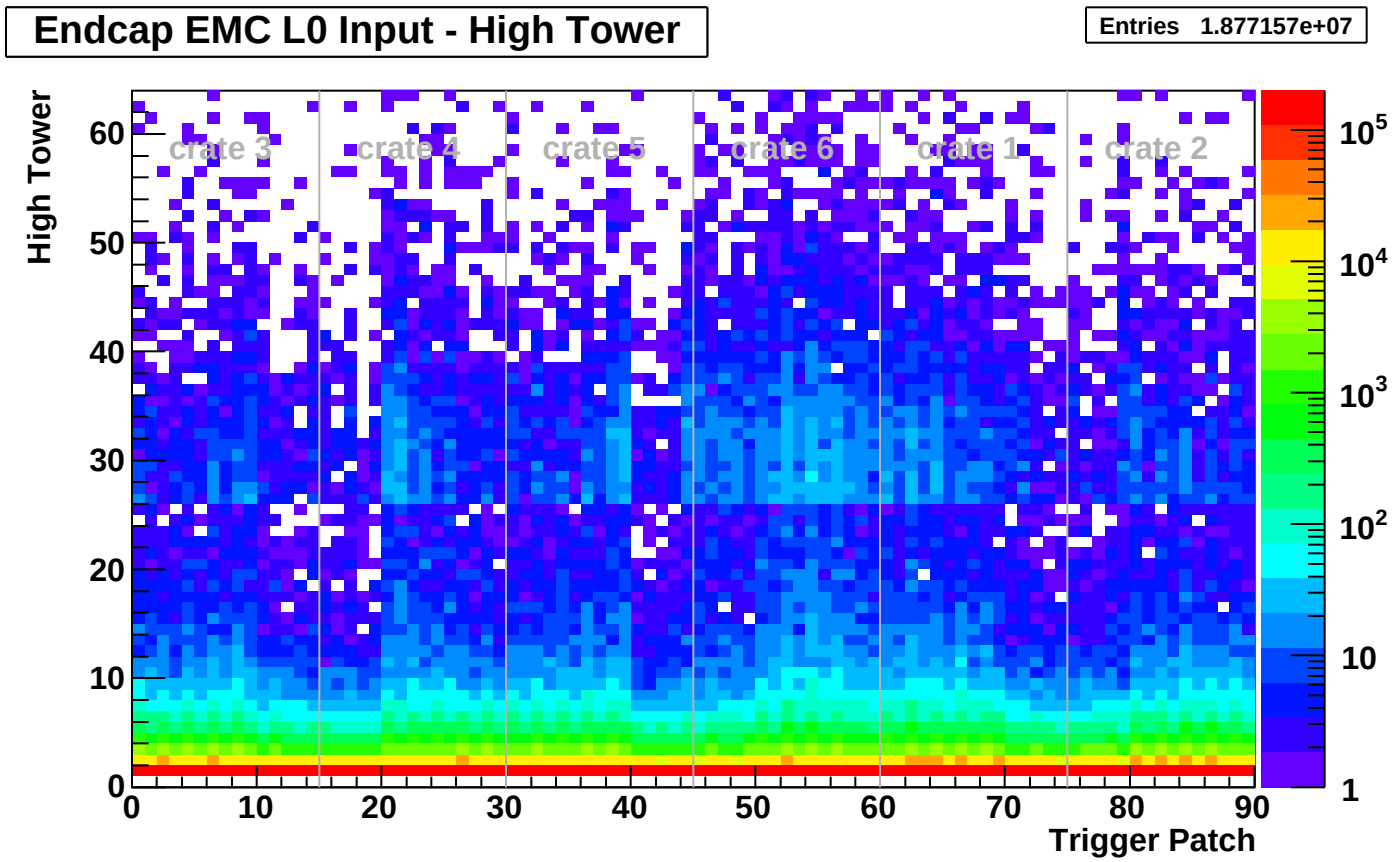

TCU bits
Entries 464138
TCU bits - Simulated
1
0
-1
-2
41311 99203 94851 11446 5635 2098 208573 1021
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

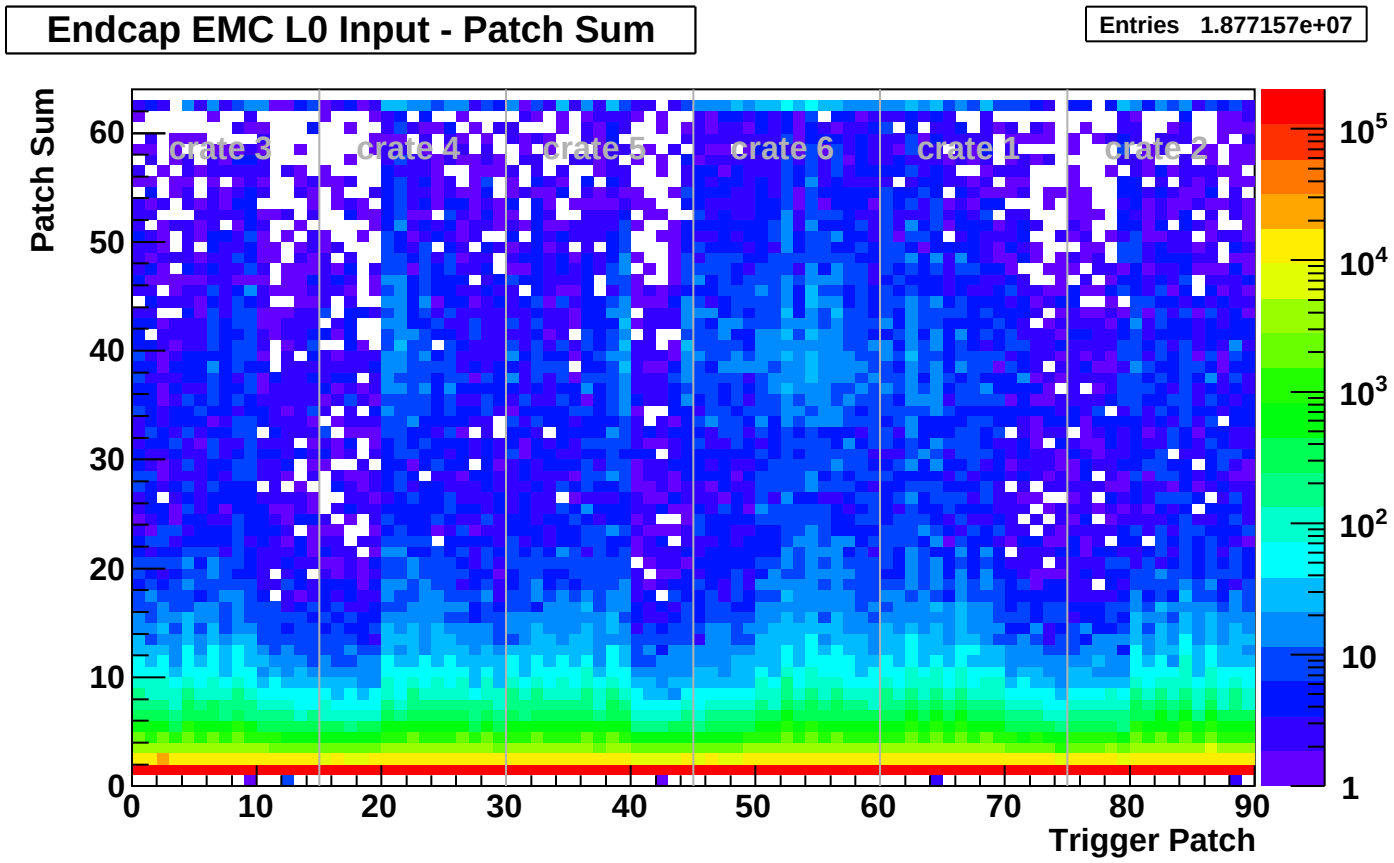

BunchId7Bit (BHT3)
Entries 8393
10^2
10
1
0 20 40 60 80 100 120

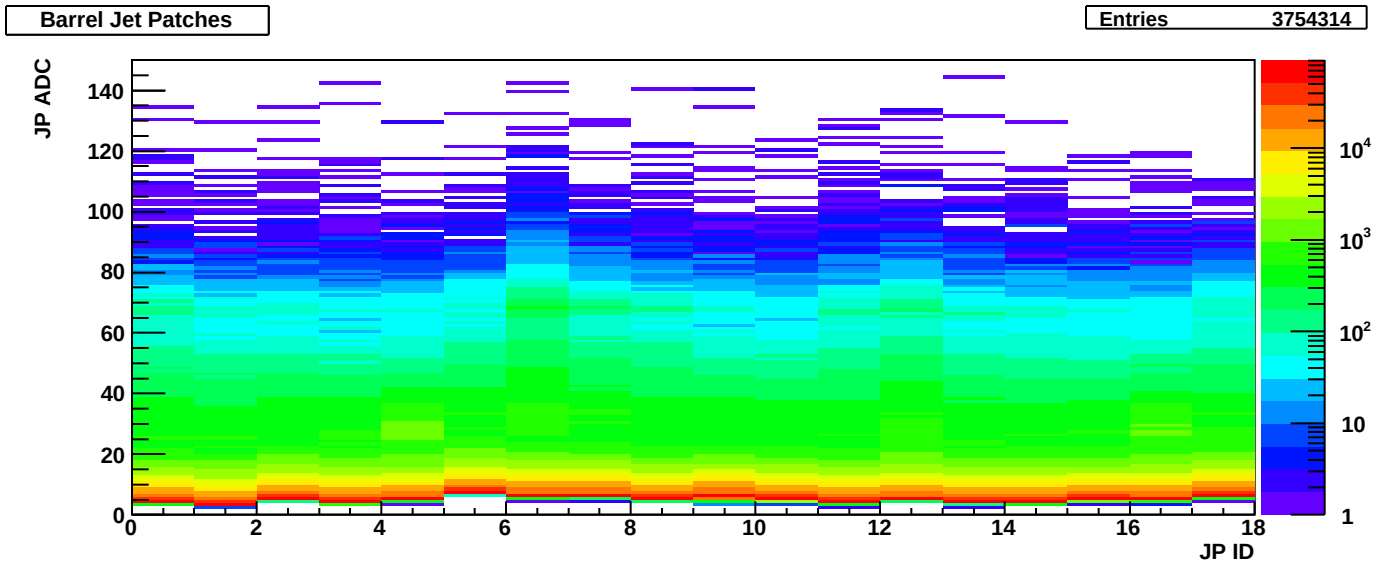

BunchId7Bit (all events)
Entries 208573
10^3
10^2
0 20 40 60 80 100 120

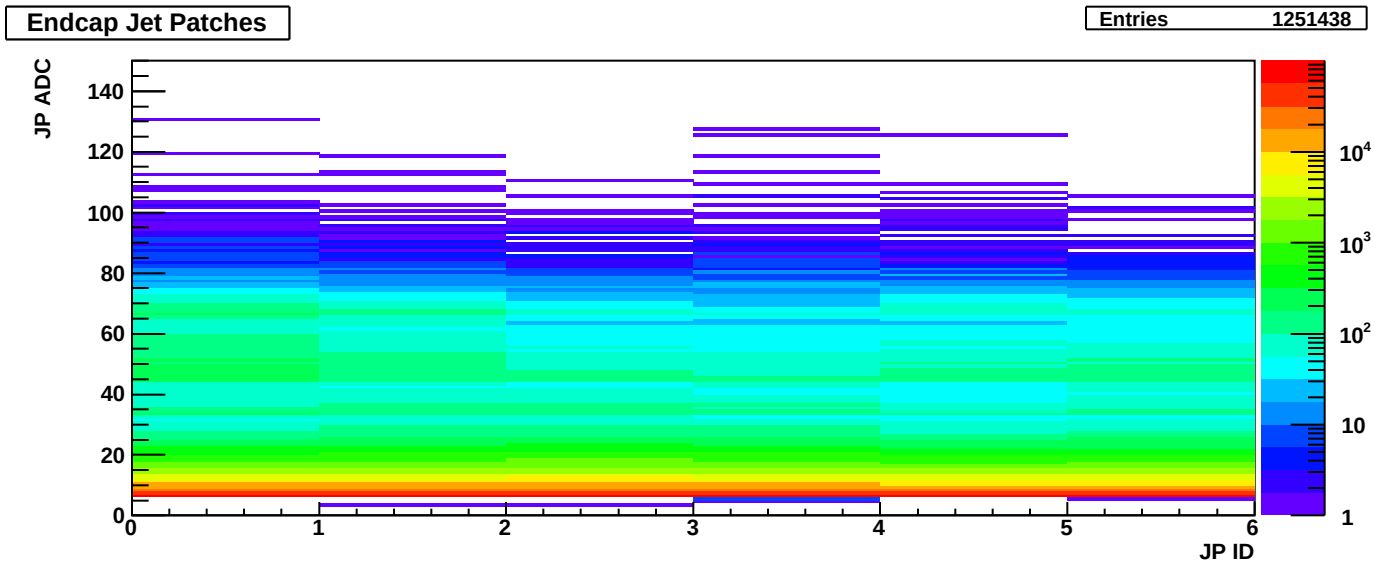

Barrel EMC L0 Input - High Tower
Entries 6.25719e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

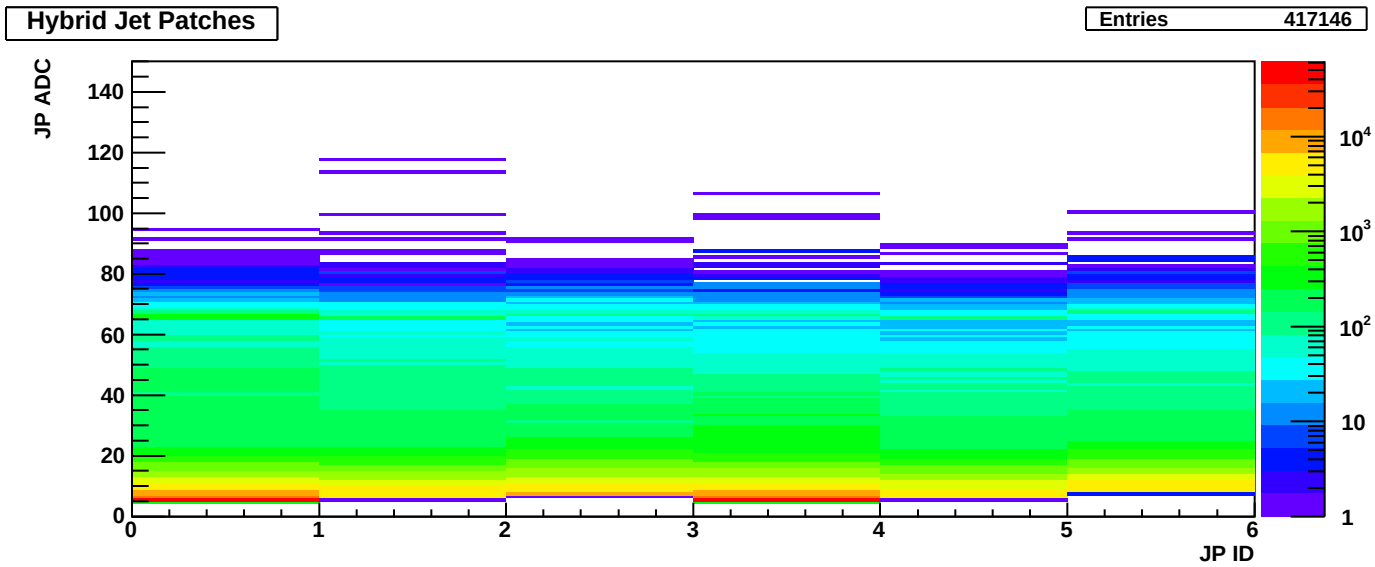

Barrel EMC L0 Input - Patch Sum
Entries 6.25719e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

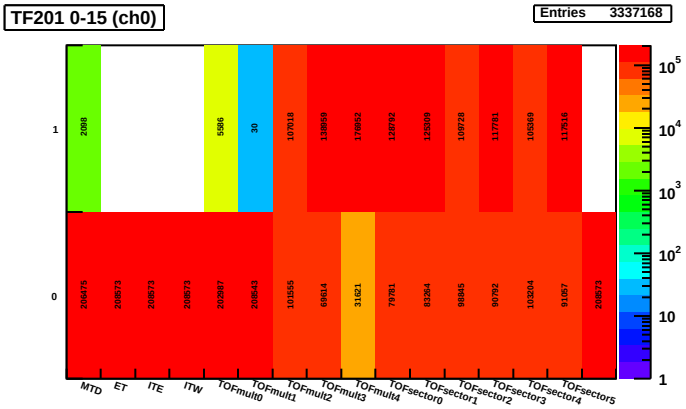

Endcap EMC L0 Input - High Tower
Entries 1.877157e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

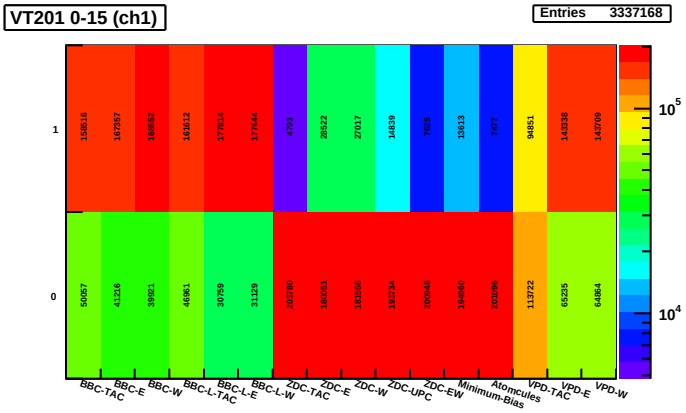

Endcap EMC L0 Input - Patch Sum
Entries 1.877157e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

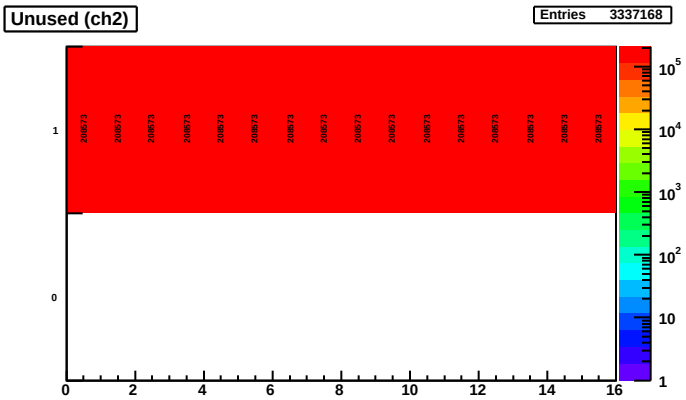

Barrel Jet Patches
Entries 3754314
JP ADC
JP ID

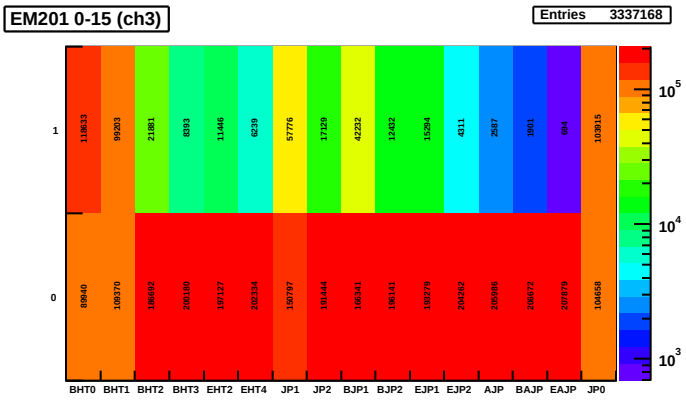

Endcap Jet Patches
Entries 1251438
JP ADC
JP ID

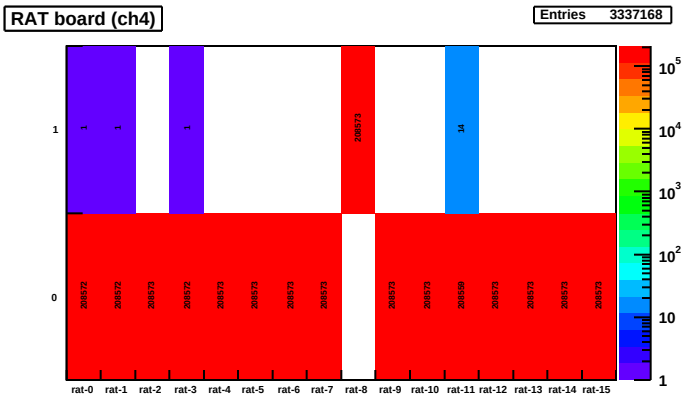

Hybrid Jet Patches
Entries 417146
JP ADC
JP ID

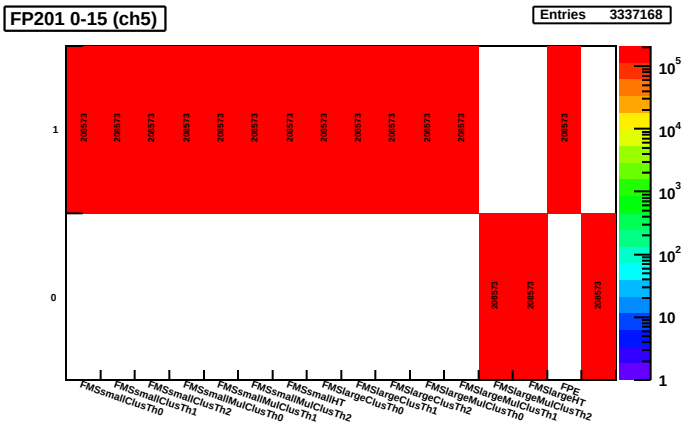

TF201 0-15 (ch0)
Entries 3337168
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

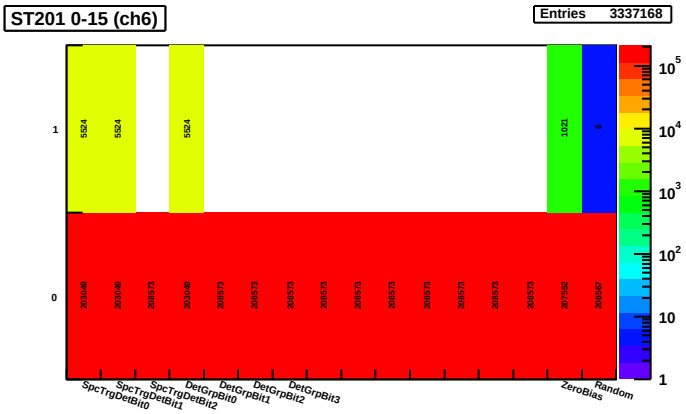

VT201 0-15 (ch1)
Entries 3337168
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

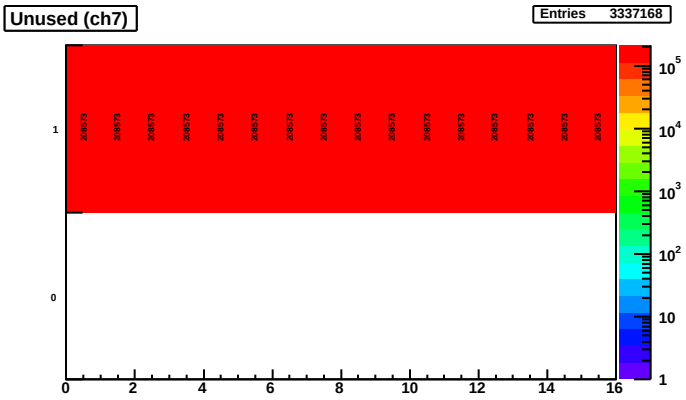

Unused (ch2)
Entries 3337168

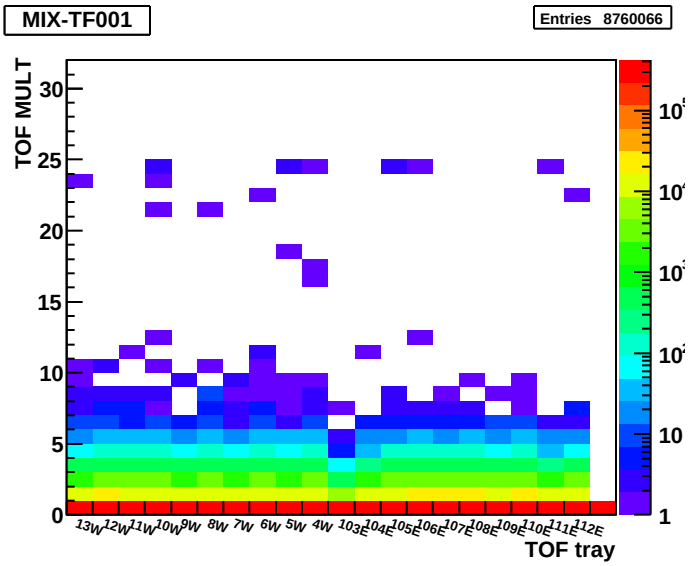

EM201 0-15 (ch3)
Entries 3337168
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

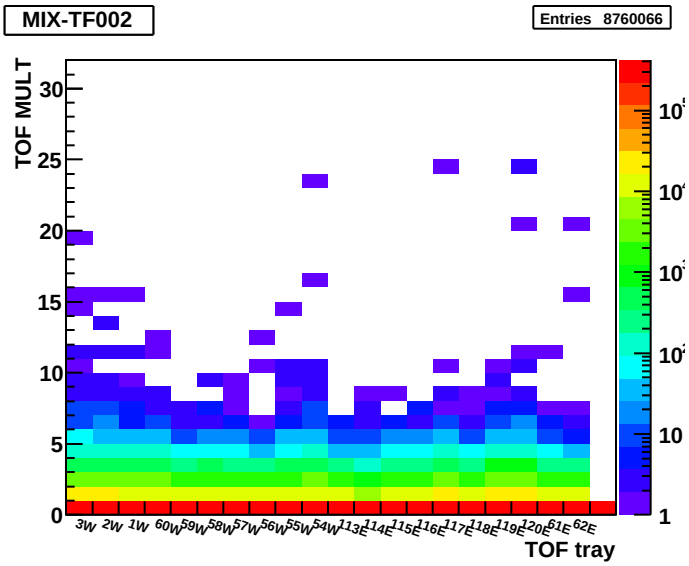

RAT board (ch4)
Entries 3337168
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

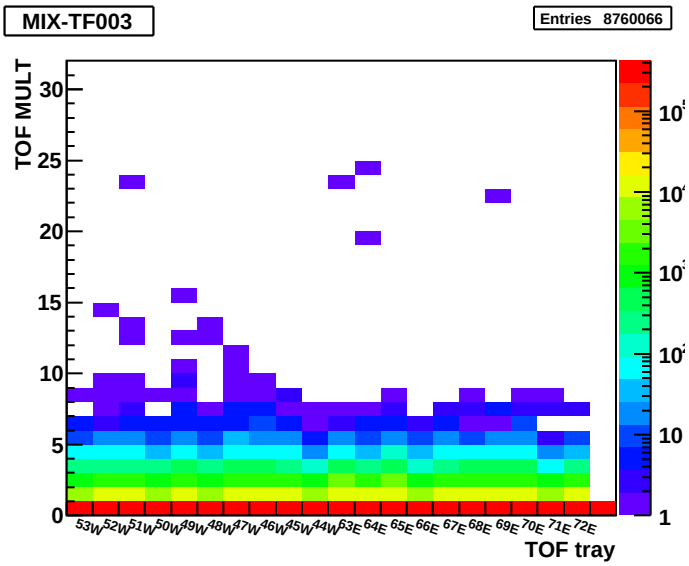

FP201 0-15 (ch5)
Entries 3337168
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

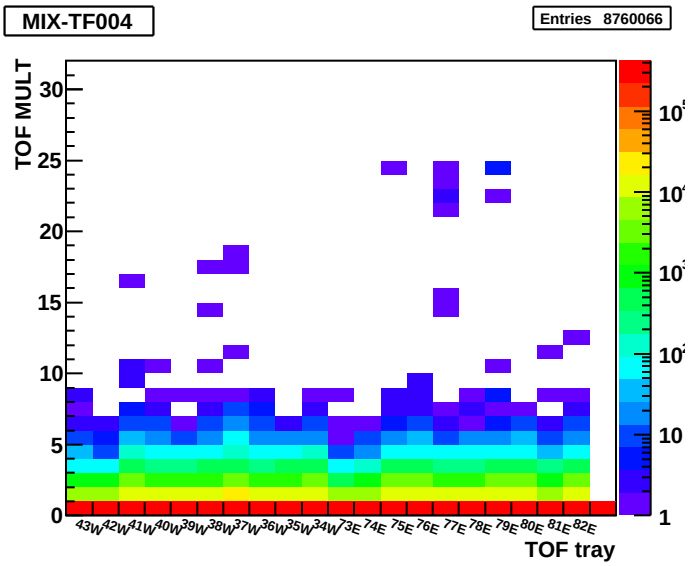

ST201 0-15 (ch6)
Entries 3337168
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

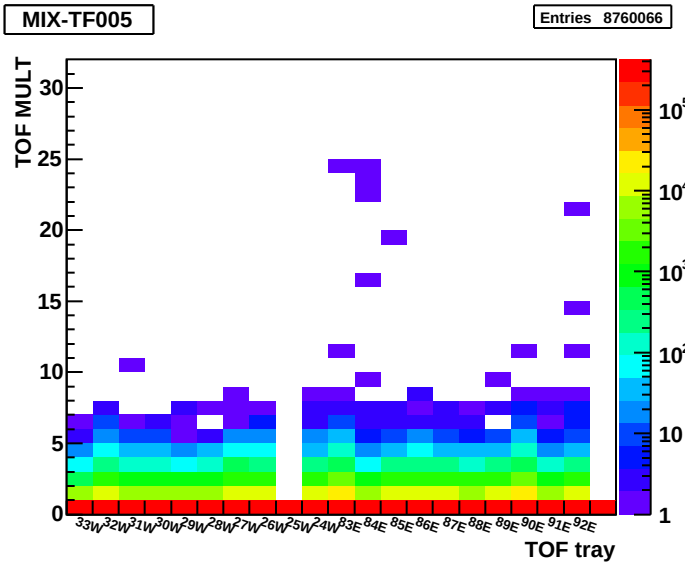

Unused (ch7)
Entries 3337168

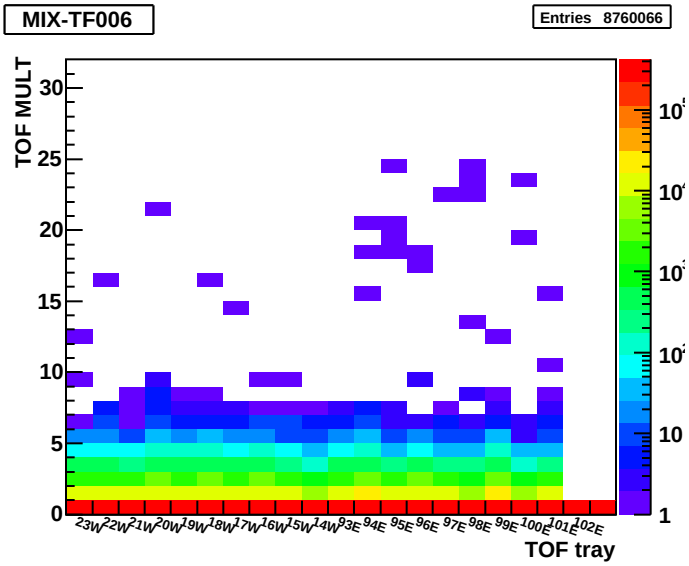

MIX-TF001
Entries 8760066
TOF MULT
TOF tray

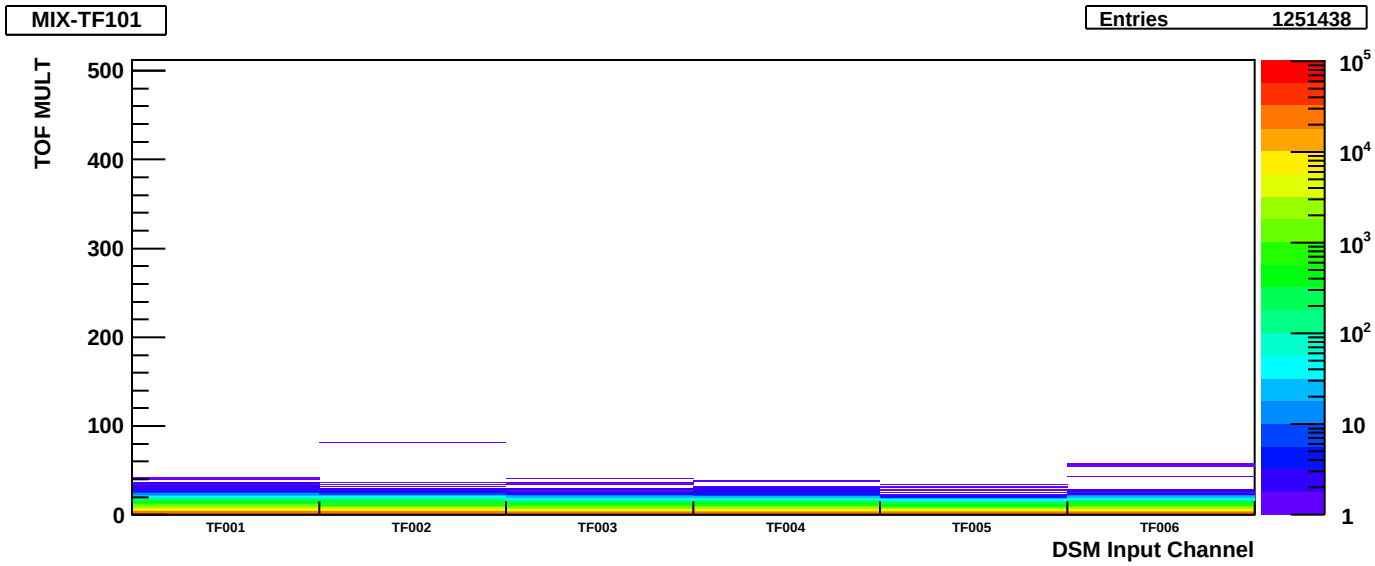

MIX-TF002
Entries 8760066
TOF MULT
TOF tray

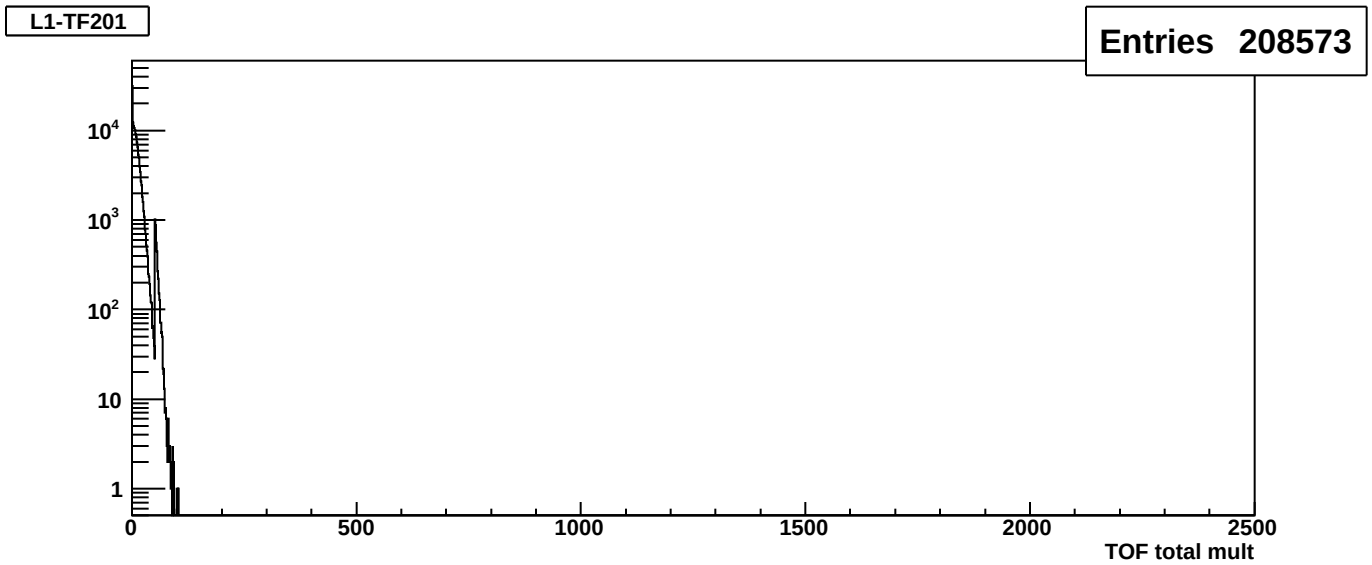

MIX-TF003
Entries 8760066
TOF MULT
TOF tray

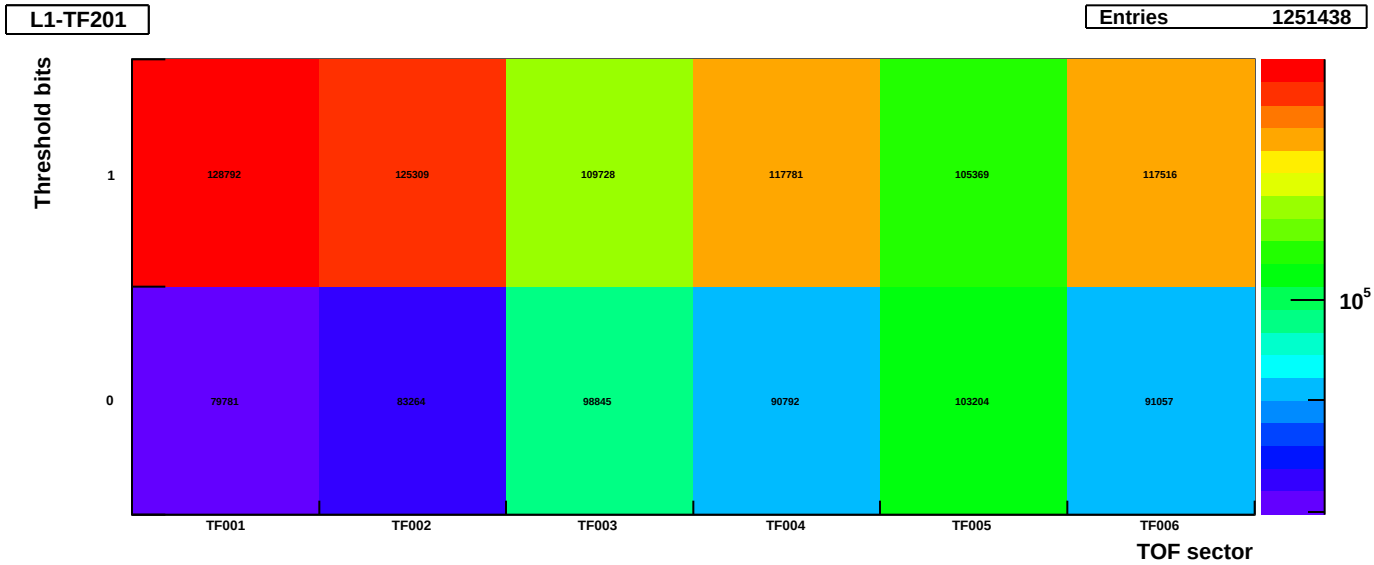

MIX-TF004
Entries 8760066
TOF MULT
TOF tray

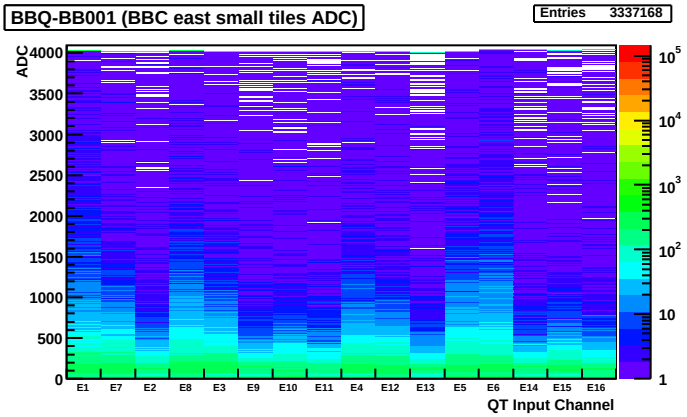

MIX-TF005
Entries 8760066
TOF MULT
TOF tray

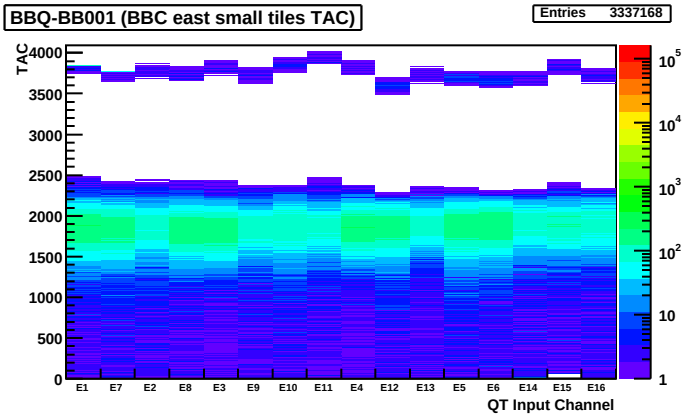

MIX-TF006
Entries 8760066
TOF MULT
TOF tray

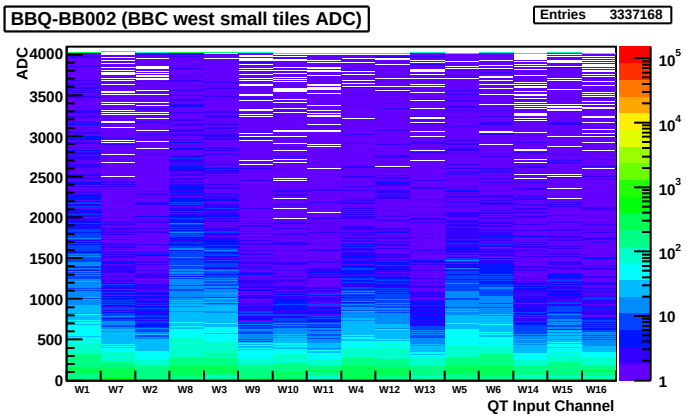

MIX-TF101
Entries 1251438
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

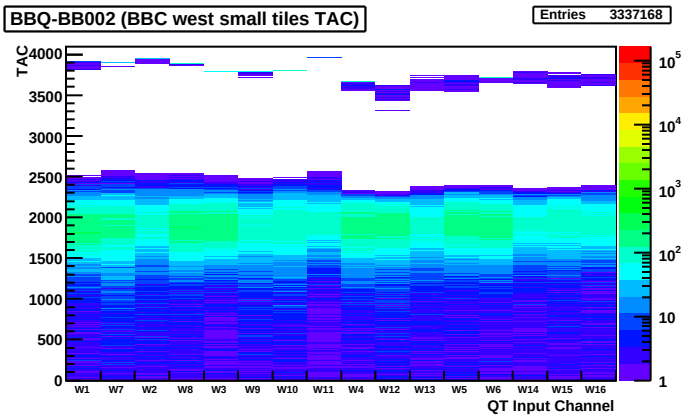

L1-TF201
Entries 208573
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

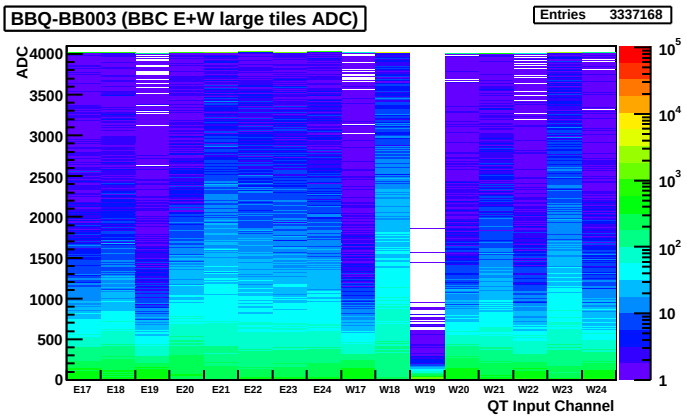

L1-TF201
Entries 1251438
Threshold bits
1
0
128792
125309
109728
117781
105369
117516
79781
83264
98845
90792
103204
91057
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

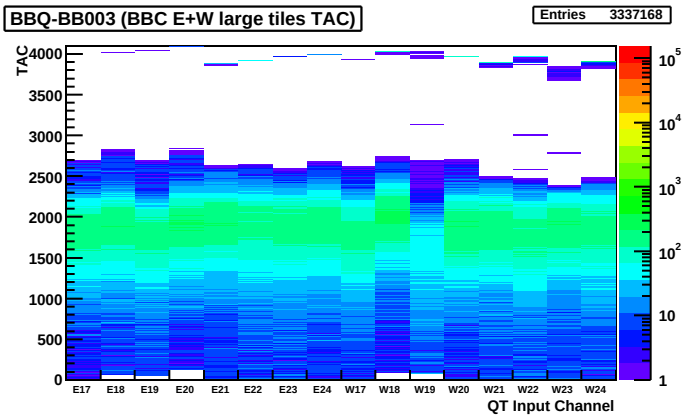

BBQ-BB001 (BBC east small tiles ADC)
Entries 3337168
ADC
QT Input Channel

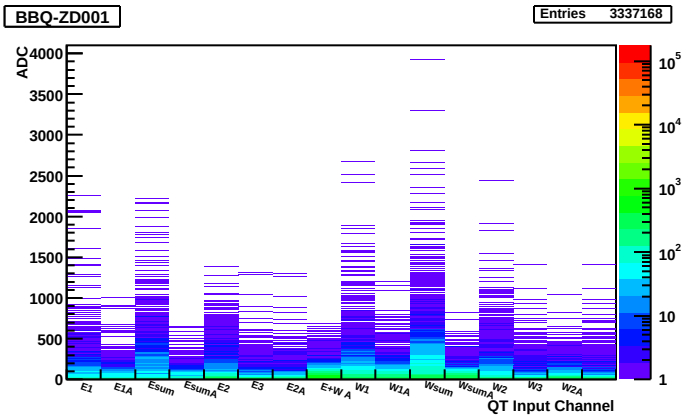

BBQ-BB001 (BBC east small tiles TAC)
Entries 3337168
TAC
QT Input Channel

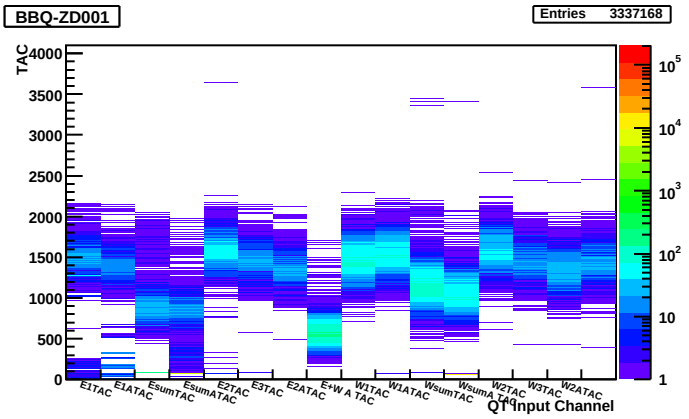

BBQ-BB002 (BBC west small tiles ADC)
Entries 3337168
ADC
QT Input Channel

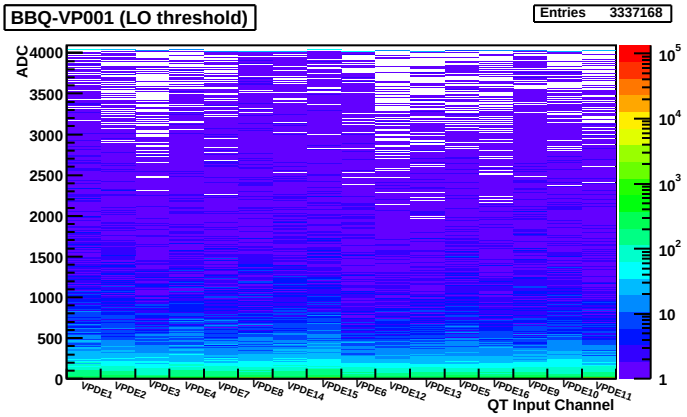

BBQ-BB002 (BBC west small tiles TAC)
Entries 3337168
TAC
QT Input Channel

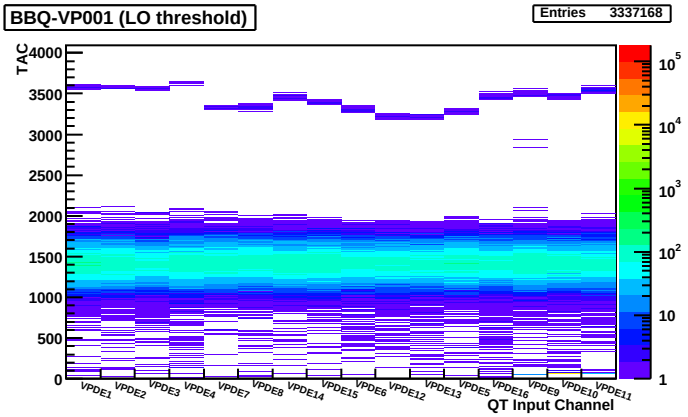

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3337168
ADC
QT Input Channel

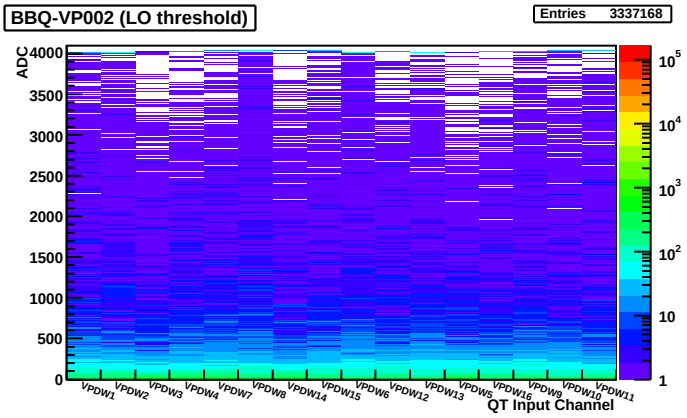

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3337168
TAC
QT Input Channel

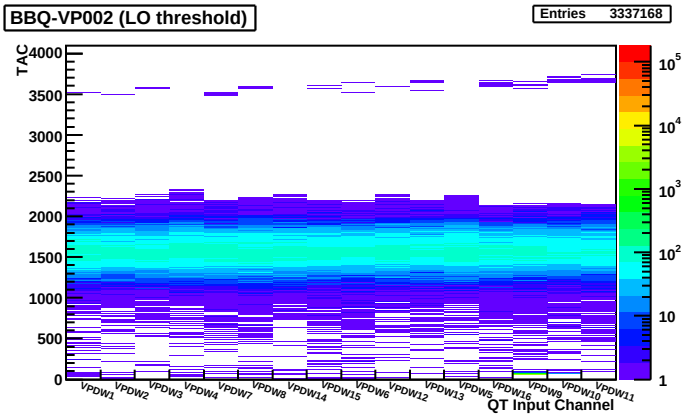

BBQ-ZD001
Entries 3337168
ADC
QT Input Channel

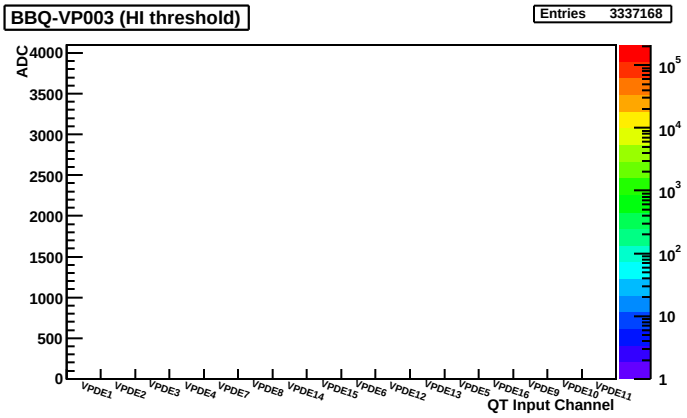

BBQ-ZD001
Entries 3337168
TAC
QT Input Channel

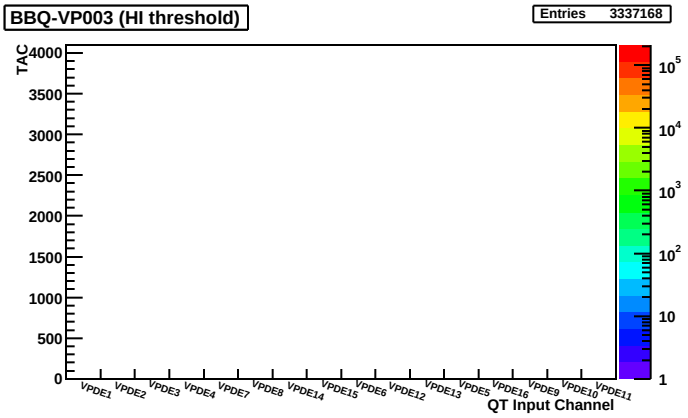

BBQ-VP001 (LO threshold)
Entries 3337168
ADC
QT Input Channel

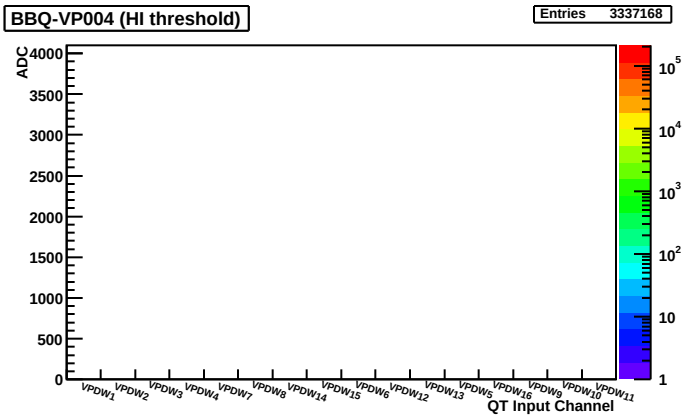

BBQ-VP001 (LO threshold)
Entries 3337168
TAC
QT Input Channel

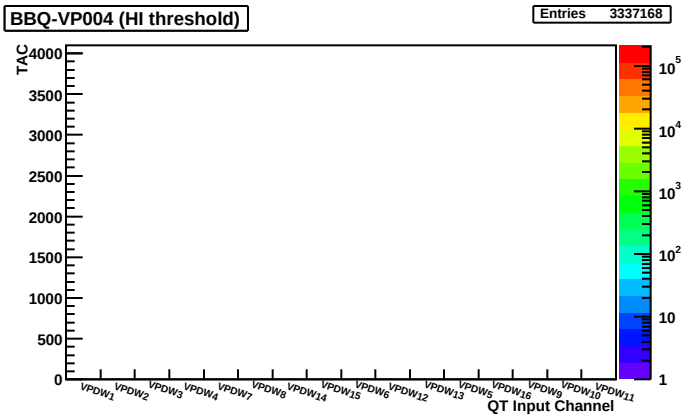

BBQ-VP002 (LO threshold)
Entries 3337168
ADC
QT Input Channel

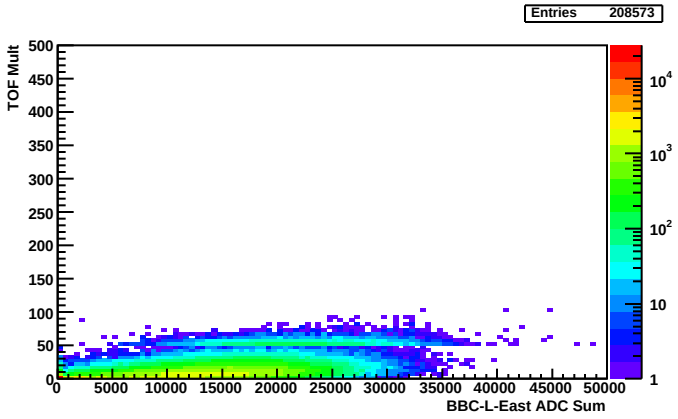

BBQ-VP002 (LO threshold)
Entries 3337168
TAC
QT Input Channel

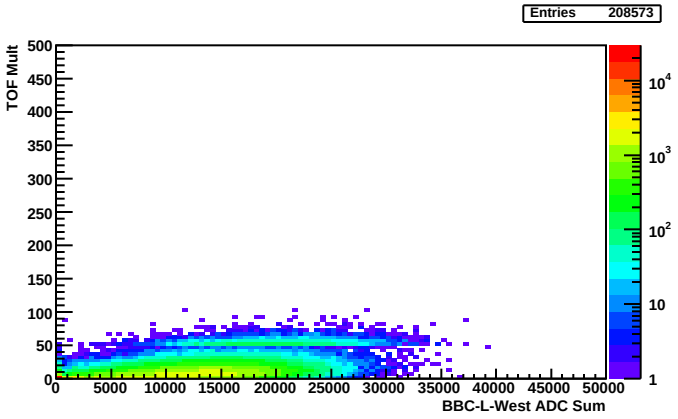

BBQ-VP003 (HI threshold)
Entries 3337168
ADC
QT Input Channel

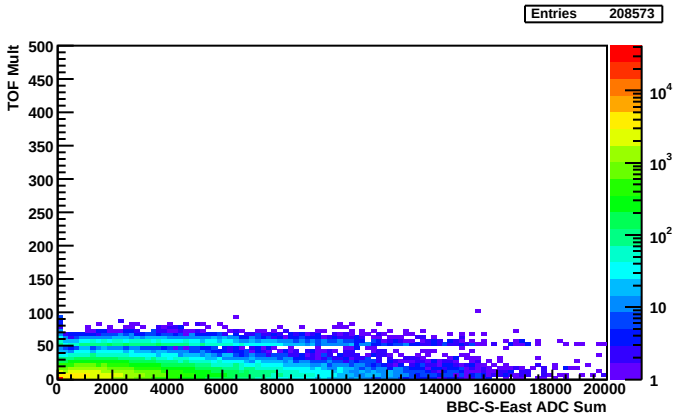

BBQ-VP003 (HI threshold)
Entries 3337168
TAC
QT Input Channel

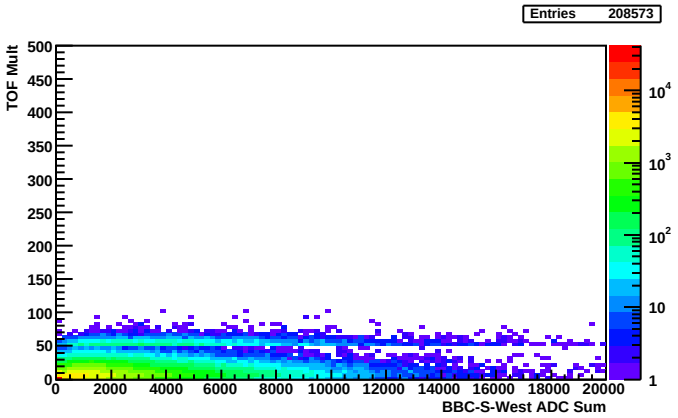

BBQ-VP004 (HI threshold)
Entries 3337168
ADC
QT Input Channel

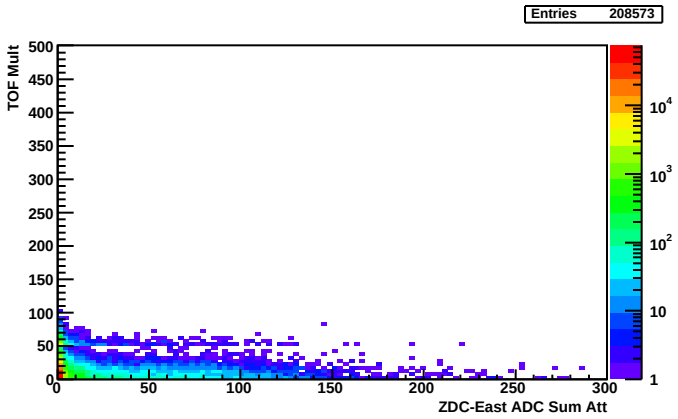

BBQ-VP004 (HI threshold)
Entries 3337168
TAC
QT Input Channel

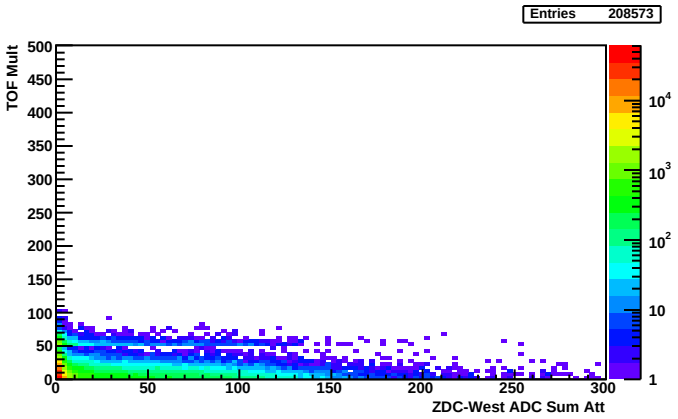

Entries 208573
TOF Mult
BBC-L-East ADC Sum

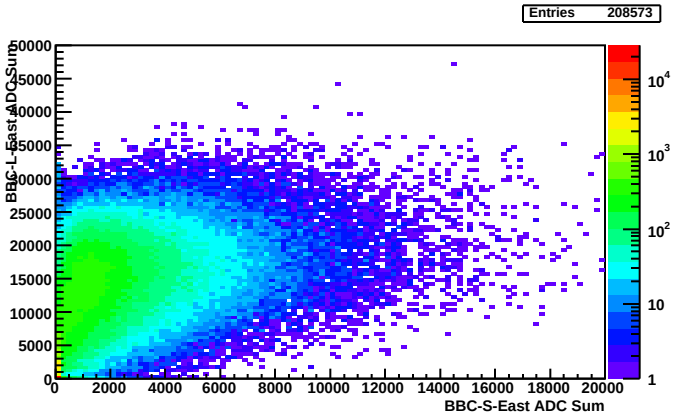

Entries 208573
TOF Mult
BBC-L-West ADC Sum

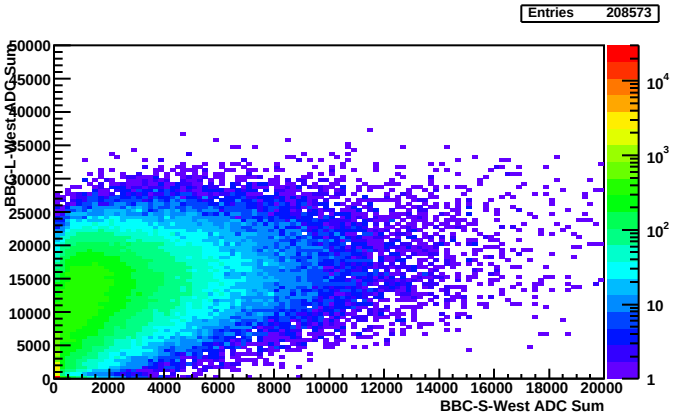

Entries 208573
TOF Mult
BBC-S-East ADC Sum

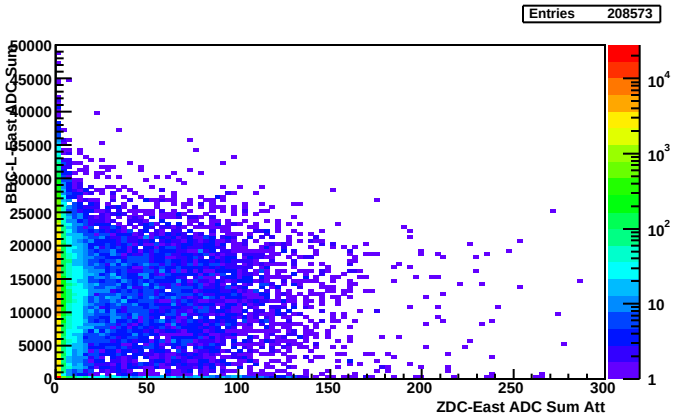

Entries 208573
TOF Mult
BBC-S-West ADC Sum

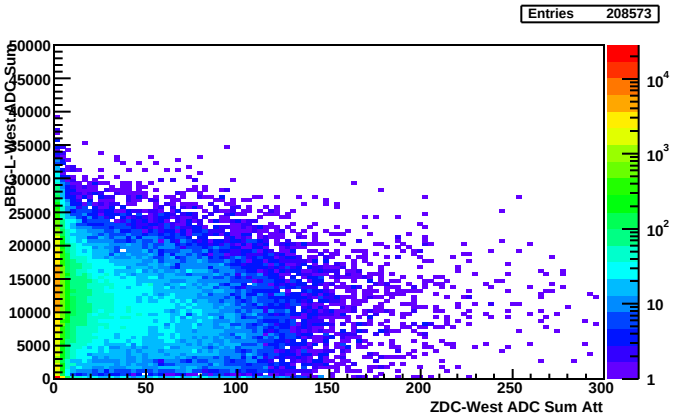

Entries 208573
TOF Mult
ZDC-East ADC Sum Att

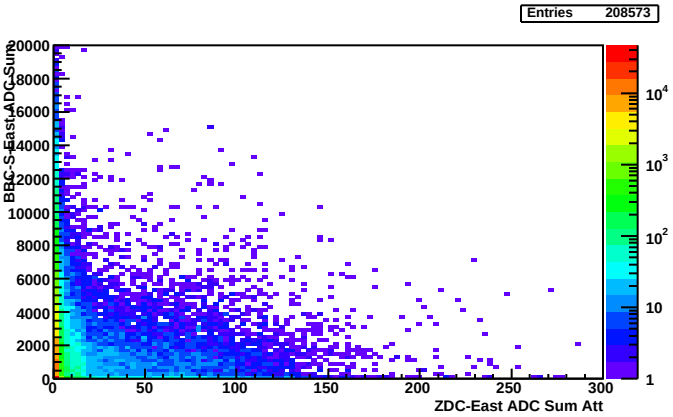

Entries 208573
TOF Mult
ZDC-West ADC Sum Att

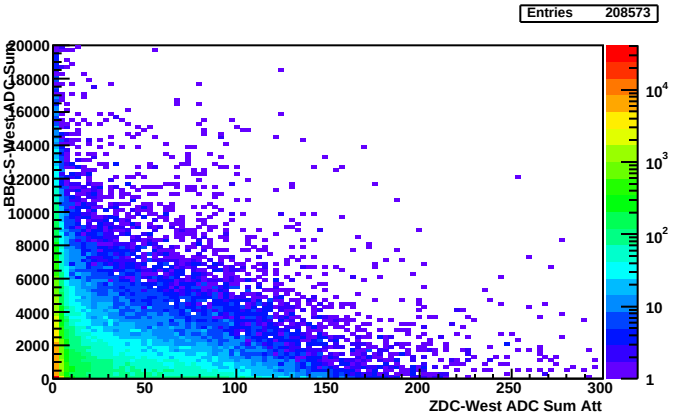

Entries 208573
BBC-L-East ADC Sum
BBC-S-East ADC Sum

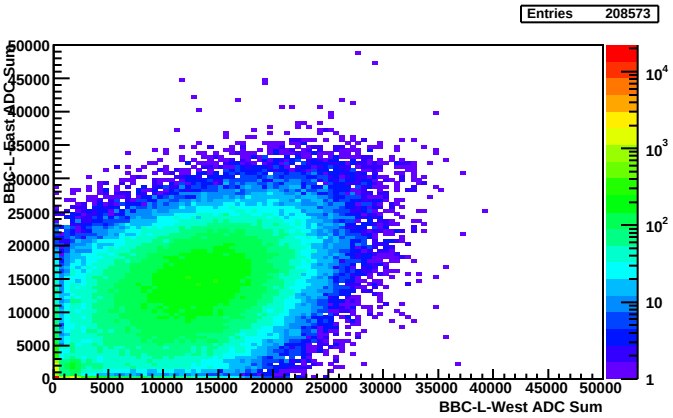

Entries 208573
BBC-L-West ADC Sum
BBC-S-West ADC Sum

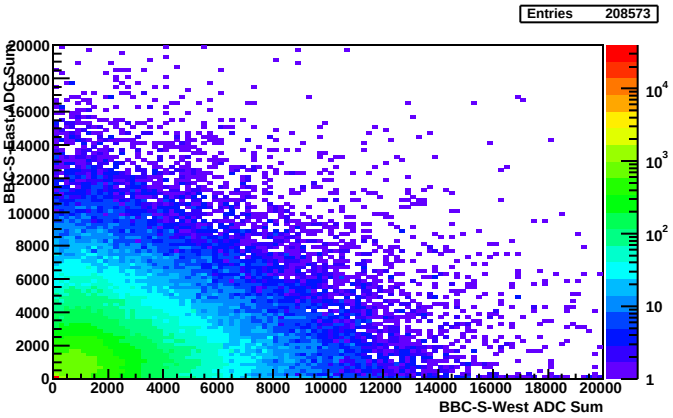

Entries 208573
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

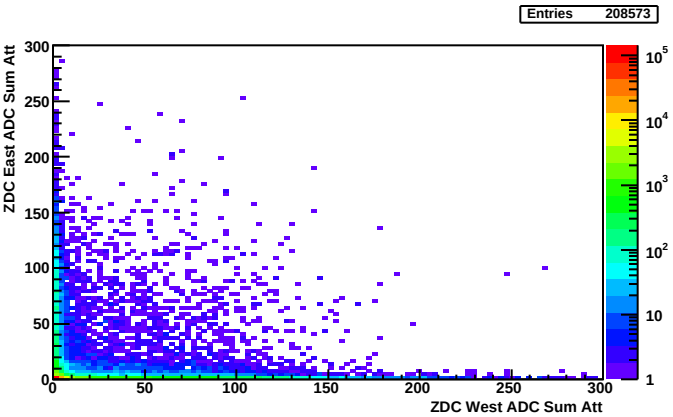

Entries 208573
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

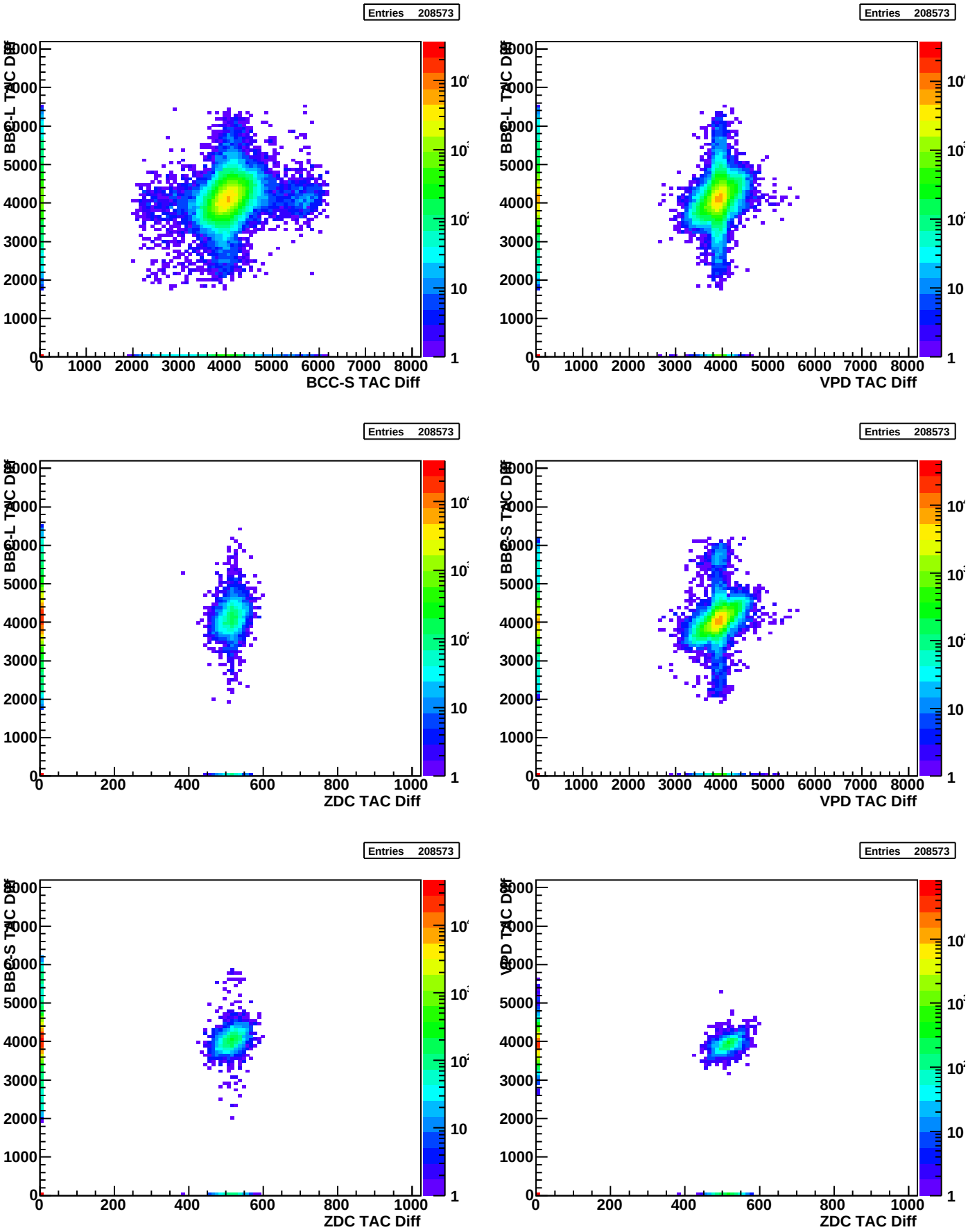

Entries 208573
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

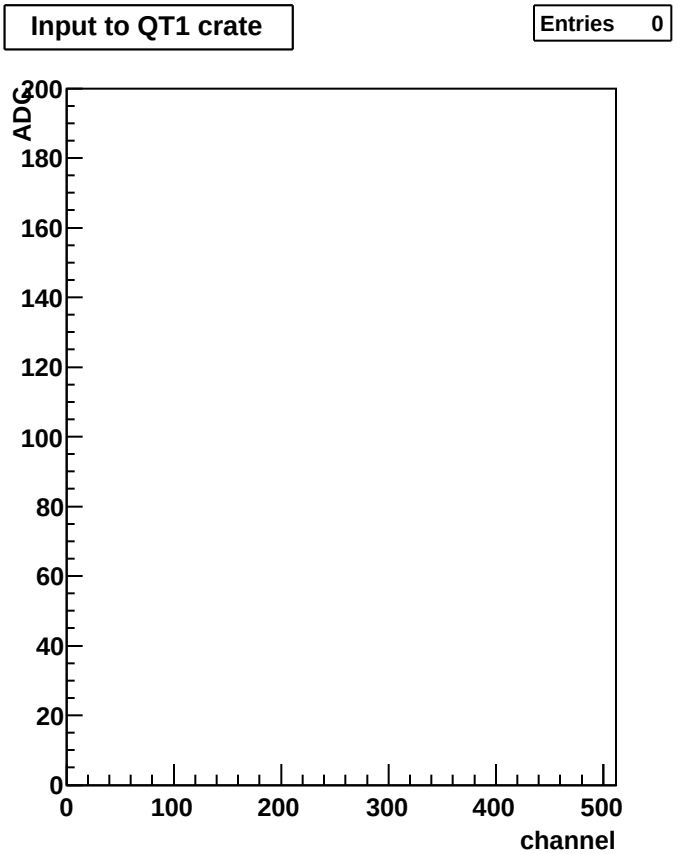

Entries 208573
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

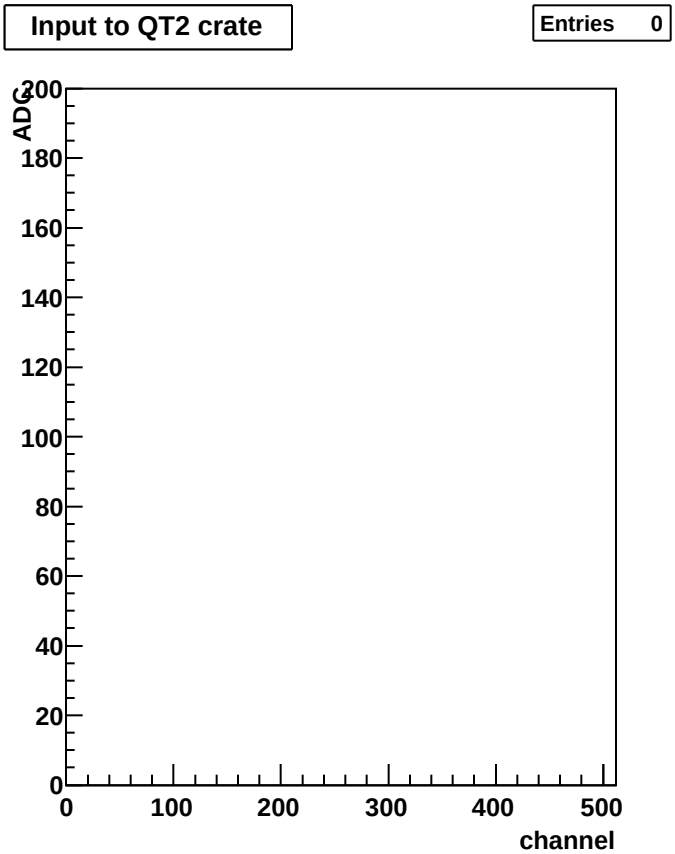

Entries 208573
BBC-L-East ADC Sum
BBC-L-West ADC Sum

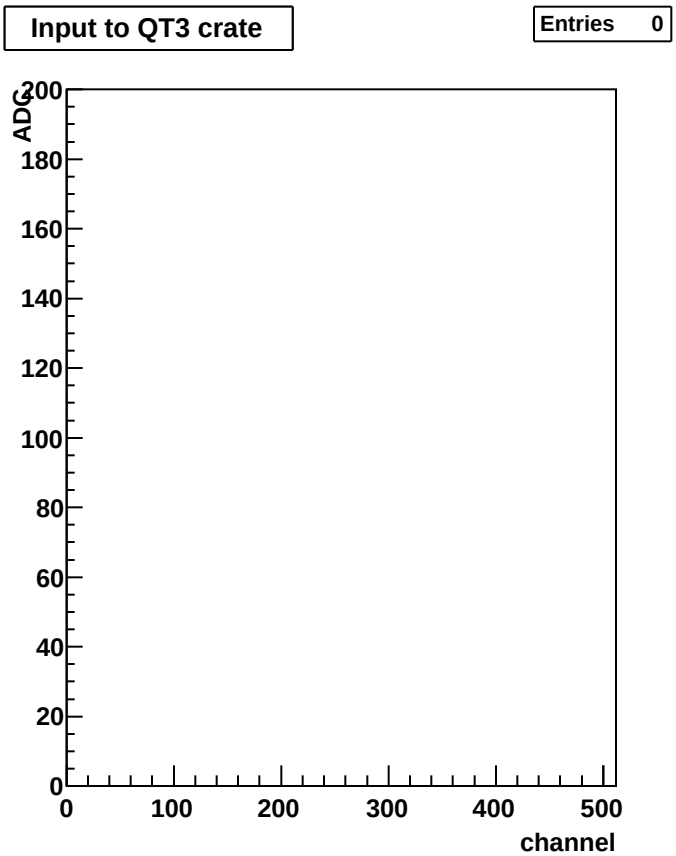

Entries 208573
BBC-S-East ADC Sum
BBC-S-West ADC Sum

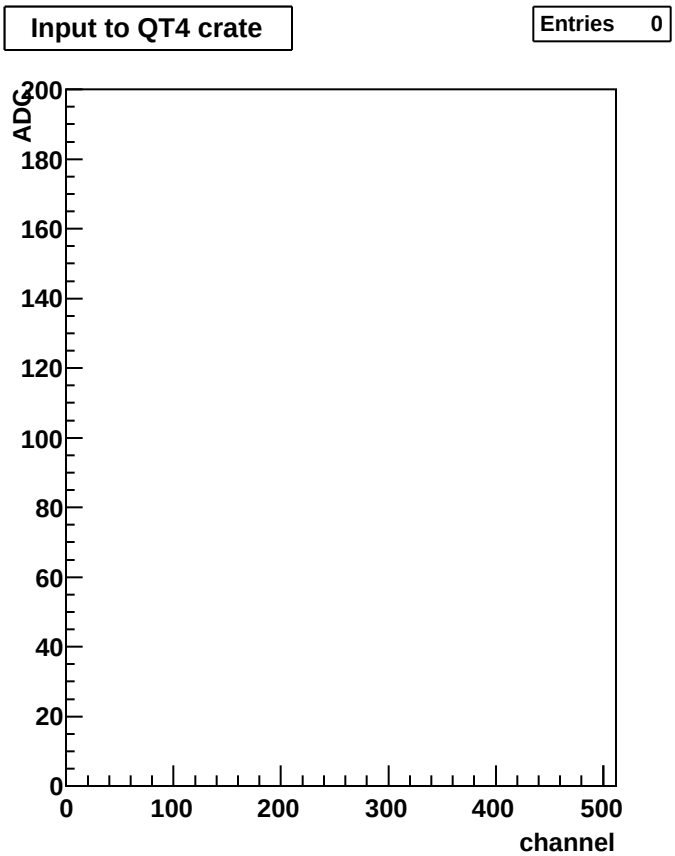

Entries 208573
ZDC East ADC Sum Att
ZDC West ADC Sum Att

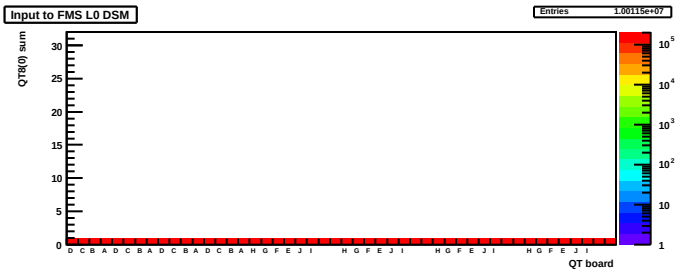

Entries 208573
BBC-L TAC Diff
BCC-S TAC Diff
Entries 208573
BBC-L TAC Diff
VPD TAC Diff
Entries 208573
BBC-L TAC Diff
ZDC TAC Diff
Entries 208573
BBC-S TAC Diff
VPD TAC Diff
Entries 208573
BBC-S TAC Diff
ZDC TAC Diff
Entries 208573
VPD TAC Diff
ZDC TAC Diff

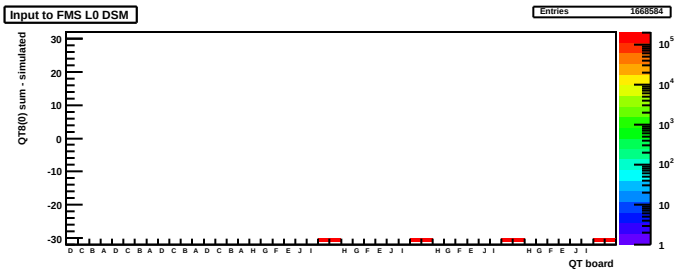

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

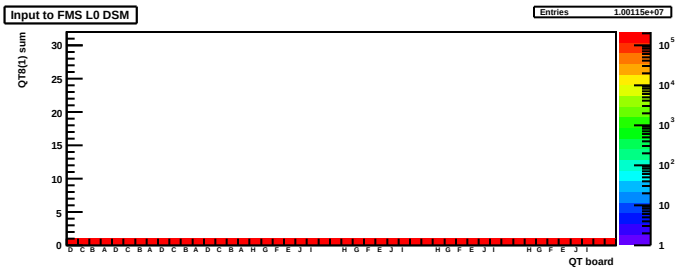

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

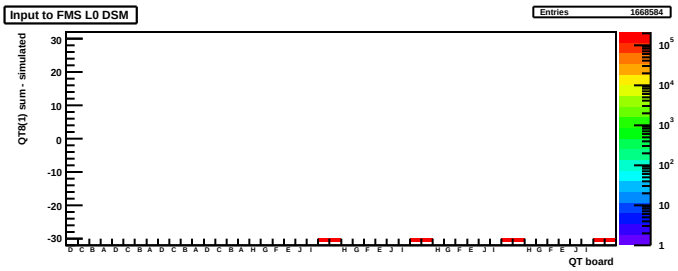

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

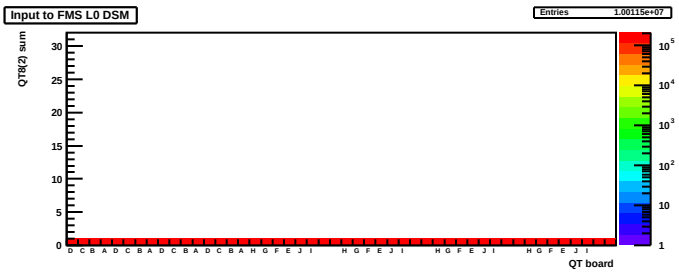

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

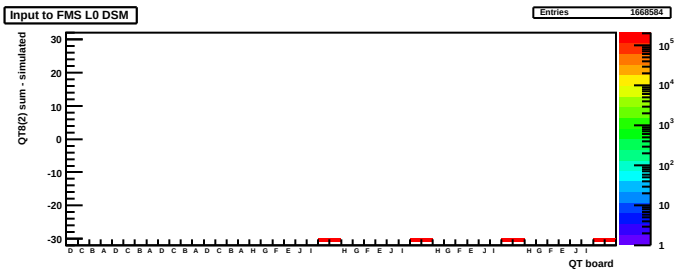

Input to FMS L0 DSM
Entries 1.00115e+07
QT8(0) sum
QT board

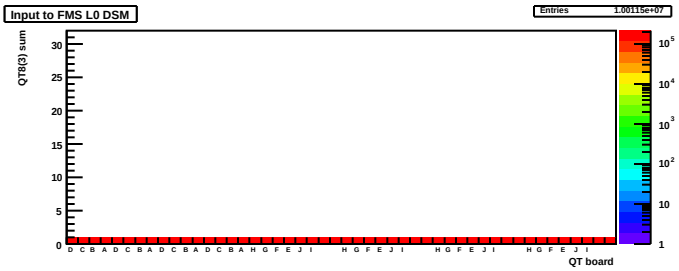

Input to FMS L0 DSM
Entries 1668584
QT8(0) sum - simulated
QT board

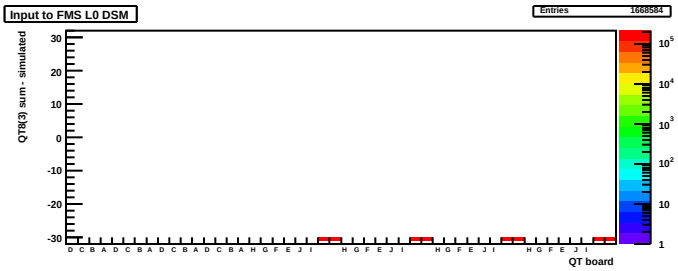

Input to FMS L0 DSM
Entries 1.00115e+07
QT8(1) sum
QT board

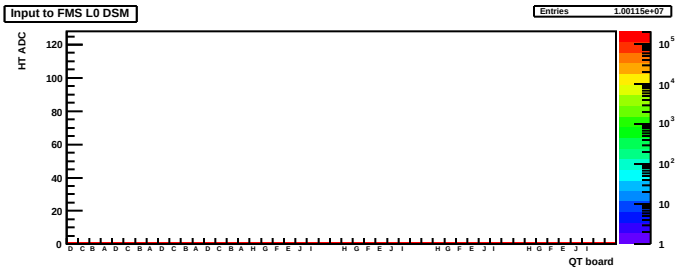

Input to FMS L0 DSM
Entries 1668584
QT8(1) sum - simulated
QT board

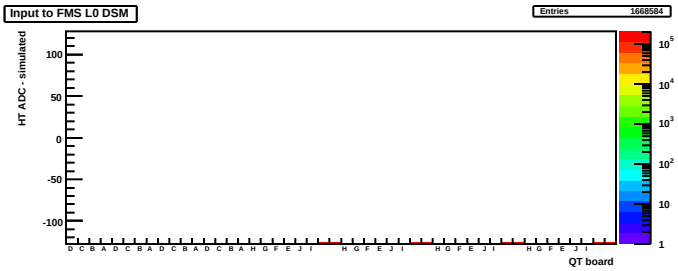

Input to FMS L0 DSM
Entries 1.00115e+07
QT8(2) sum
QT board

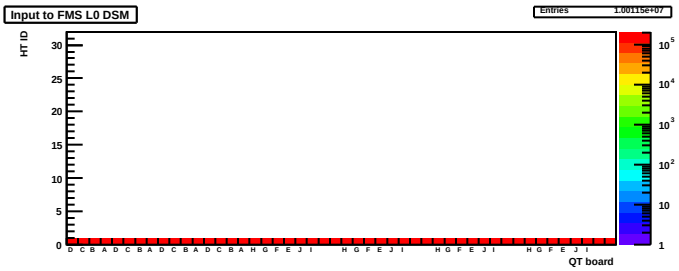

Input to FMS L0 DSM
Entries 1668584
QT8(2) sum - simulated
QT board

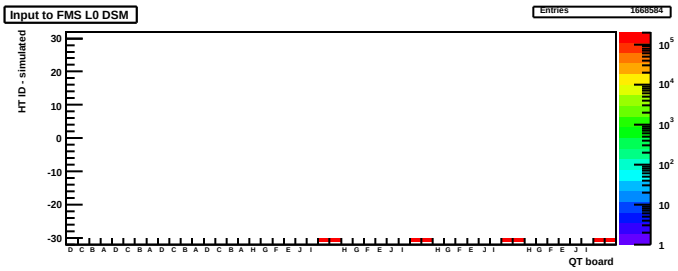

Input to FMS L0 DSM
Entries 1.00115e+07
QT8(3) sum
QT board

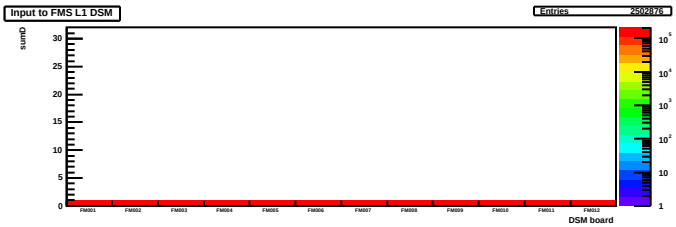

Input to FMS L0 DSM
Entries 1668584
QT8(3) sum - simulated
QT board

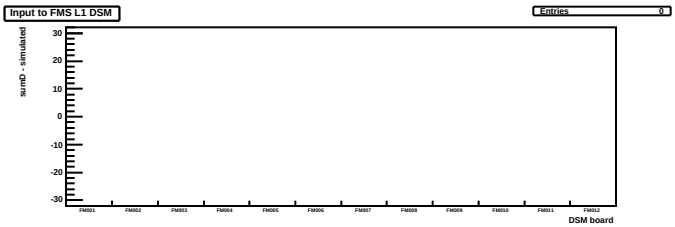

Input to FMS L0 DSM
Entries 1.00115e+07
HT ADC
QT board

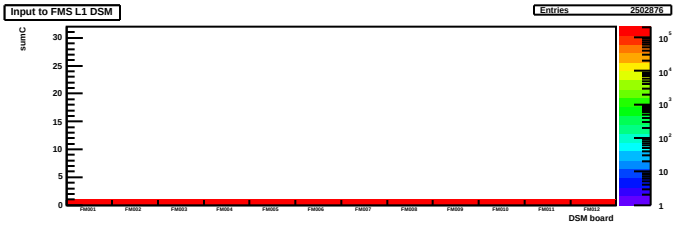

Input to FMS L0 DSM
Entries 1668584
HT ADC - simulated
QT board

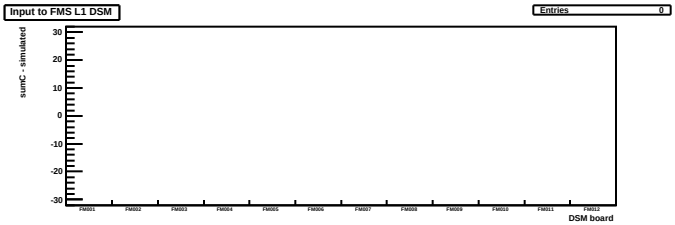

Input to FMS L0 DSM
Entries 1.00115e+07
HT ID
QT board

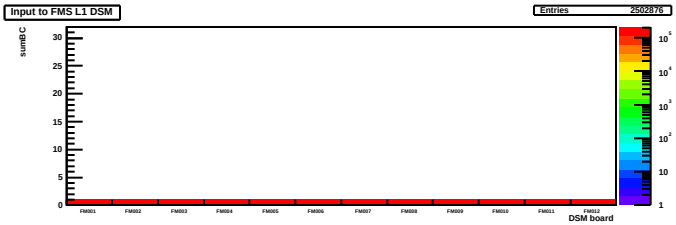

Input to FMS L0 DSM
Entries 1668584
HT ID - simulated
QT board

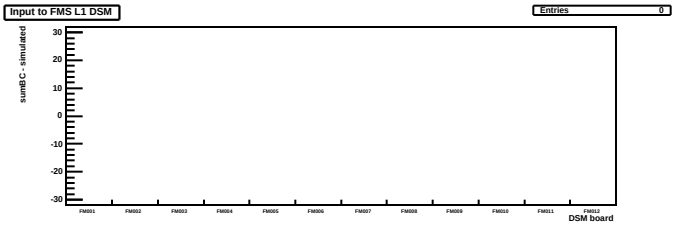

Input to FMS L1 DSM
Entries 2502876
sumD
DSM board

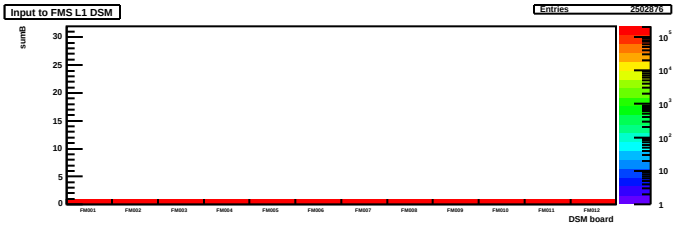

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

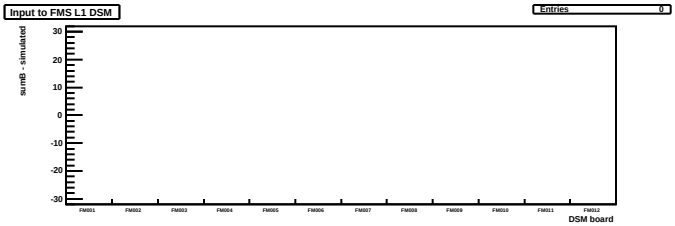

Input to FMS L1 DSM
Entries 2502876
sumC
DSM board

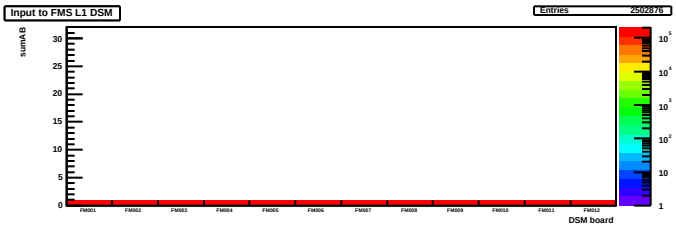

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

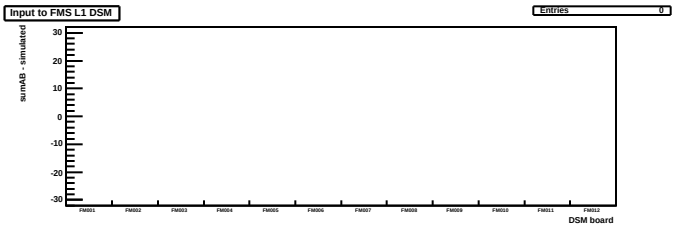

Input to FMS L1 DSM
Entries 2502876
sumBC
DSM board

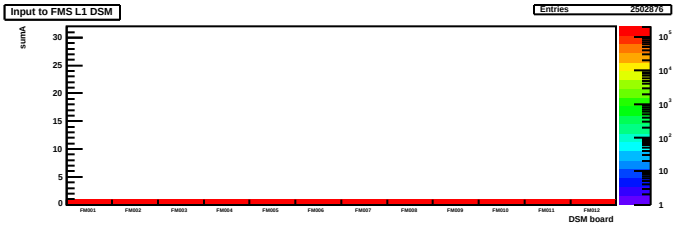

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

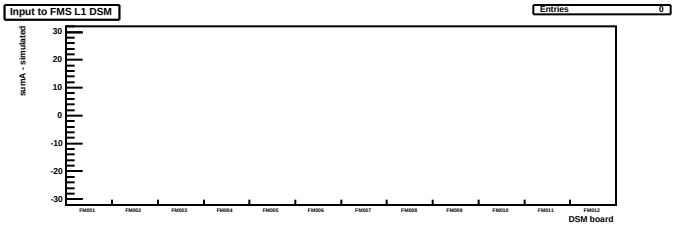

Input to FMS L1 DSM
Entries 2502876
sumB
DSM board

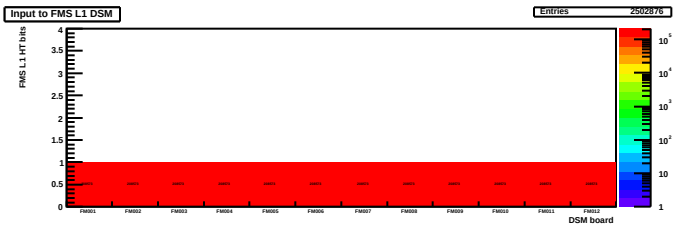

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

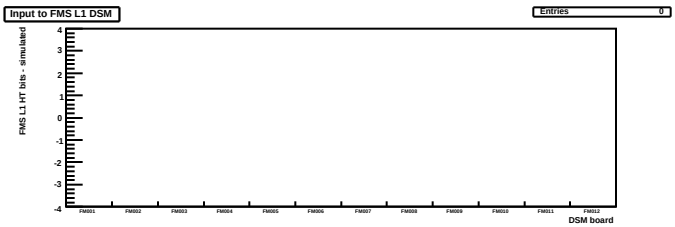

Input to FMS L1 DSM
Entries 2502876
sumAB
DSM board

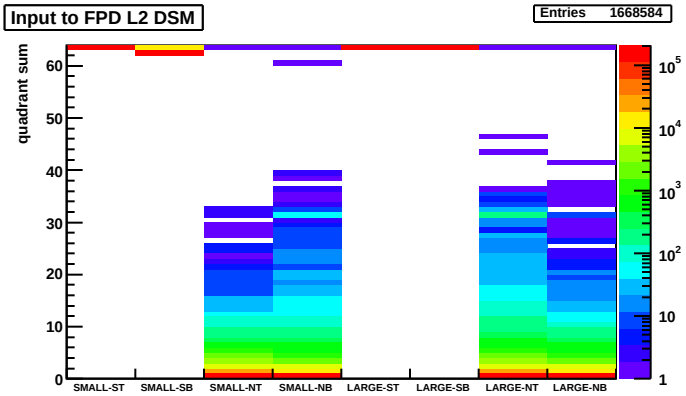

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

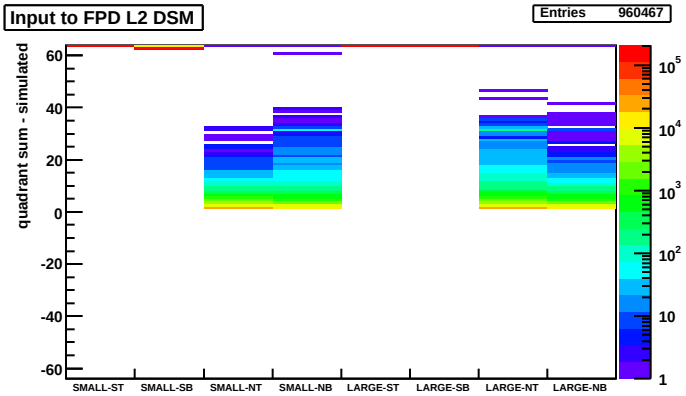

Input to FMS L1 DSM
Entries 2502876
sumA
DSM board

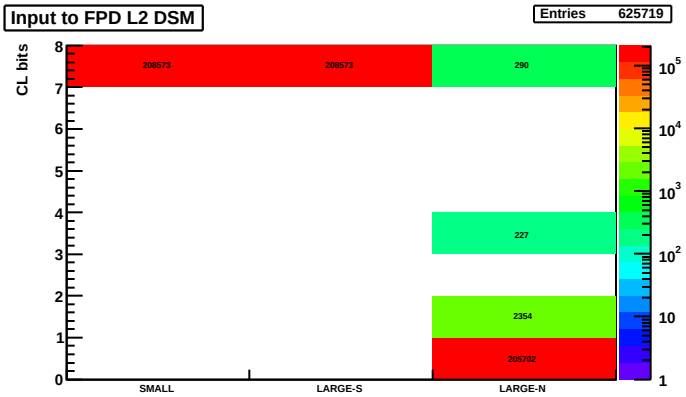

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

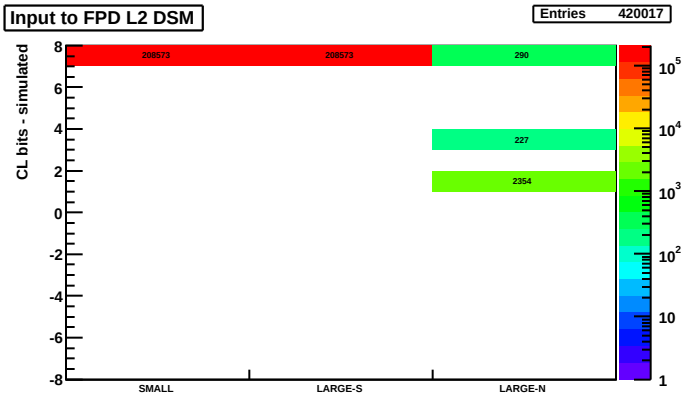

Input to FMS L1 DSM
Entries 2502876
FMS L1 HT bits
DSM board

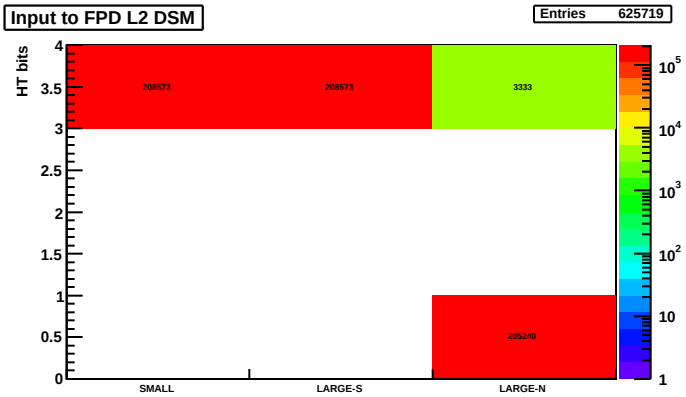

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

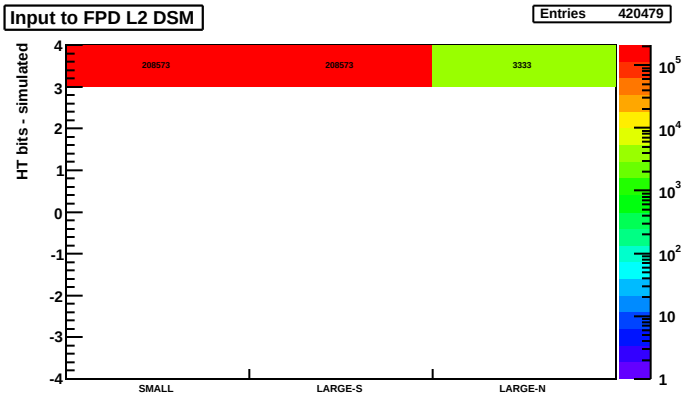

Input to FPD L2 DSM
Entries 1668584
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

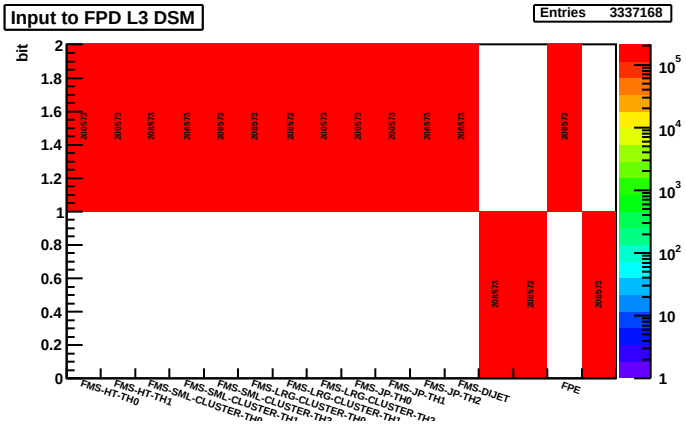

Input to FPD L2 DSM
Entries 960467
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

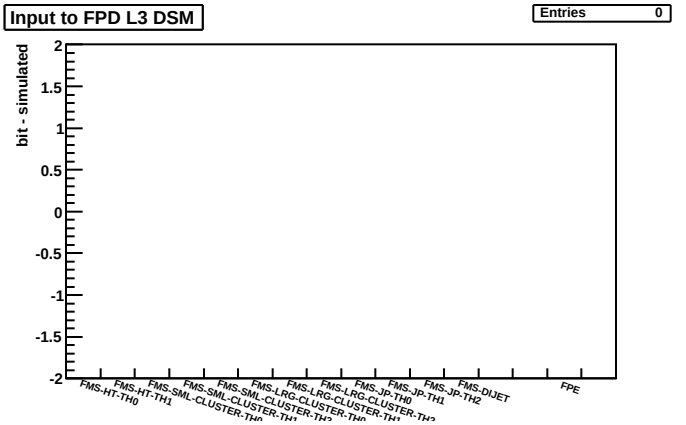

Input to FPD L2 DSM
Entries 625719
CL bits
208573
208573
290
227
2354
205702
SMALL
LARGE-S
LARGE-N

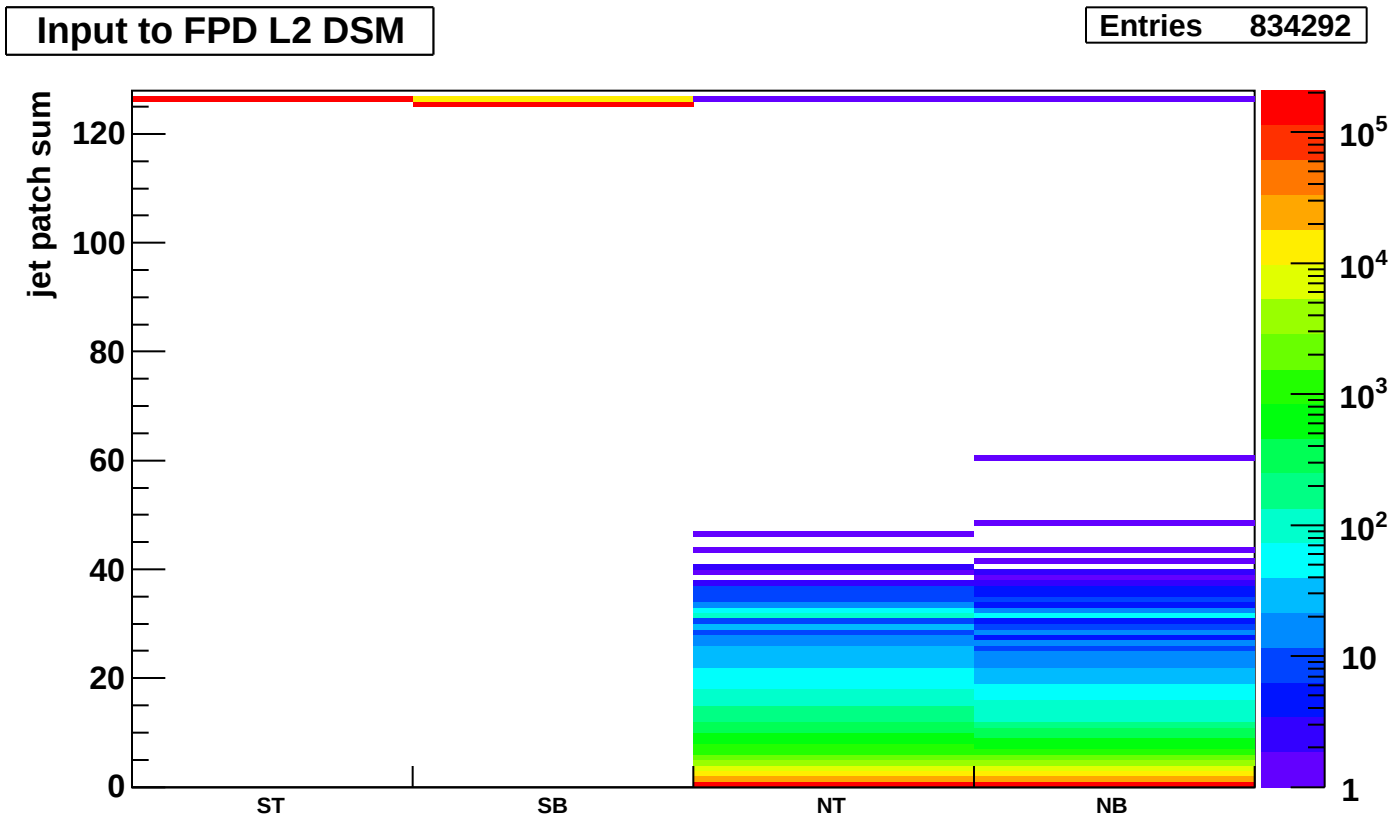

Input to FPD L2 DSM
Entries 420017
CL bits - simulated
208573
208573
290
227
2354
SMALL
LARGE-S
LARGE-N

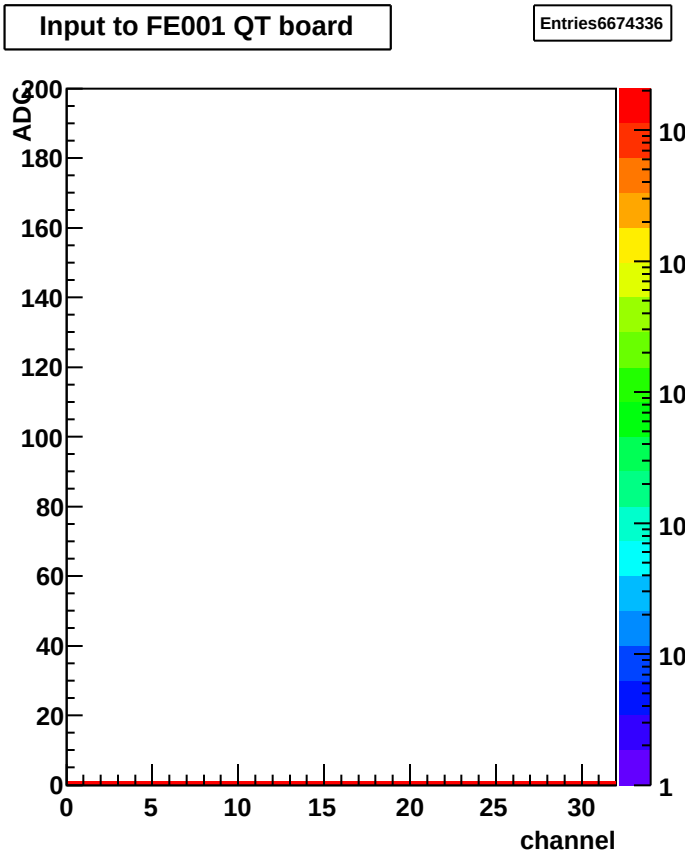

Input to FPD L2 DSM
Entries 625719
HT bits
208573
208573
3333
205240
SMALL
LARGE-S
LARGE-N

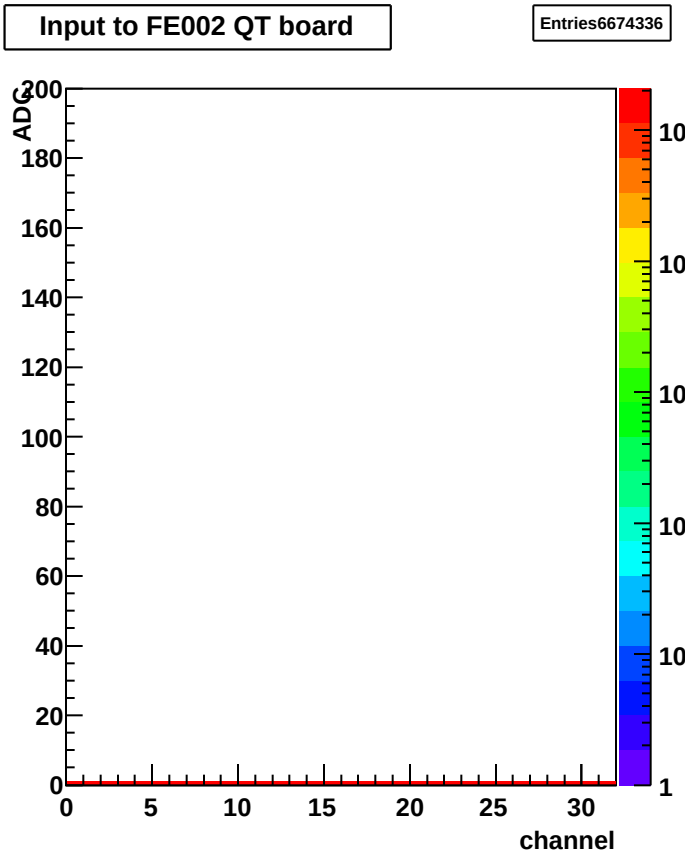

Input to FPD L2 DSM
Entries 420479
HT bits - simulated
208573
208573
3333
SMALL
LARGE-S
LARGE-N

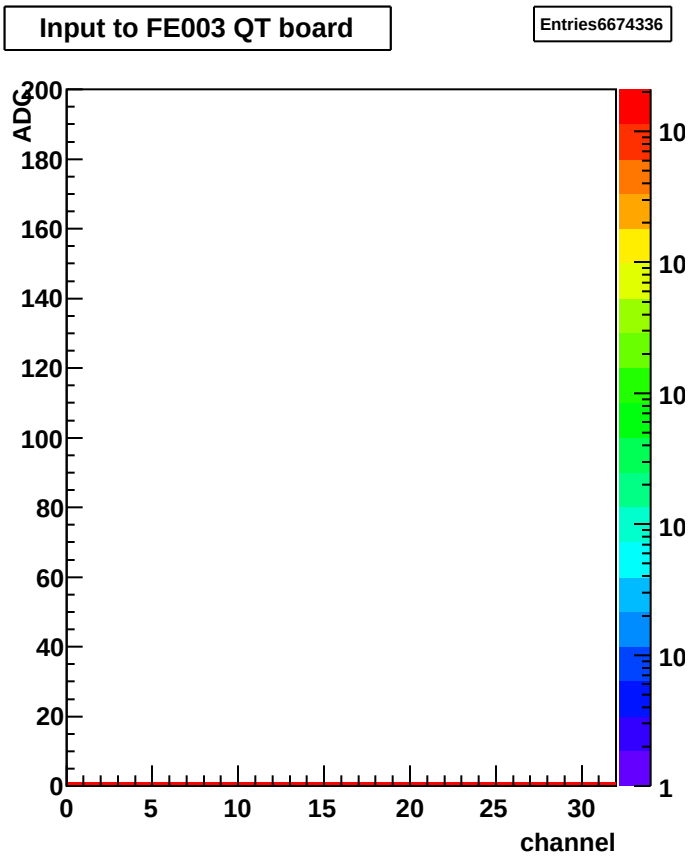

Input to FPD L3 DSM
Entries 3337168
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

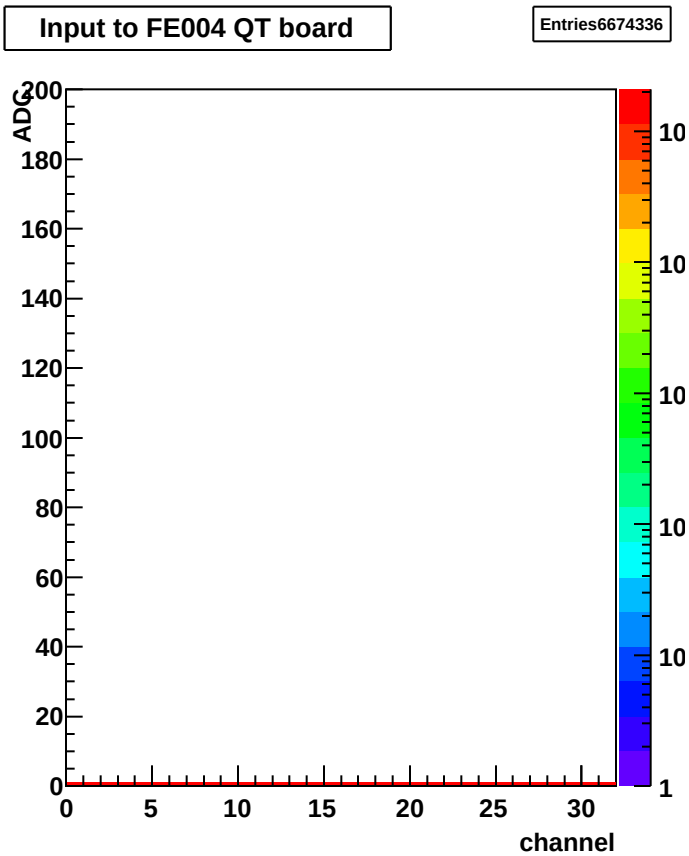

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

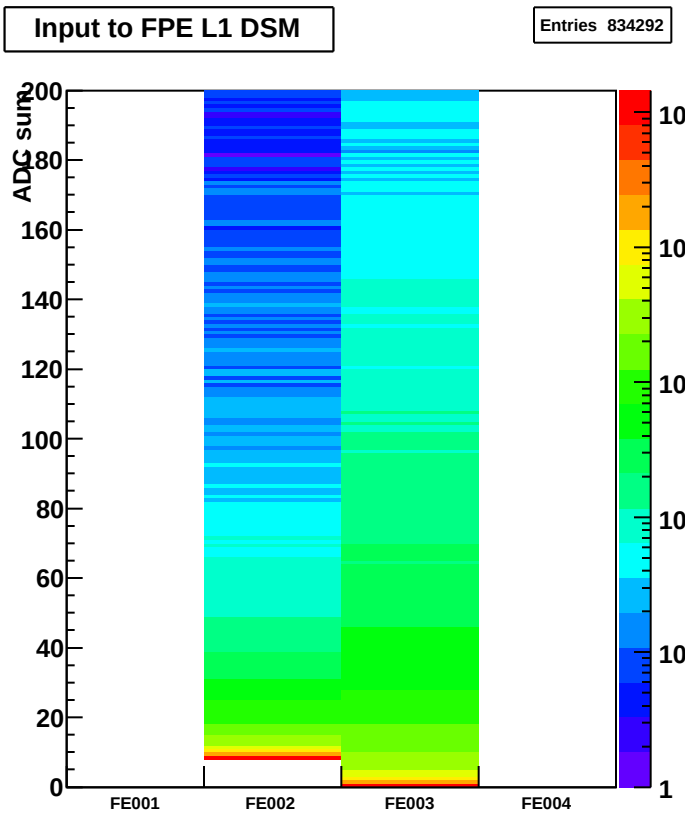

Input to FPD L2 DSM
Entries 834292
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

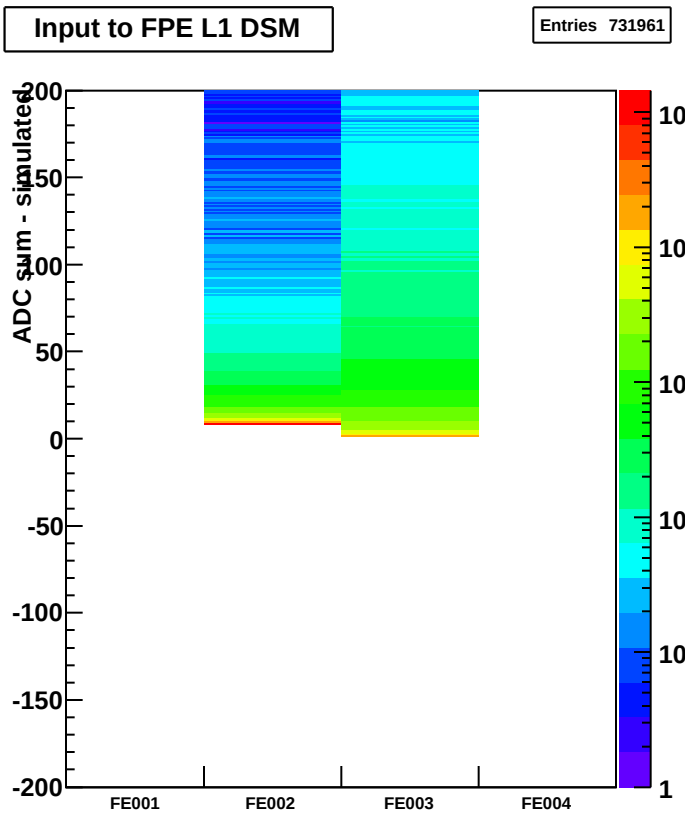

Input to FE001 QT board
Entries6674336
ADC
channel

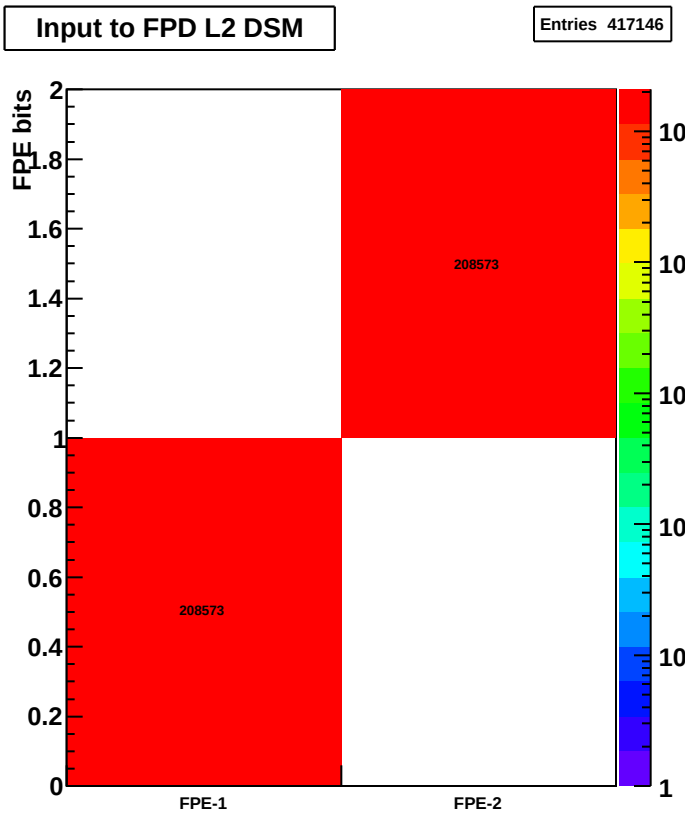

Input to FE002 QT board
Entries6674336
ADC
channel

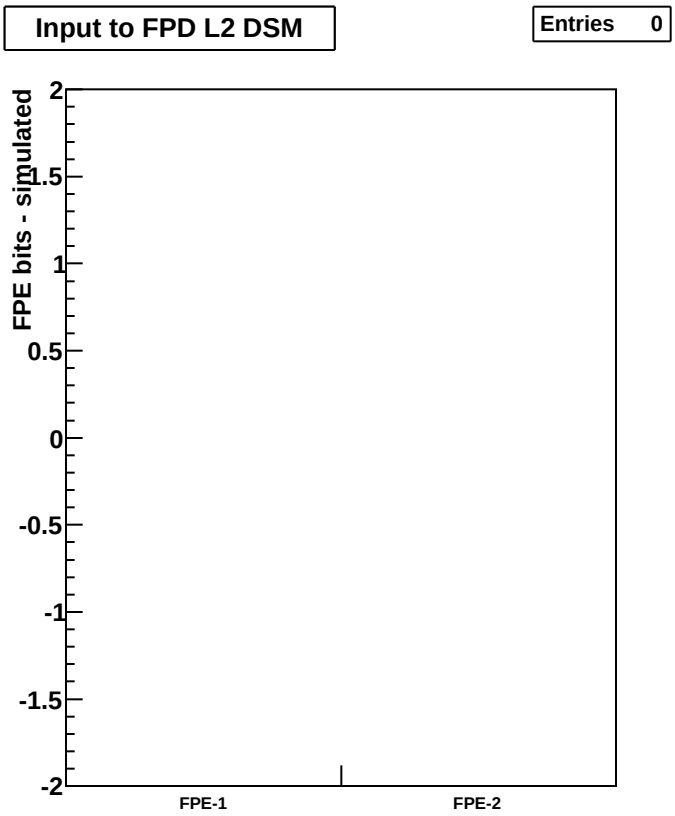

Input to FE003 QT board
Entries6674336
ADC
channel


Input to FE004 QT board
Entries6674336
ADC
channel


Input to FPE L1 DSM
Entries 834292
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 731961
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 417146
FPE bits
208573
208573
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2