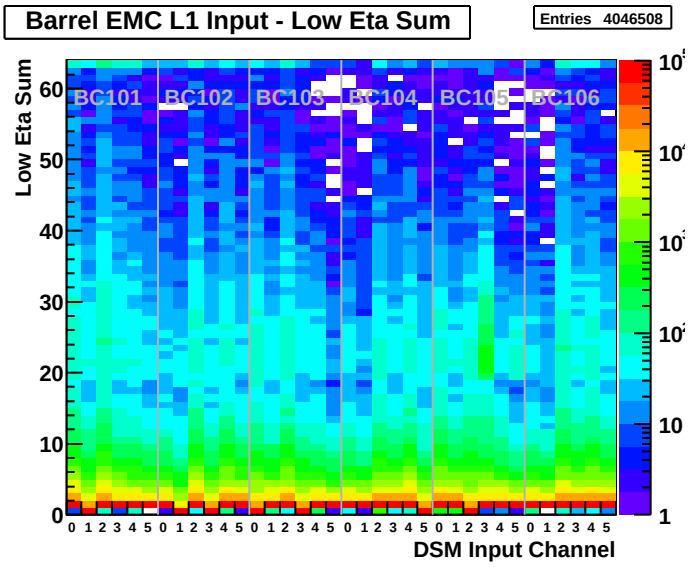
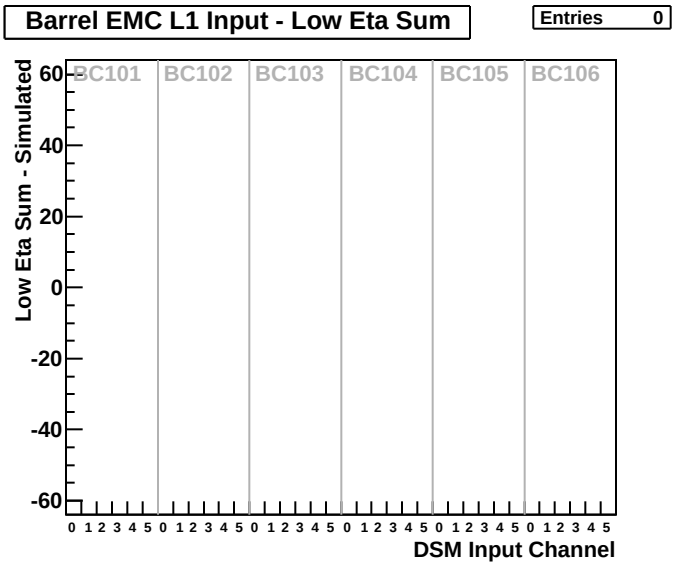
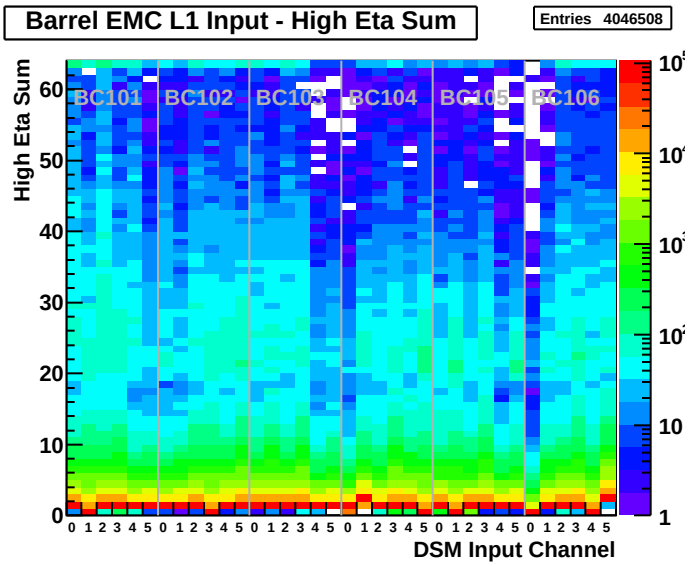
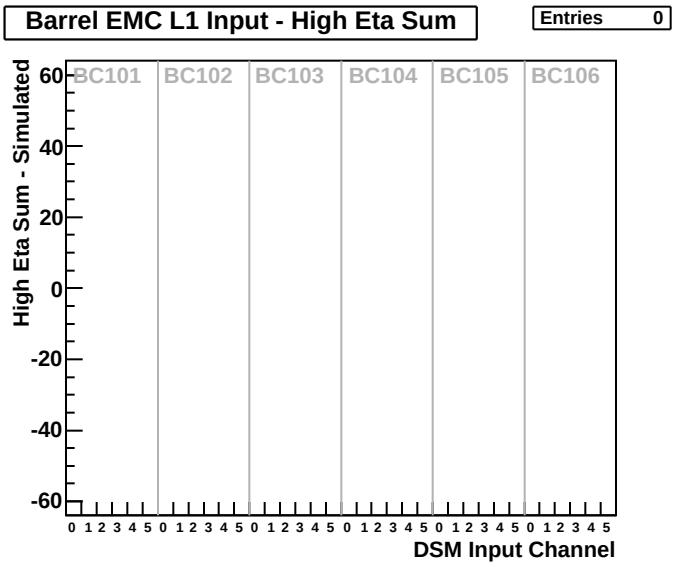
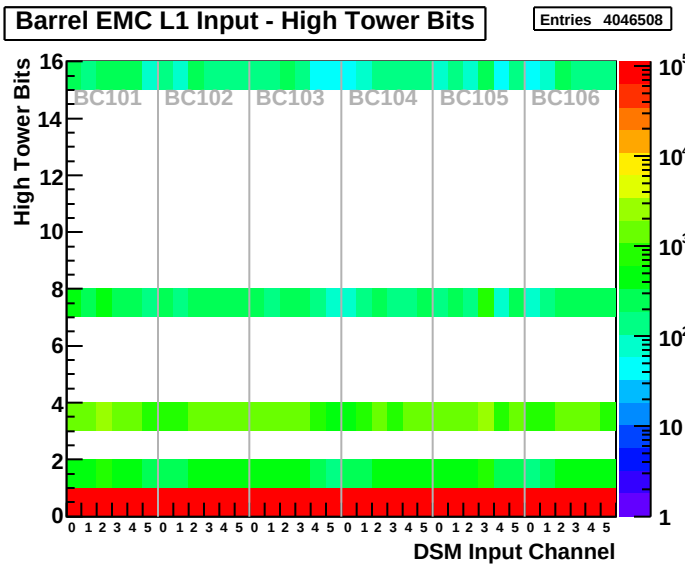
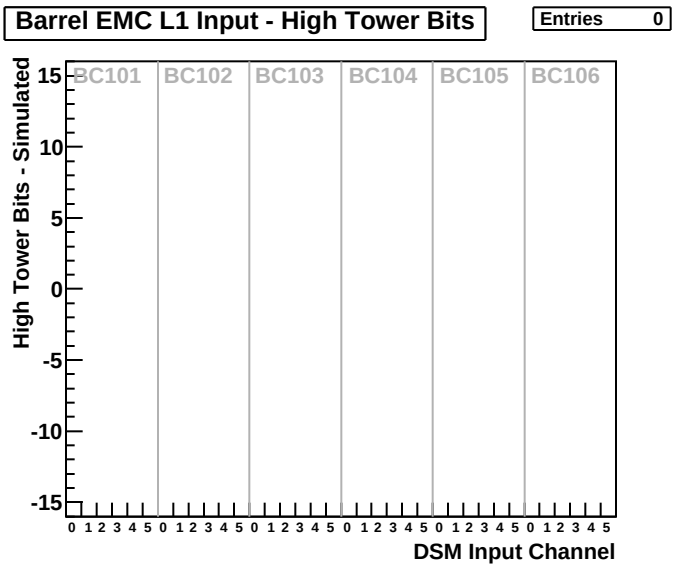

Barrel EMC L1 Input - Low Eta Sum
Entries 4046508
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

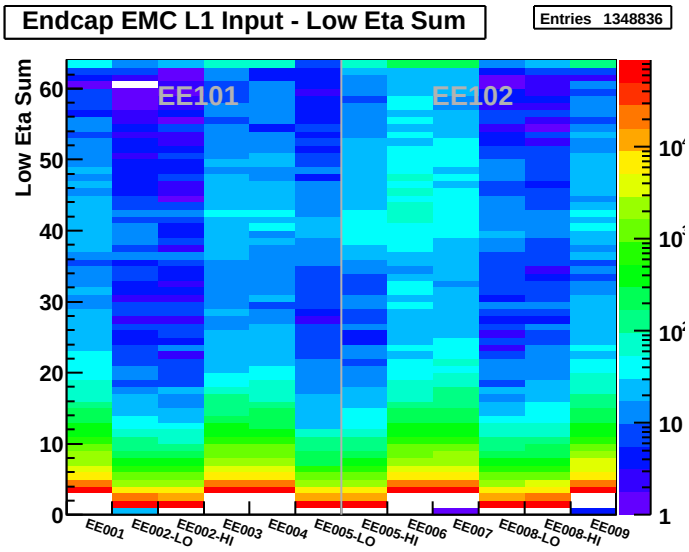

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

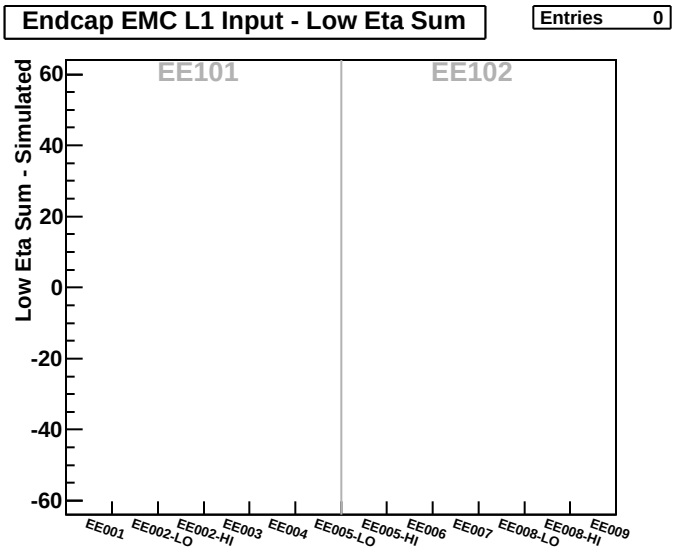

Barrel EMC L1 Input - High Eta Sum
Entries 4046508
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

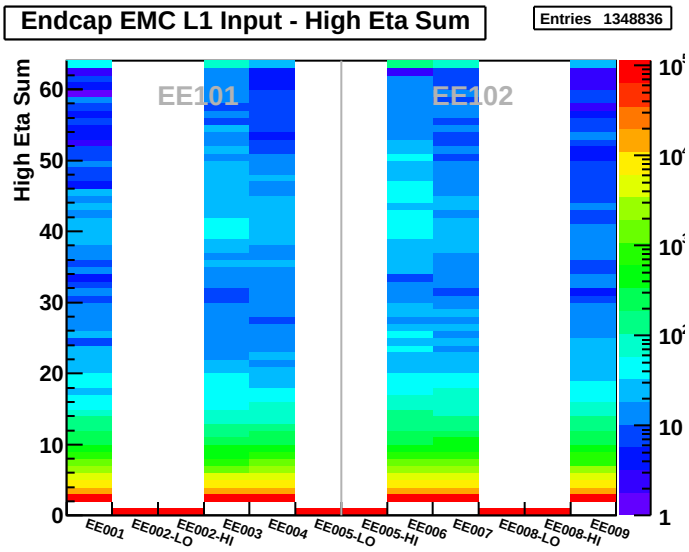

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

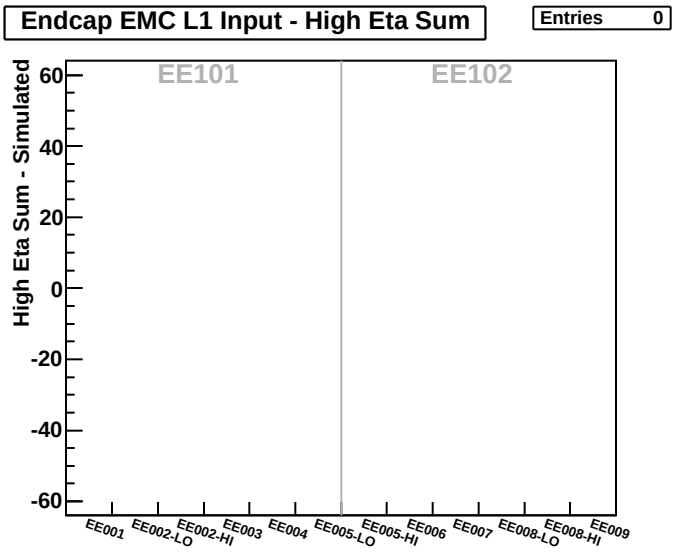

Barrel EMC L1 Input - High Tower Bits
Entries 4046508
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

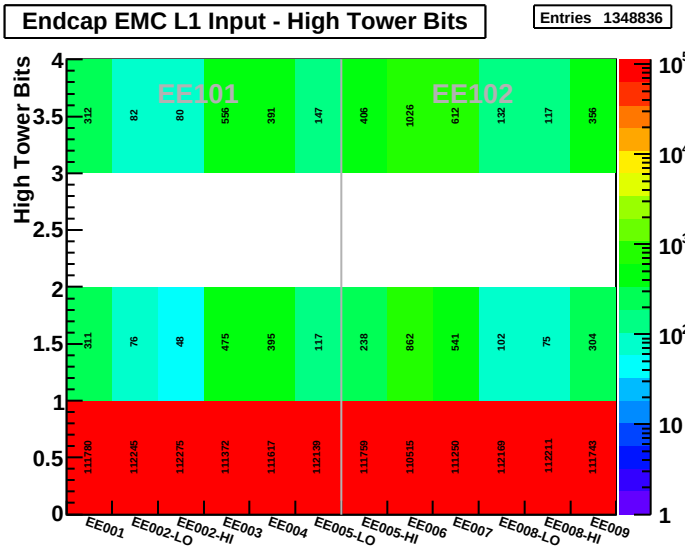

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

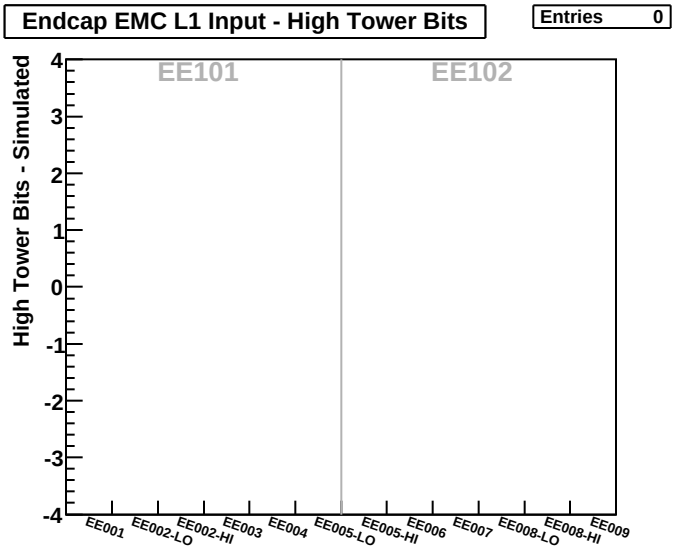

Endcap EMC L1 Input - Low Eta Sum
Entries 1348836
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

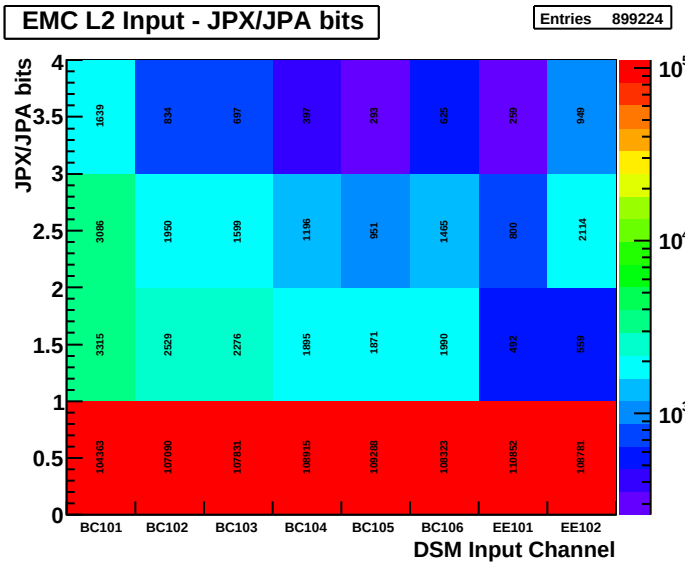

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

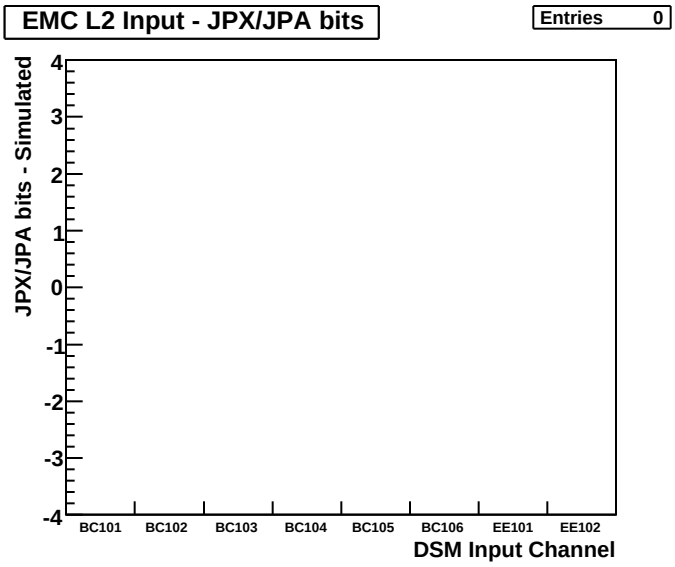

Endcap EMC L1 Input - High Eta Sum
Entries 1348836
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

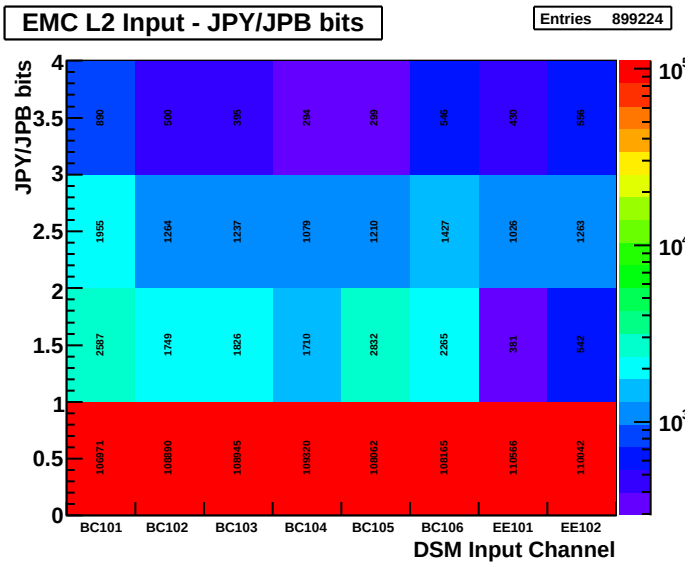

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

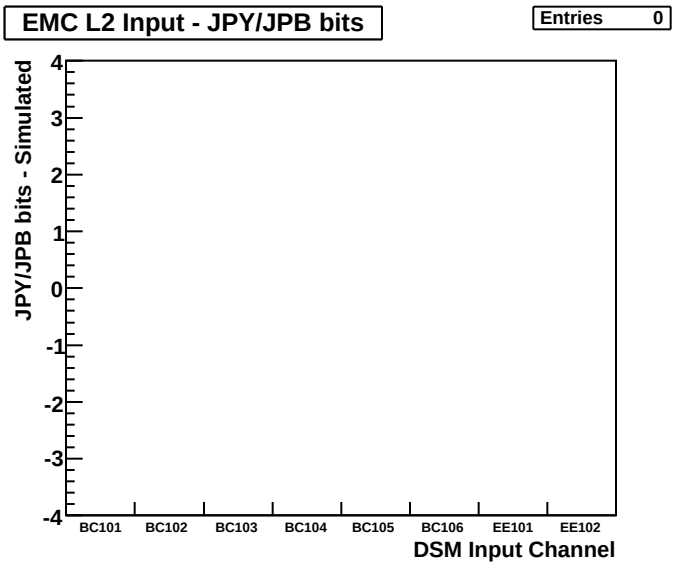

Endcap EMC L1 Input - High Tower Bits
Entries 1348836
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

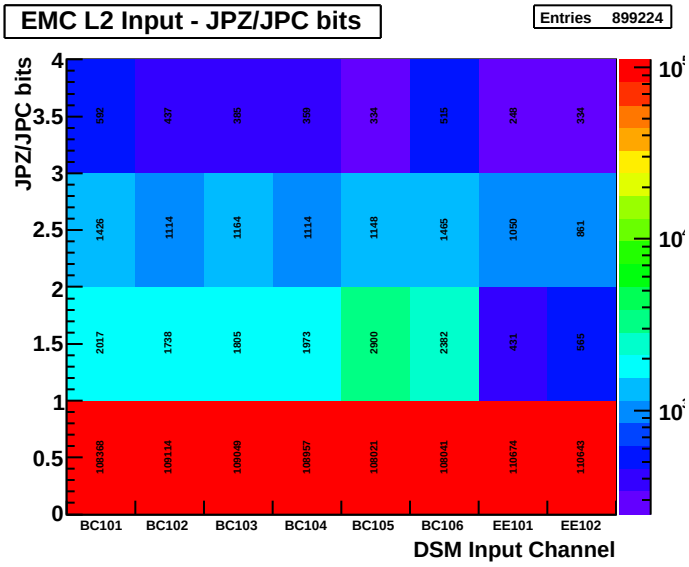

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

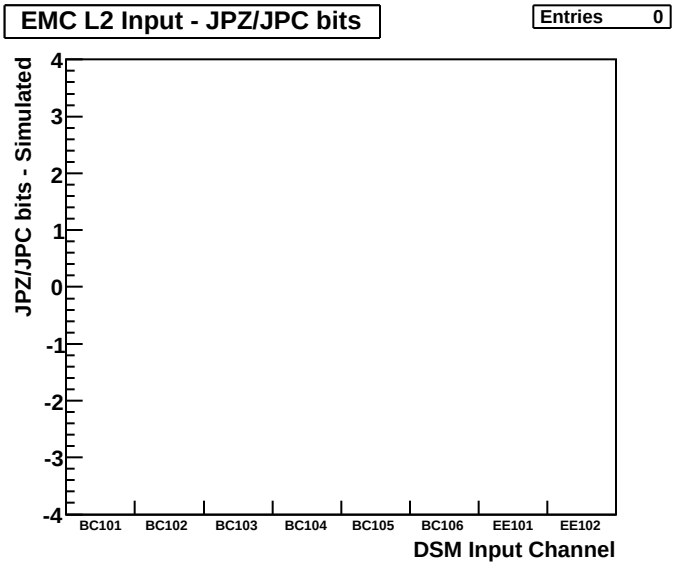

EMC L2 Input - JPX/JPA bits
Entries 899224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1639 834 697 397 293 625 259 940
3086 1950 1599 1196 951 1465 800 2114
3315 2529 2276 1895 1871 1990 492 559
104363 107090 107831 108915 109288 108323 110852 108781

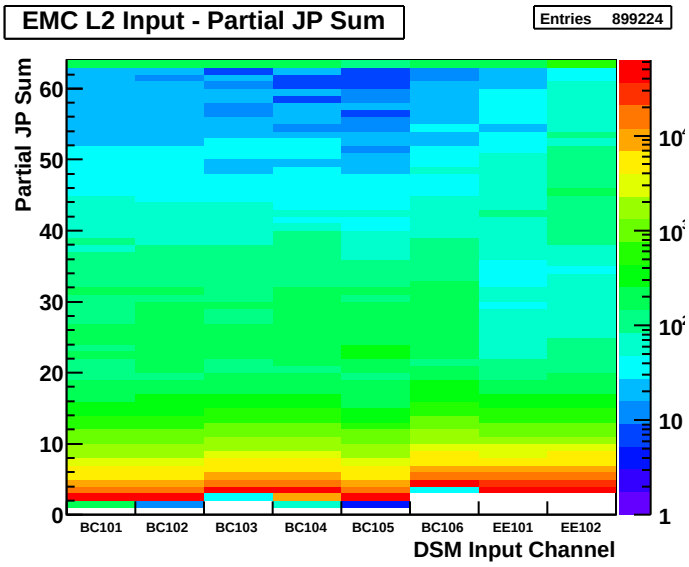

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

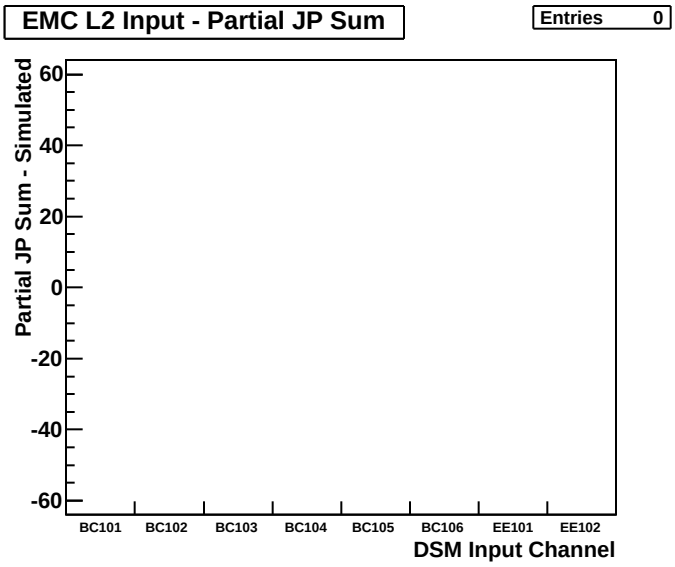

EMC L2 Input - JPY/JPB bits
Entries 899224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
890 500 395 294 299 546 430 556
1955 1264 1237 1079 1210 1427 1026 1263
2587 1749 1826 1710 2832 2265 381 542
106971 108890 108945 109320 109062 108165 110566 110042

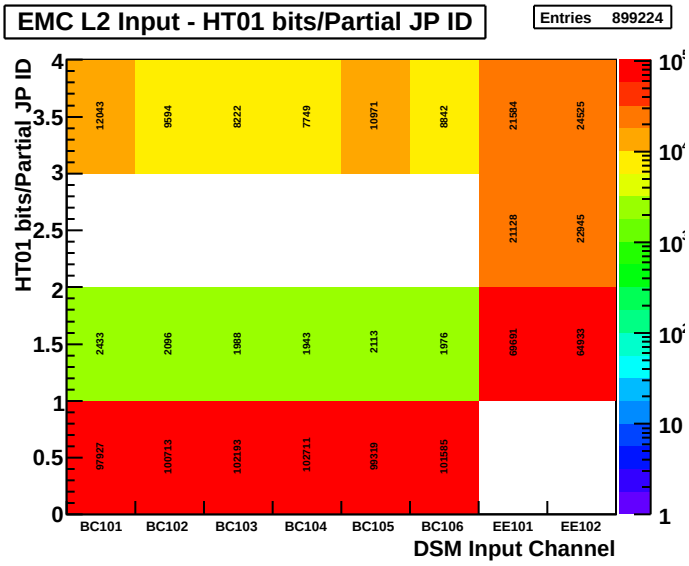

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

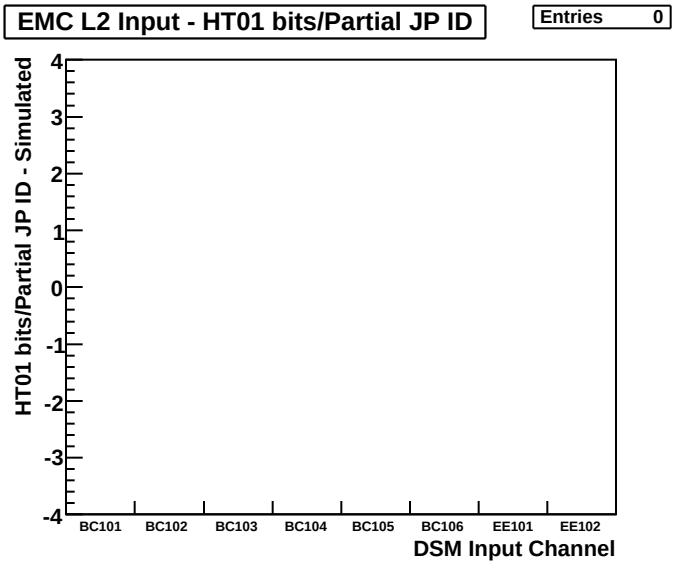

EMC L2 Input - JPZ/JPC bits
Entries 899224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
582 437 385 359 334 515 248 334
1426 1114 1164 1114 1148 1465 1050 861
2017 1738 1805 1973 2900 2382 431 565
108368 109114 109049 108957 108021 108041 110574 110543

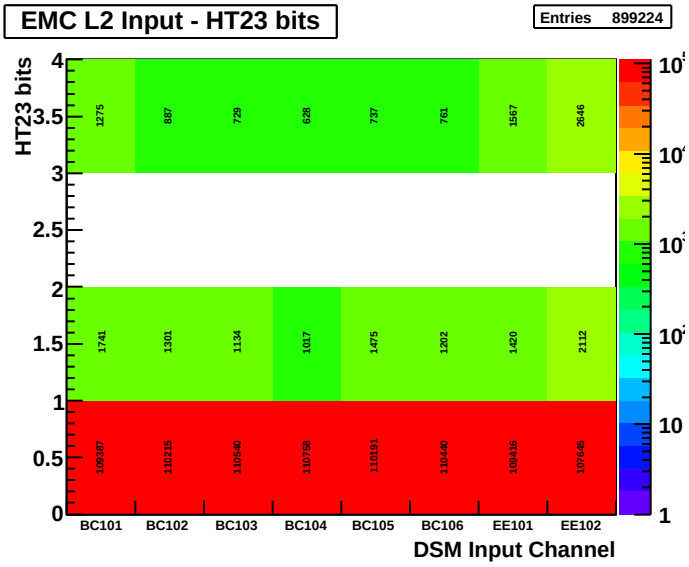

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

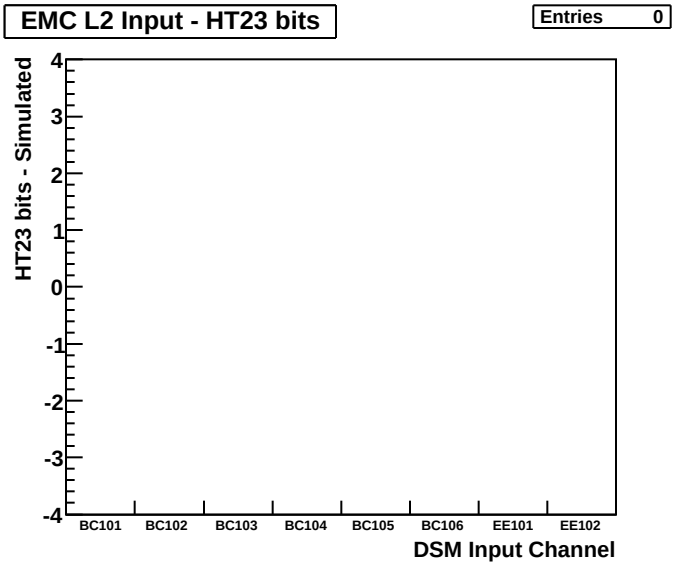

EMC L2 Input - Partial JP Sum
Entries 899224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

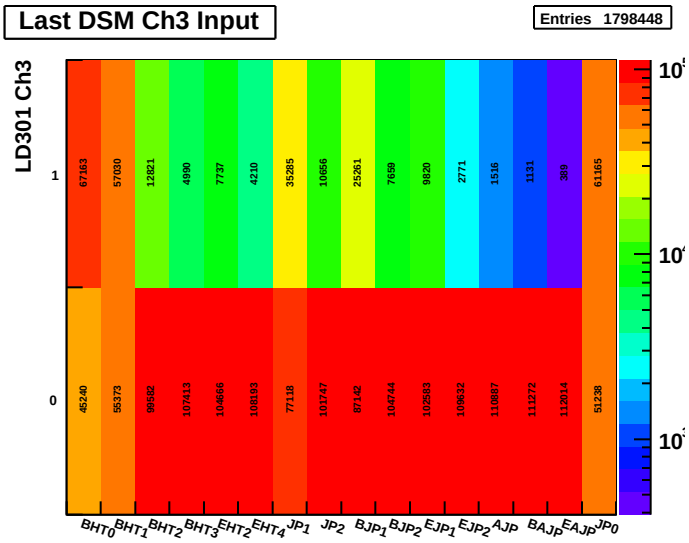

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

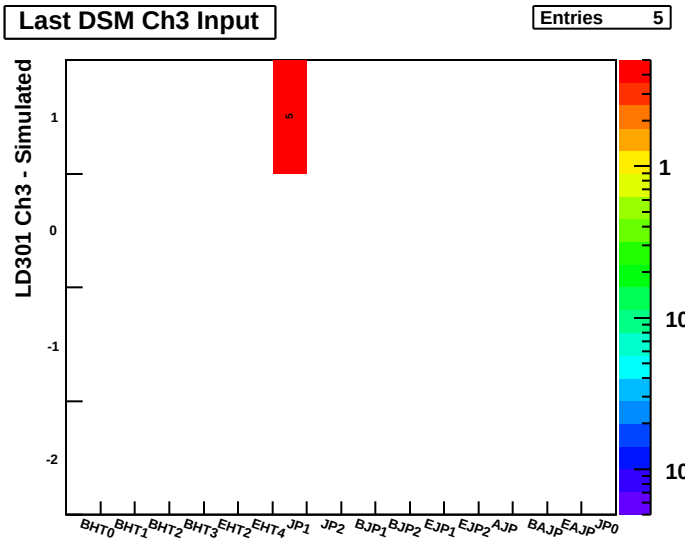

EMC L2 Input - HT01 bits/Partial JP ID
Entries 899224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

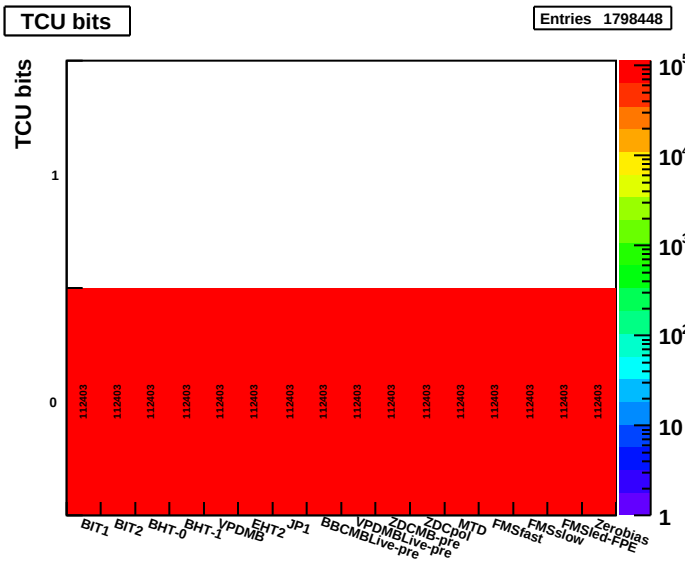

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

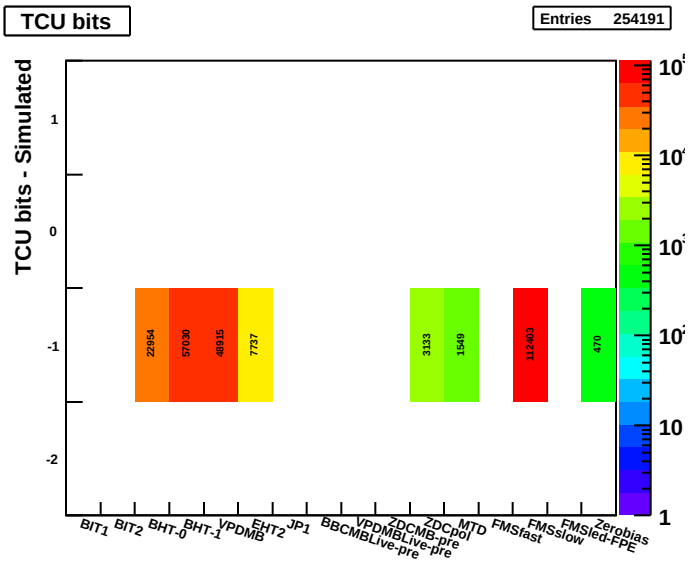

EMC L2 Input - HT23 bits
Entries 899224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

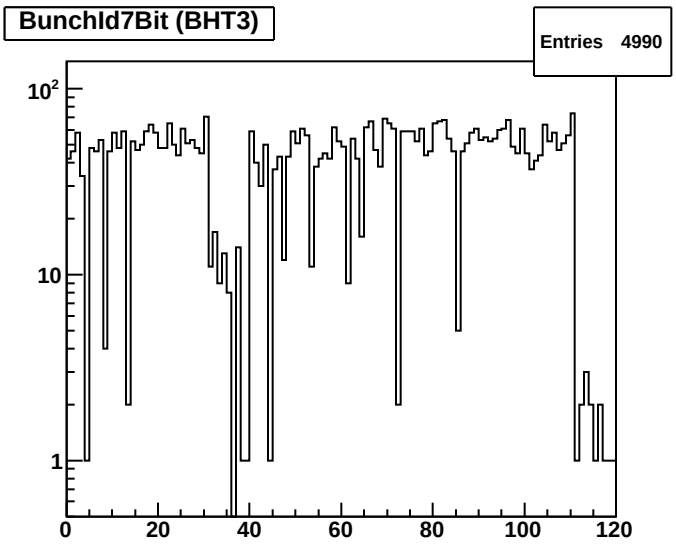

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

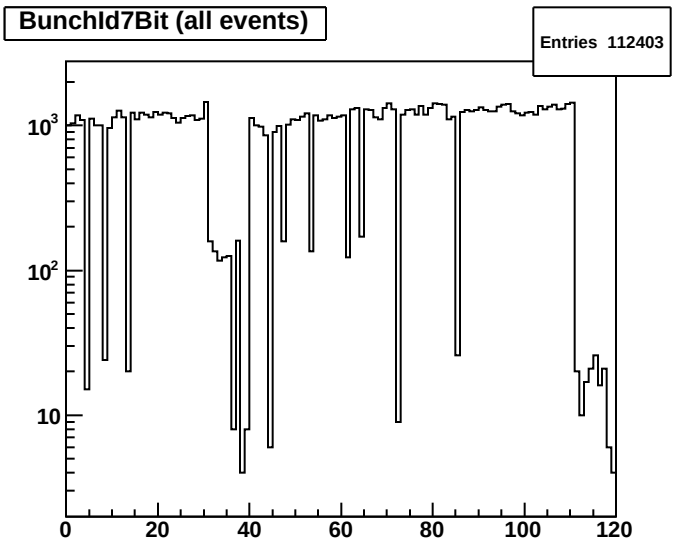

Last DSM Ch3 Input
Entries 1798448
LD301 Ch3
67163 57030 12821 4990 7737 4210 35295 10656 25261 7659 9820 2771 1516 1131 389 61165
45240 55373 99582 107413 104666 108193 77118 101747 87142 104744 102583 109632 110887 111272 112014 51238
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

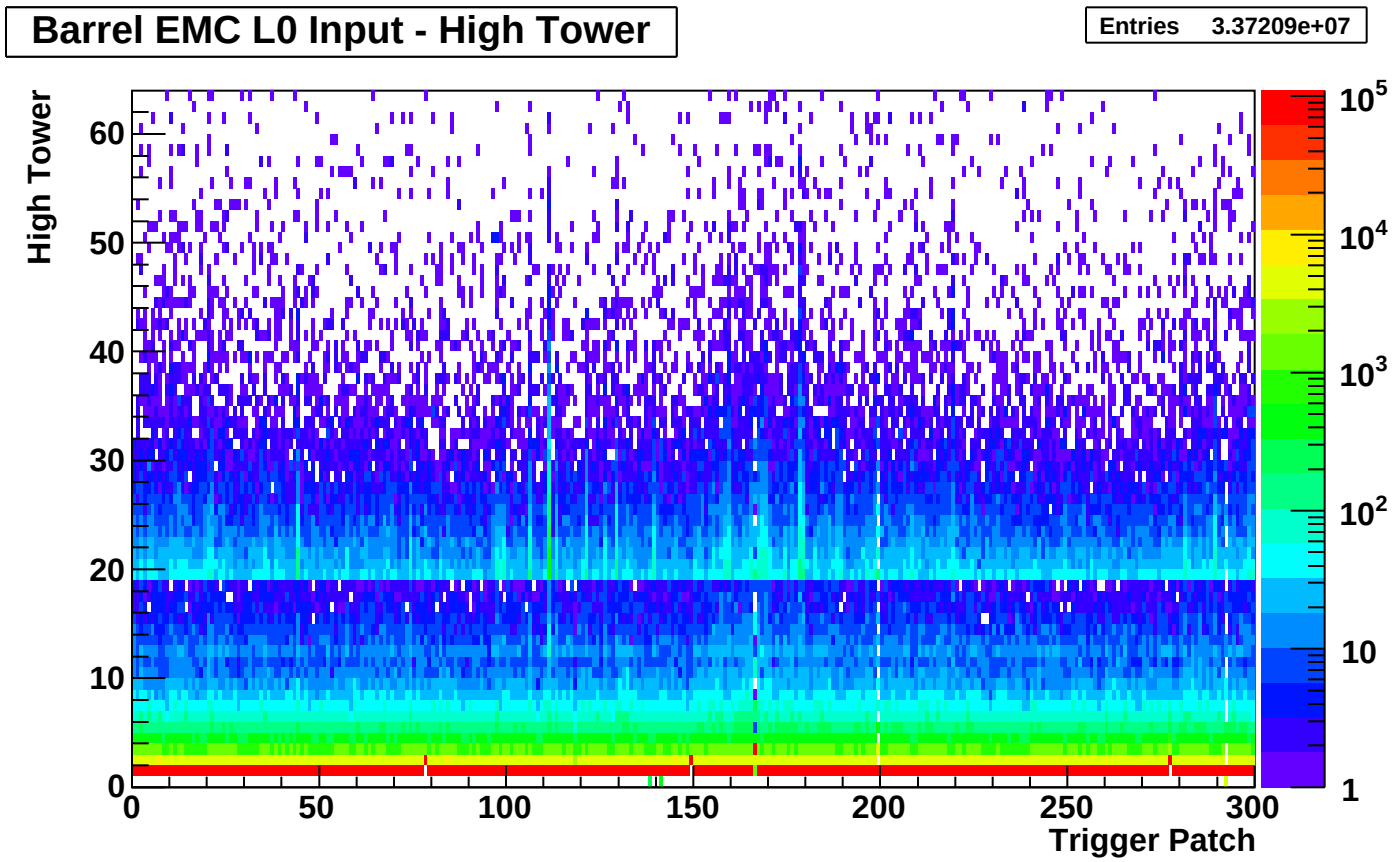

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
5
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

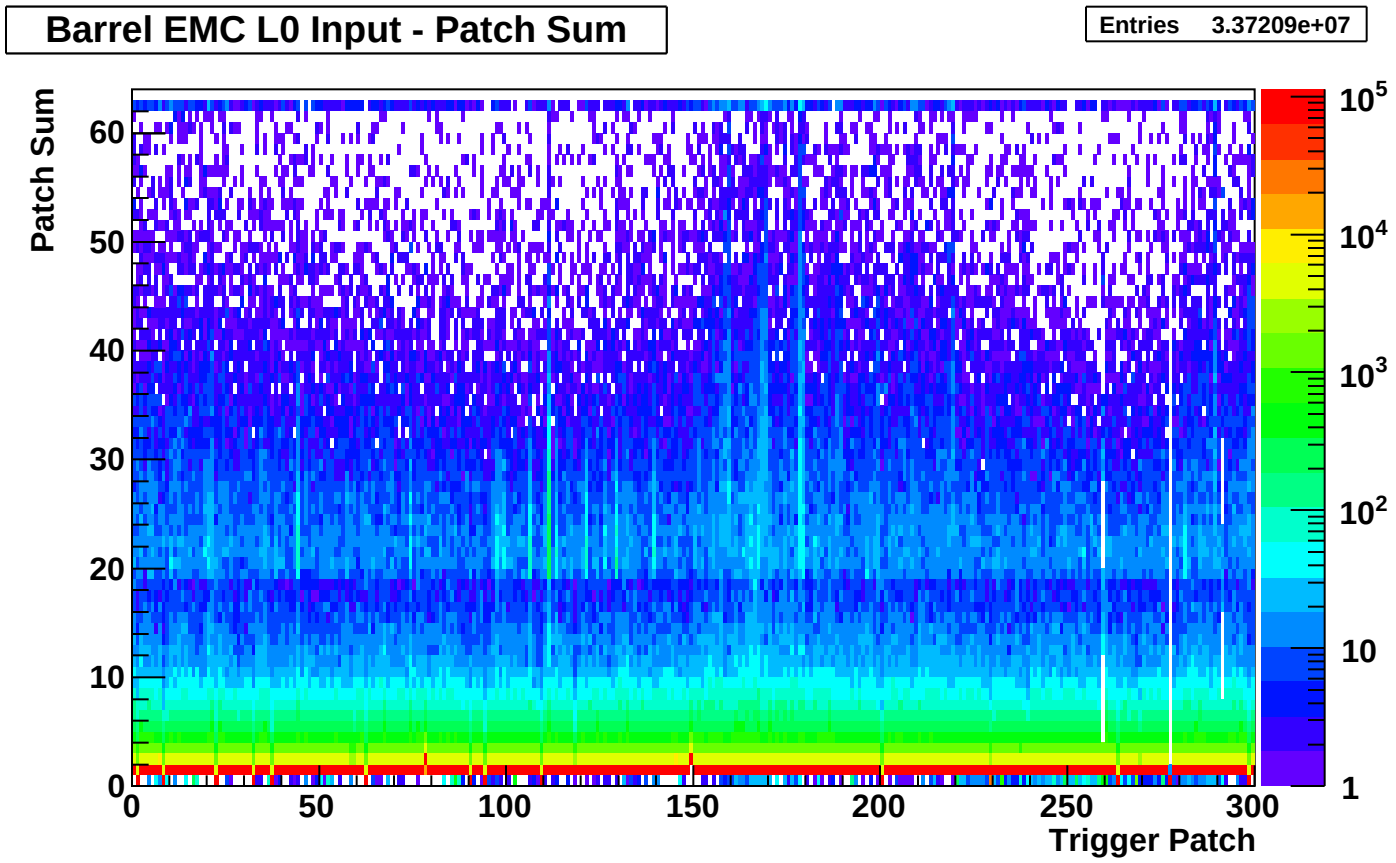

TCU bits
Entries 1798448
TCU bits
112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403 112403
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

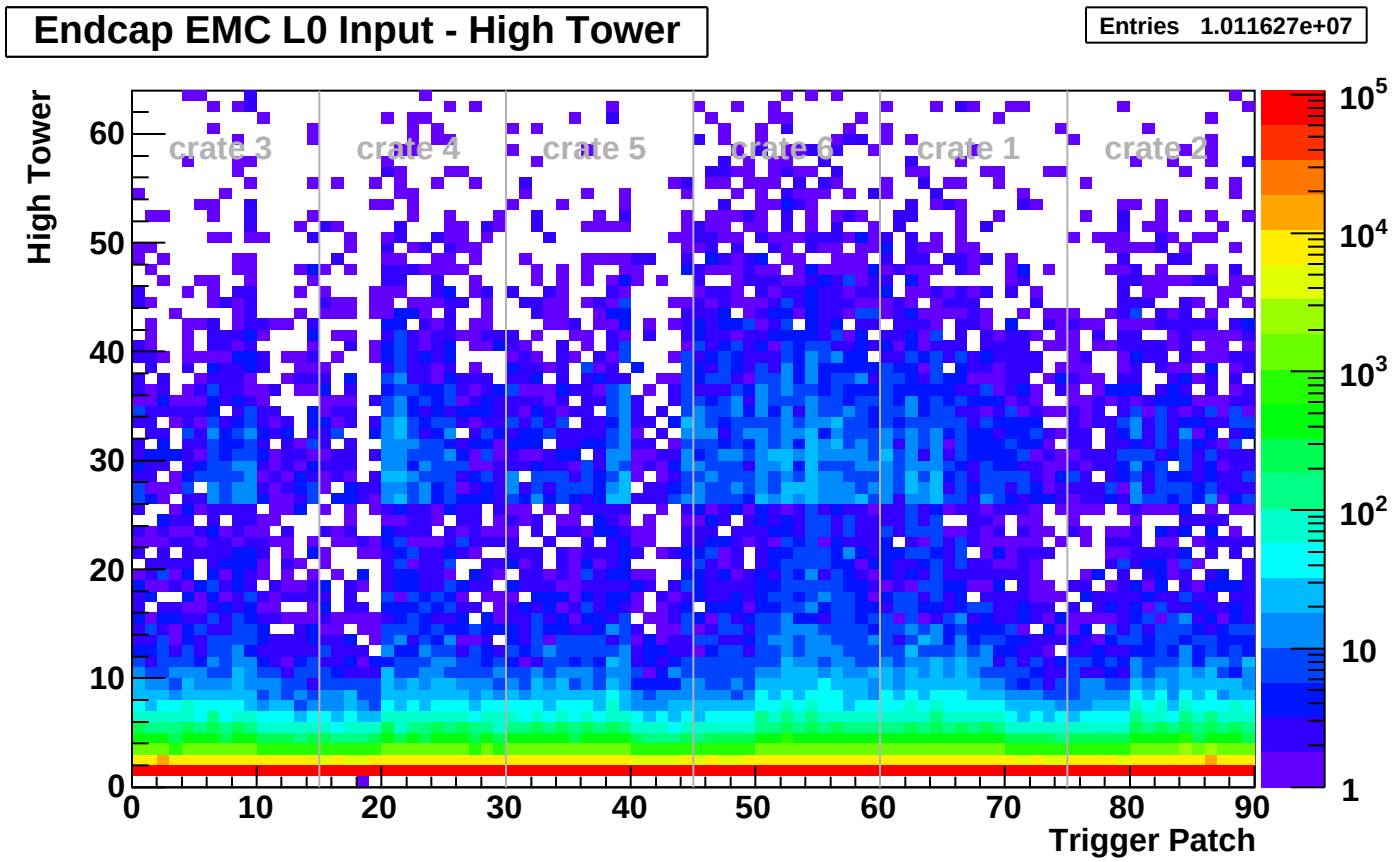

TCU bits
Entries 254191
TCU bits - Simulated
22954 57030 48915 7737 3133 1549 112403 470
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

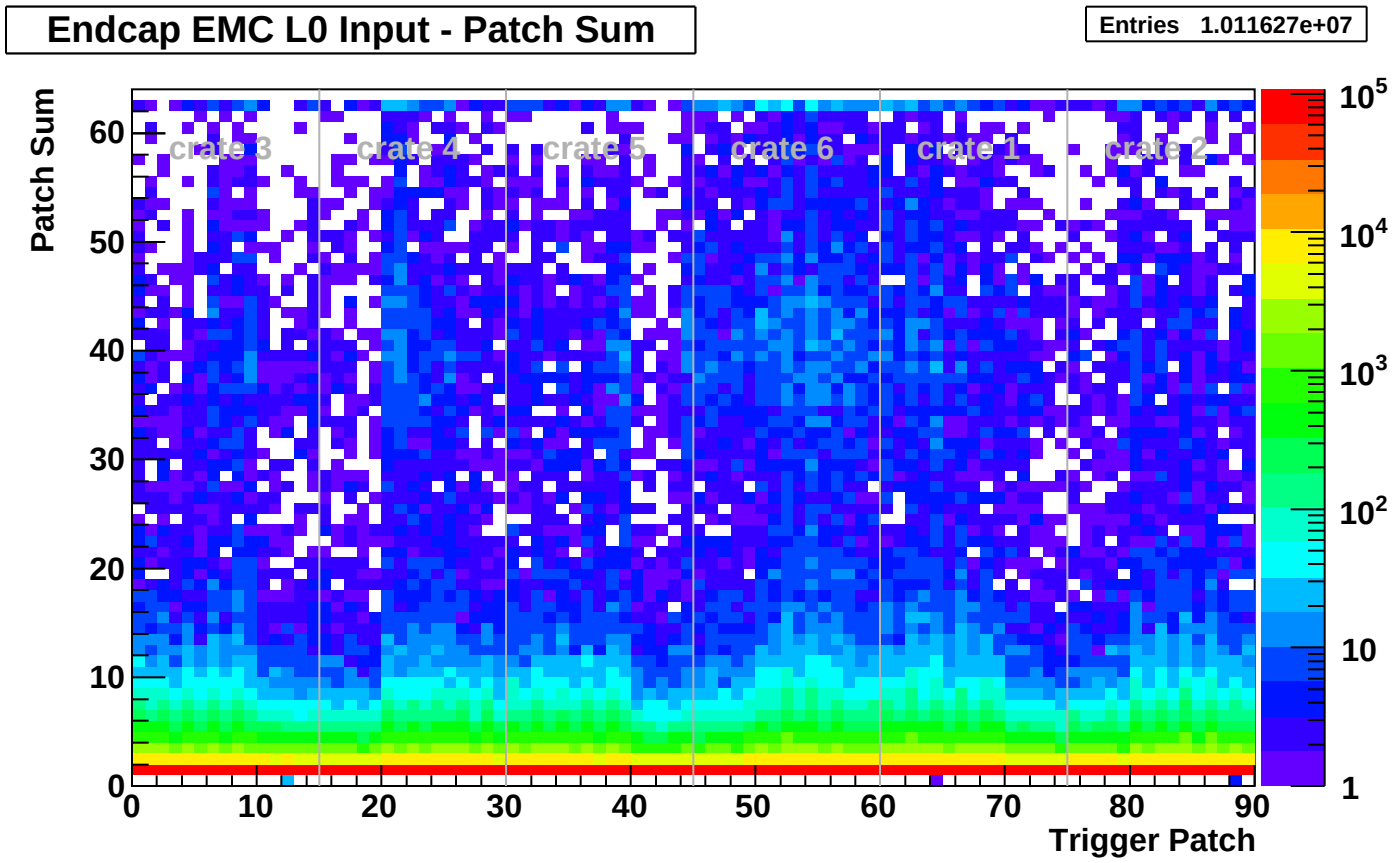

BunchId7Bit (BHT3)
Entries 4990

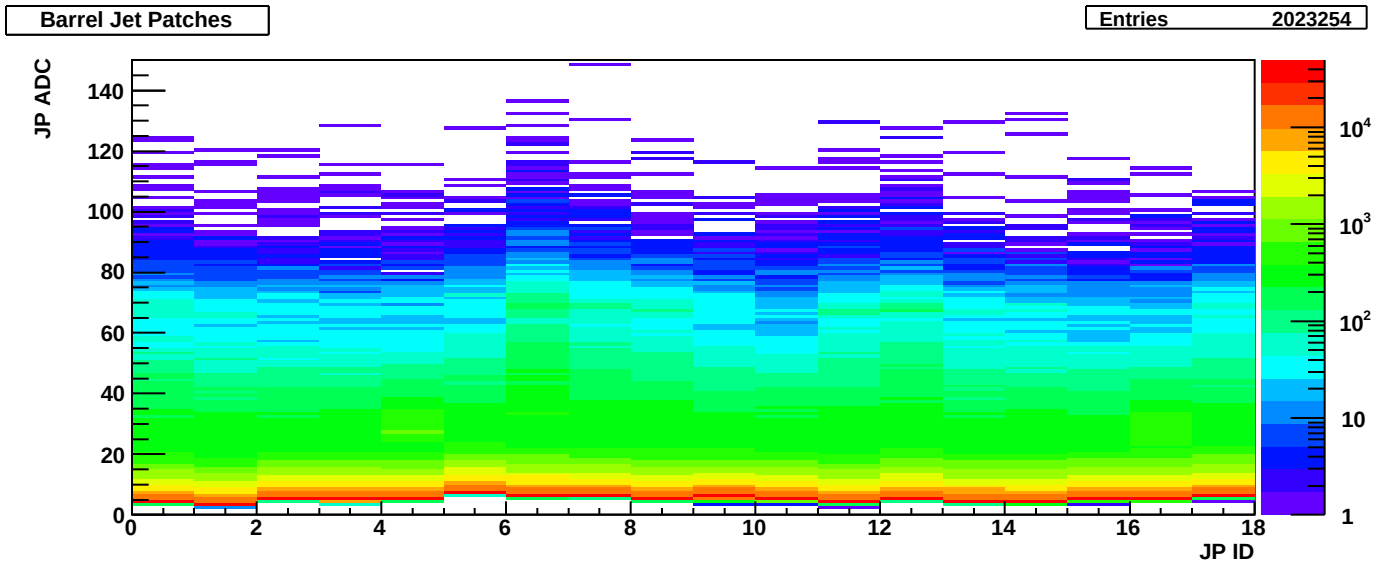

BunchId7Bit (all events)
Entries 112403

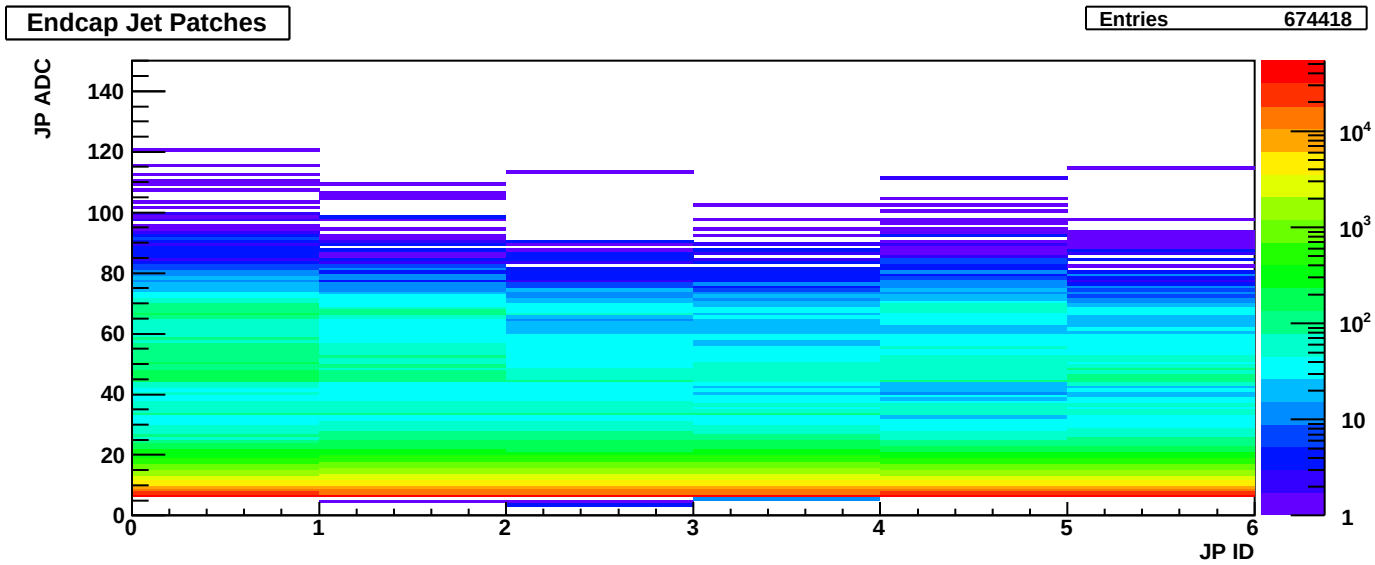

Barrel EMC L0 Input - High Tower
Entries 3.37209e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

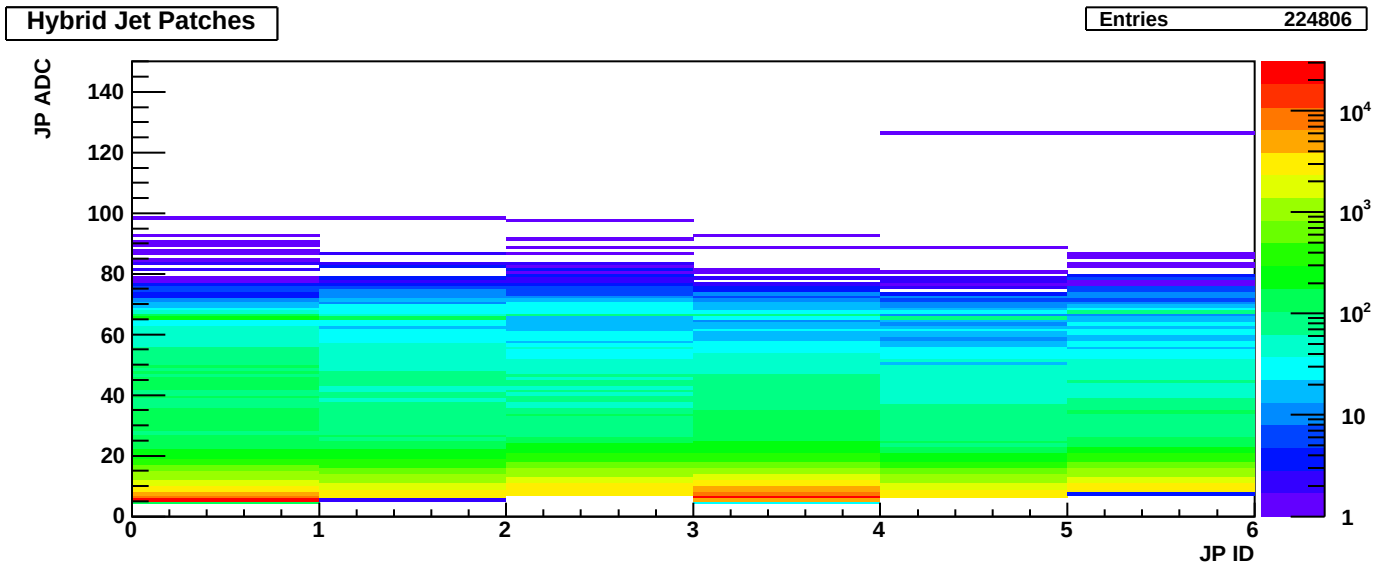

Barrel EMC L0 Input - Patch Sum
Entries 3.37209e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

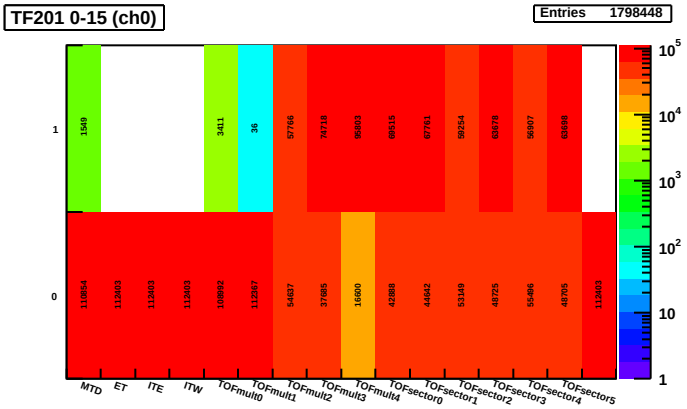

Endcap EMC L0 Input - High Tower
Entries 1.011627e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

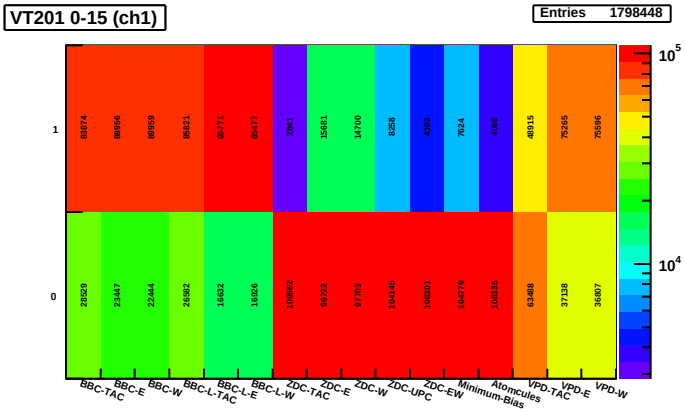

Endcap EMC L0 Input - Patch Sum
Entries 1.011627e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

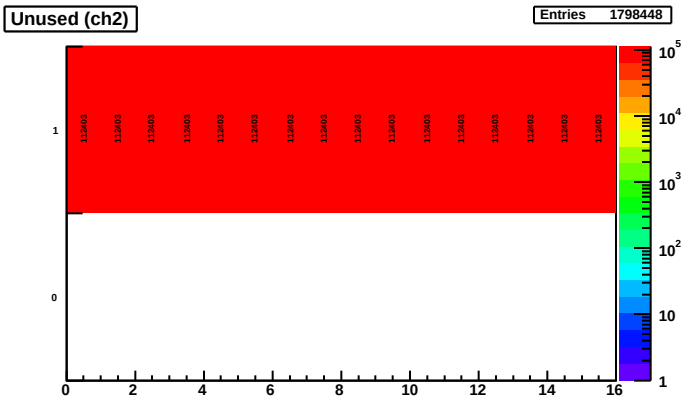

Barrel Jet Patches
Entries 2023254
JP ADC
JP ID

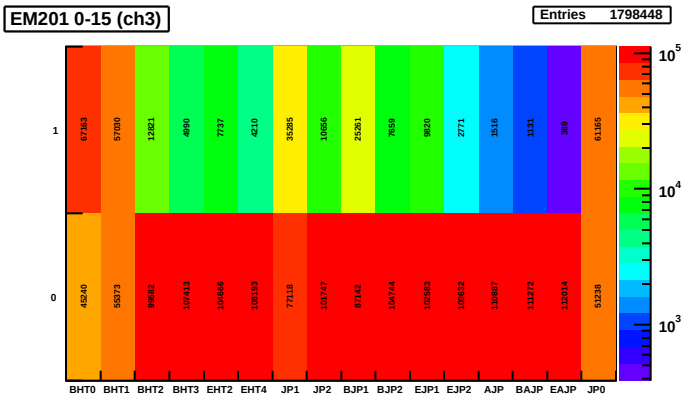

Endcap Jet Patches
Entries 674418
JP ADC
JP ID

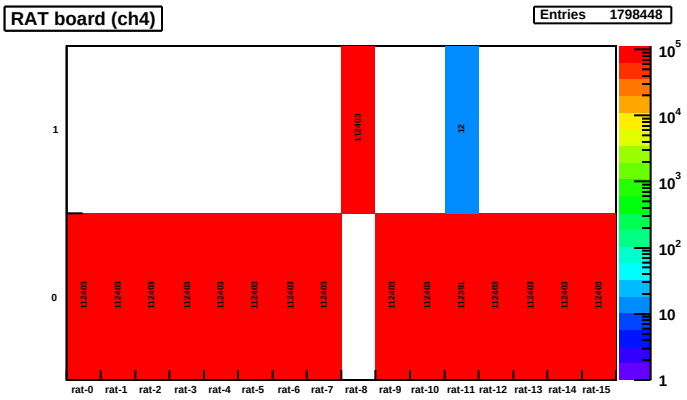

Hybrid Jet Patches
Entries 224806
JP ADC
JP ID

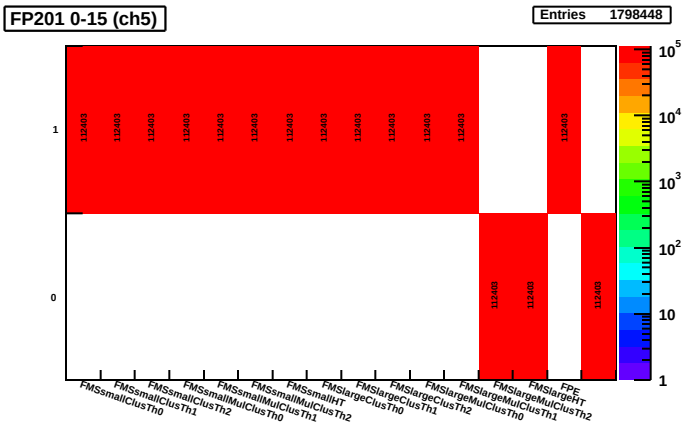

TF201 0-15 (ch0)
Entries 1798448
1549
3411
36
57766
74718
86803
69615
67761
59254
63878
56907
63698
110854
112403
112403
112403
108992
112367
54637
37885
16600
42888
44642
53149
48725
55486
48705
112403
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

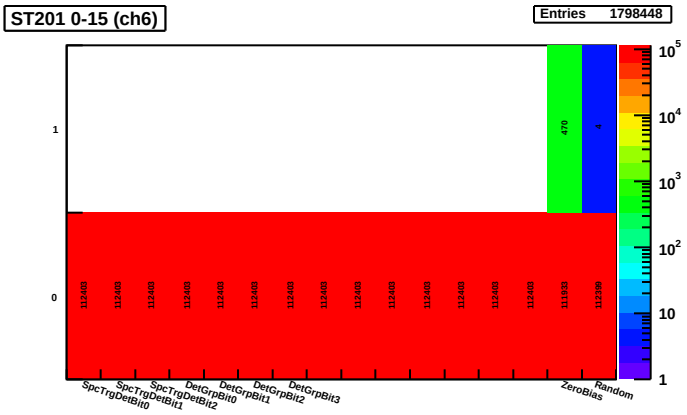

VT201 0-15 (ch1)
Entries 1798448
83874
88956
89959
86821
95771
95477
2361
15681
14700
8258
4192
7624
4068
49915
75265
75586
28529
23447
22444
26582
16632
16926
109562
96722
97703
104145
108301
104779
108535
63488
37138
36807
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

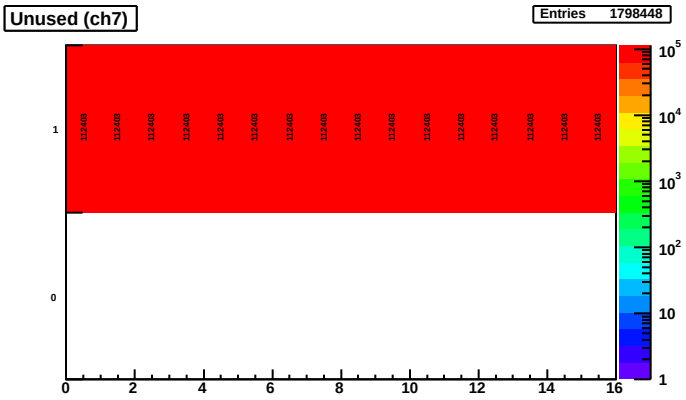

Unused (ch2)
Entries 1798448
112403

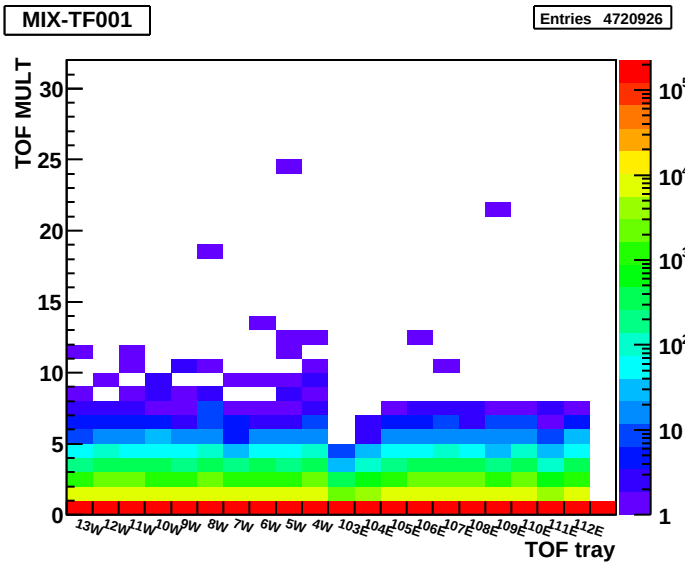

EM201 0-15 (ch3)
Entries 1798448
67163
57030
12821
4990
7737
4210
35285
10866
25361
7659
9820
2771
1516
1131
389
61165
45240
53373
96882
107413
104666
108193
77118
101747
87142
104744
102583
108632
110897
111272
112014
51238
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

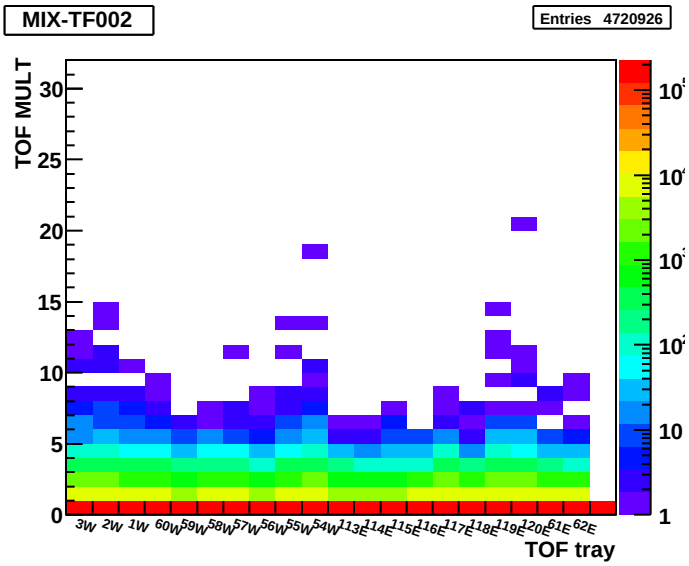

RAT board (ch4)
Entries 1798448
112403
12
112403
112403
112403
112403
112403
112403
112403
112403
112403
112391
112403
112403
112403
112403
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

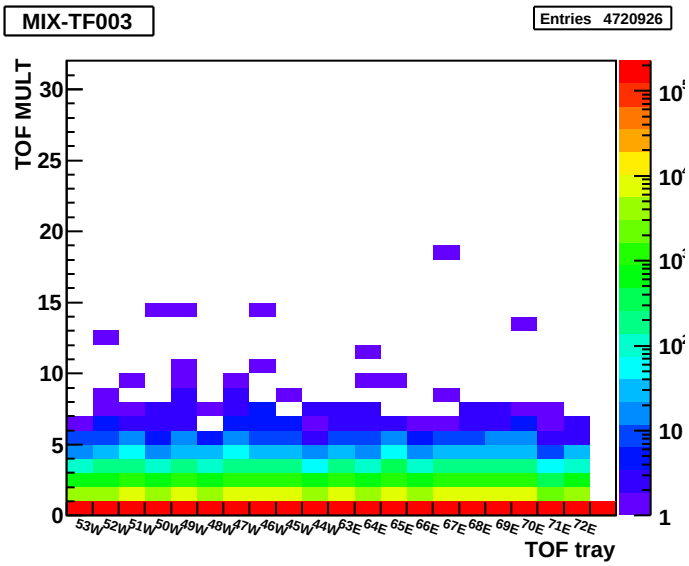

FP201 0-15 (ch5)
Entries 1798448
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

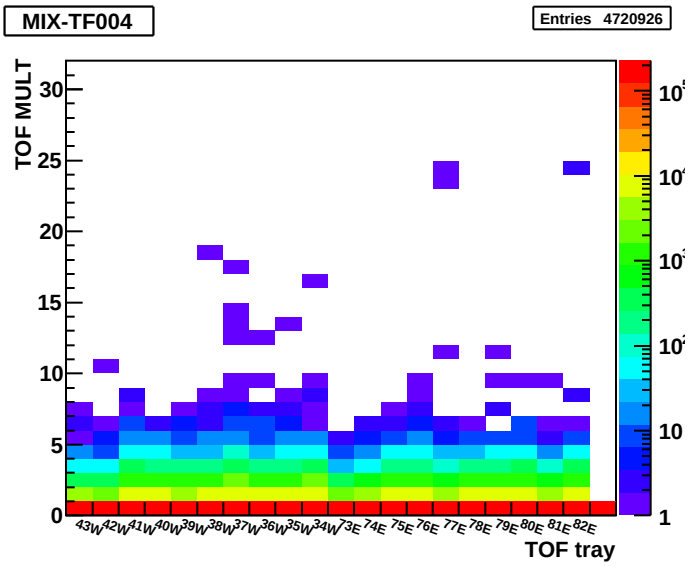

ST201 0-15 (ch6)
Entries 1798448
470
4
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
112403
111933
112399
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

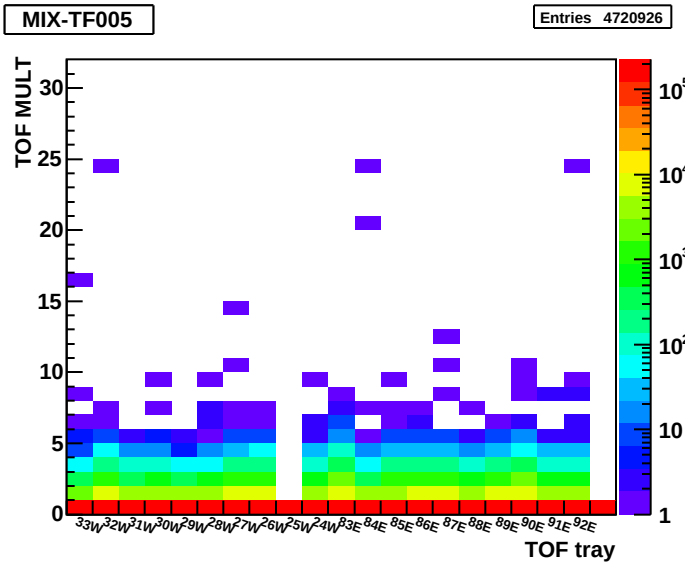

Unused (ch7)
Entries 1798448
112403

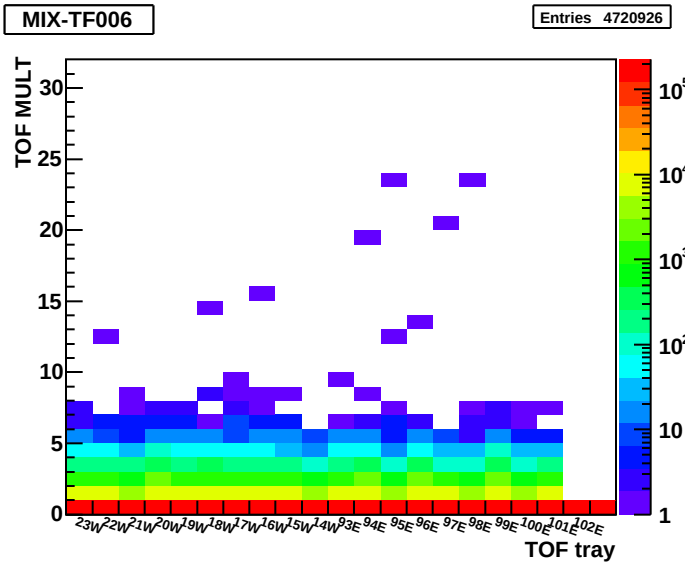

MIX-TF001
Entries 4720926
TOF MULT
TOF tray

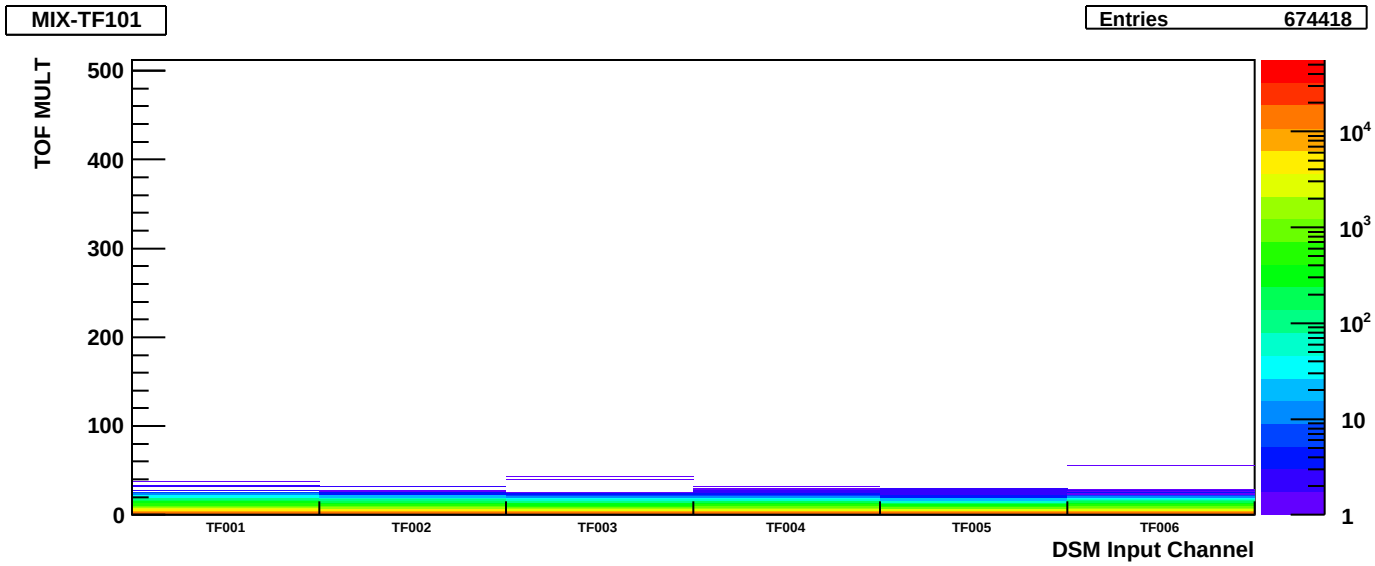

MIX-TF002
Entries 4720926
TOF MULT
TOF tray

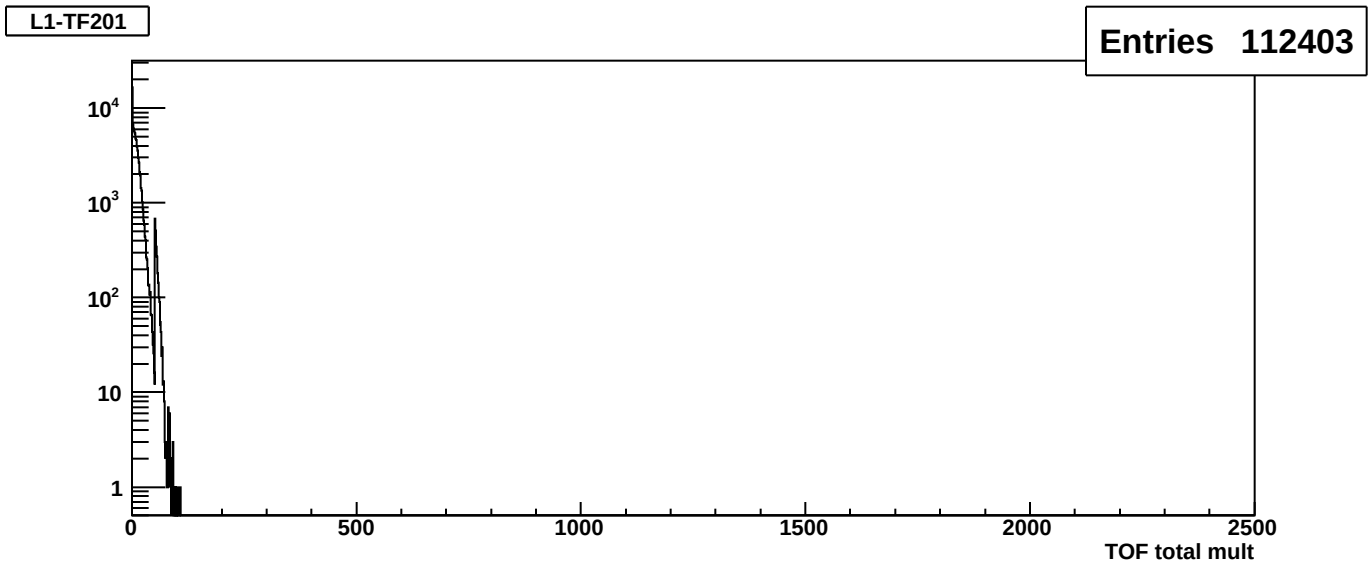

MIX-TF003
Entries 4720926
TOF MULT
TOF tray

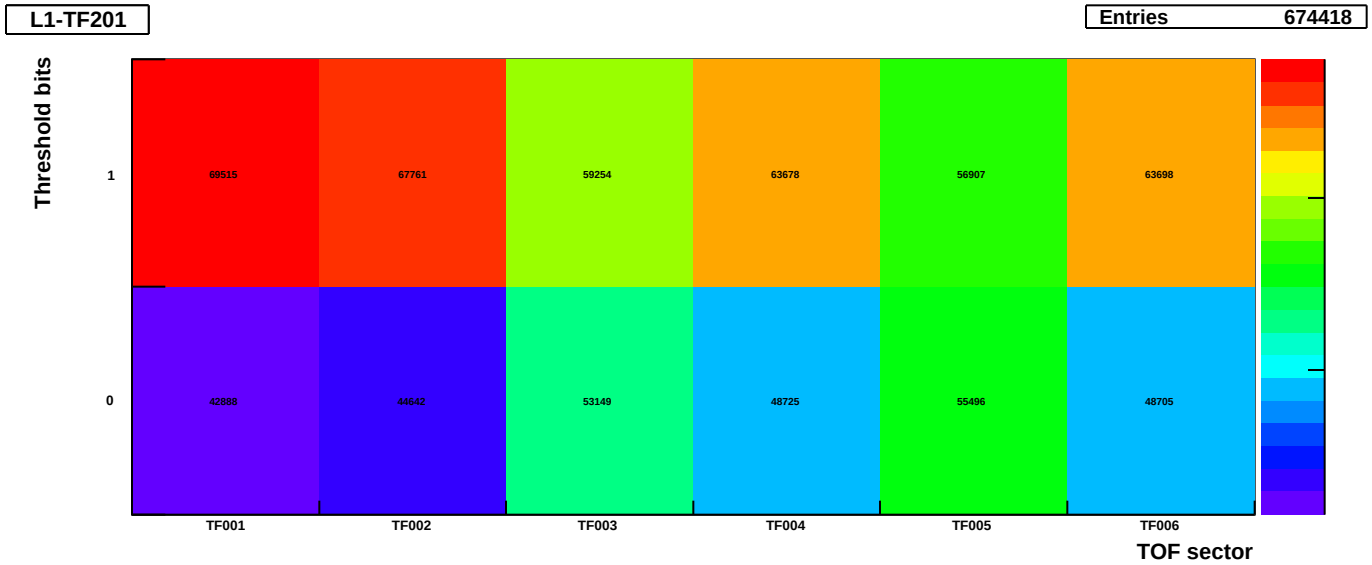

MIX-TF004
Entries 4720926
TOF MULT
TOF tray

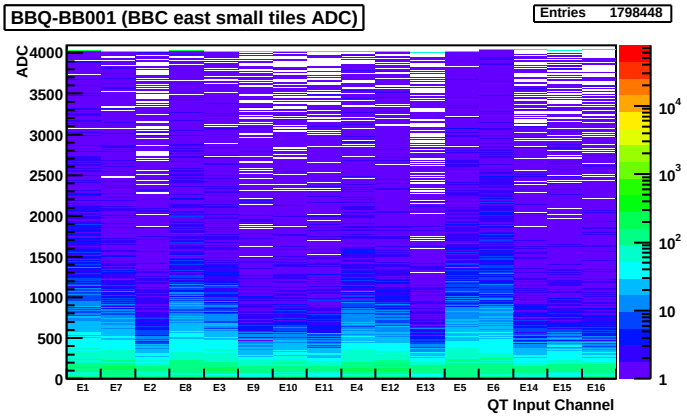

MIX-TF005
Entries 4720926
TOF MULT
TOF tray

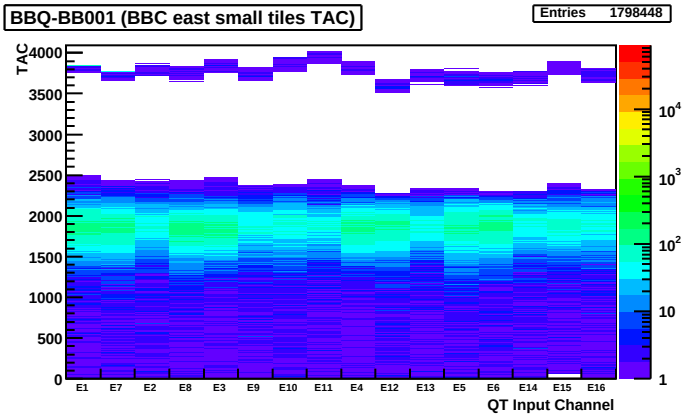

MIX-TF006
Entries 4720926
TOF MULT
TOF tray

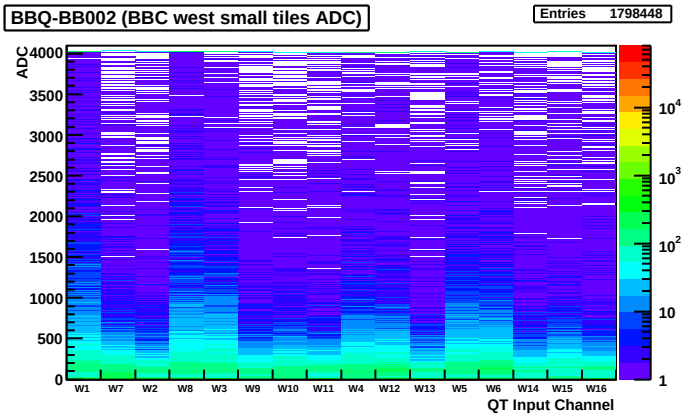

MIX-TF101
Entries 674418
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

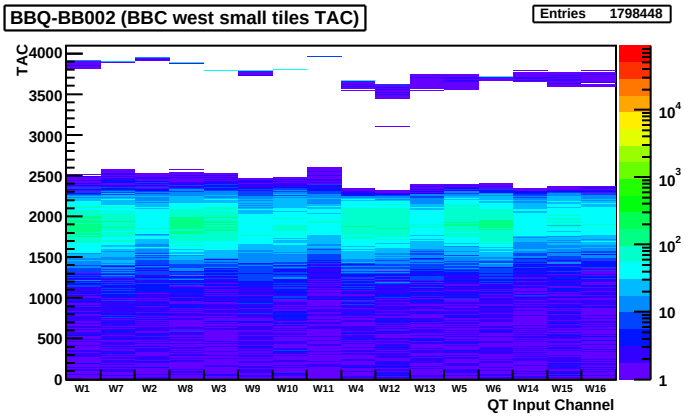

L1-TF201
Entries 112403
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

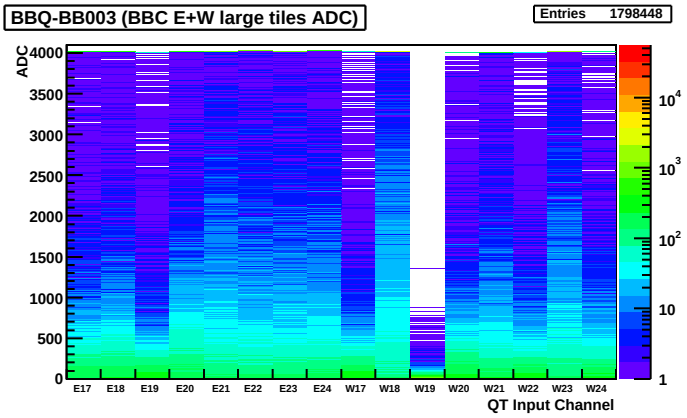

L1-TF201
Entries 674418
Threshold bits
1
0
69515
67761
59254
63678
56907
63698
42888
44642
53149
48725
55496
48705
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

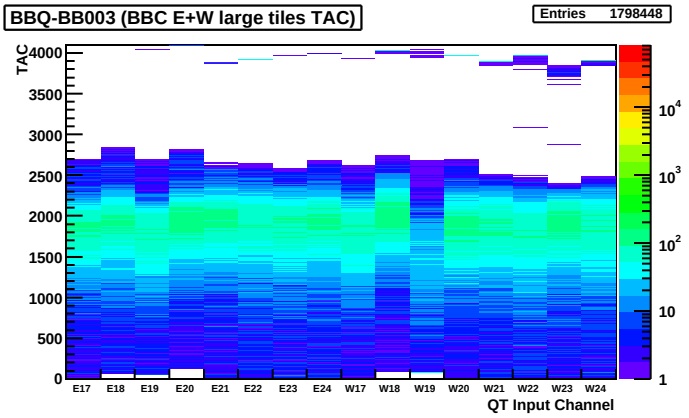

BBQ-BB001 (BBC east small tiles ADC)
Entries 1798448
ADC
QT Input Channel

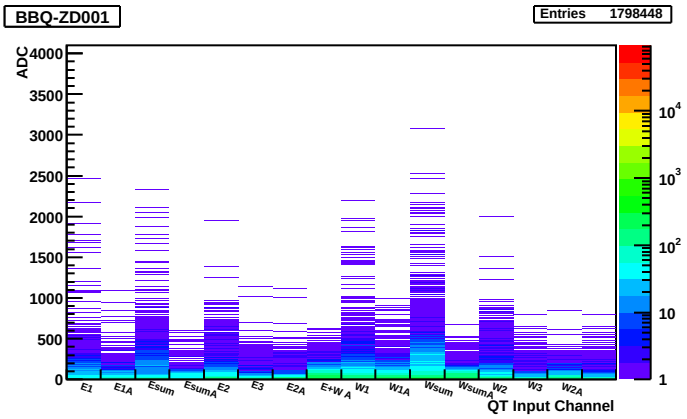

BBQ-BB001 (BBC east small tiles TAC)
Entries 1798448
TAC
QT Input Channel

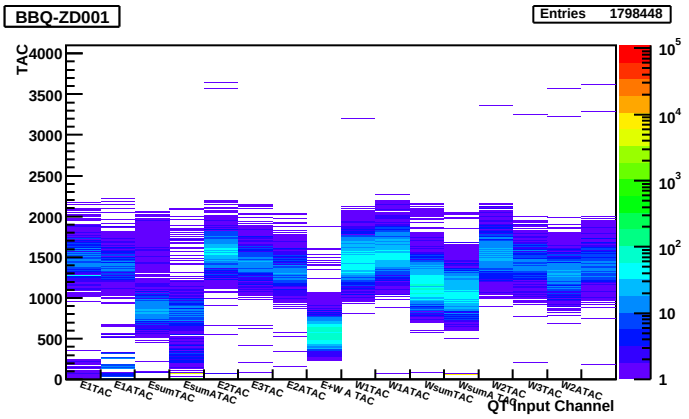

BBQ-BB002 (BBC west small tiles ADC)
Entries 1798448
ADC
QT Input Channel

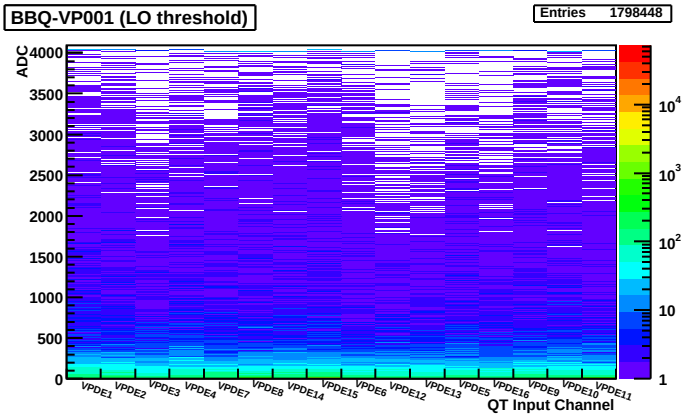

BBQ-BB002 (BBC west small tiles TAC)
Entries 1798448
TAC
QT Input Channel

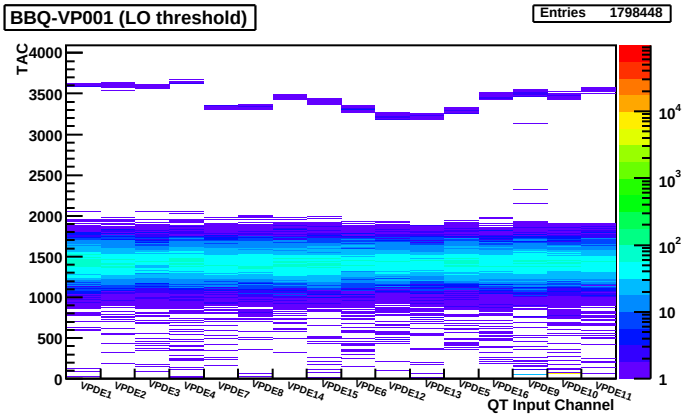

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1798448
ADC
QT Input Channel

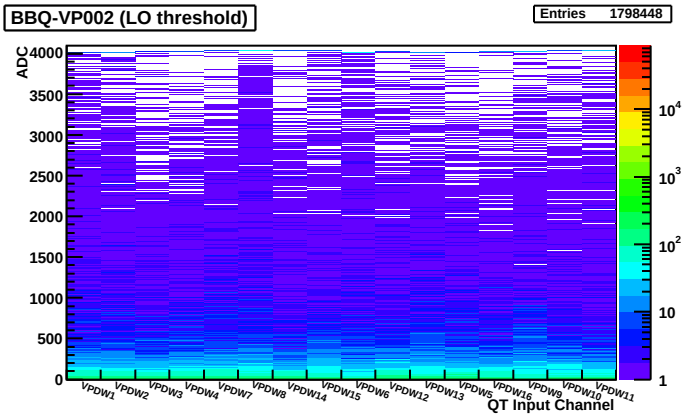

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1798448
TAC
QT Input Channel

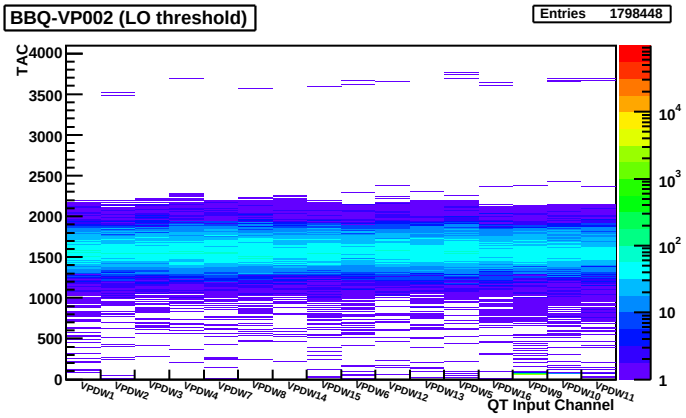

BBQ-ZD001
Entries 1798448
ADC
QT Input Channel

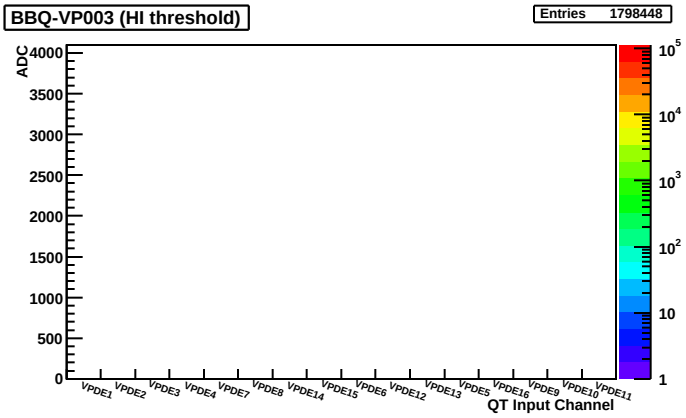

BBQ-ZD001
Entries 1798448
TAC
QT Input Channel

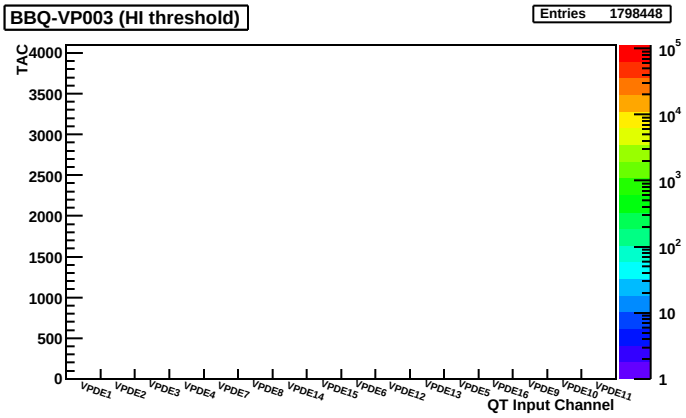

BBQ-VP001 (LO threshold)
Entries 1798448
ADC
QT Input Channel

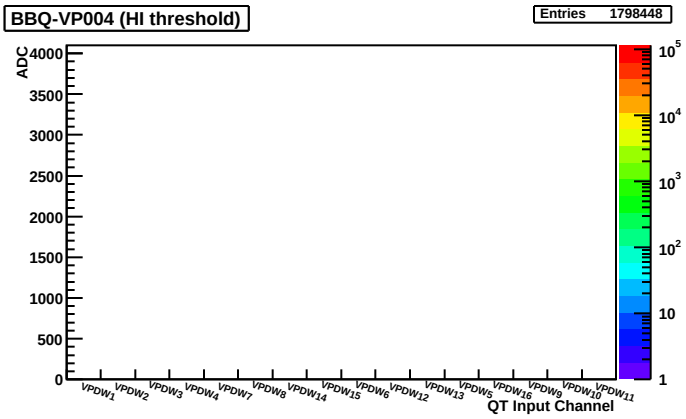

BBQ-VP001 (LO threshold)
Entries 1798448
TAC
QT Input Channel

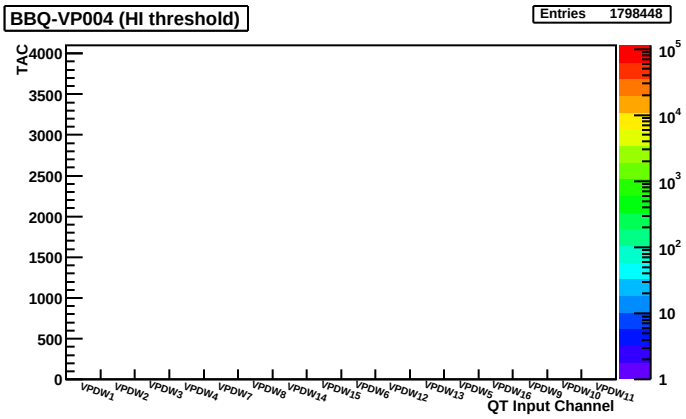

BBQ-VP002 (LO threshold)
Entries 1798448
ADC
QT Input Channel

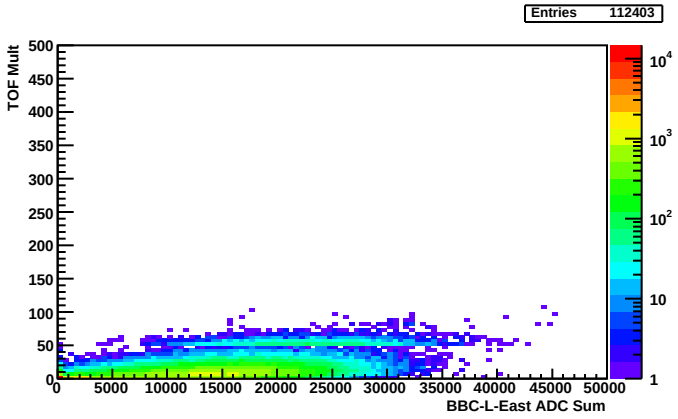

BBQ-VP002 (LO threshold)
Entries 1798448
TAC
QT Input Channel

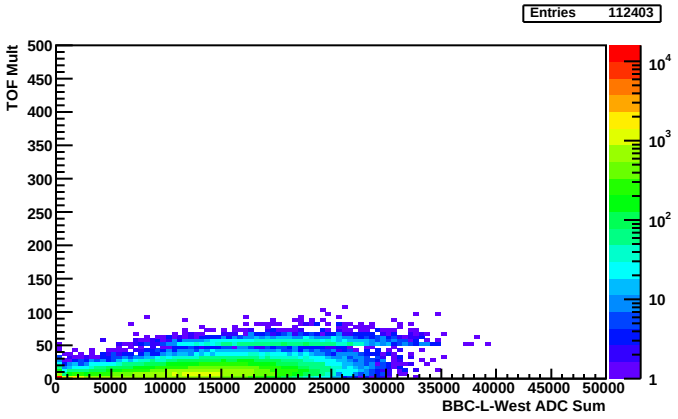

BBQ-VP003 (HI threshold)
Entries 1798448
ADC
QT Input Channel

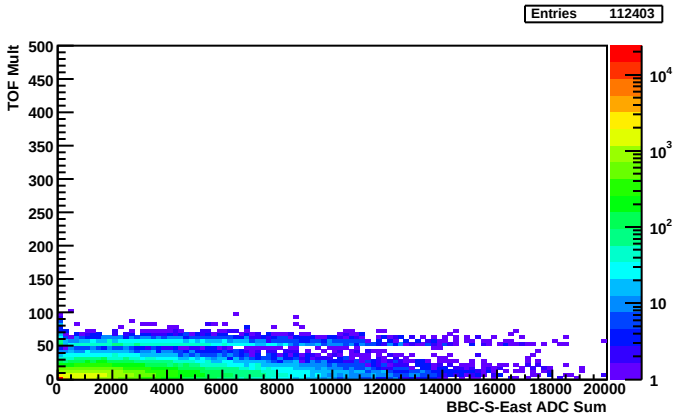

BBQ-VP003 (HI threshold)
Entries 1798448
TAC
QT Input Channel

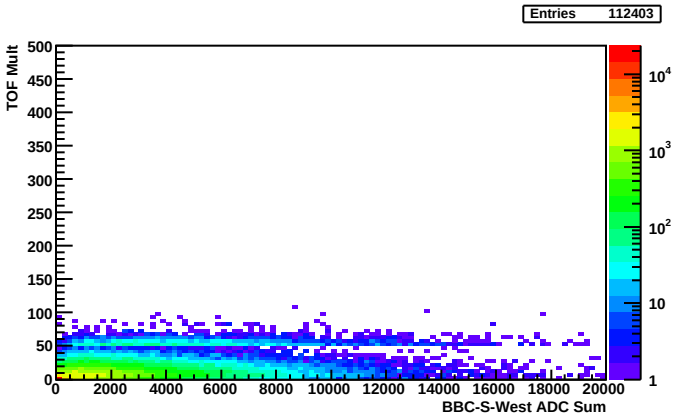

BBQ-VP004 (HI threshold)
Entries 1798448
ADC
QT Input Channel

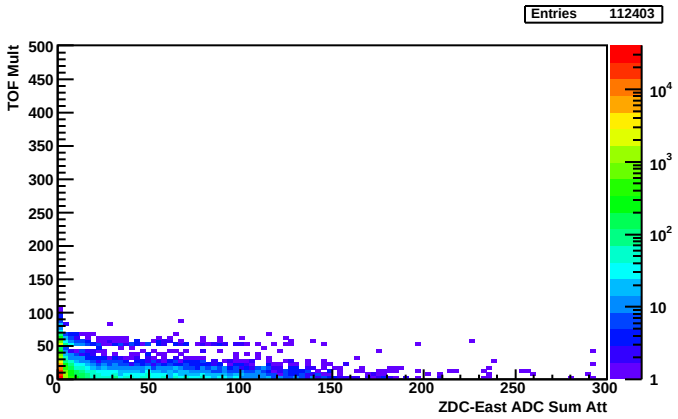

BBQ-VP004 (HI threshold)
Entries 1798448
TAC
QT Input Channel

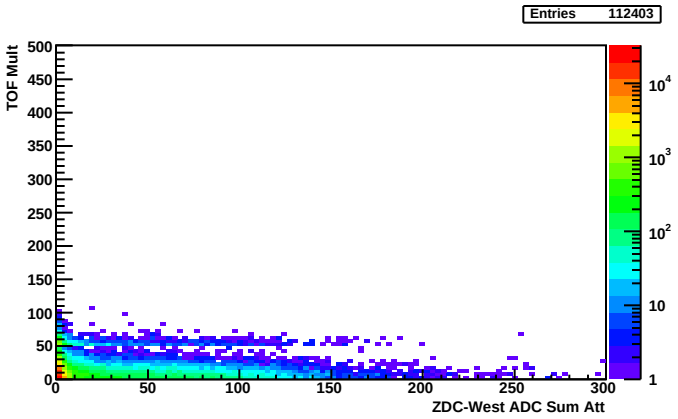

Entries 112403
TOF Mult
BBC-L-East ADC Sum

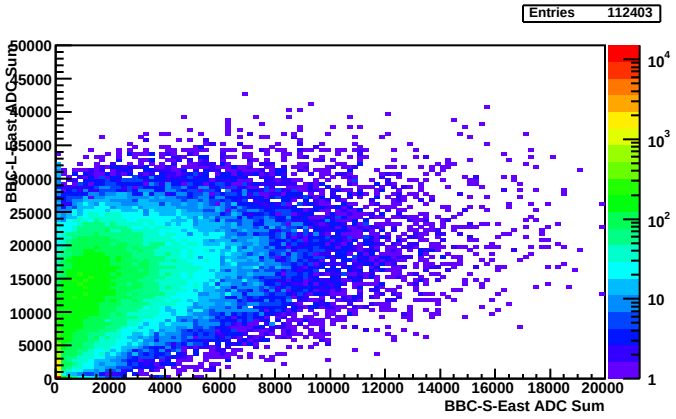

Entries 112403
TOF Mult
BBC-L-West ADC Sum

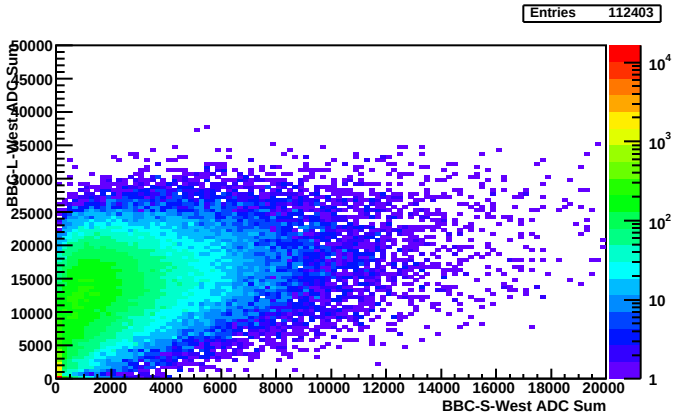

Entries 112403
TOF Mult
BBC-S-East ADC Sum

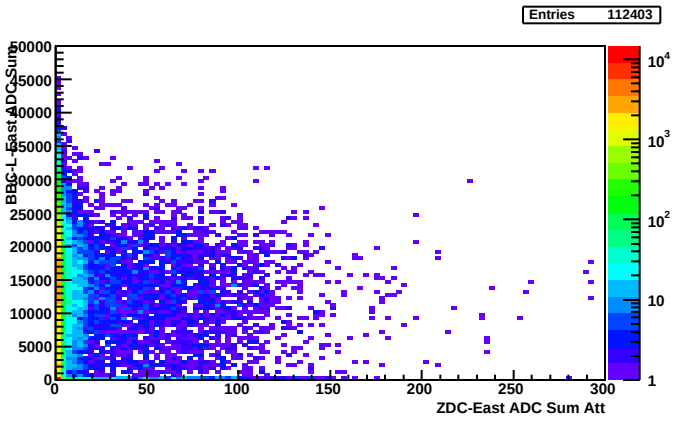

Entries 112403
TOF Mult
BBC-S-West ADC Sum

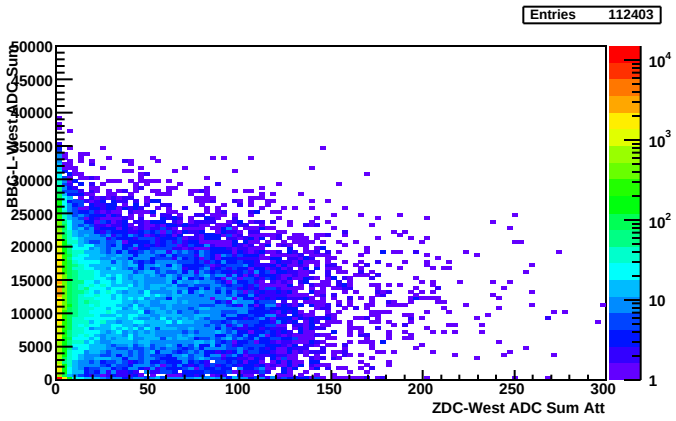

Entries 112403
TOF Mult
ZDC-East ADC Sum Att

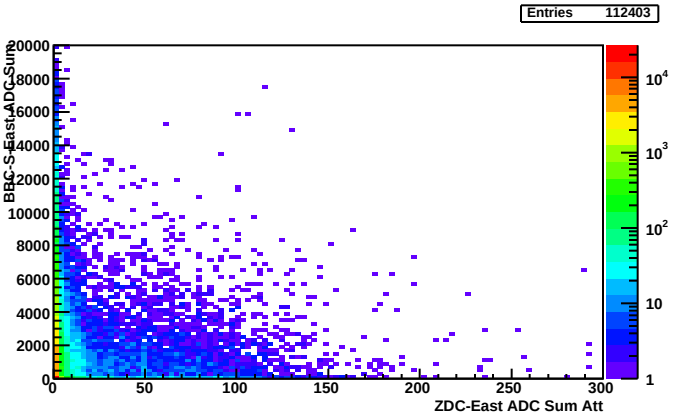

Entries 112403
TOF Mult
ZDC-West ADC Sum Att

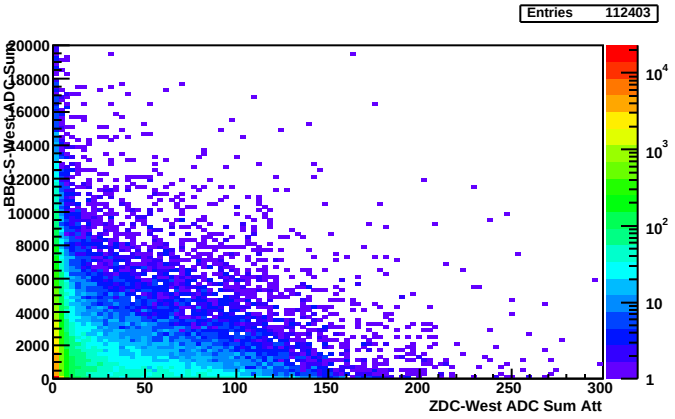

Entries 112403
BBC-L-East ADC Sum
BBC-S-East ADC Sum

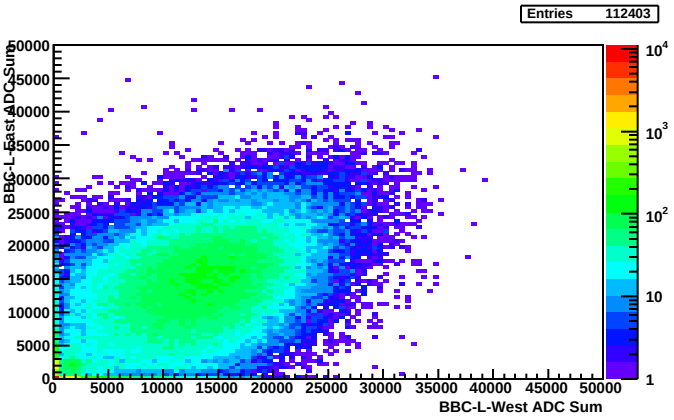

Entries 112403
BBC-L-West ADC Sum
BBC-S-West ADC Sum

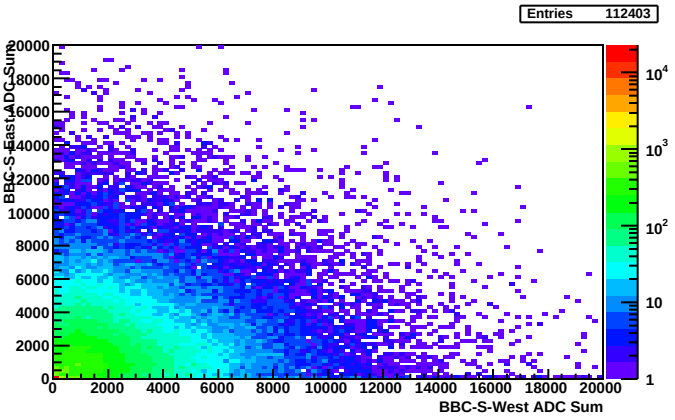

Entries 112403
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

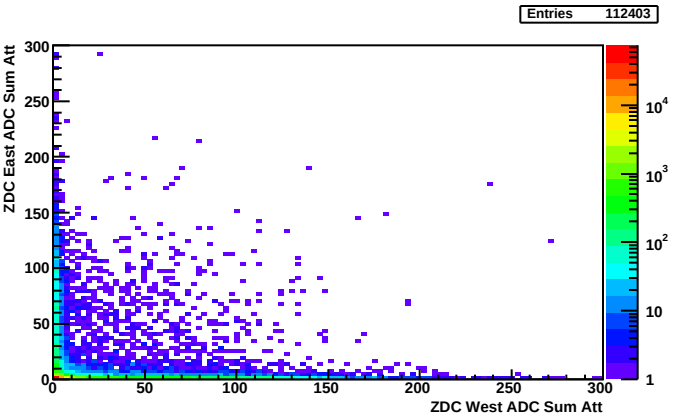

Entries 112403
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

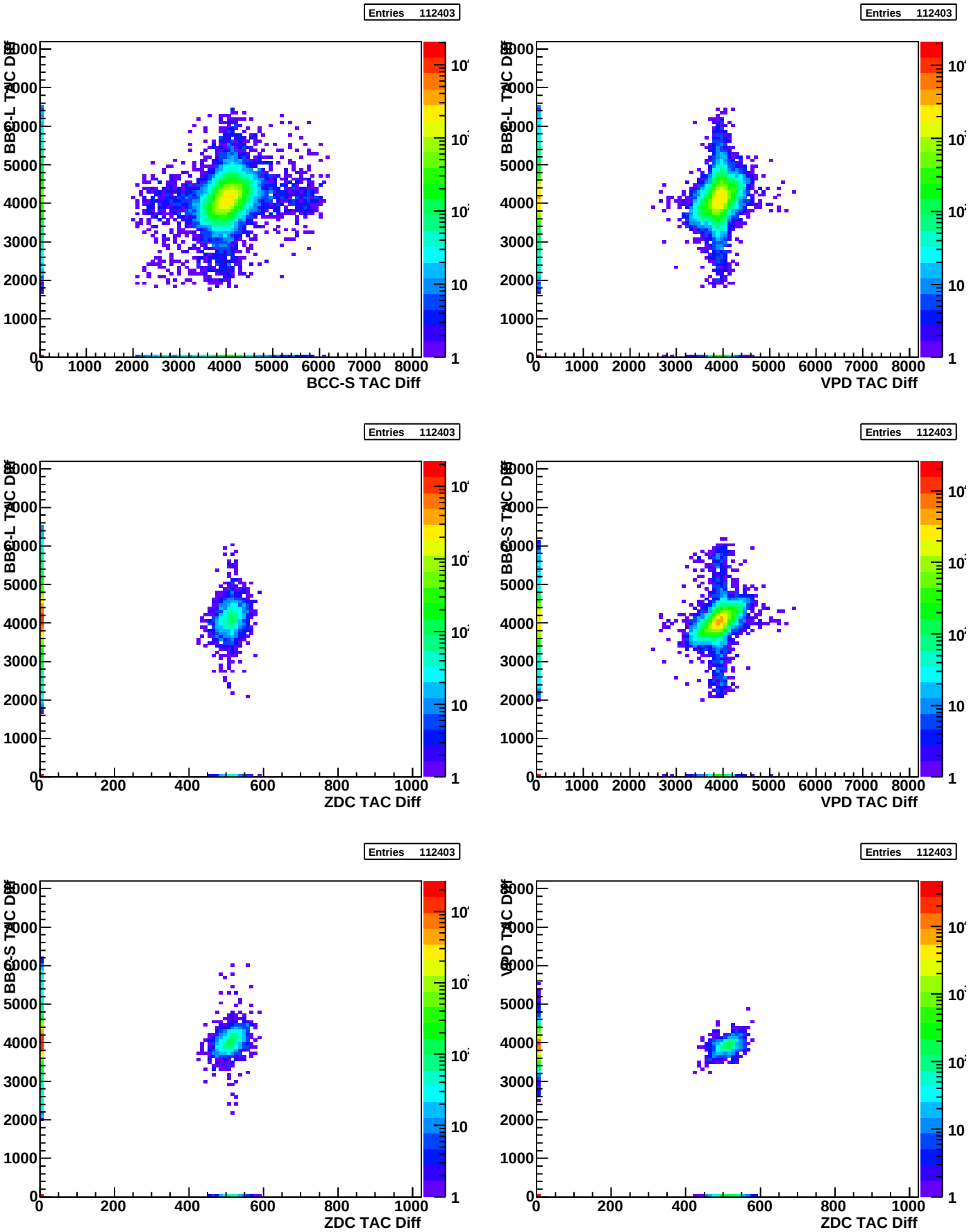

Entries 112403
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

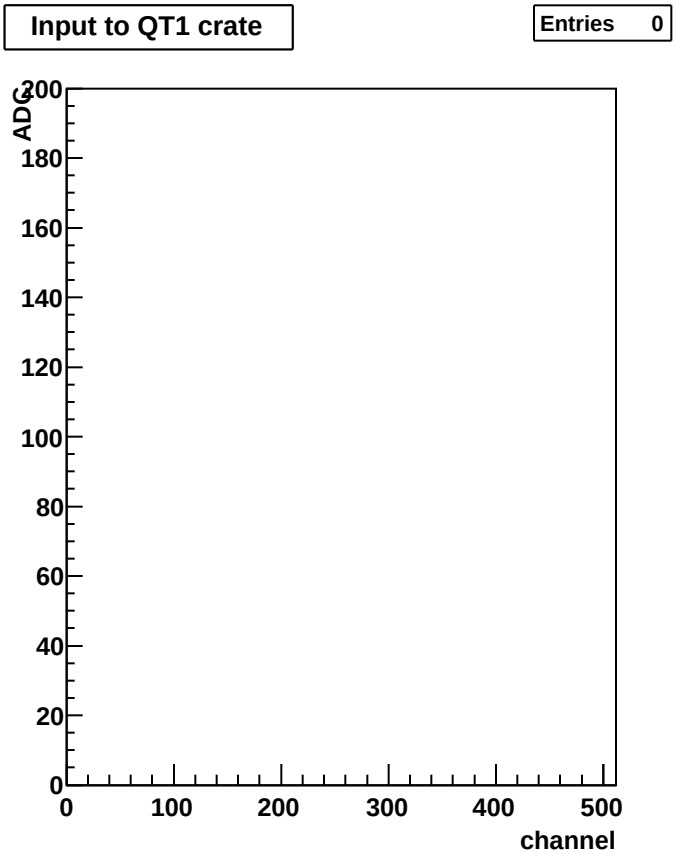

Entries 112403
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

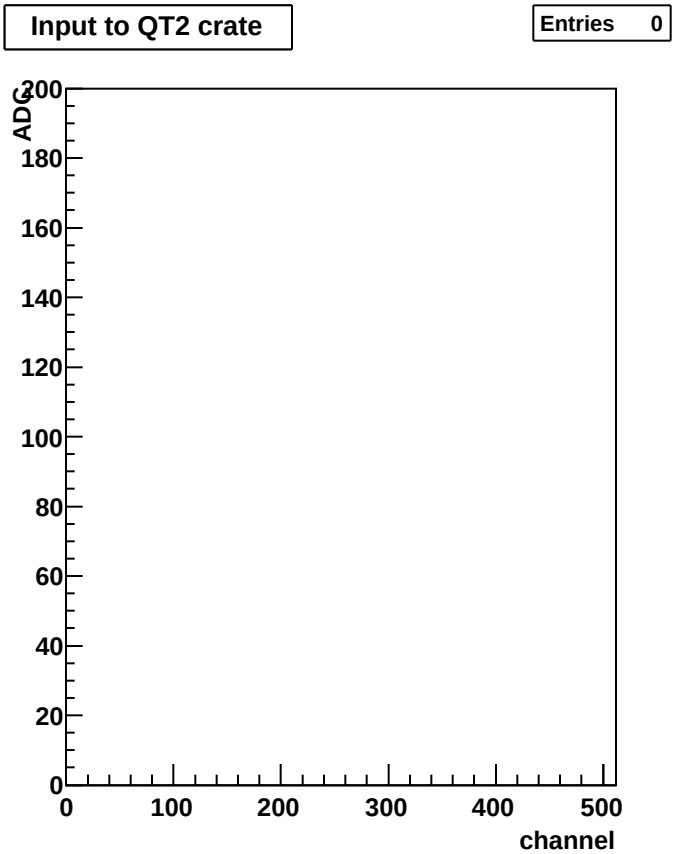

Entries 112403
BBC-L-East ADC Sum
BBC-L-West ADC Sum

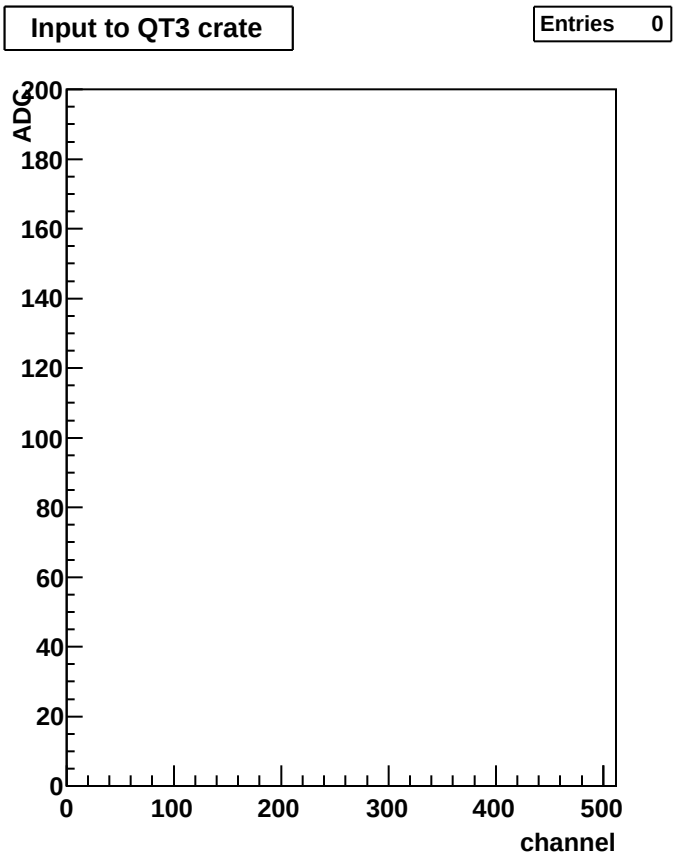

Entries 112403
BBC-S-East ADC Sum
BBC-S-West ADC Sum

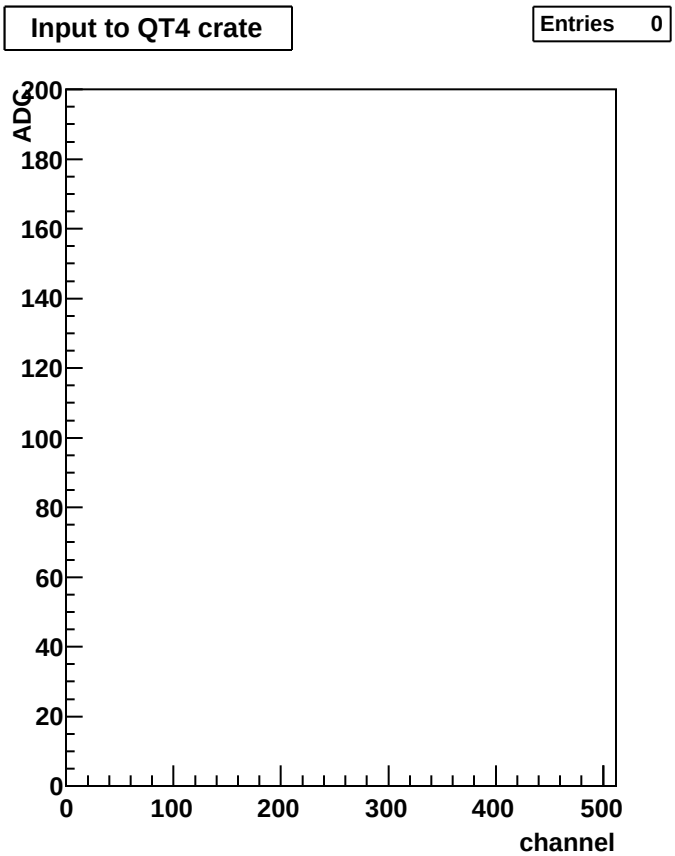

Entries 112403
ZDC East ADC Sum Att
ZDC West ADC Sum Att

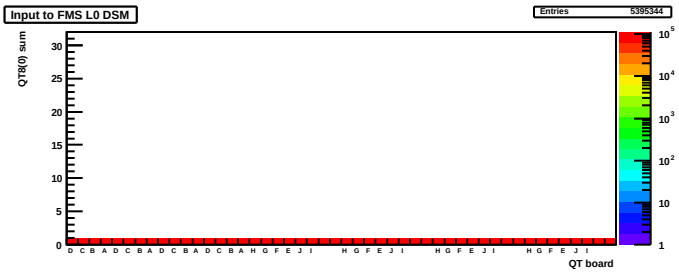

Entries 112403
BBC-L TAC Diff
BCC-S TAC Diff
Entries 112403
BBC-L TAC Diff
VPD TAC Diff
Entries 112403
BBC-L TAC Diff
ZDC TAC Diff
Entries 112403
BBC-S TAC Diff
VPD TAC Diff
Entries 112403
BBC-S TAC Diff
ZDC TAC Diff
Entries 112403
VPD TAC Diff
ZDC TAC Diff

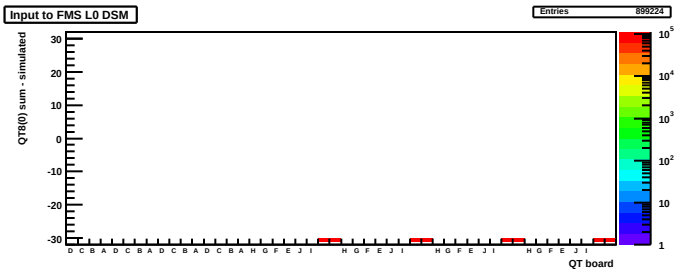

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

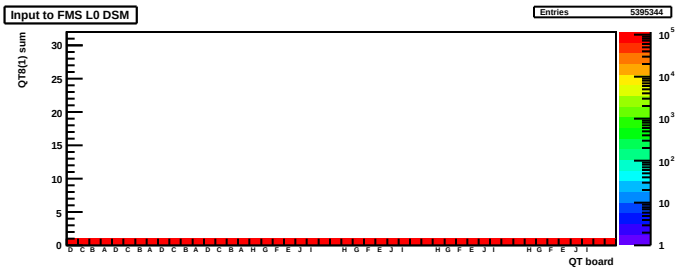

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

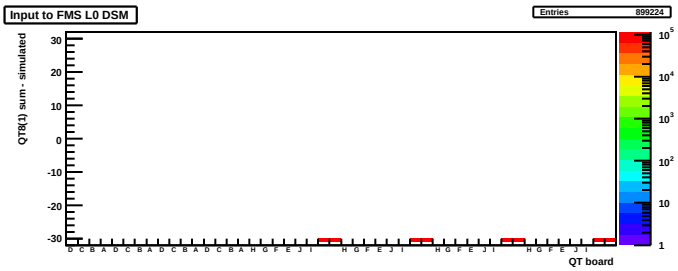

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

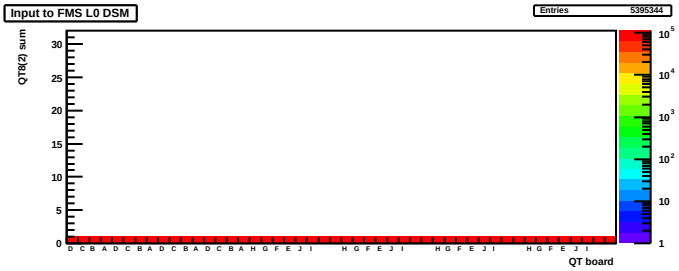

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

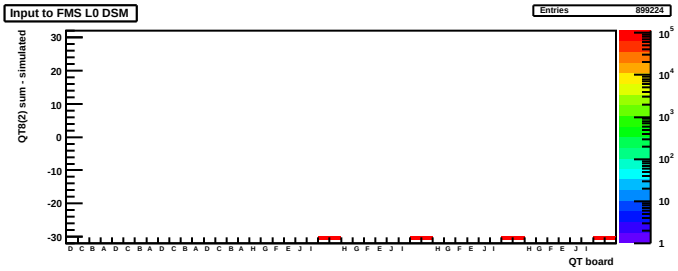

Input to FMS L0 DSM
Entries 5395344
QT8(0) sum
QT board

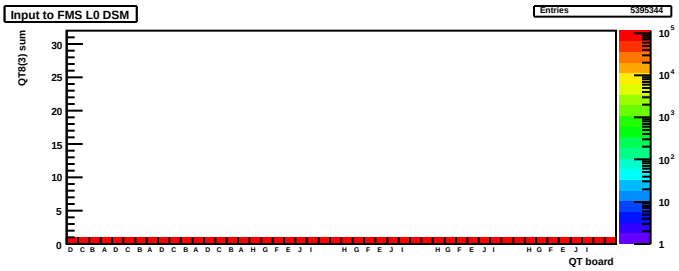

Input to FMS L0 DSM
Entries 899224
QT8(0) sum - simulated
QT board

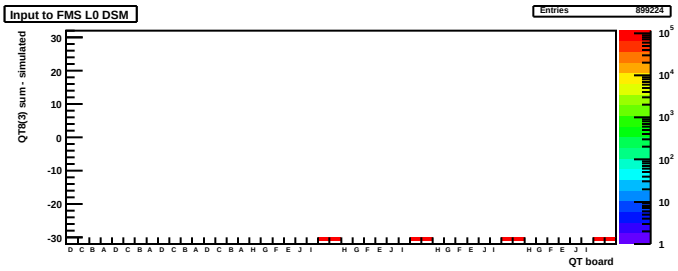

Input to FMS L0 DSM
Entries 5395344
QT8(1) sum
QT board

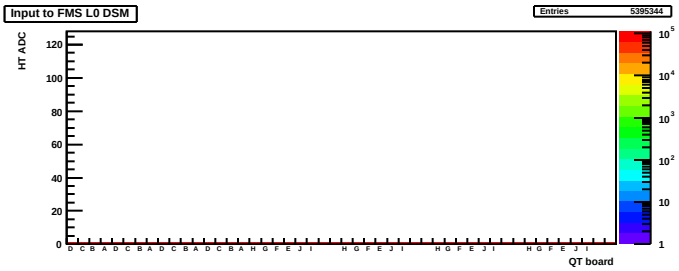

Input to FMS L0 DSM
Entries 899224
QT8(1) sum - simulated
QT board

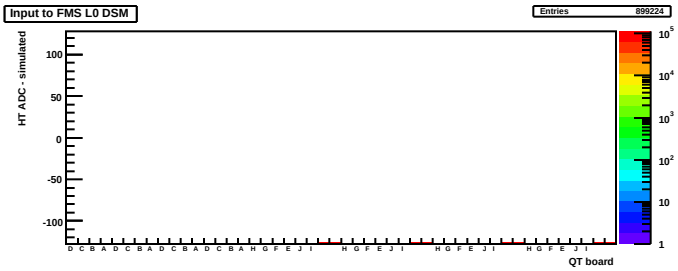

Input to FMS L0 DSM
Entries 5395344
QT8(2) sum
QT board

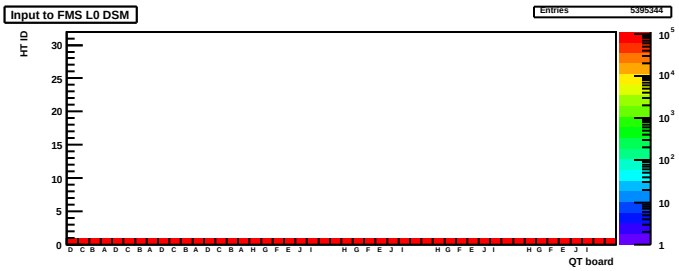

Input to FMS L0 DSM
Entries 899224
QT8(2) sum - simulated
QT board

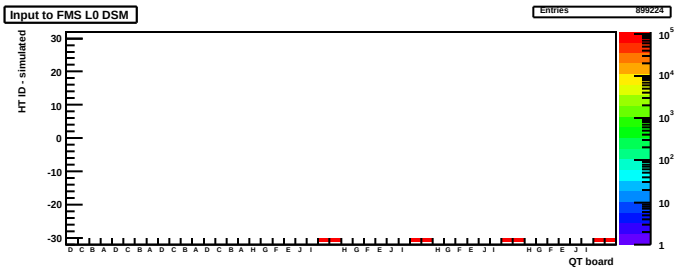

Input to FMS L0 DSM
Entries 5395344
QT8(3) sum
QT board

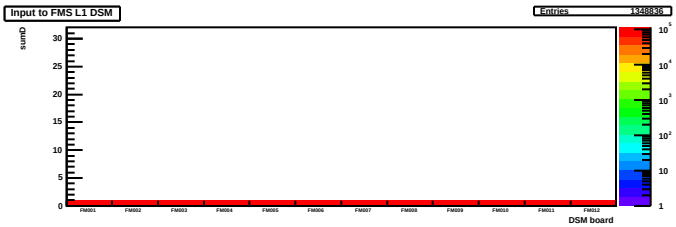

Input to FMS L0 DSM
Entries 899224
QT8(3) sum - simulated
QT board

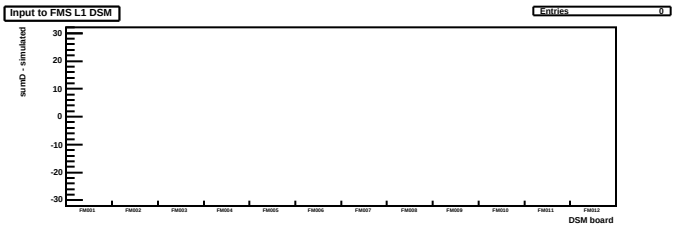

Input to FMS L0 DSM
Entries 5395344
HT ADC
QT board

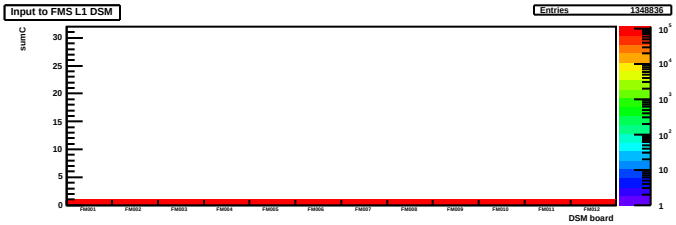

Input to FMS L0 DSM
Entries 899224
HT ADC - simulated
QT board

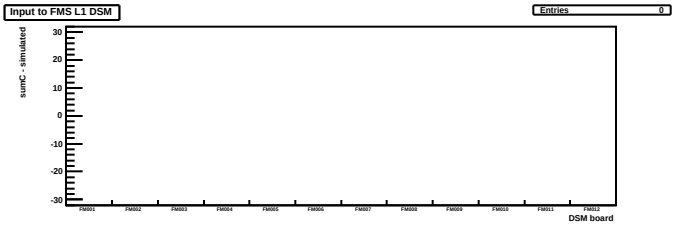

Input to FMS L0 DSM
Entries 5395344
HT ID
QT board

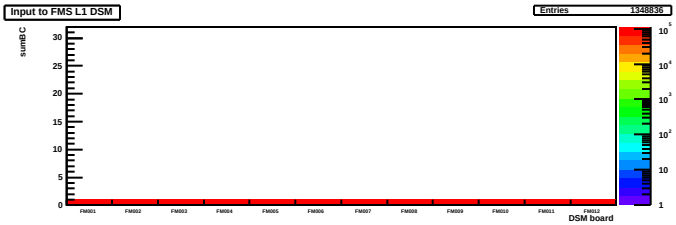

Input to FMS L0 DSM
Entries 899224
HT ID - simulated
QT board

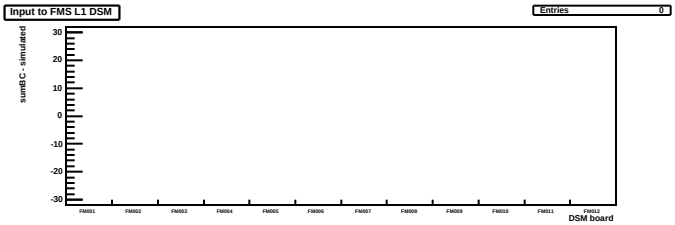

Input to FMS L1 DSM
Entries 1348836
sumD
DSM board

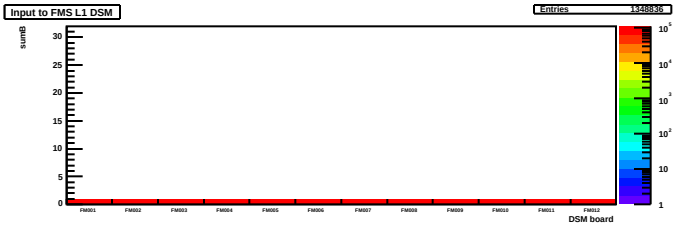

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

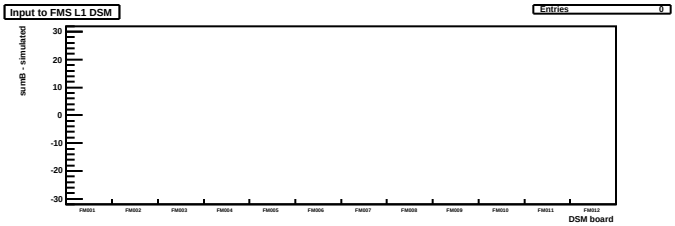

Input to FMS L1 DSM
Entries 1348836
sumC
DSM board

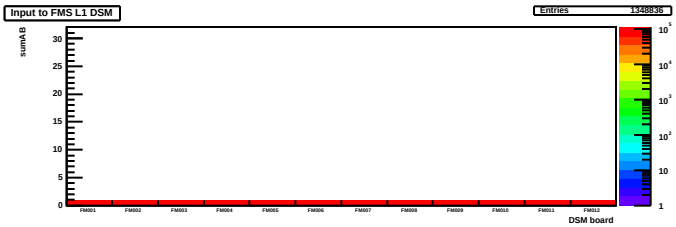

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

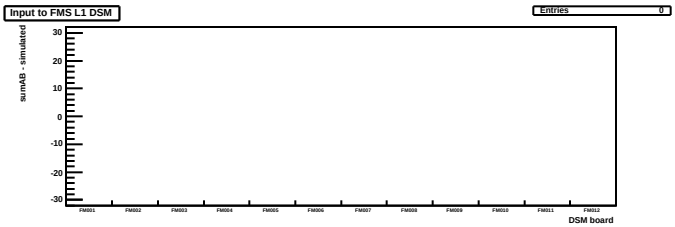

Input to FMS L1 DSM
Entries 1348836
sumBC
DSM board

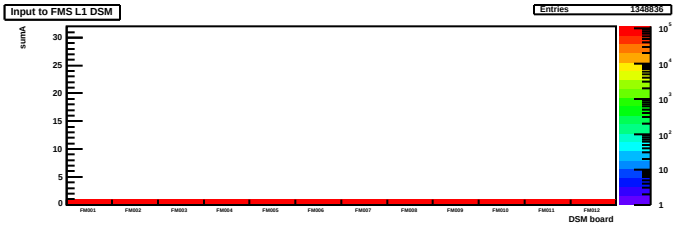

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

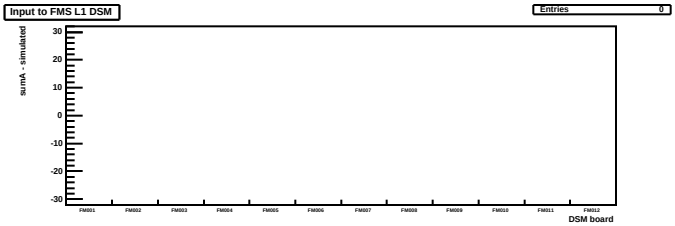

Input to FMS L1 DSM
Entries 1348836
sumB
DSM board

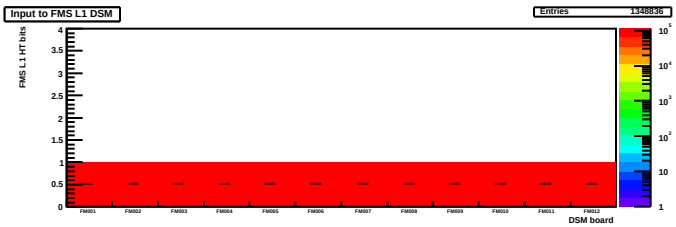

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

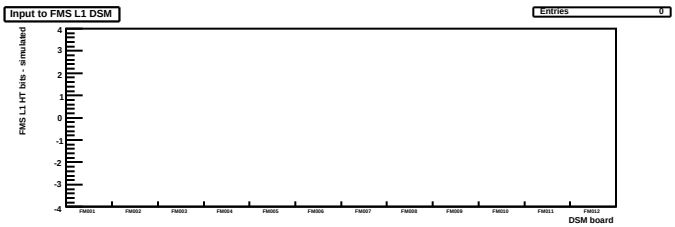

Input to FMS L1 DSM
Entries 1348836
sumAB
DSM board

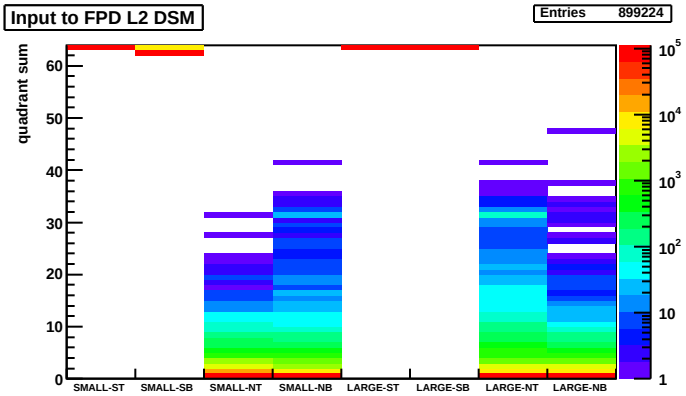

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

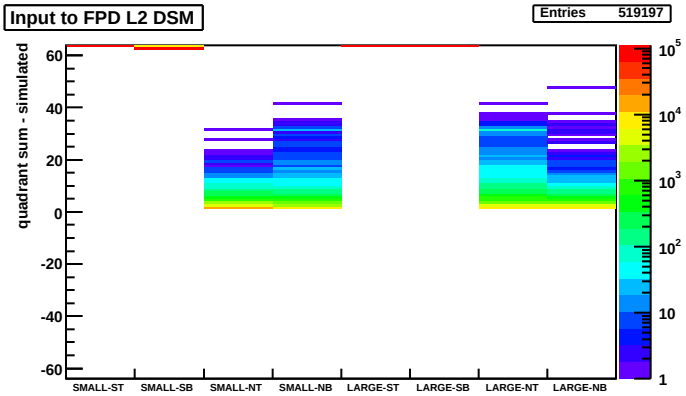

Input to FMS L1 DSM
Entries 1348836
sumA
DSM board

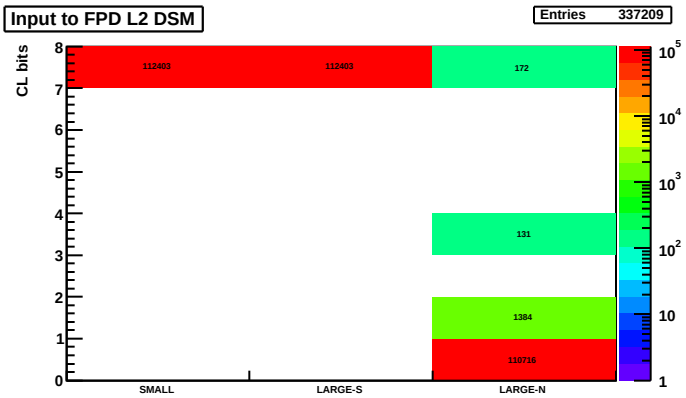

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

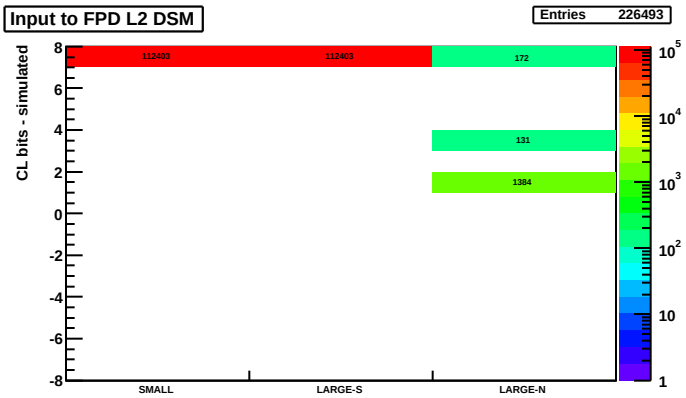

Input to FMS L1 DSM
Entries 1348836
FMS L1 HT bits
DSM board

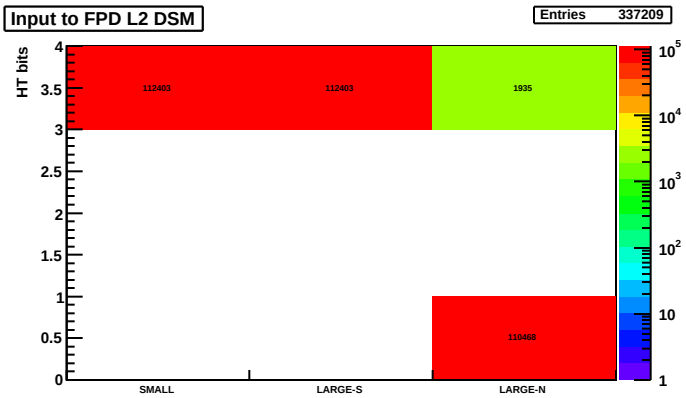

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

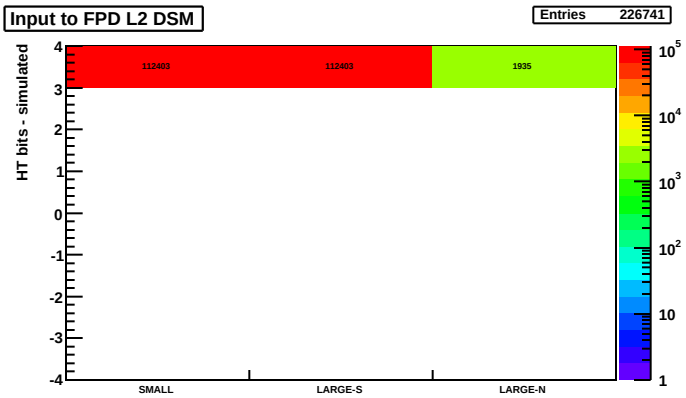

Input to FPD L2 DSM
Entries 899224
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

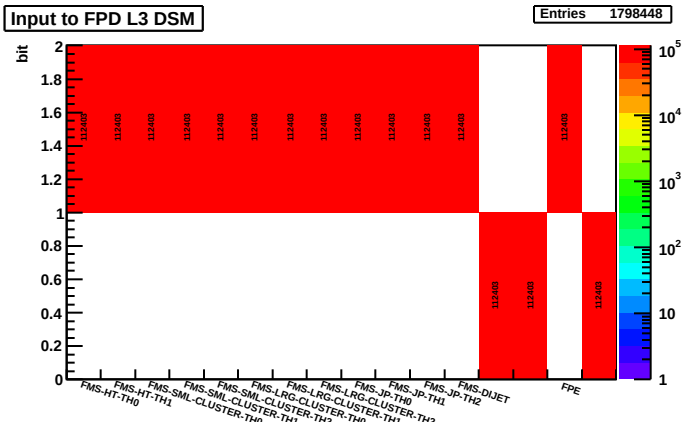

Input to FPD L2 DSM
Entries 519197
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

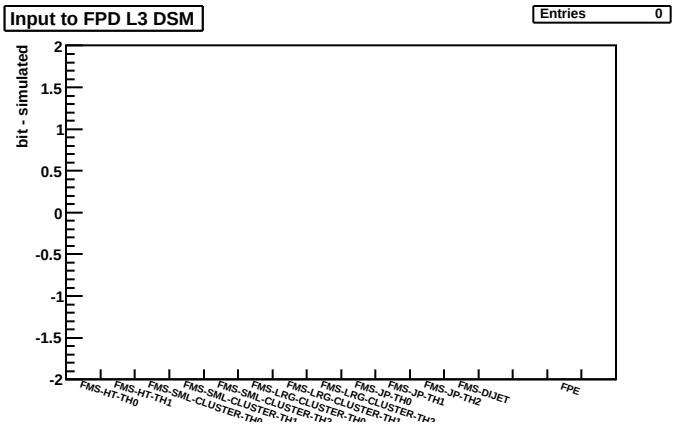

Input to FPD L2 DSM
Entries 337209
CL bits
112403
112403
172
131
1384
110716
SMALL
LARGE-S
LARGE-N

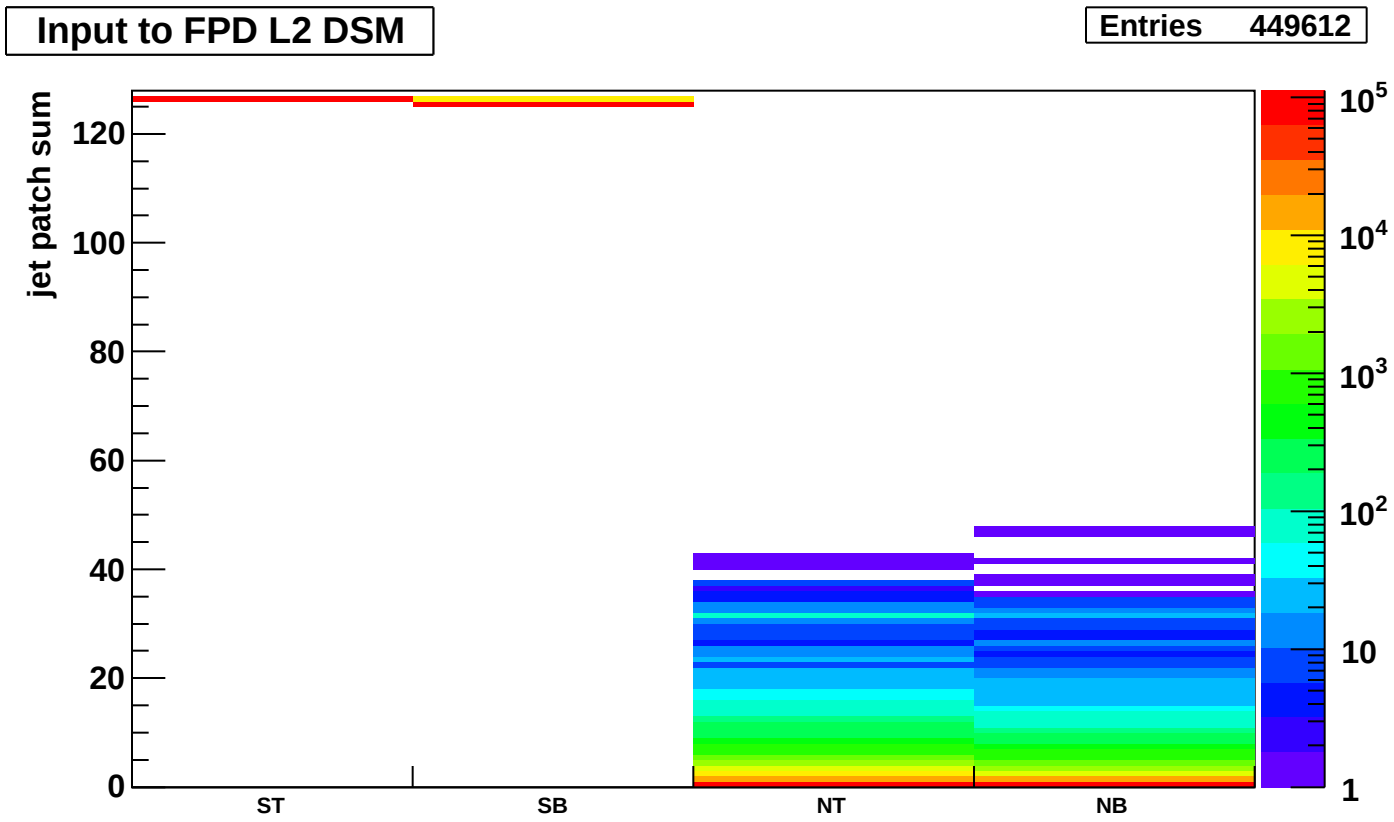

Input to FPD L2 DSM
Entries 226493
CL bits - simulated
112403
112403
172
131
1384
SMALL
LARGE-S
LARGE-N

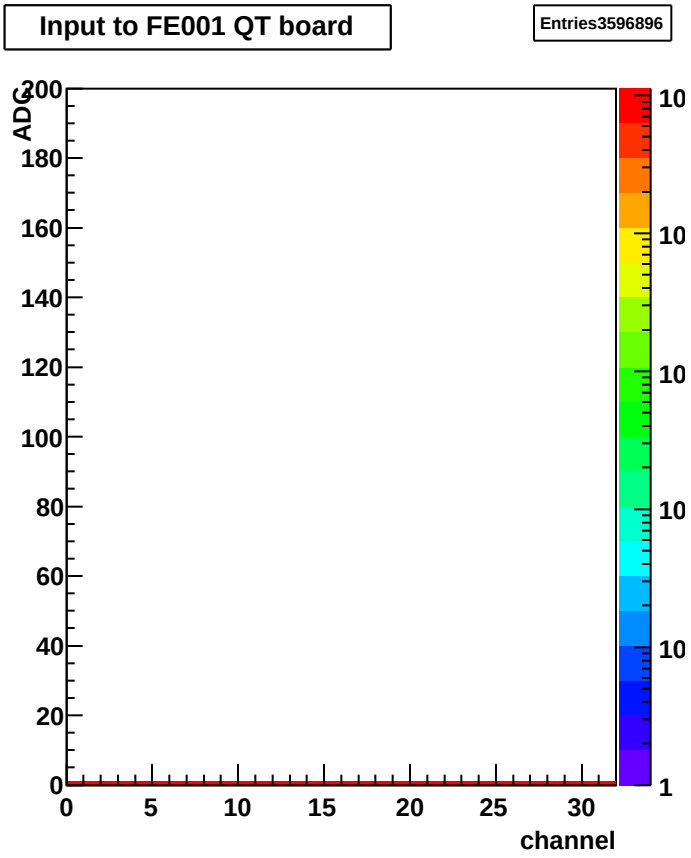

Input to FPD L2 DSM
Entries 337209
HT bits
112403
112403
1935
110468
SMALL
LARGE-S
LARGE-N

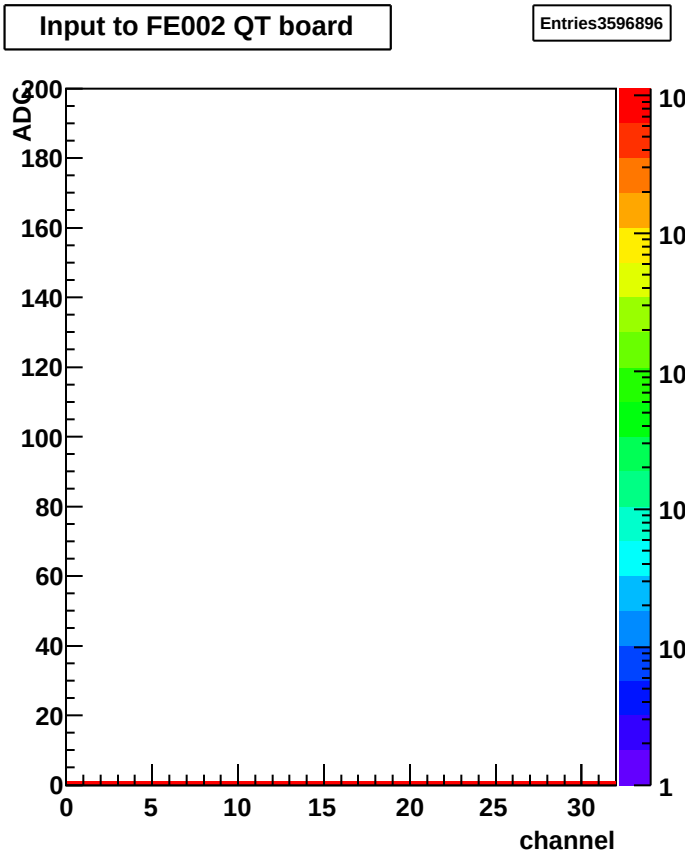

Input to FPD L2 DSM
Entries 226741
HT bits - simulated
112403
112403
1935
SMALL
LARGE-S
LARGE-N

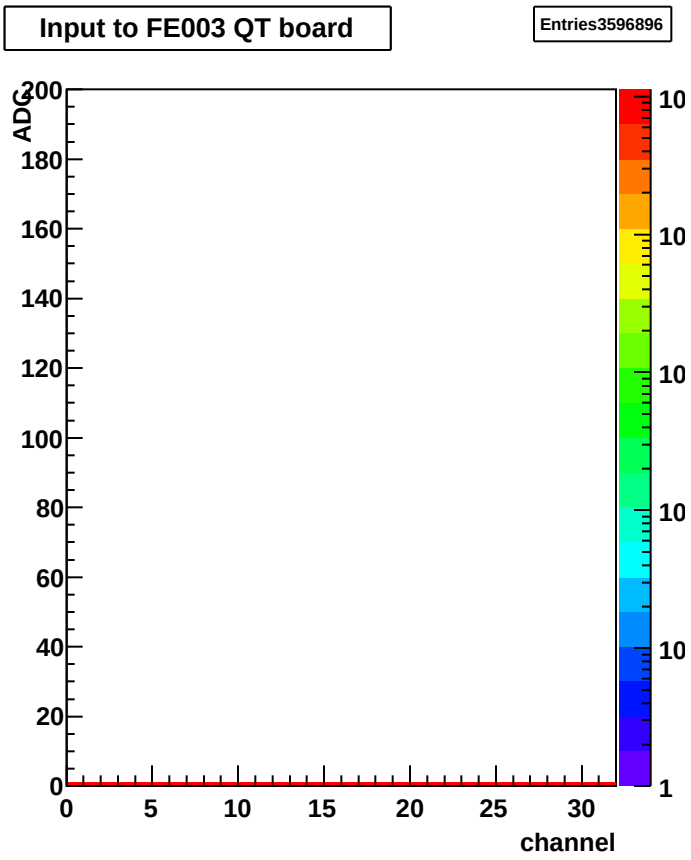

Input to FPD L3 DSM
Entries 1798448
bit
112403
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

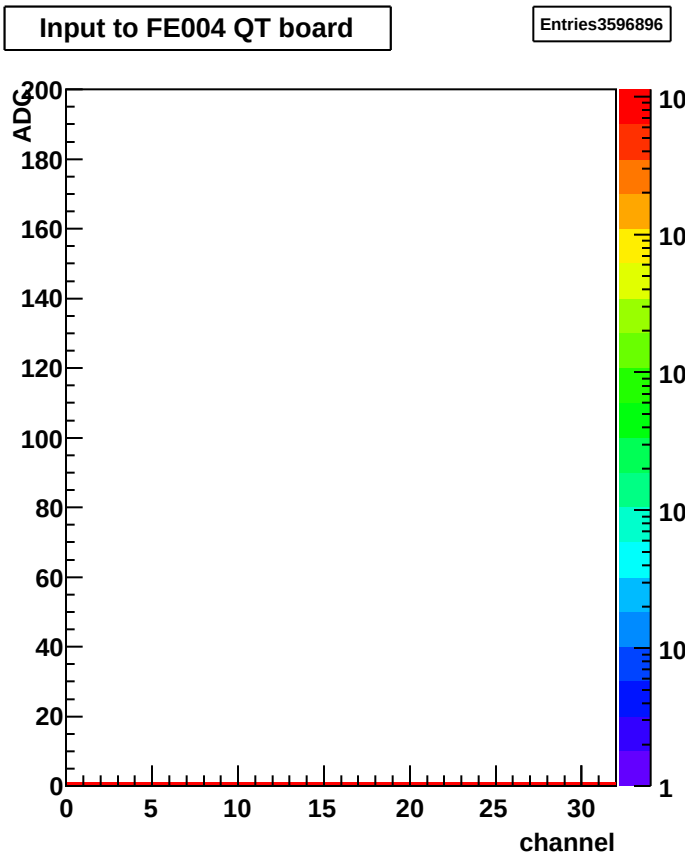

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

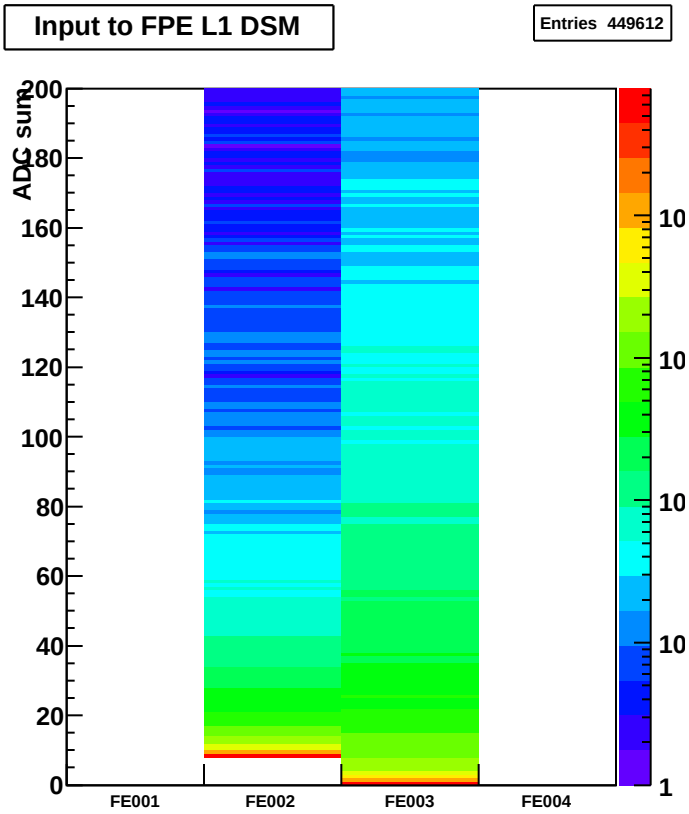

Input to FPD L2 DSM
Entries 449612
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

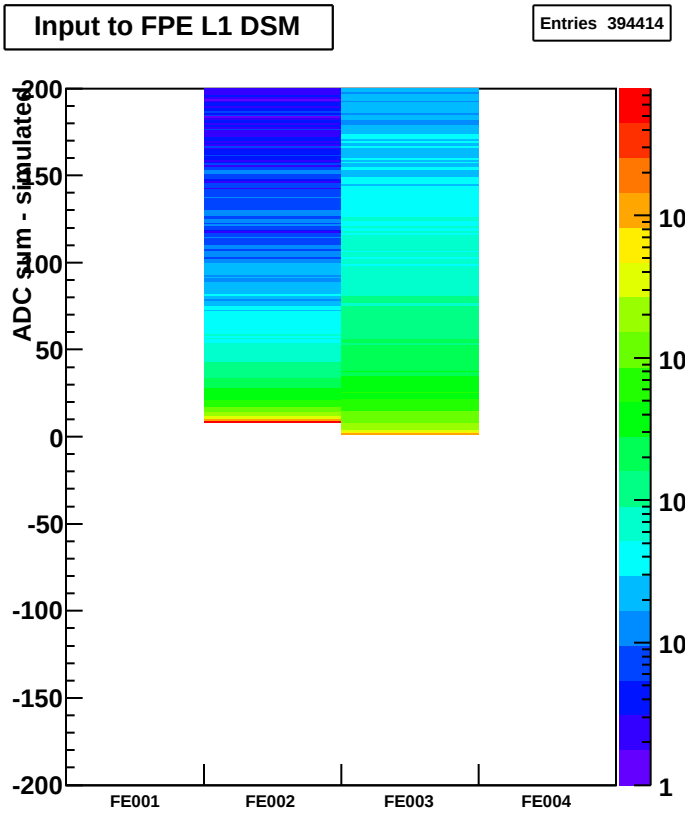

Input to FE001 QT board
Entries3596896
ADC
channel

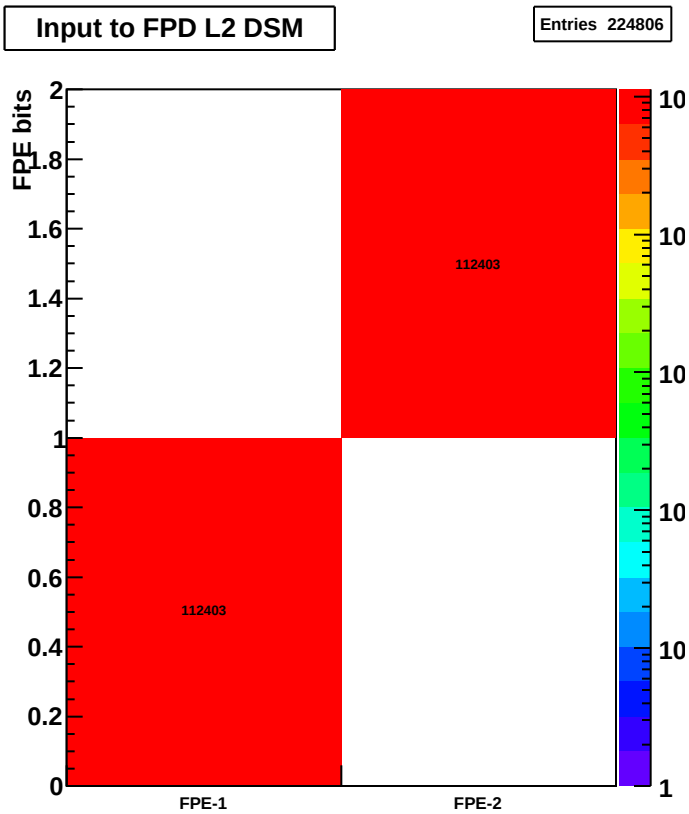

Input to FE002 QT board
Entries3596896
ADC
channel

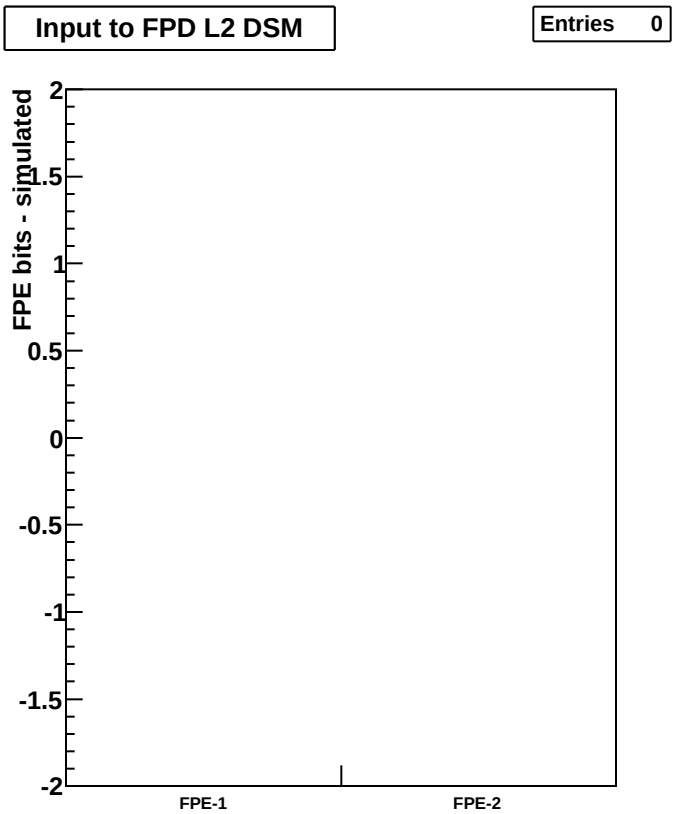

Input to FE003 QT board
Entries3596896
ADC
channel


Input to FE004 QT board
Entries3596896
ADC
channel


Input to FPE L1 DSM
Entries 449612
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 394414
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 224806
FPE bits
112403
112403
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2