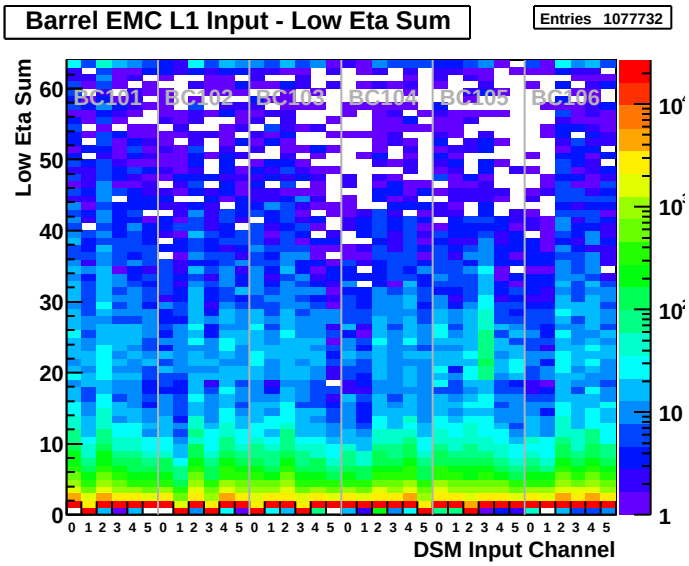
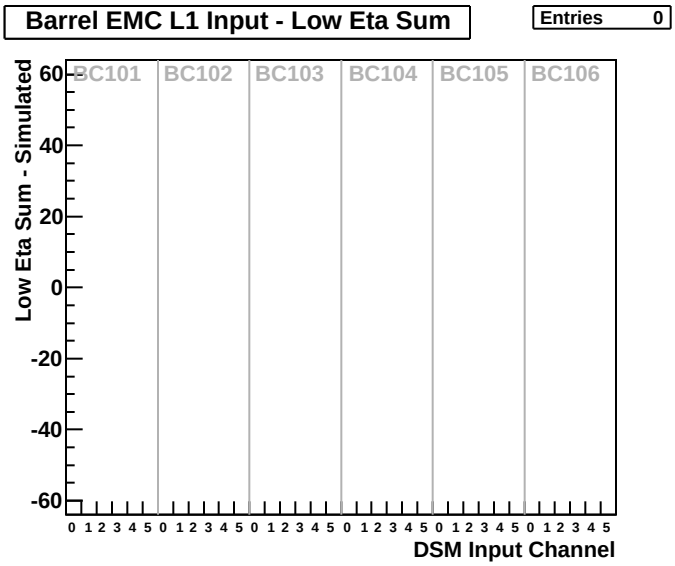
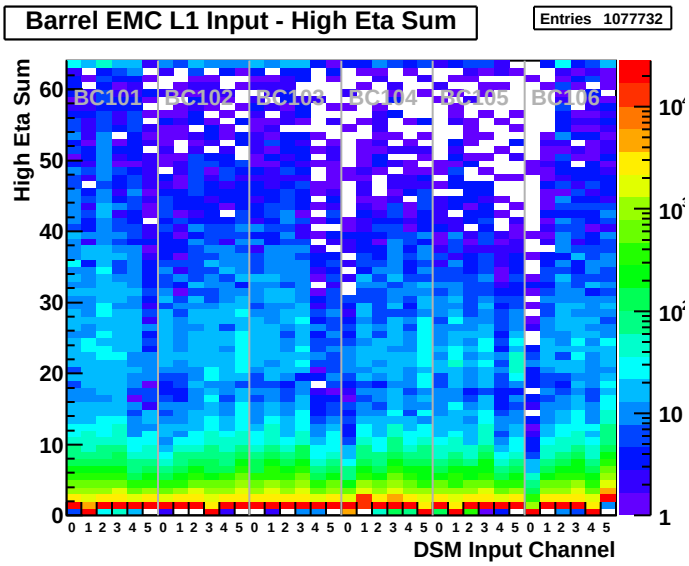
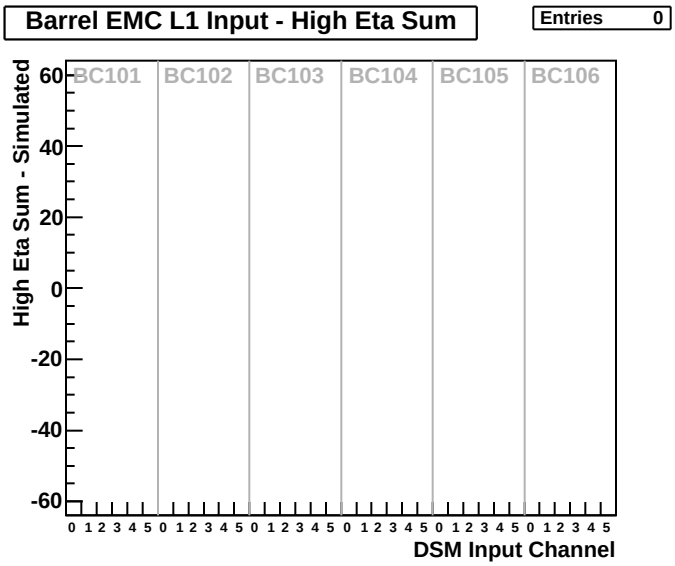
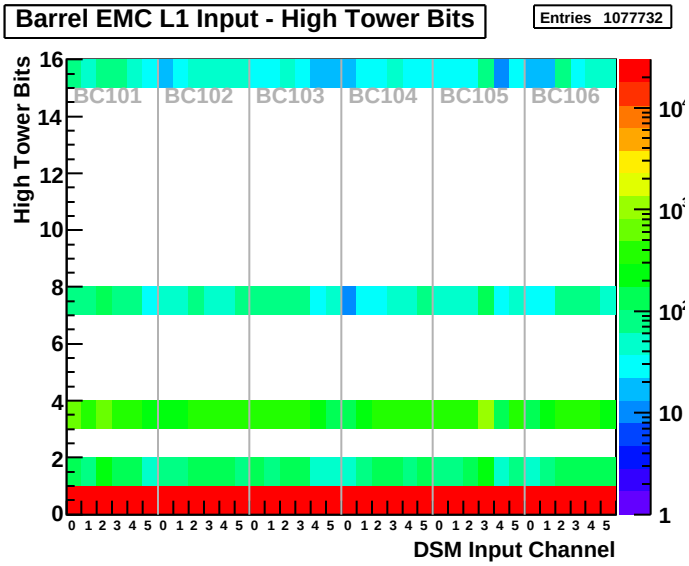
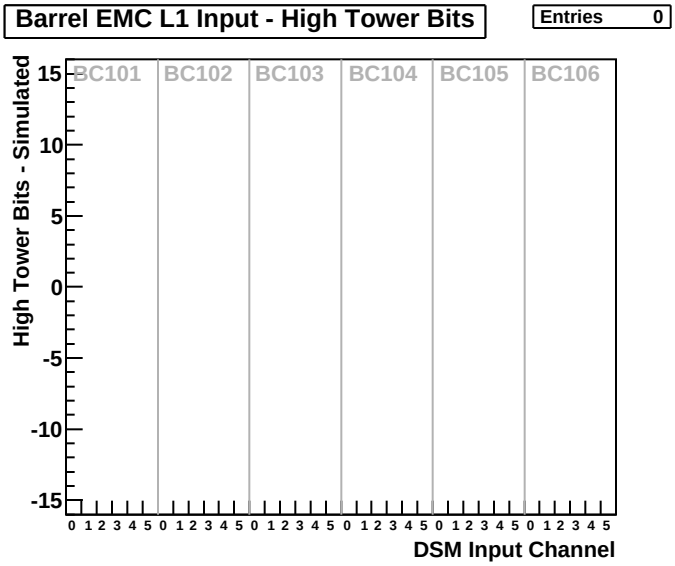

Barrel EMC L1 Input - Low Eta Sum
Entries 1077732
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

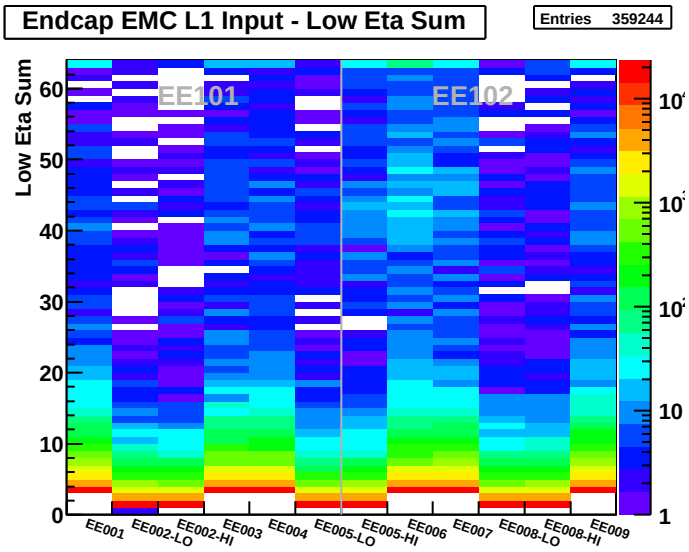

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

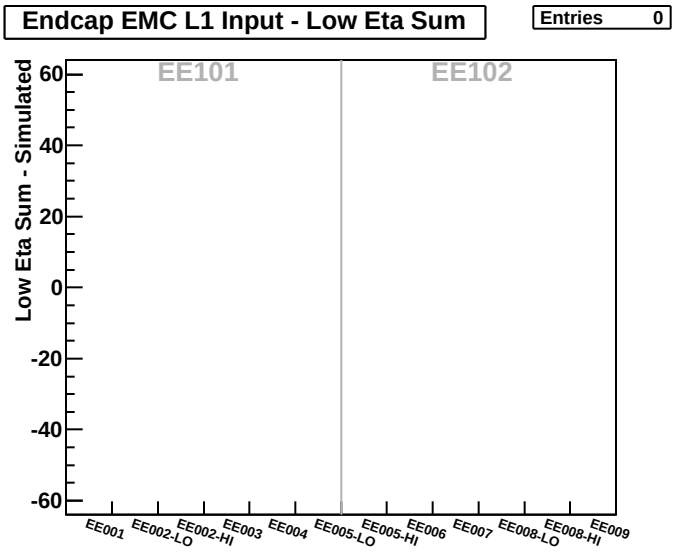

Barrel EMC L1 Input - High Eta Sum
Entries 1077732
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

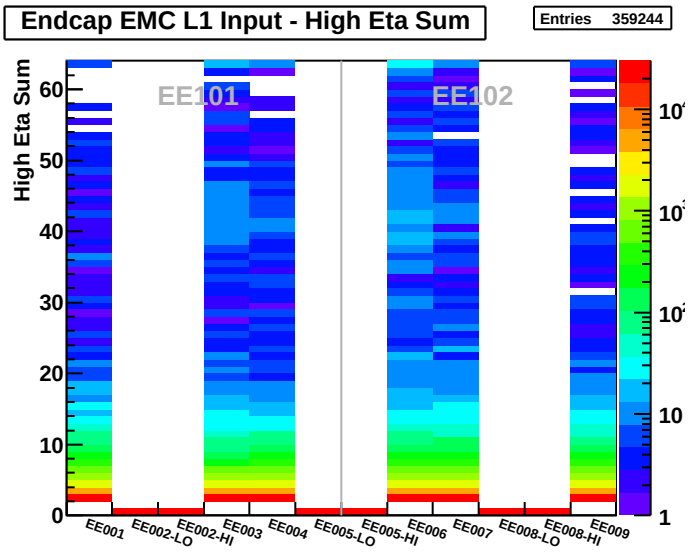

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

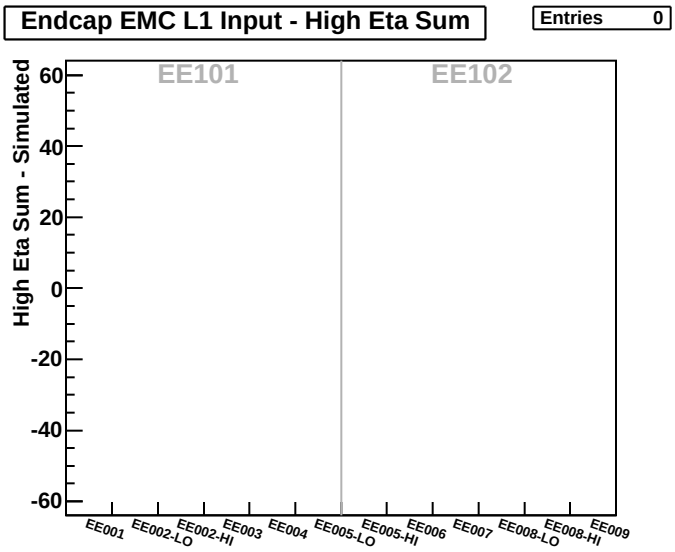

Barrel EMC L1 Input - High Tower Bits
Entries 1077732
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

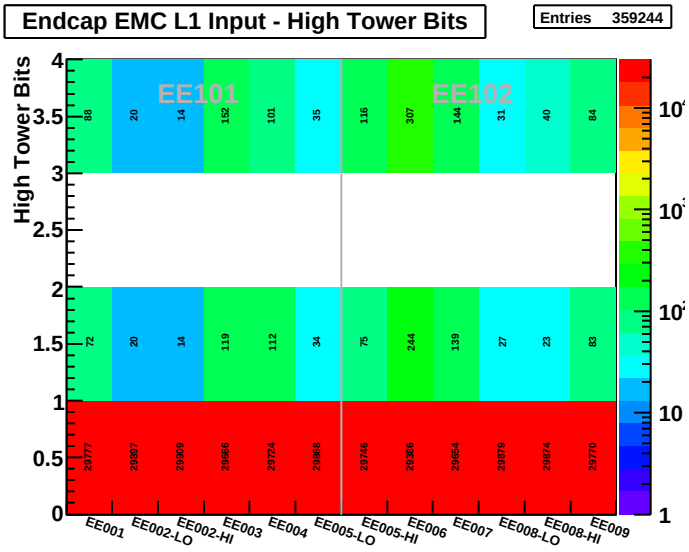

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

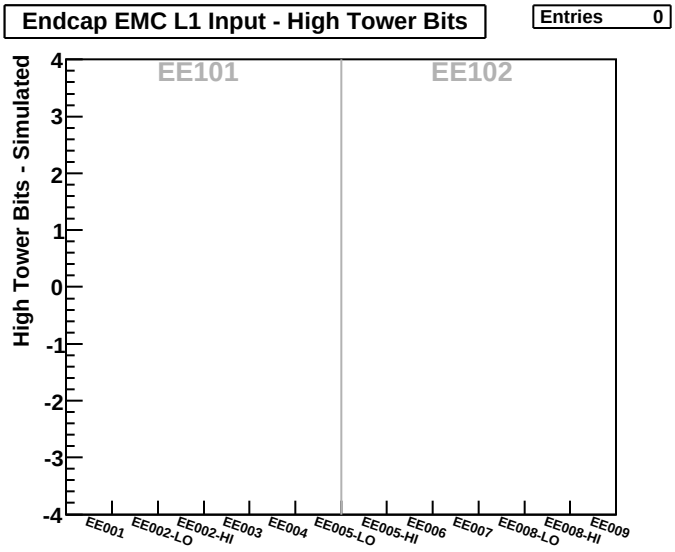

Endcap EMC L1 Input - Low Eta Sum
Entries 359244
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

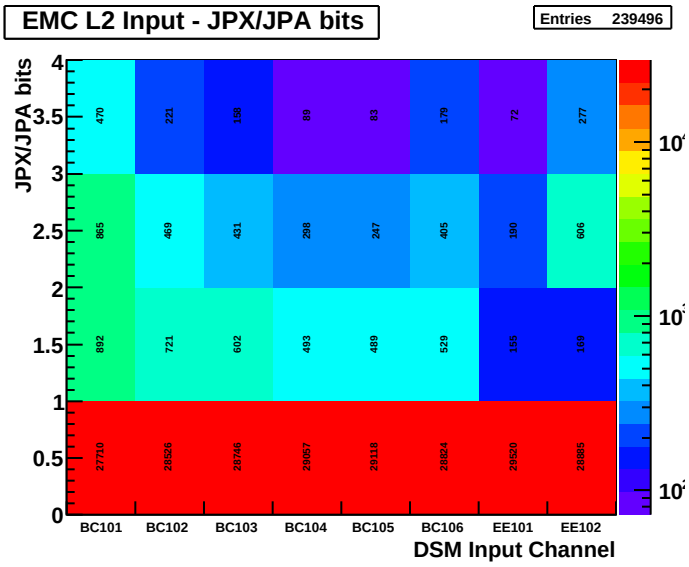

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

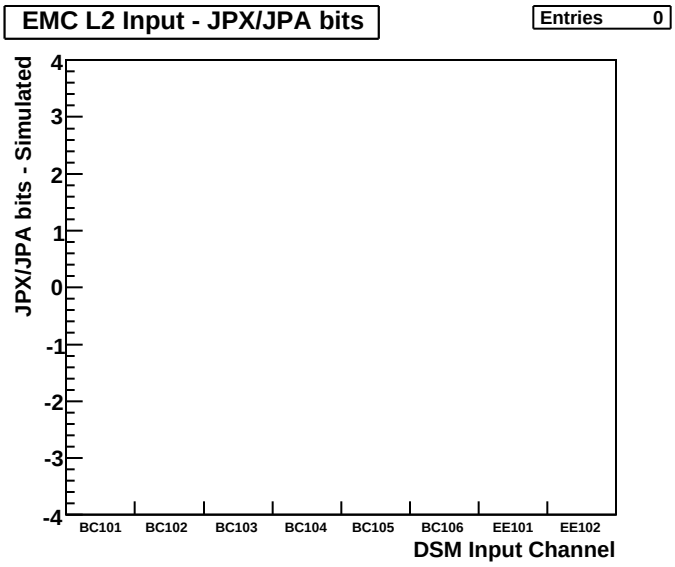

Endcap EMC L1 Input - High Eta Sum
Entries 359244
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

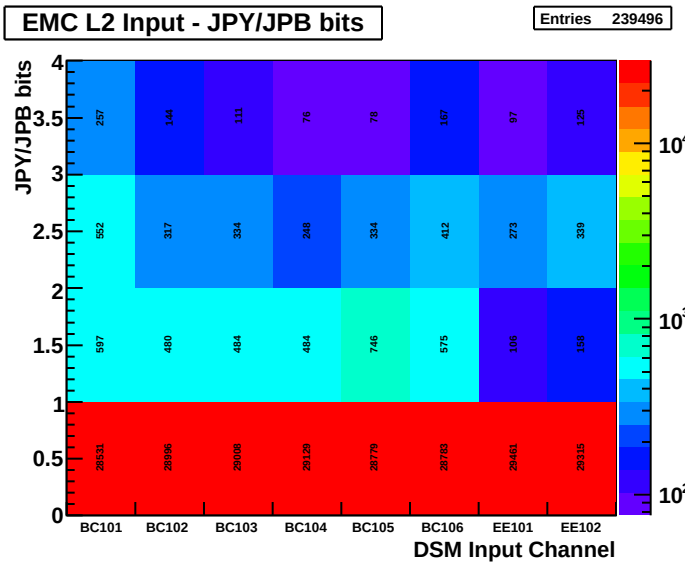

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

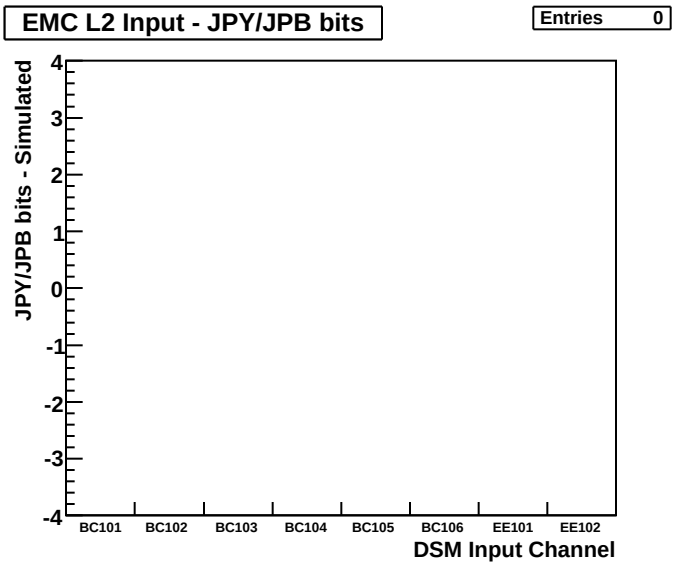

Endcap EMC L1 Input - High Tower Bits
Entries 359244
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

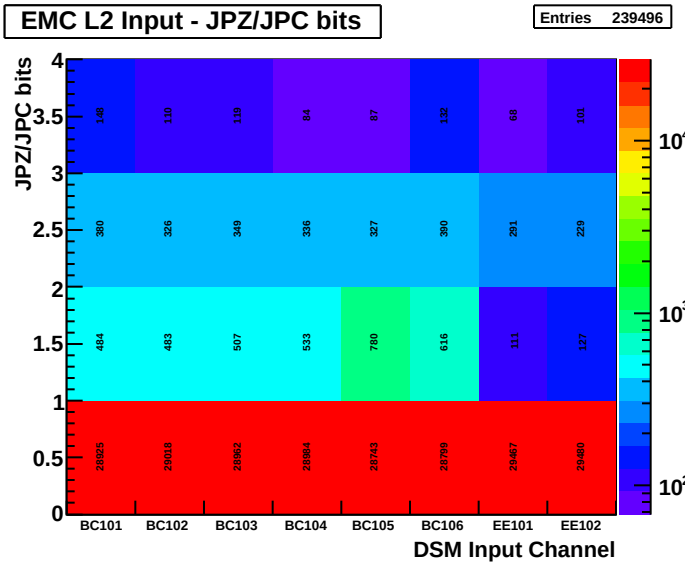

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

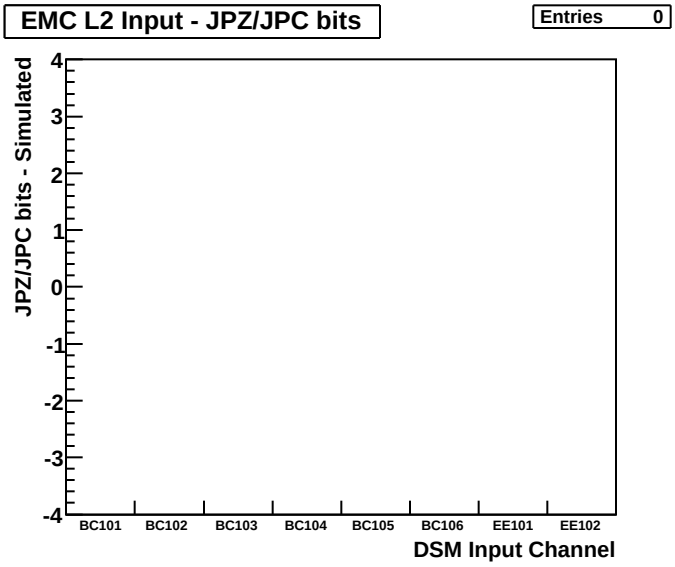

EMC L2 Input - JPX/JPA bits
Entries 239496
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
470 221 158 89 83 179 72 277
865 469 431 298 247 405 190 606
892 721 602 493 489 529 155 169
27710 28526 28746 29057 29118 28824 29520 28885

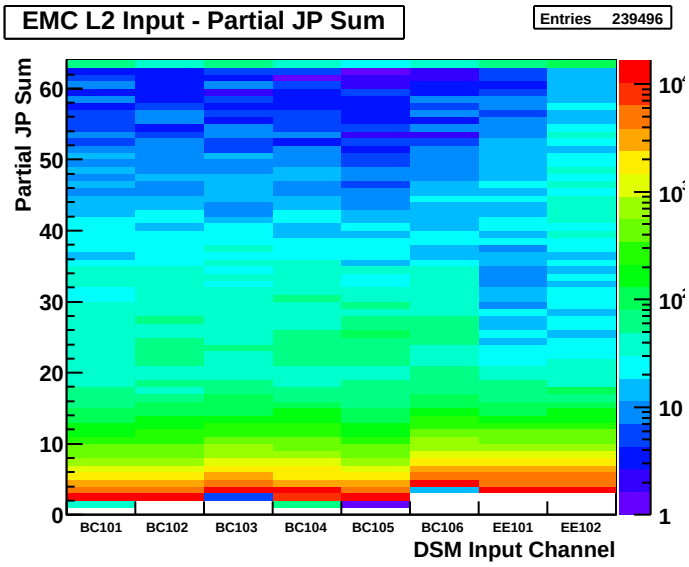

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

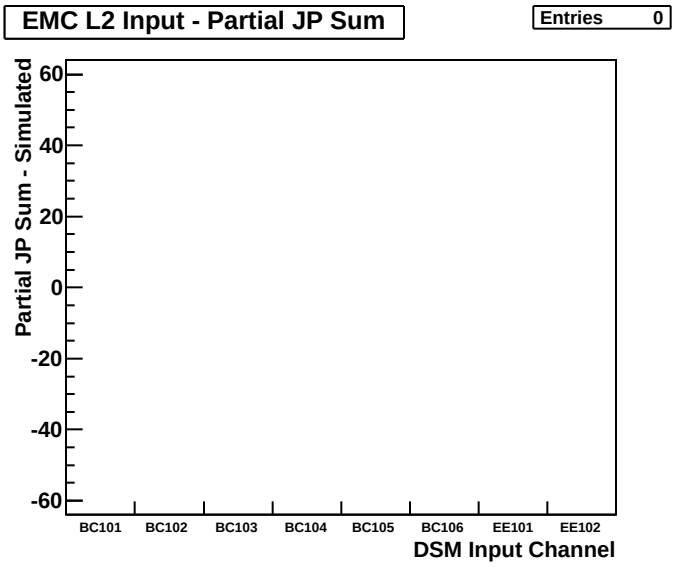

EMC L2 Input - JPY/JPB bits
Entries 239496
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
257 144 111 76 78 167 97 125
552 317 334 248 334 412 273 339
597 480 484 484 746 575 106 158
28531 28996 29008 29129 28779 28783 29461 29315

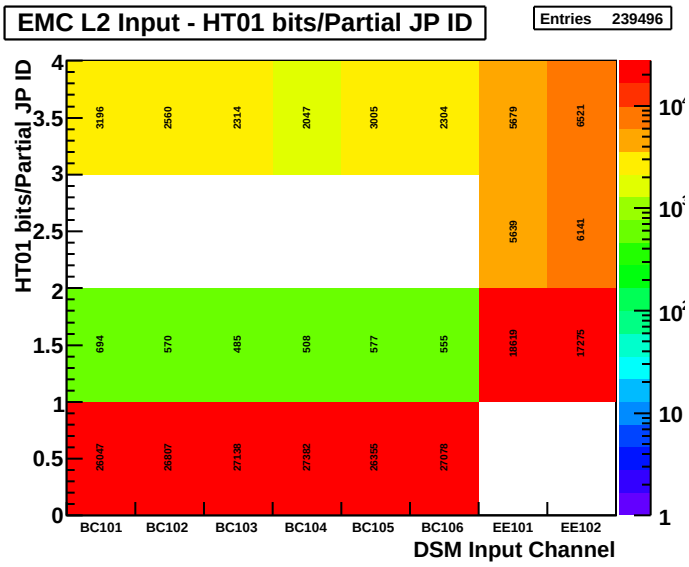

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

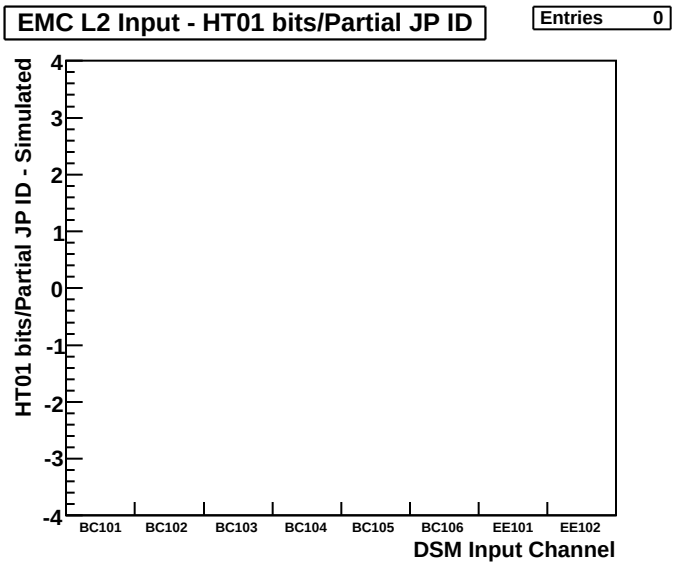

EMC L2 Input - JPZ/JPC bits
Entries 239496
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
148 110 119 84 87 132 68 101
380 326 349 336 327 390 291 229
484 483 507 533 780 616 111 127
28925 29018 28962 28984 28743 28799 29467 29480

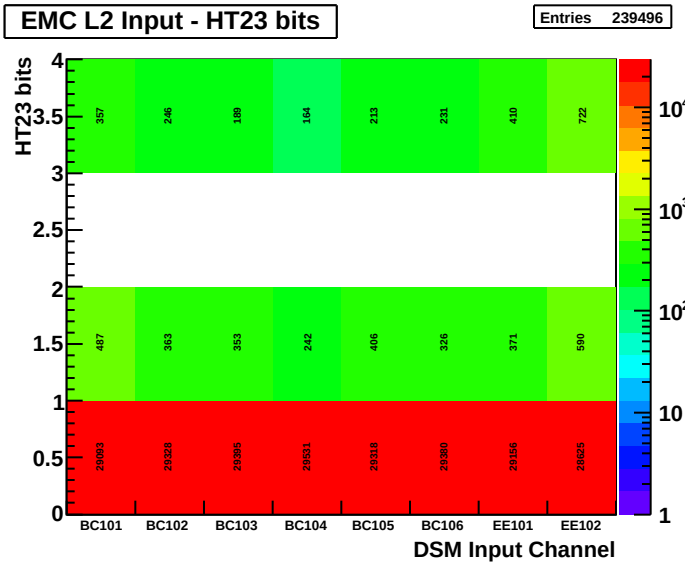

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

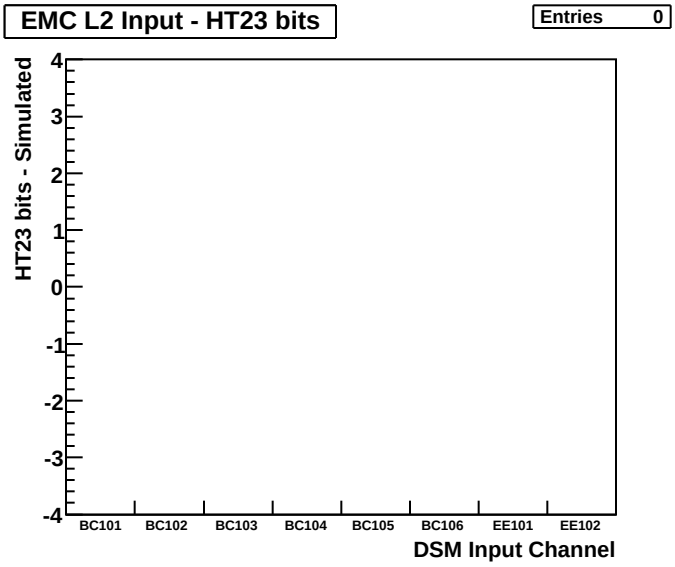

EMC L2 Input - Partial JP Sum
Entries 239496
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

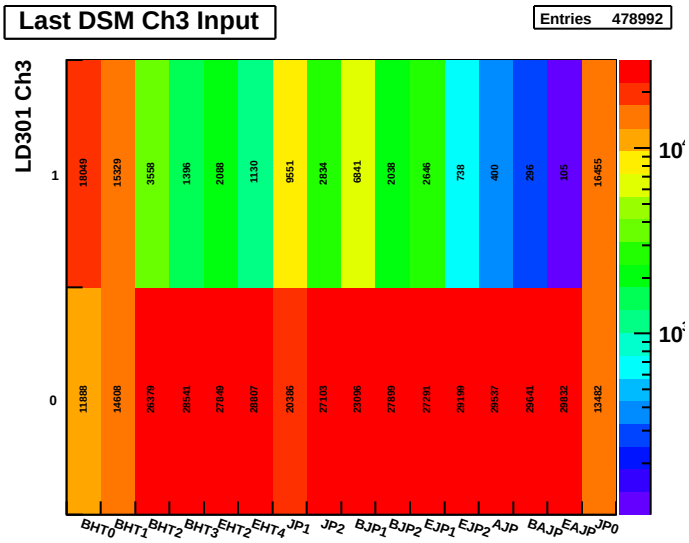

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

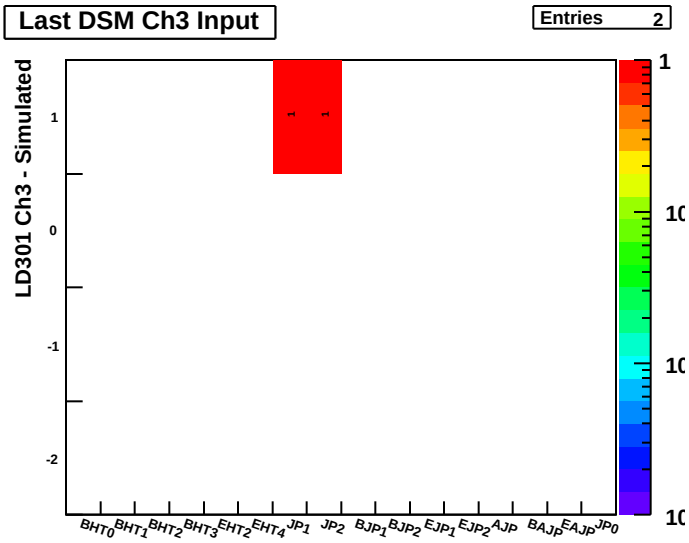

EMC L2 Input - HT01 bits/Partial JP ID
Entries 239496
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

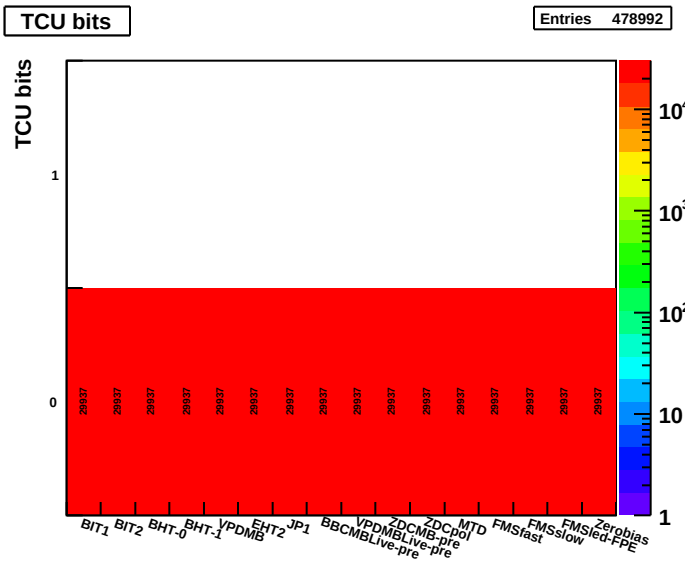

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

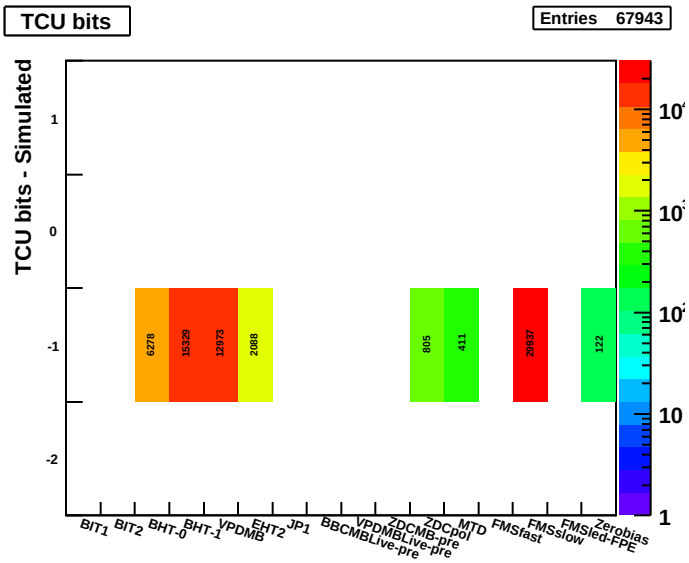

EMC L2 Input - HT23 bits
Entries 239496
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

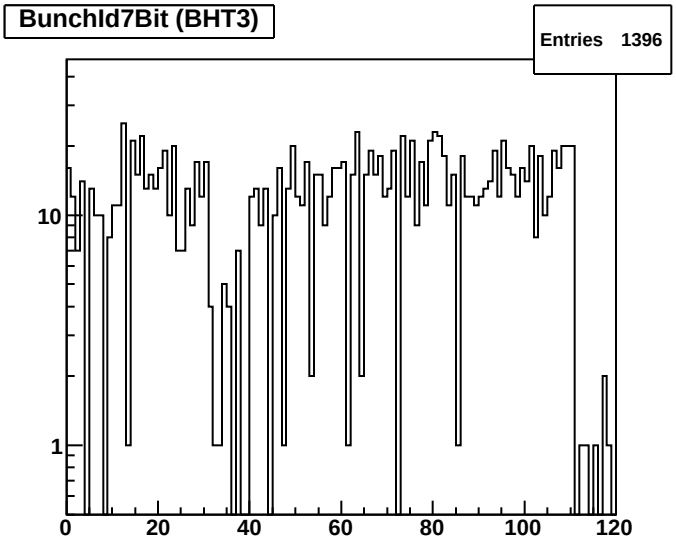

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

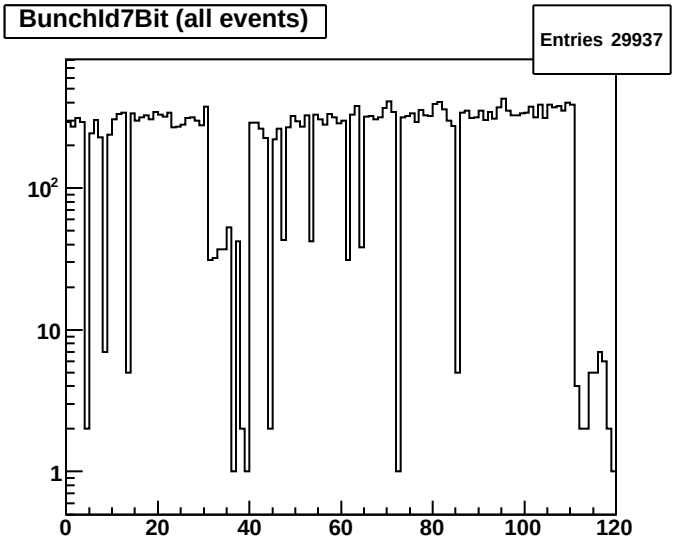

Last DSM Ch3 Input
Entries 478992
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

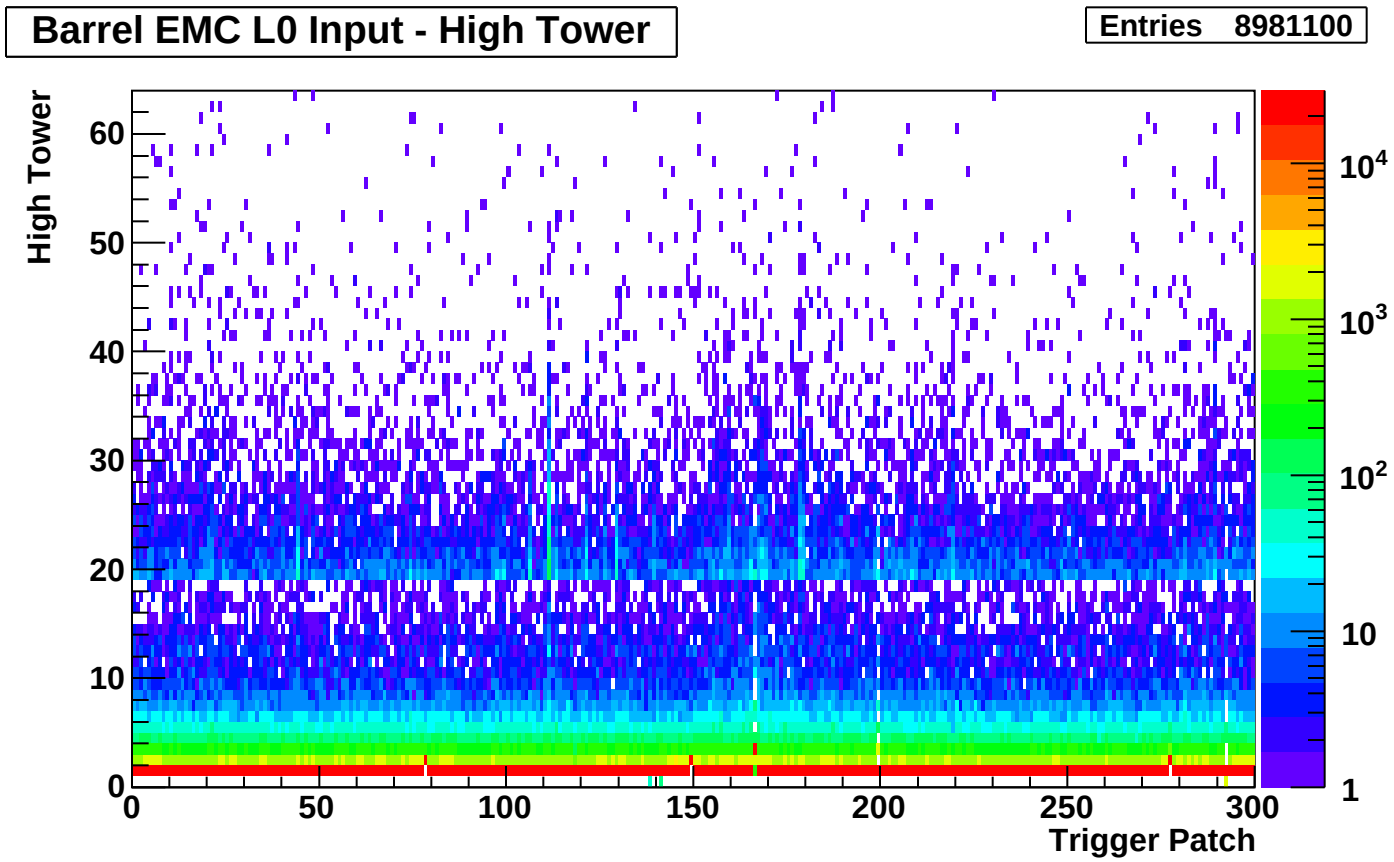

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

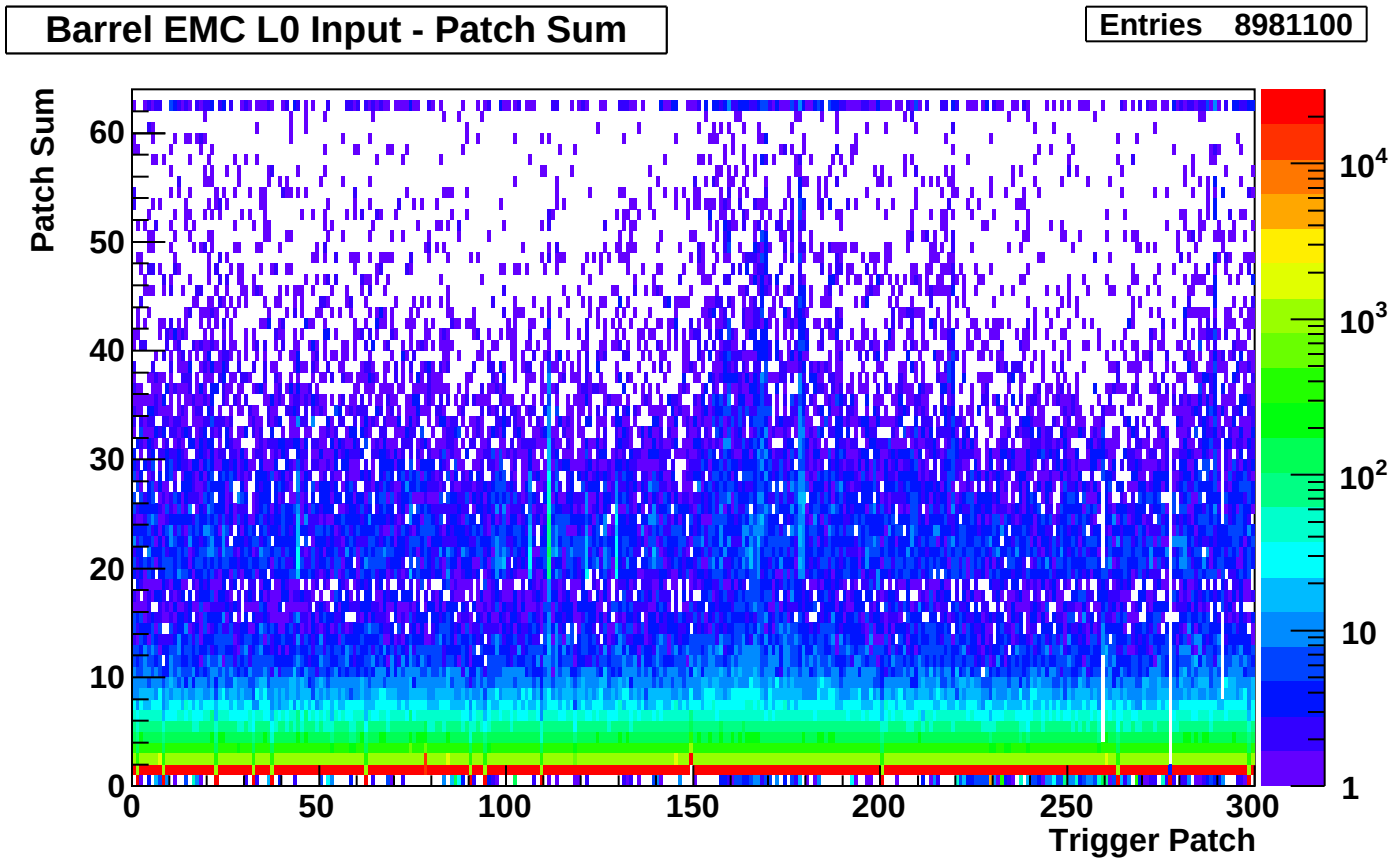

TCU bits
Entries 478992
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

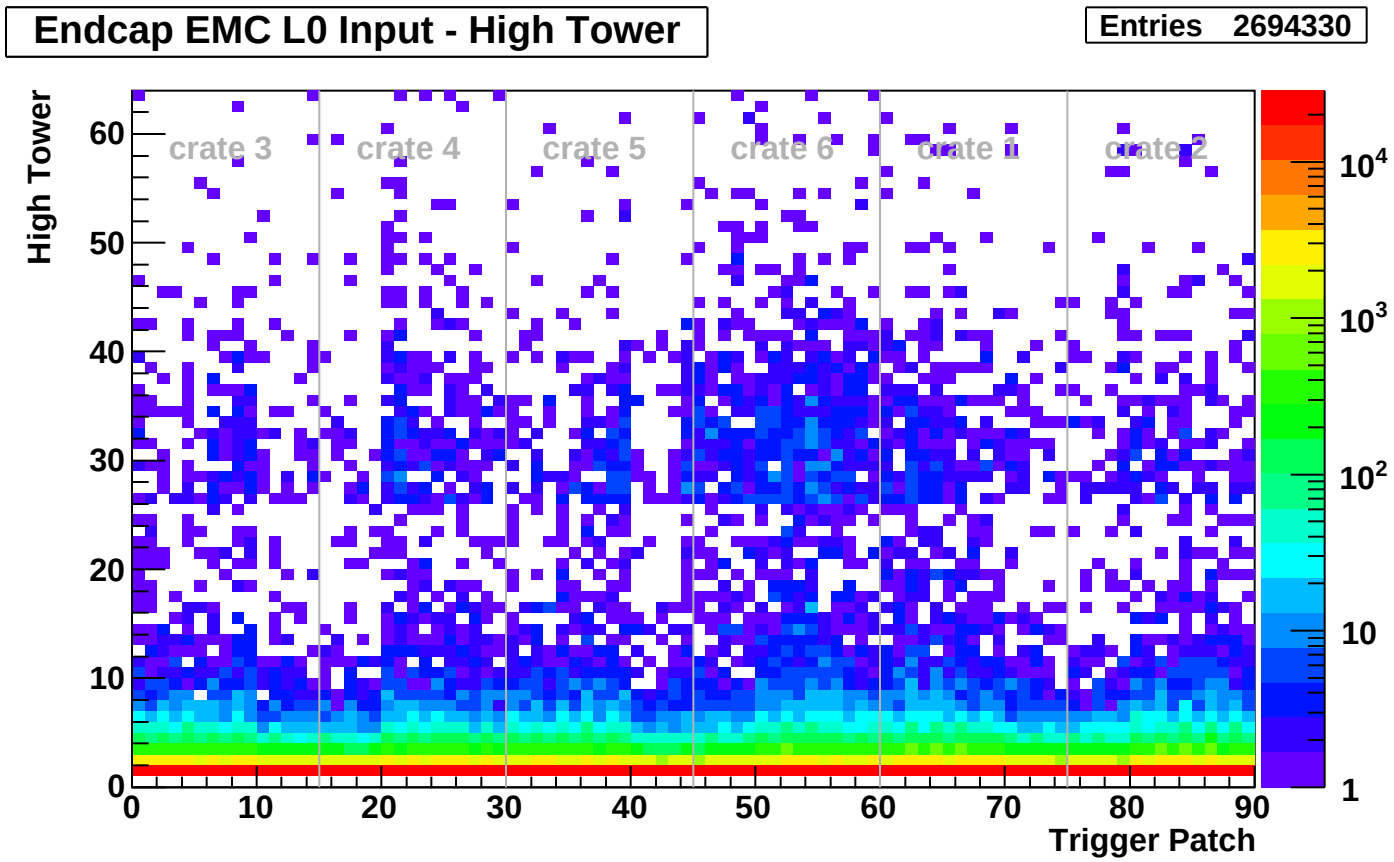

TCU bits
Entries 67943
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

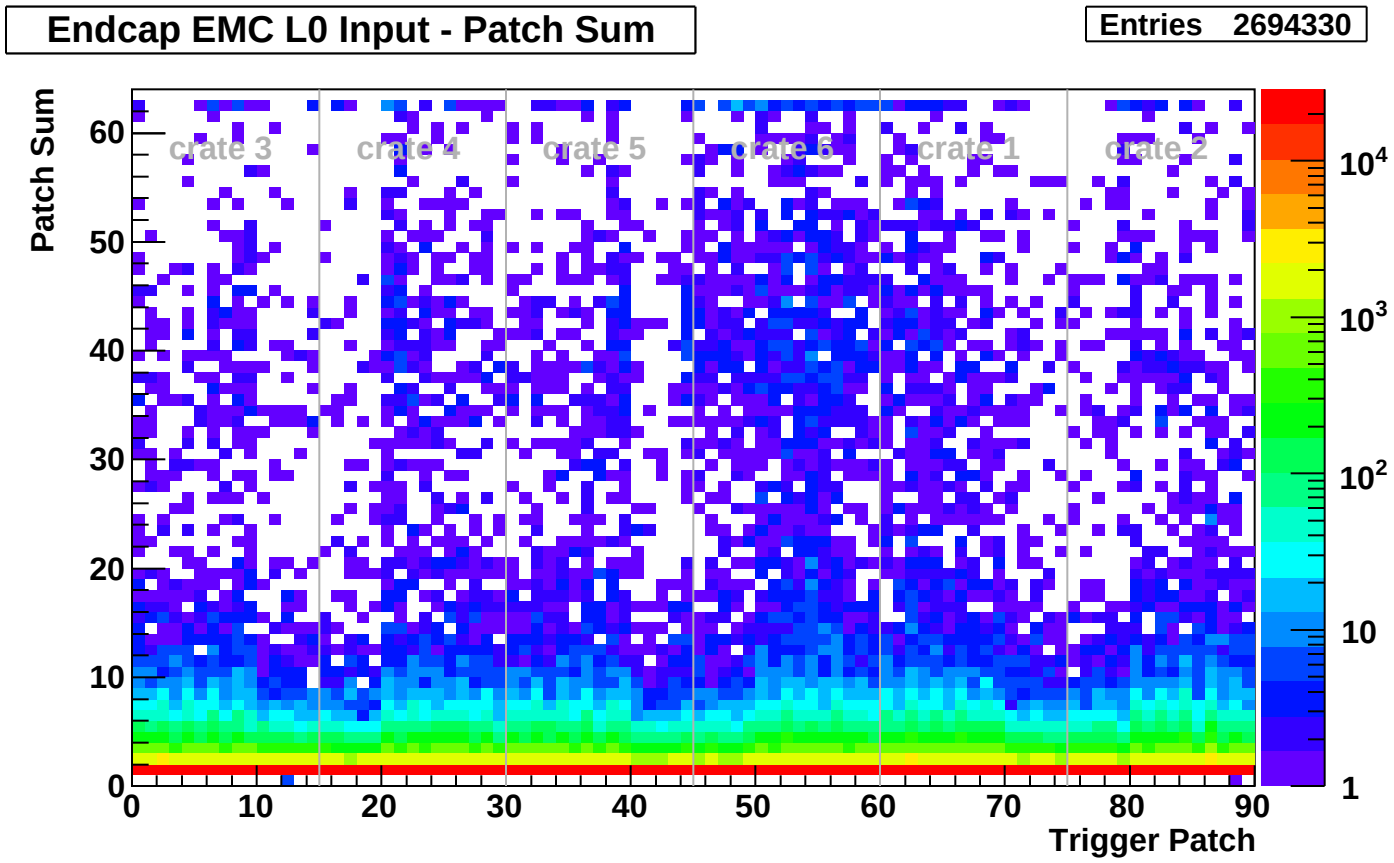

BunchId7Bit (BHT3)
Entries 1396

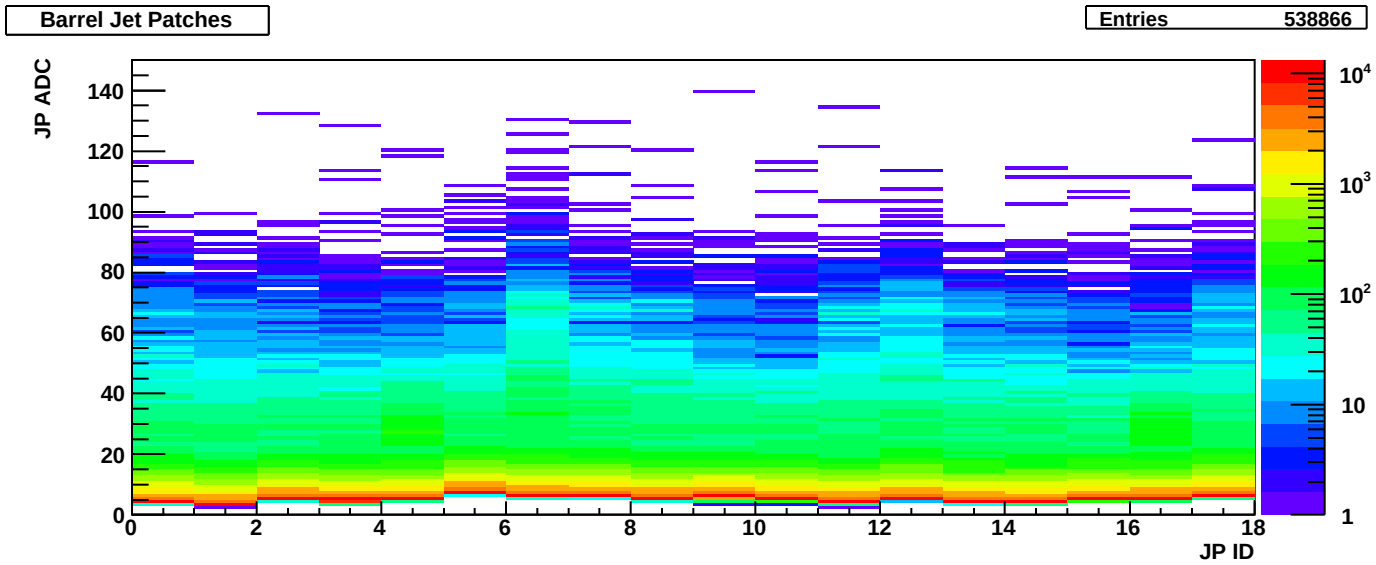

BunchId7Bit (all events)
Entries 29937

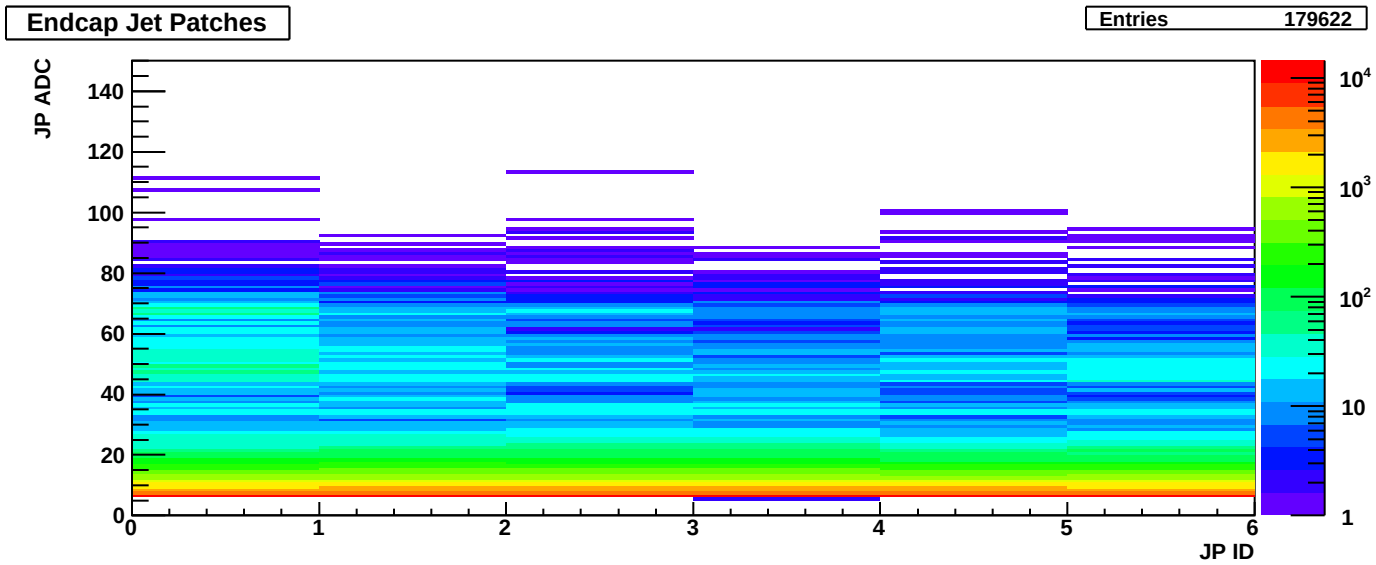

Barrel EMC L0 Input - High Tower
Entries 8981100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

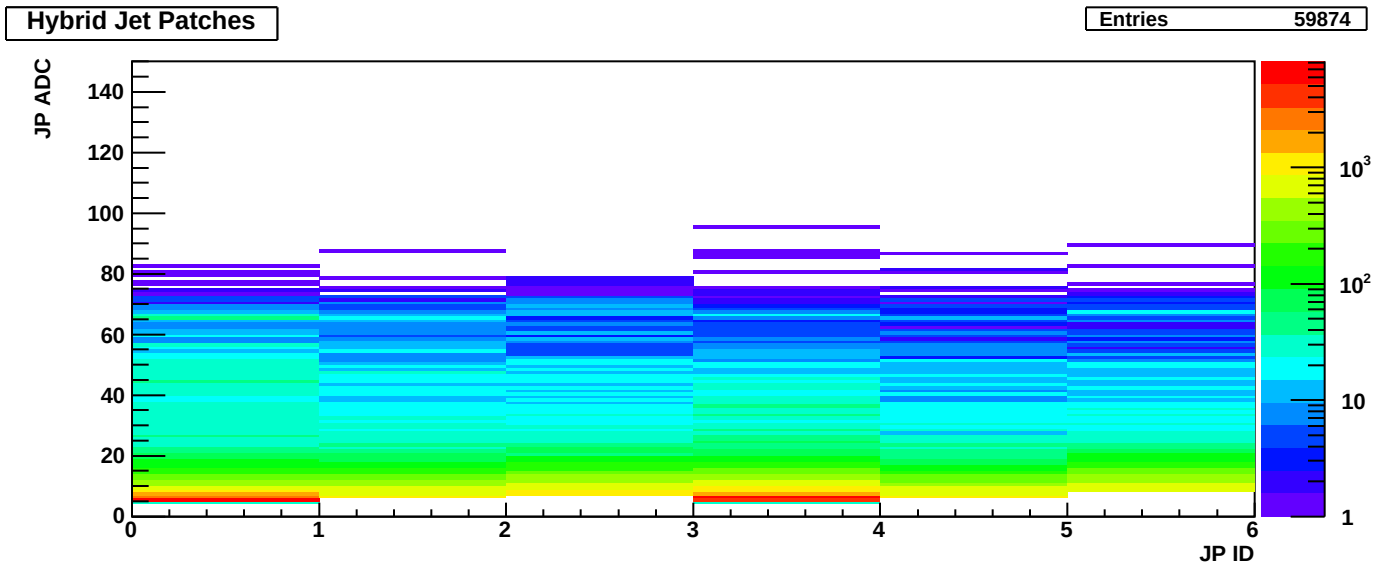

Barrel EMC L0 Input - Patch Sum
Entries 8981100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

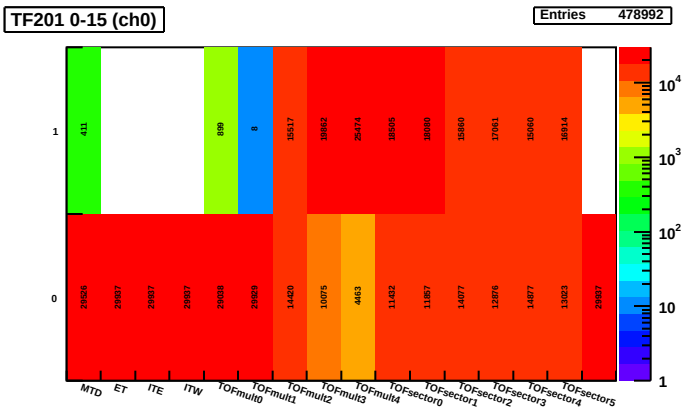

Endcap EMC L0 Input - High Tower
Entries 2694330
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

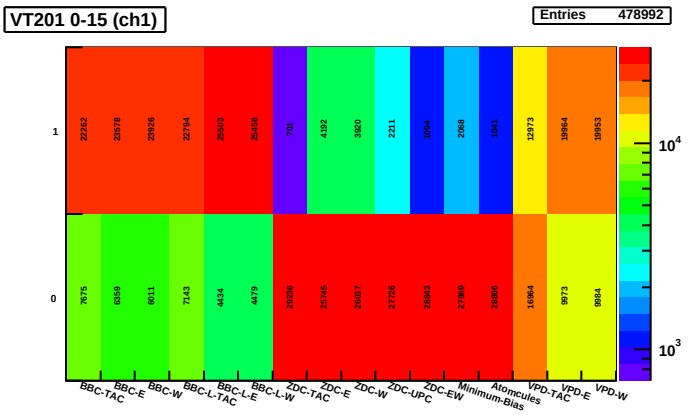

Endcap EMC L0 Input - Patch Sum
Entries 2694330
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

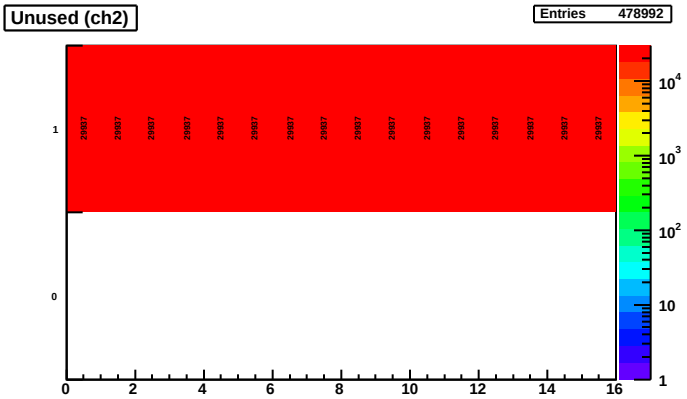

Barrel Jet Patches
Entries 538866
JP ADC
JP ID

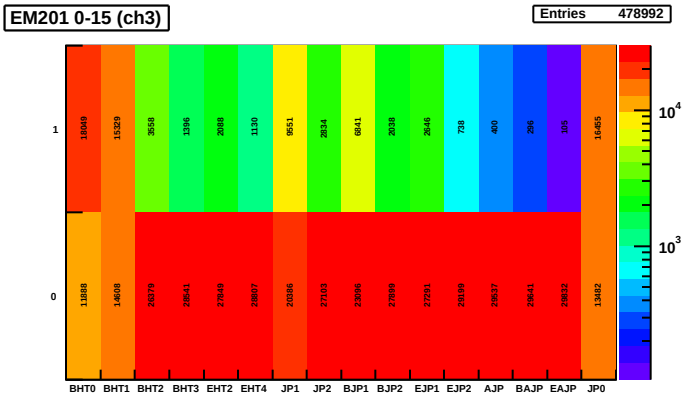

Endcap Jet Patches
Entries 179622
JP ADC
JP ID

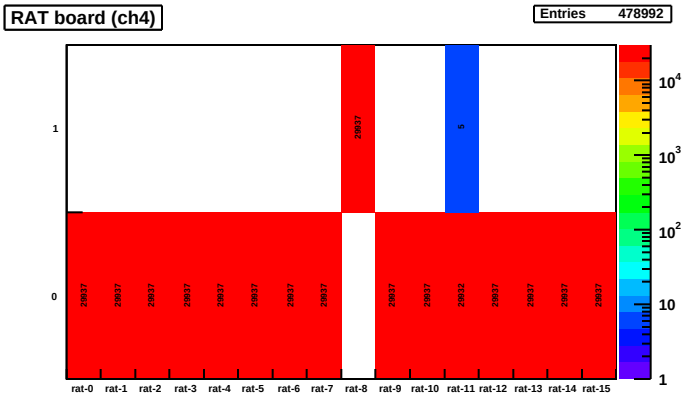

Hybrid Jet Patches
Entries 59874
JP ADC
JP ID

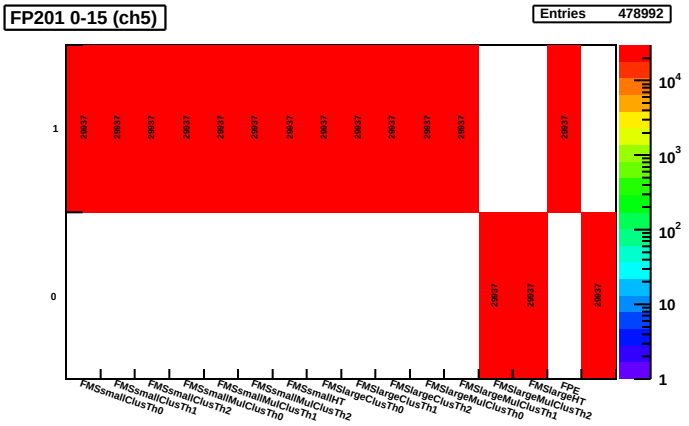

TF201 0-15 (ch0)
Entries 478992
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

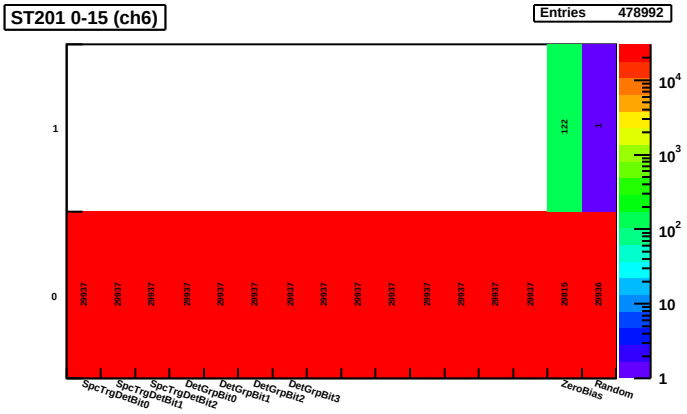

VT201 0-15 (ch1)
Entries 478992
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Atomcules
Minimum-Bias
VPD-TAC
VPD-E
VPD-W

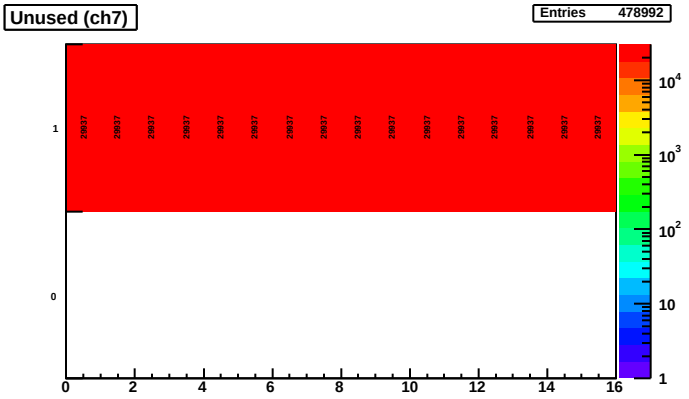

Unused (ch2)
Entries 478992

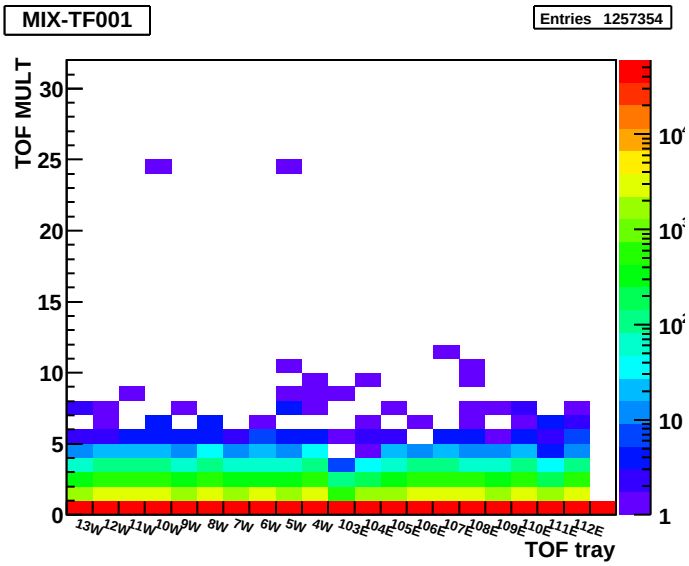

EM201 0-15 (ch3)
Entries 478992
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

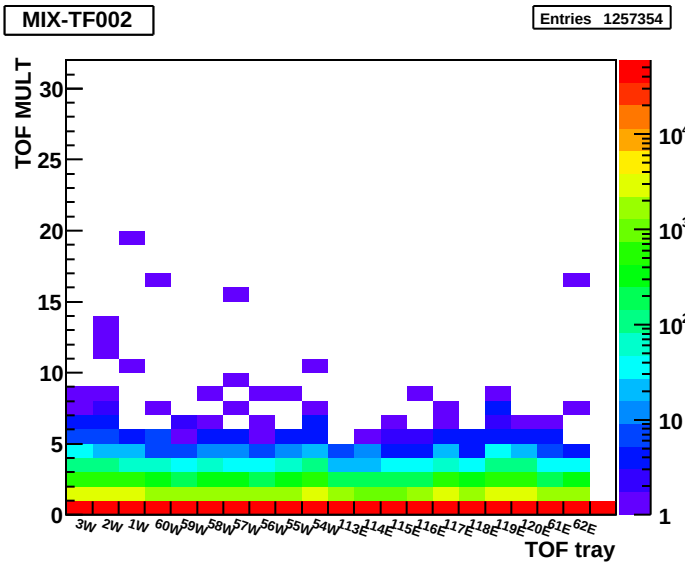

RAT board (ch4)
Entries 478992
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

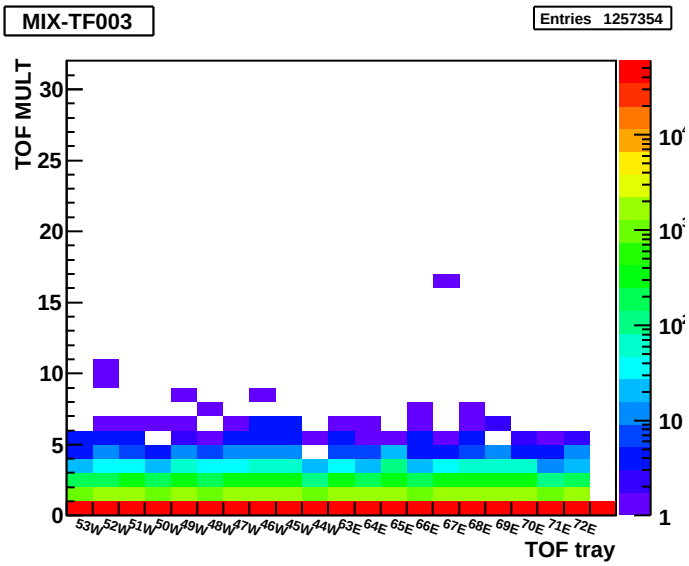

FP201 0-15 (ch5)
Entries 478992
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

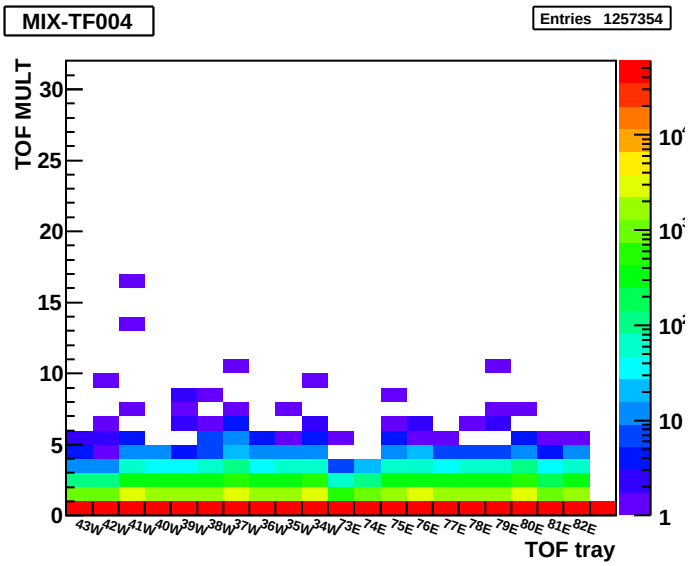

ST201 0-15 (ch6)
Entries 478992
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

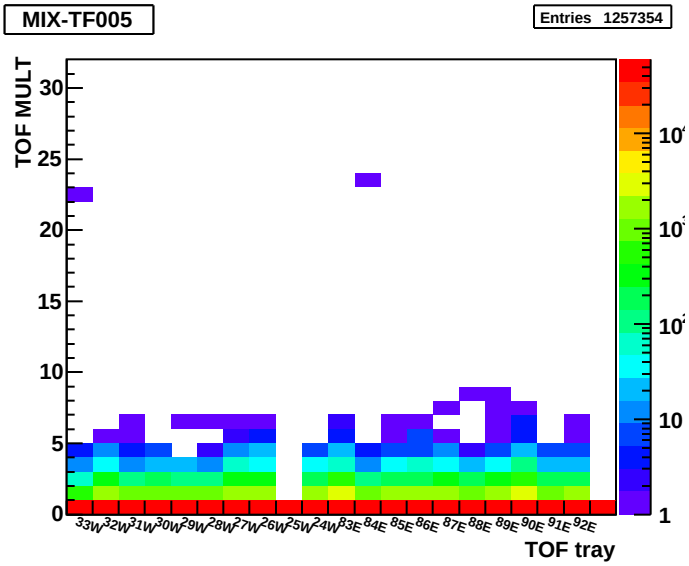

Unused (ch7)
Entries 478992

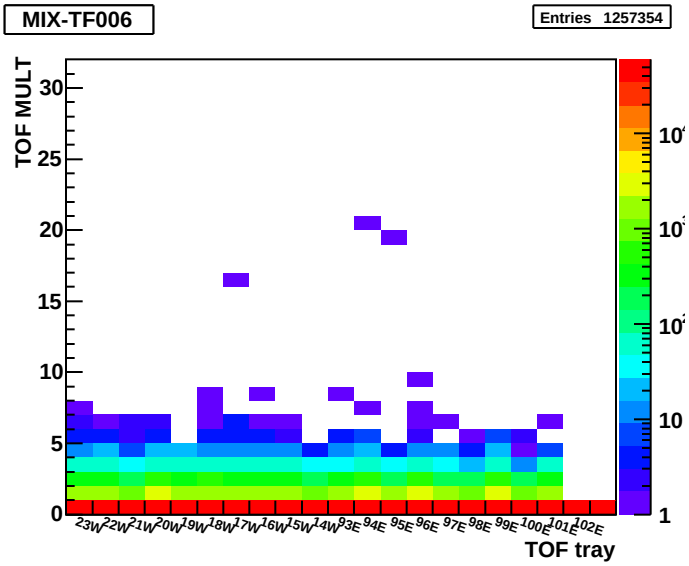

MIX-TF001
Entries 1257354
TOF MULT
TOF tray

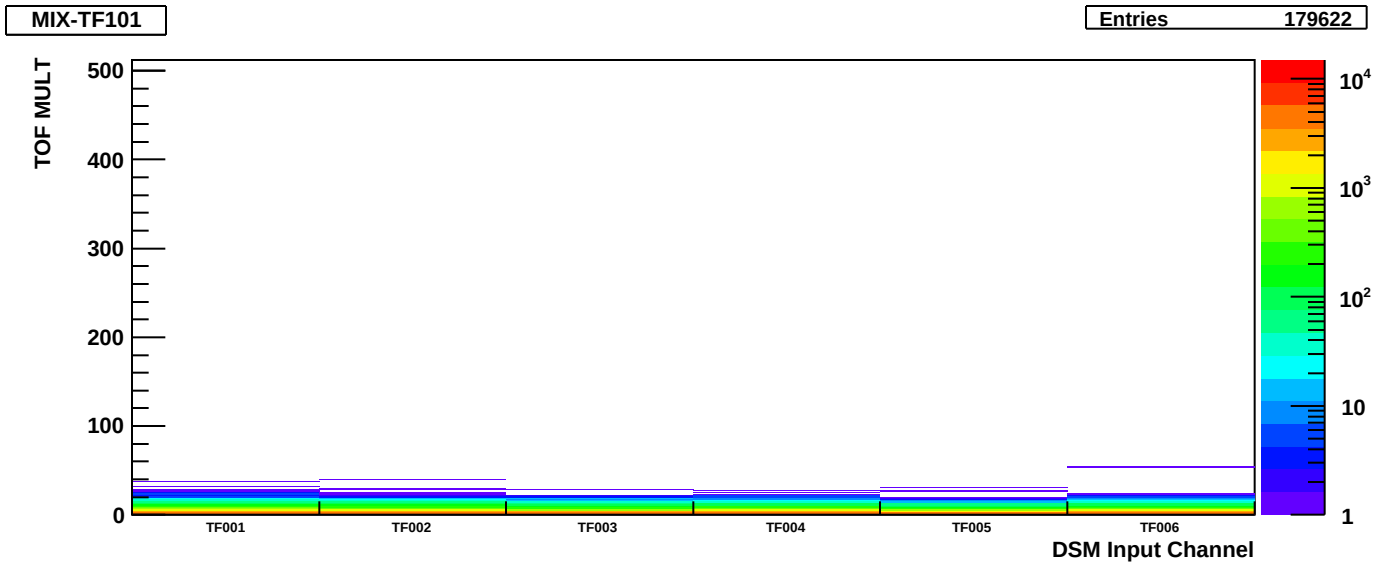

MIX-TF002
Entries 1257354
TOF MULT
TOF tray

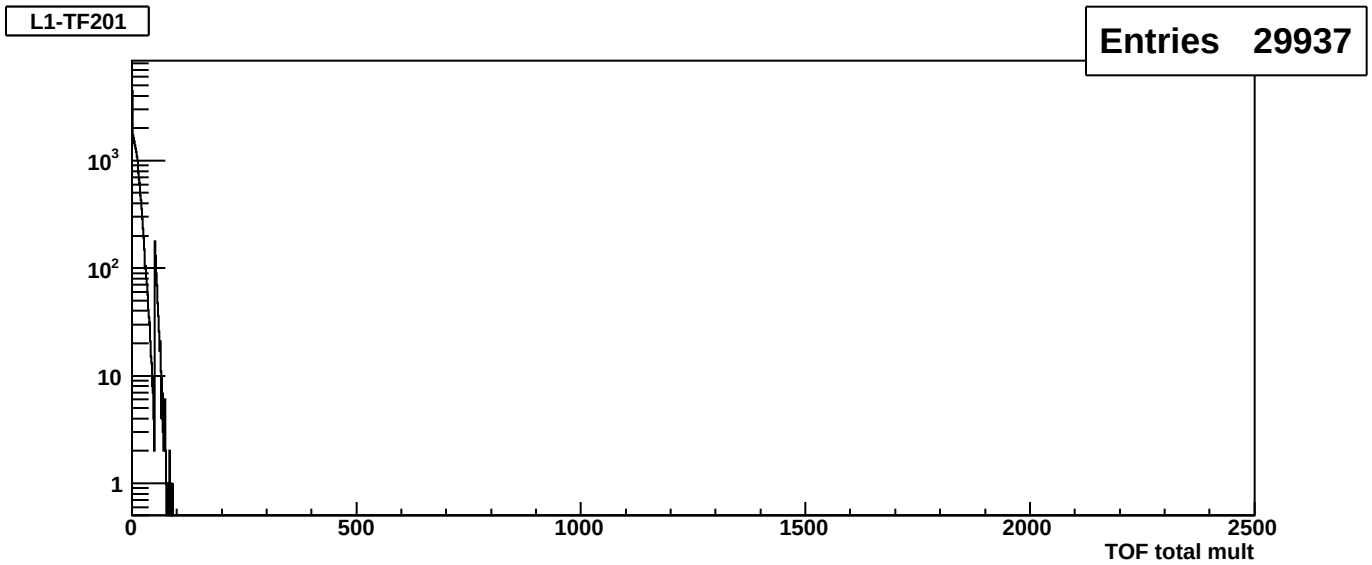

MIX-TF003
Entries 1257354
TOF MULT
TOF tray

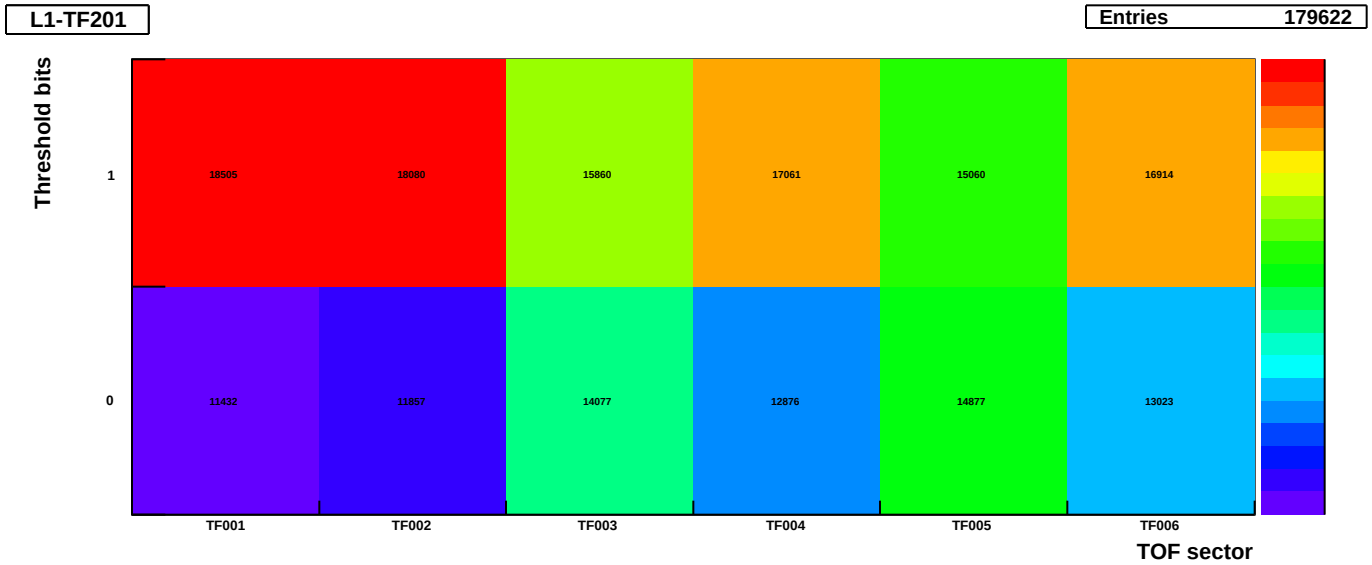

MIX-TF004
Entries 1257354
TOF MULT
TOF tray

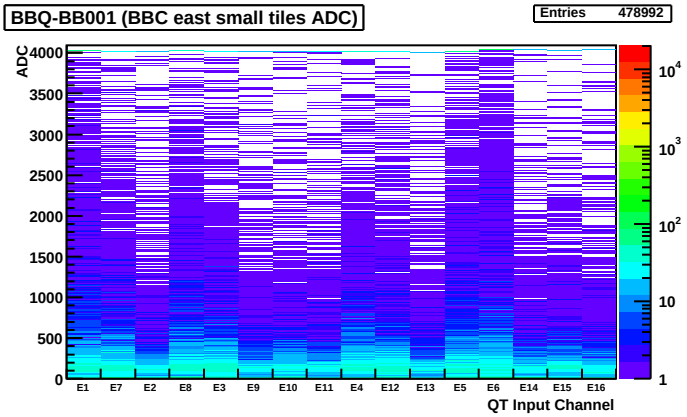

MIX-TF005
Entries 1257354
TOF MULT
TOF tray

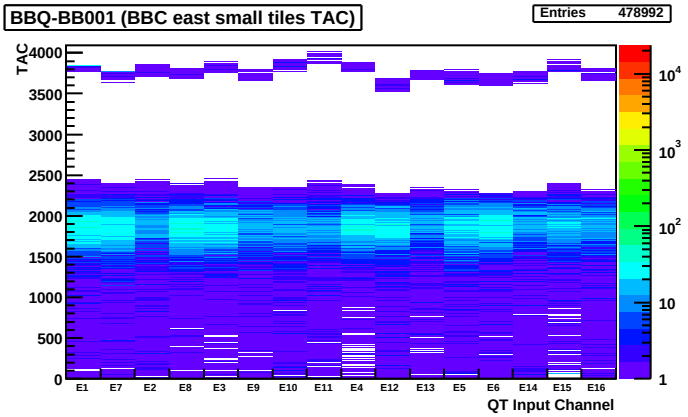

MIX-TF006
Entries 1257354
TOF MULT
TOF tray

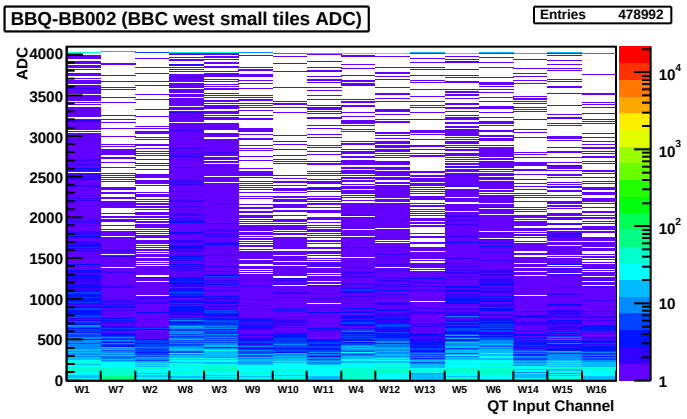

MIX-TF101
Entries 179622
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

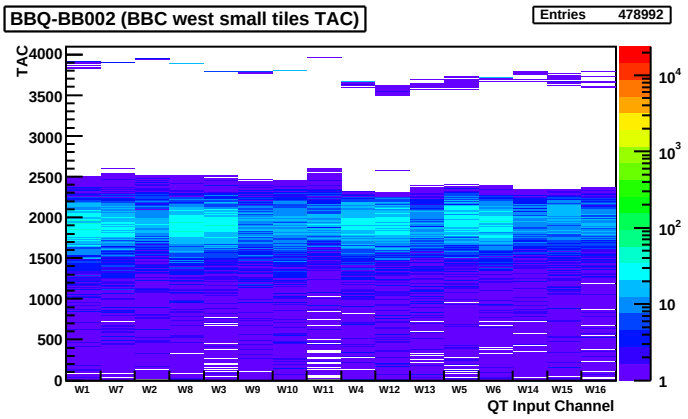

L1-TF201
Entries 29937
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

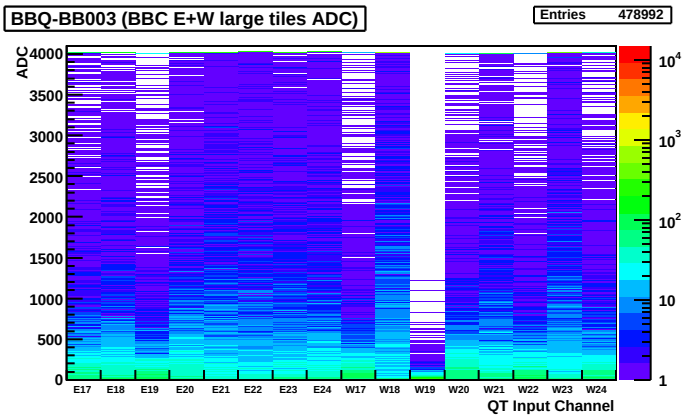

L1-TF201
Entries 179622
Threshold bits
1
0
18505
18080
15860
17061
15060
16914
11432
11857
14077
12876
14877
13023
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

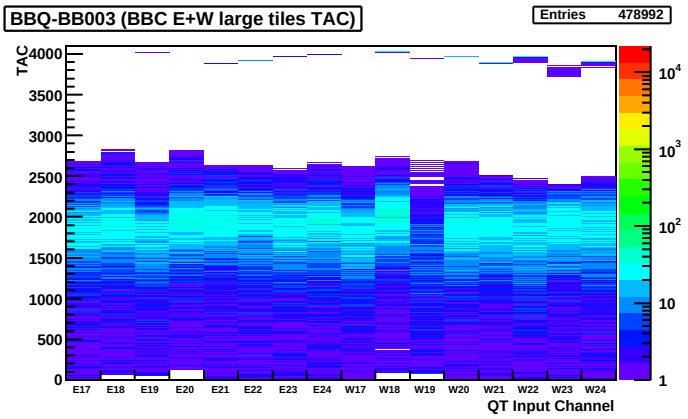

BBQ-BB001 (BBC east small tiles ADC)
Entries 478992
ADC
QT Input Channel

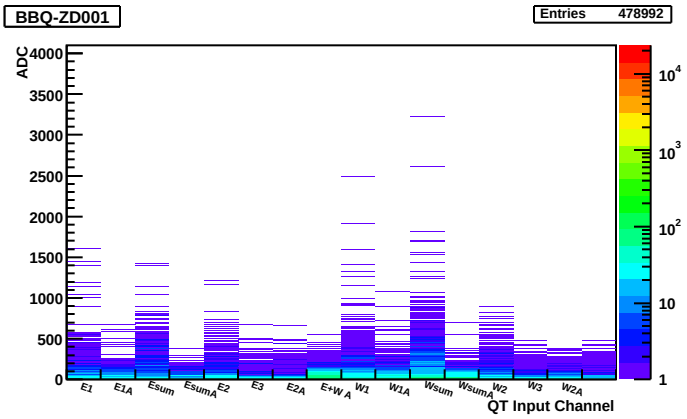

BBQ-BB001 (BBC east small tiles TAC)
Entries 478992
TAC
QT Input Channel

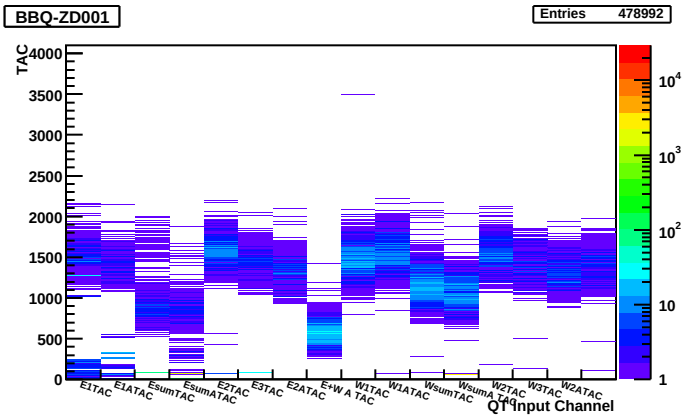

BBQ-BB002 (BBC west small tiles ADC)
Entries 478992
ADC
QT Input Channel

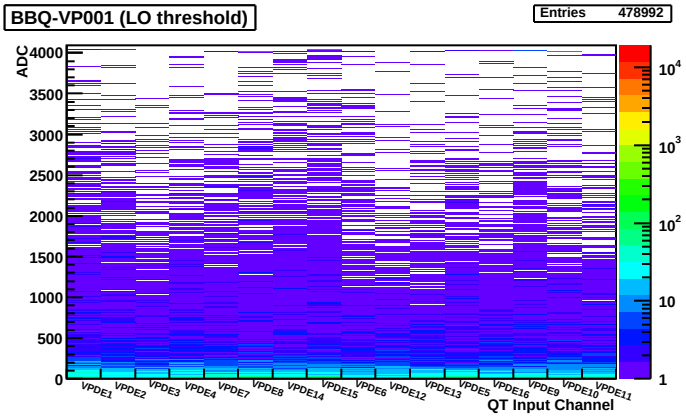

BBQ-BB002 (BBC west small tiles TAC)
Entries 478992
TAC
QT Input Channel

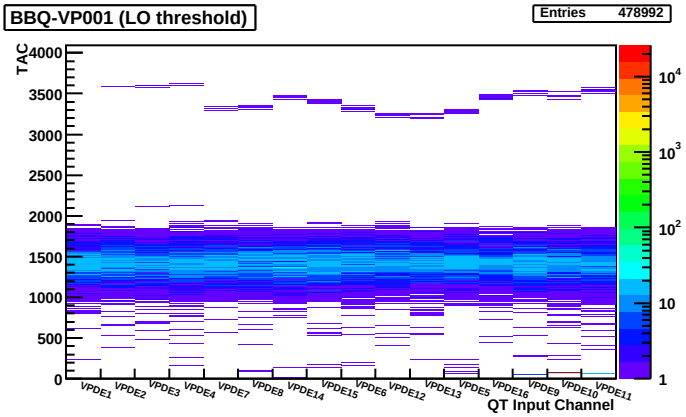

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 478992
ADC
QT Input Channel

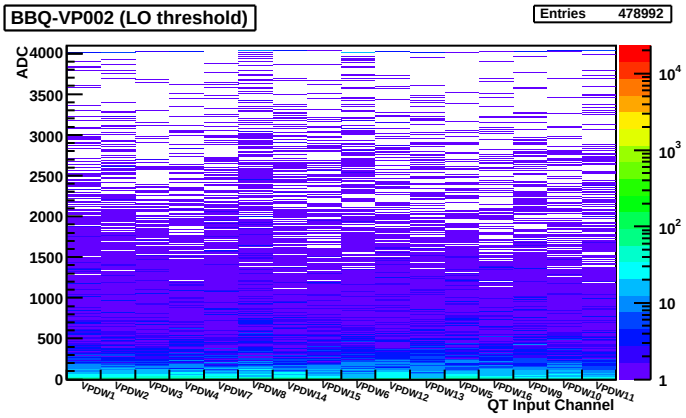

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 478992
TAC
QT Input Channel

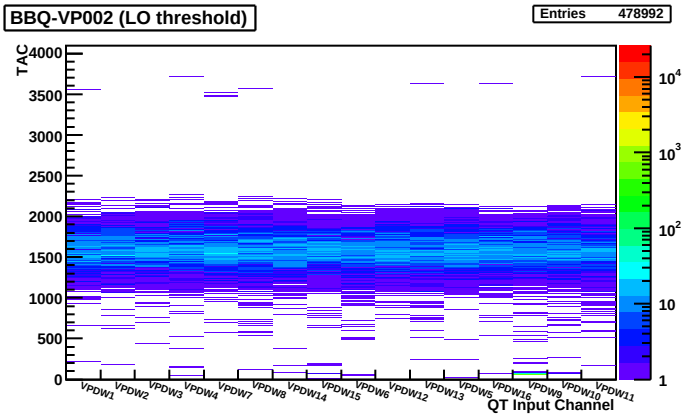

BBQ-ZD001
Entries 478992
ADC
QT Input Channel

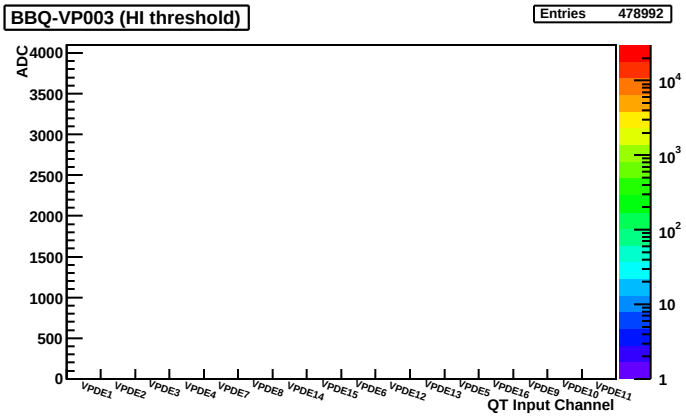

BBQ-ZD001
Entries 478992
TAC
QT Input Channel

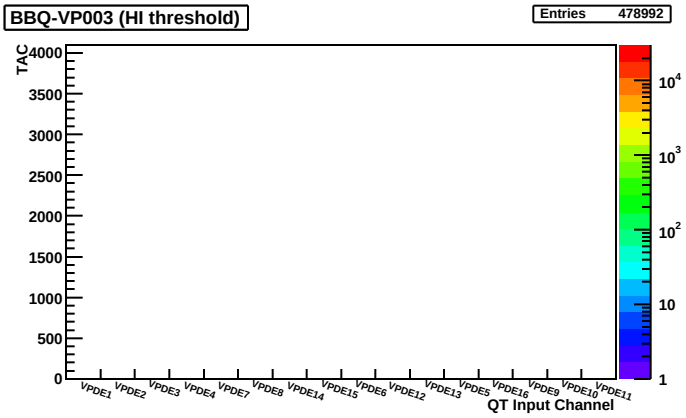

BBQ-VP001 (LO threshold)
Entries 478992
ADC
QT Input Channel

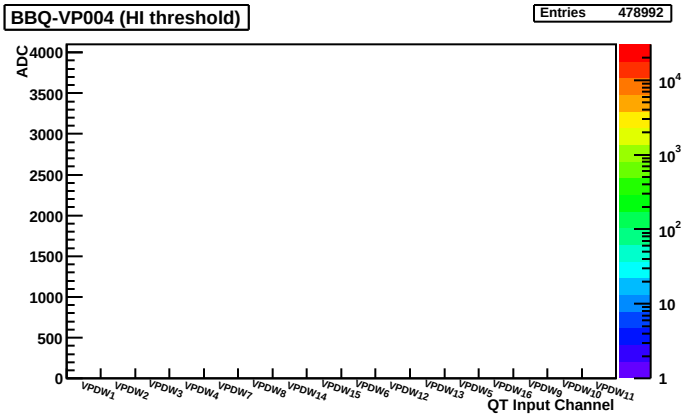

BBQ-VP001 (LO threshold)
Entries 478992
TAC
QT Input Channel

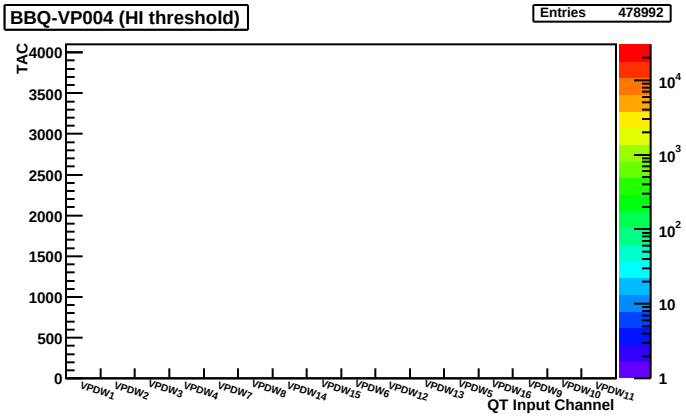

BBQ-VP002 (LO threshold)
Entries 478992
ADC
QT Input Channel

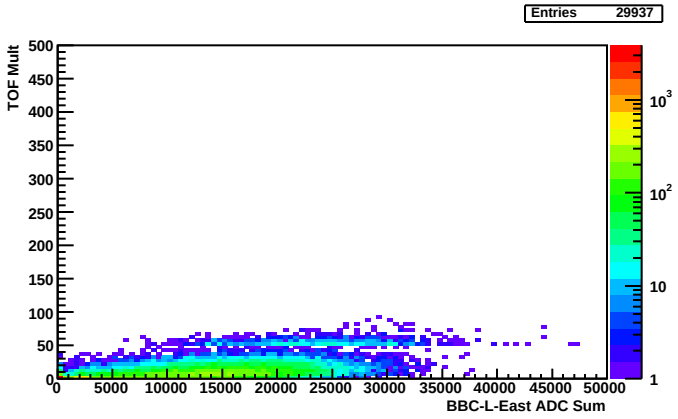

BBQ-VP002 (LO threshold)
Entries 478992
TAC
QT Input Channel

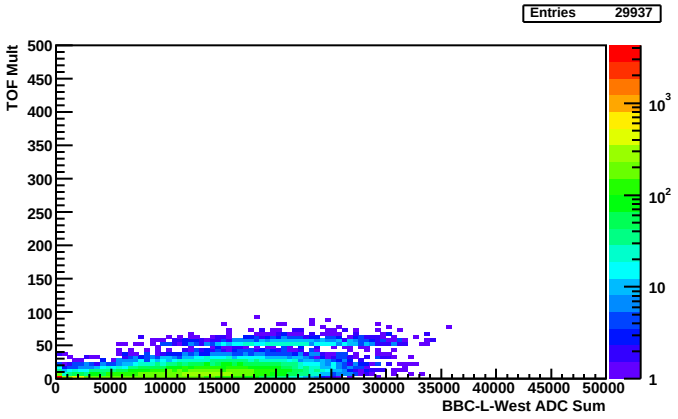

BBQ-VP003 (HI threshold)
Entries 478992
ADC
QT Input Channel

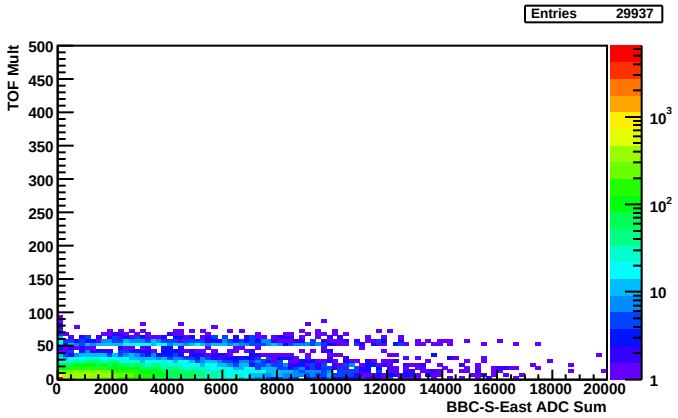

BBQ-VP003 (HI threshold)
Entries 478992
TAC
QT Input Channel

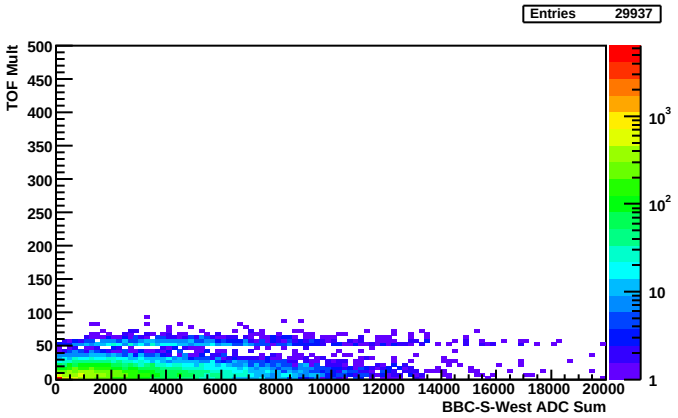

BBQ-VP004 (HI threshold)
Entries 478992
ADC
QT Input Channel

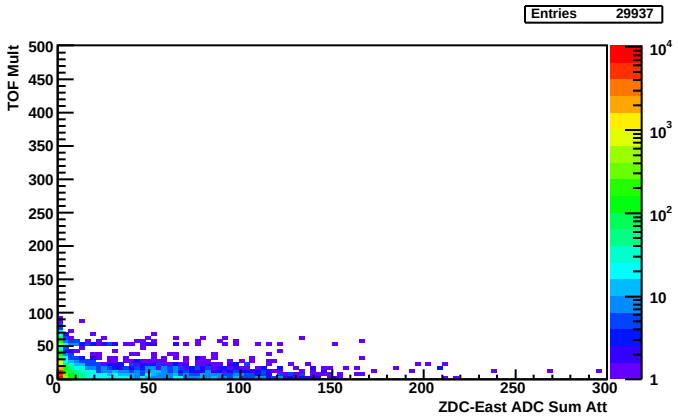

BBQ-VP004 (HI threshold)
Entries 478992
TAC
QT Input Channel

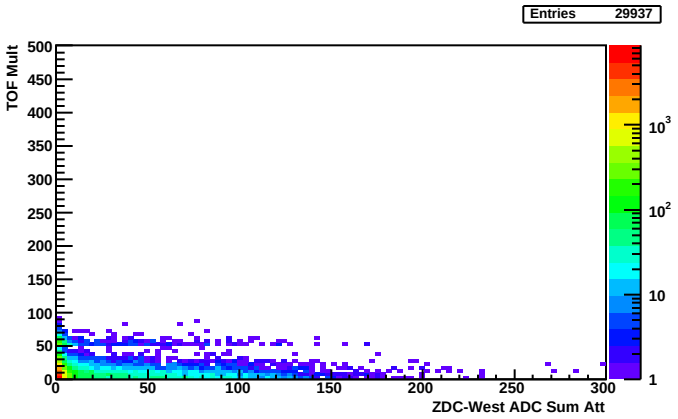

Entries 29937
TOF Mult
BBC-L-East ADC Sum

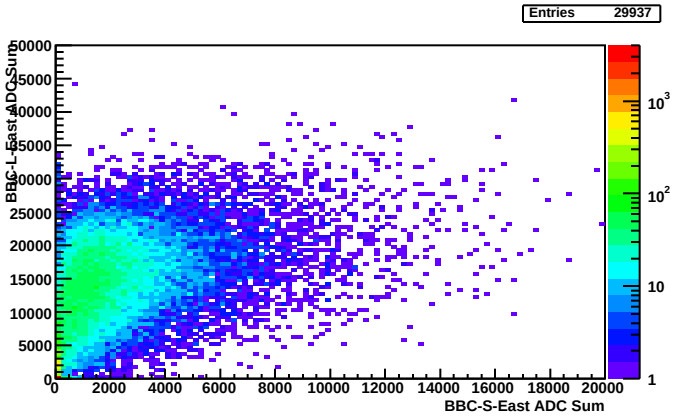

Entries 29937
TOF Mult
BBC-L-West ADC Sum

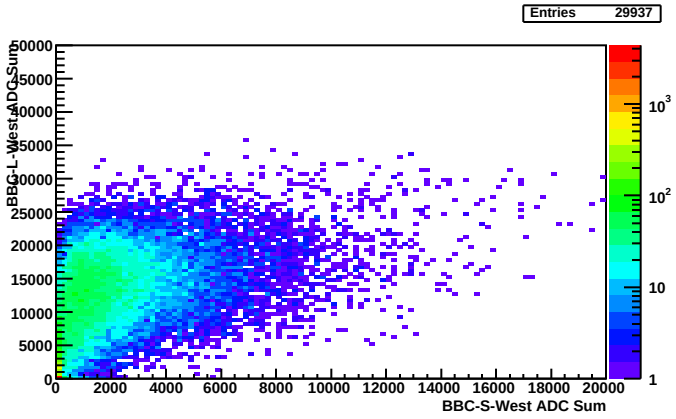

Entries 29937
TOF Mult
BBC-S-East ADC Sum

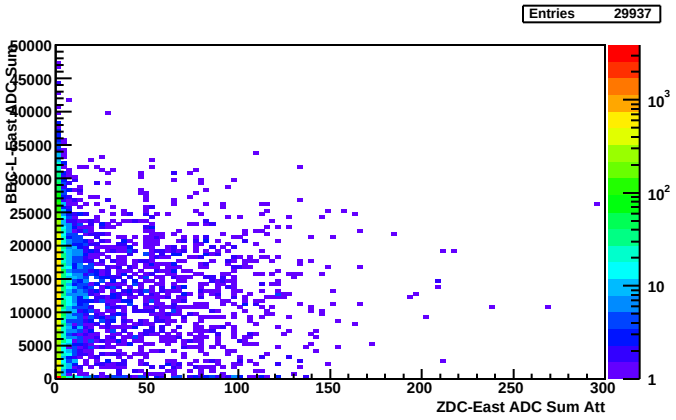

Entries 29937
TOF Mult
BBC-S-West ADC Sum

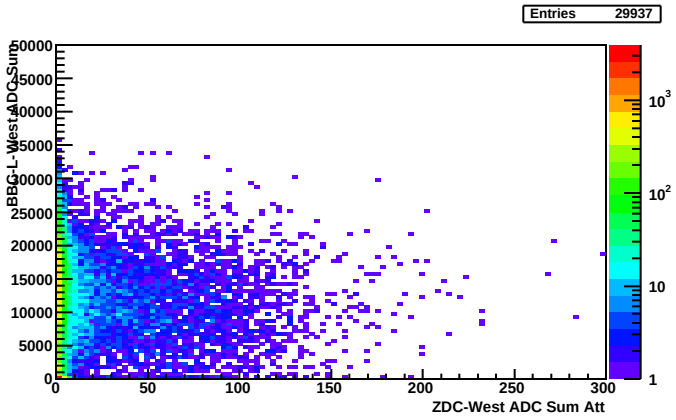

Entries 29937
TOF Mult
ZDC-East ADC Sum Att

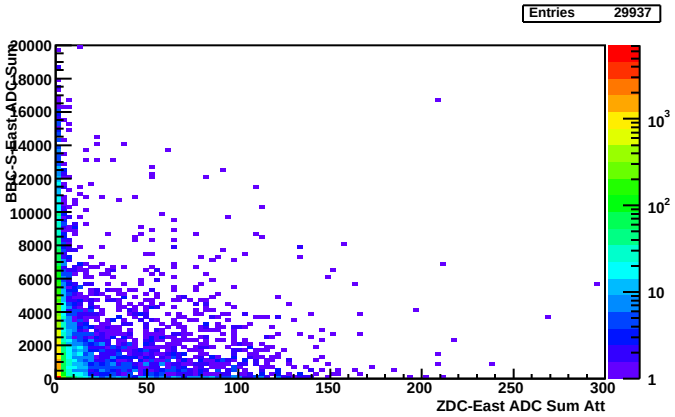

Entries 29937
TOF Mult
ZDC-West ADC Sum Att

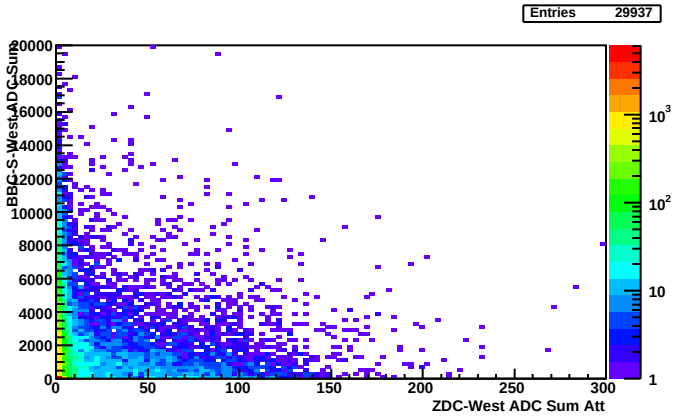

Entries 29937
BBC-L-East ADC Sum
BBC-S-East ADC Sum

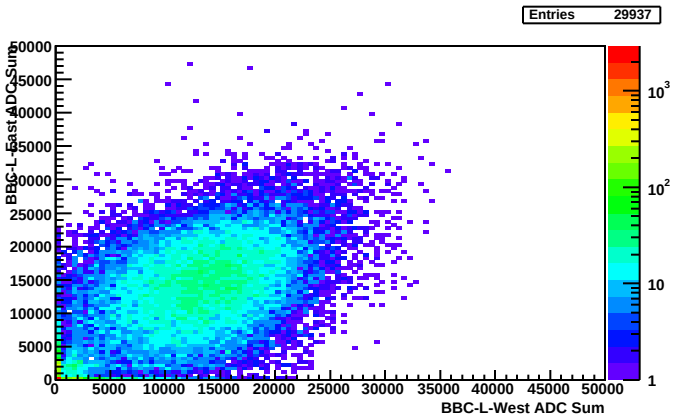

Entries 29937
BBC-L-West ADC Sum
BBC-S-West ADC Sum

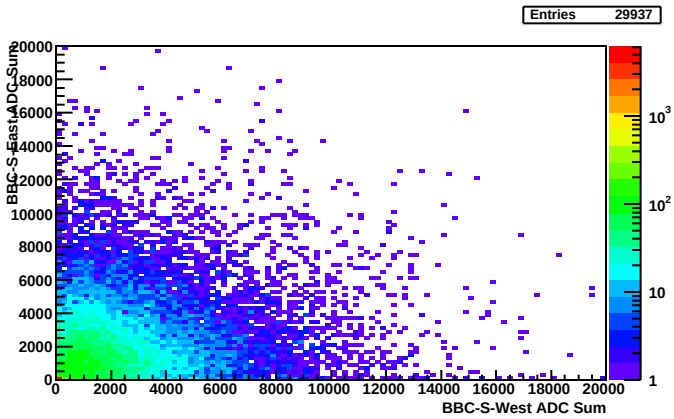

Entries 29937
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

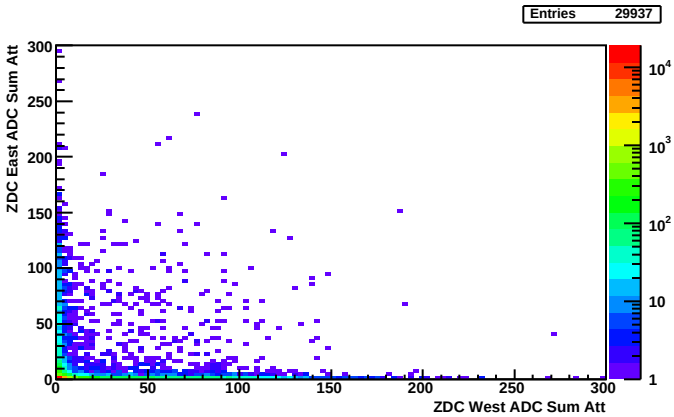

Entries 29937
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

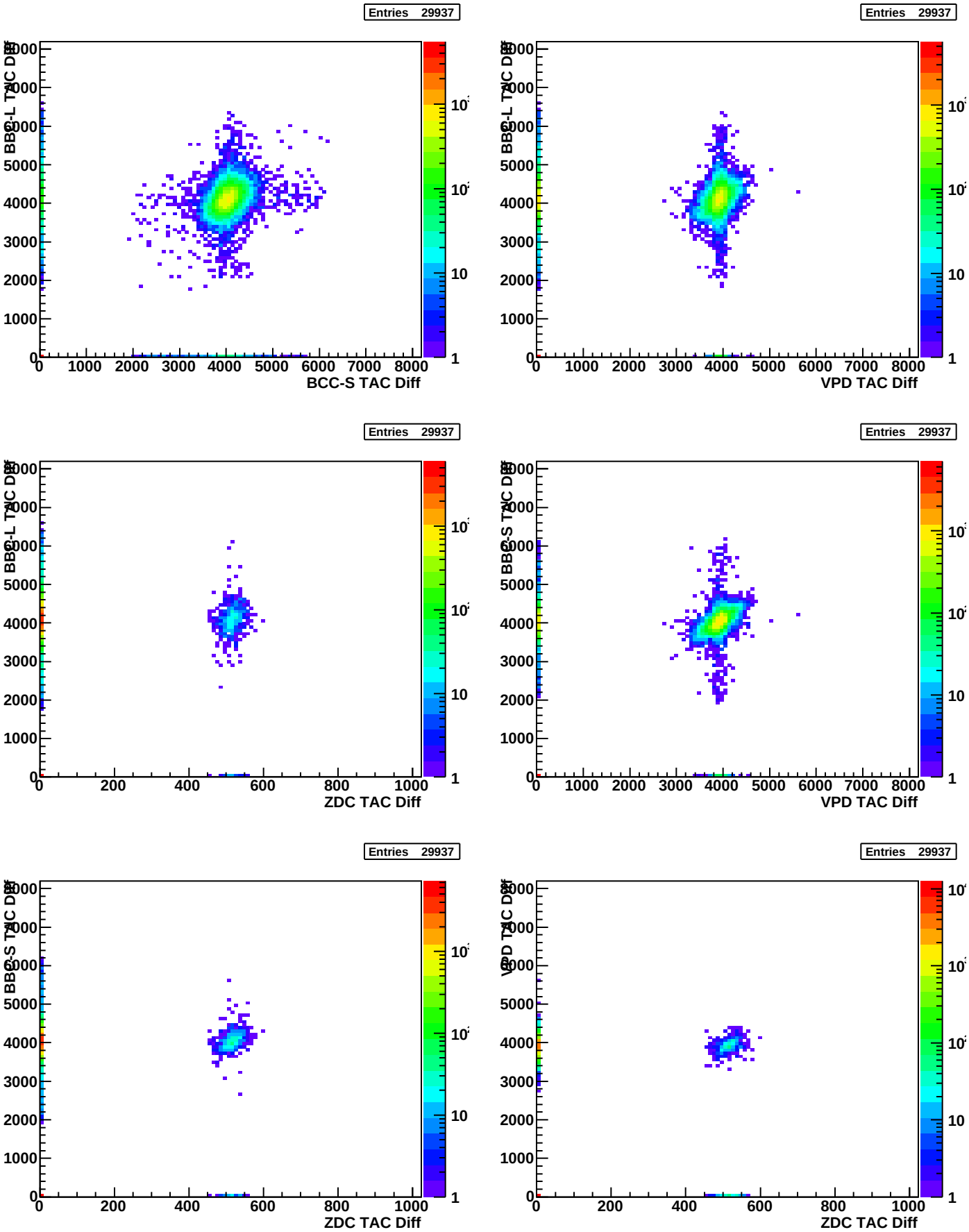

Entries 29937
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

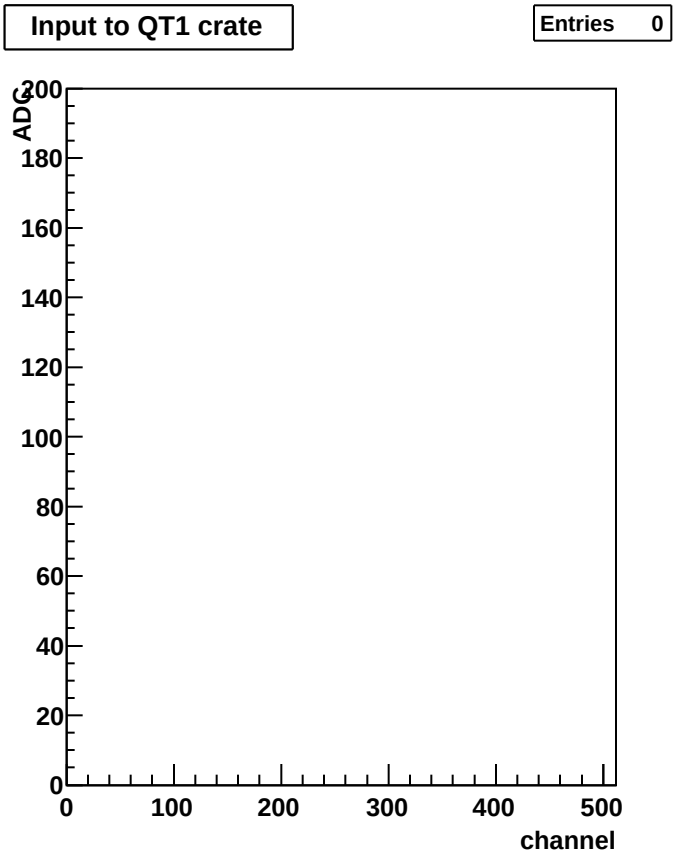

Entries 29937
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

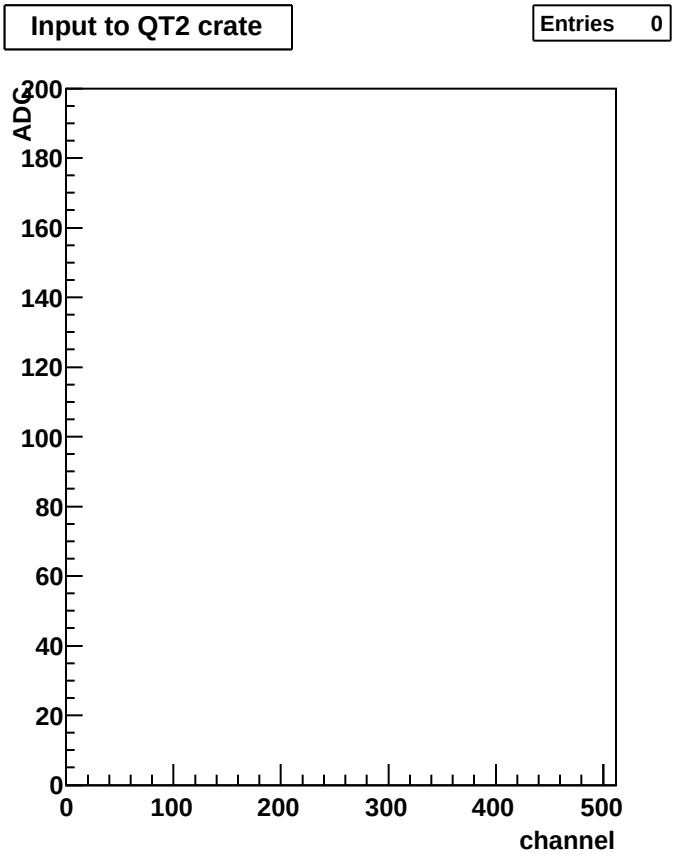

Entries 29937
BBC-L-East ADC Sum
BBC-L-West ADC Sum

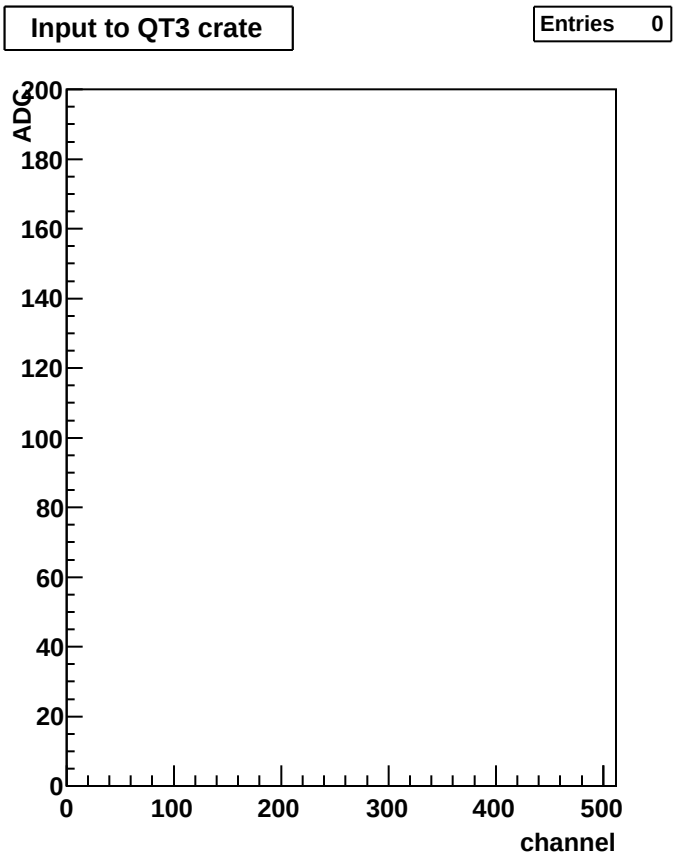

Entries 29937
BBC-S-East ADC Sum
BBC-S-West ADC Sum

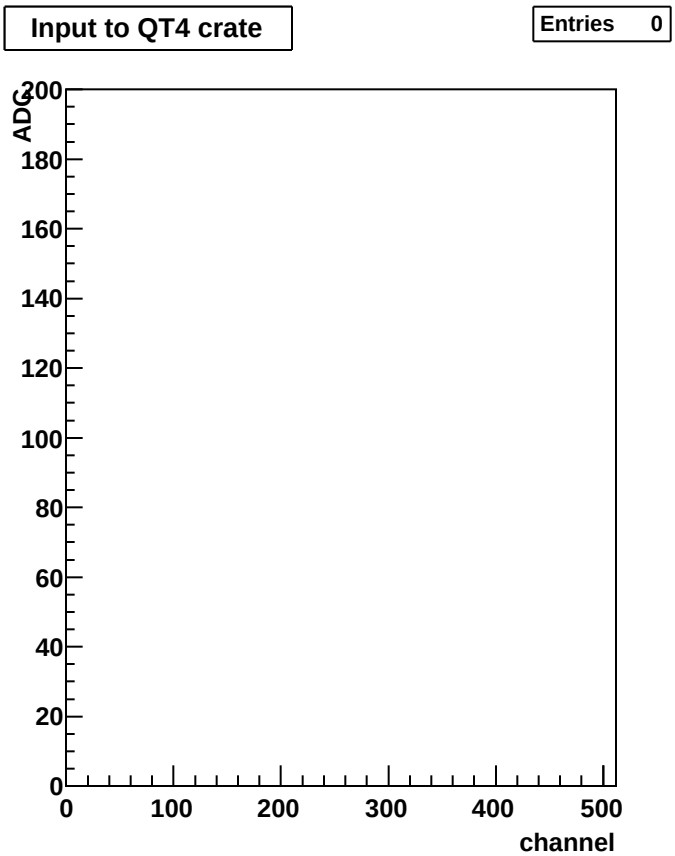

Entries 29937
ZDC East ADC Sum Att
ZDC West ADC Sum Att

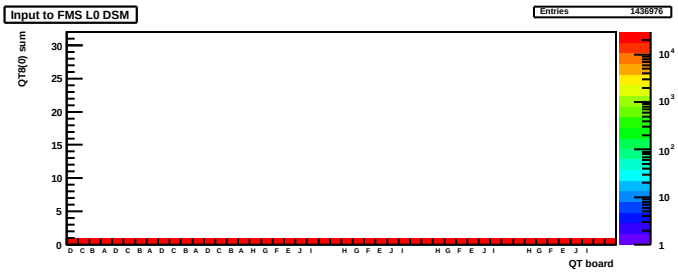

Entries 29937
BBC-L TAC Diff
BCC-S TAC Diff
Entries 29937
BBC-L TAC Diff
VPD TAC Diff
Entries 29937
BBC-L TAC Diff
ZDC TAC Diff
Entries 29937
BBC-S TAC Diff
VPD TAC Diff
Entries 29937
BBC-S TAC Diff
ZDC TAC Diff
Entries 29937
VPD TAC Diff
ZDC TAC Diff

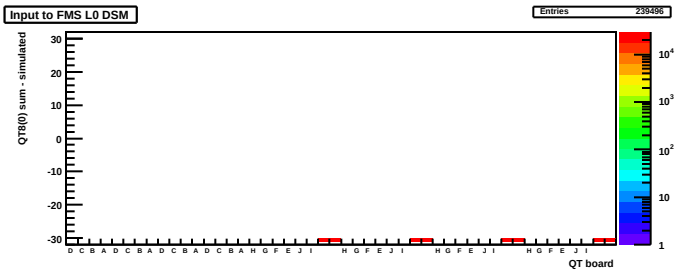

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

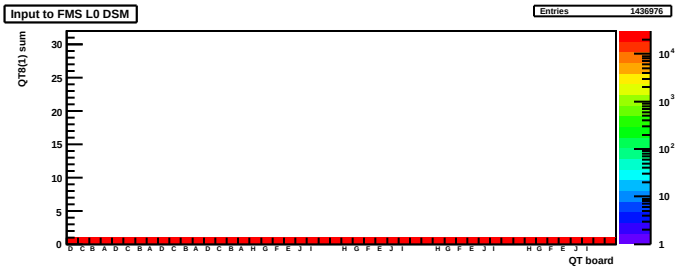

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

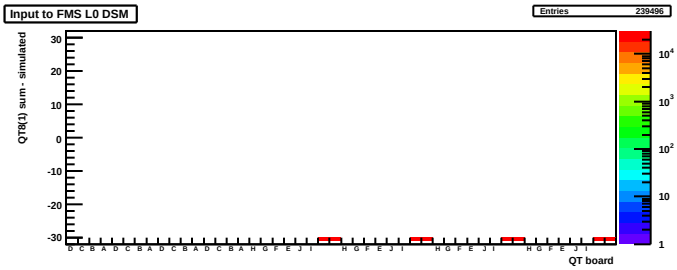

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

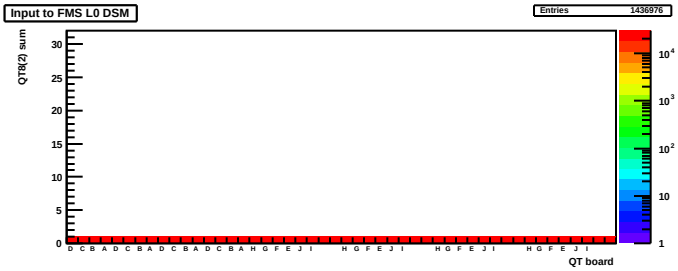

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

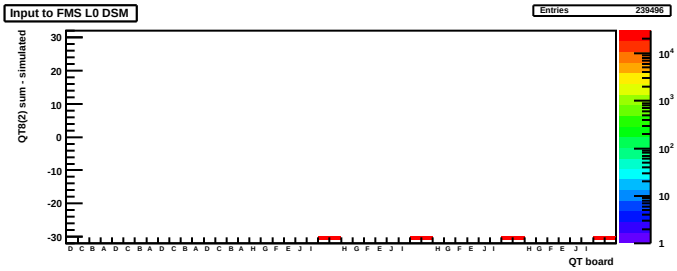

Input to FMS L0 DSM
Entries 1436976
QT8(0) sum
QT board

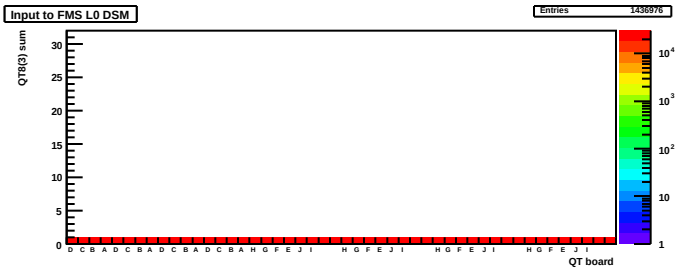

Input to FMS L0 DSM
Entries 239496
QT8(0) sum - simulated
QT board

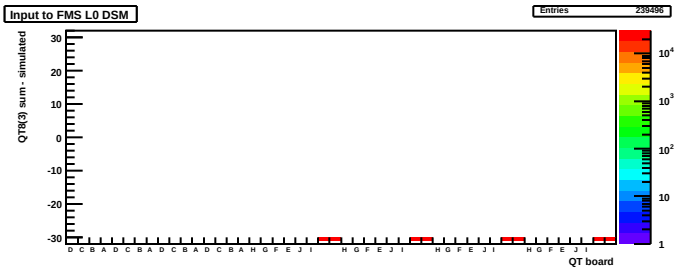

Input to FMS L0 DSM
Entries 1436976
QT8(1) sum
QT board

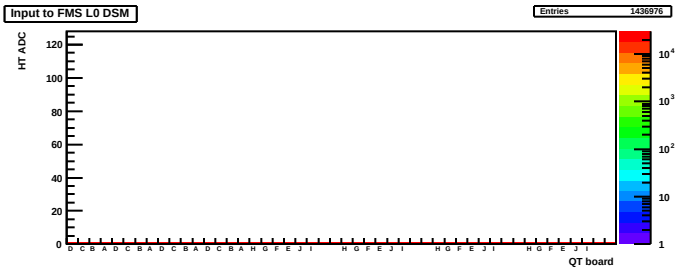

Input to FMS L0 DSM
Entries 239496
QT8(1) sum - simulated
QT board

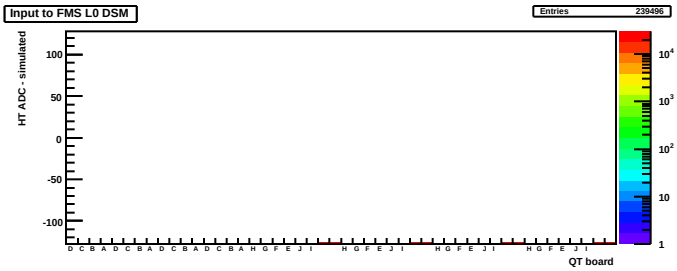

Input to FMS L0 DSM
Entries 1436976
QT8(2) sum
QT board

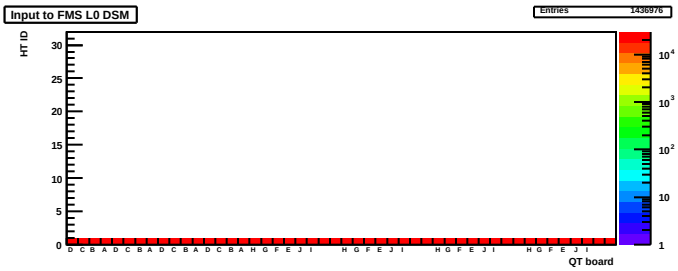

Input to FMS L0 DSM
Entries 239496
QT8(2) sum - simulated
QT board

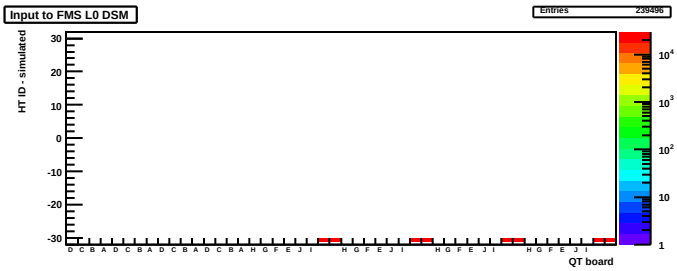

Input to FMS L0 DSM
Entries 1436976
QT8(3) sum
QT board

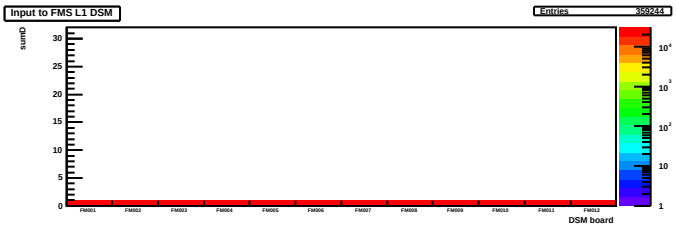

Input to FMS L0 DSM
Entries 239496
QT8(3) sum - simulated
QT board

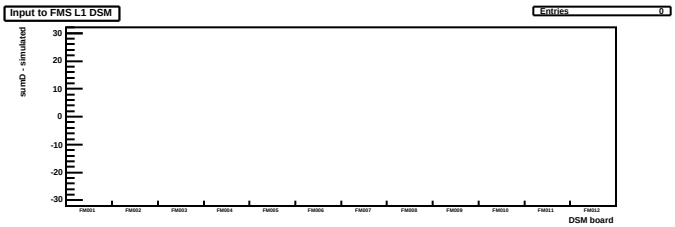

Input to FMS L0 DSM
Entries 1436976
HT ADC
QT board

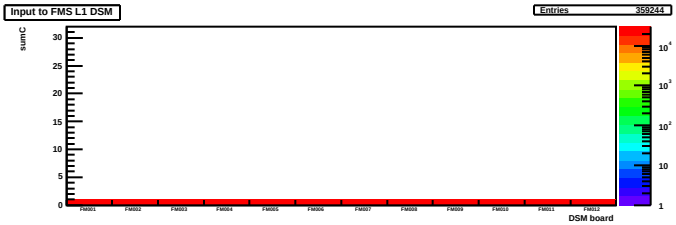

Input to FMS L0 DSM
Entries 239496
HT ADC - simulated
QT board

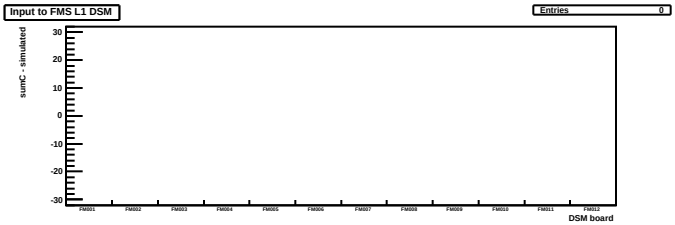

Input to FMS L0 DSM
Entries 1436976
HT ID
QT board

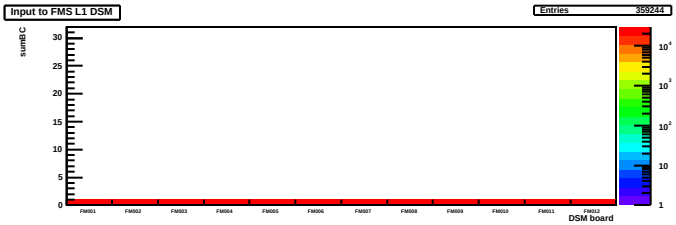

Input to FMS L0 DSM
Entries 239496
HT ID - simulated
QT board

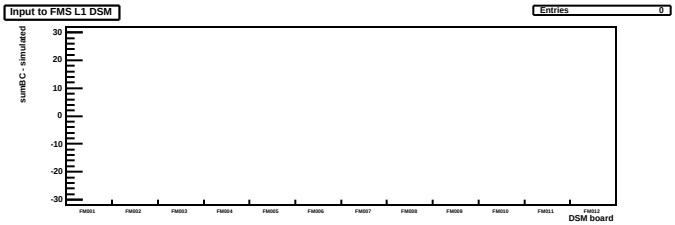

Input to FMS L1 DSM
Entries 359244
sumD
DSM board

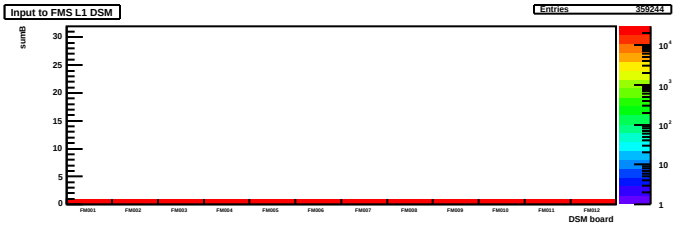

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

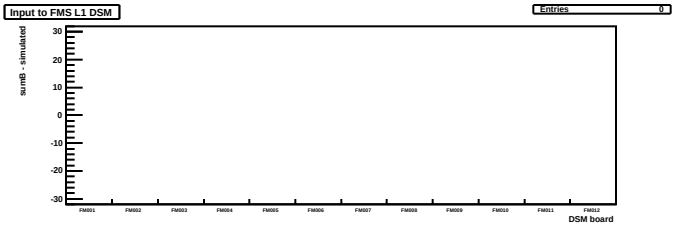

Input to FMS L1 DSM
Entries 359244
sumC
DSM board

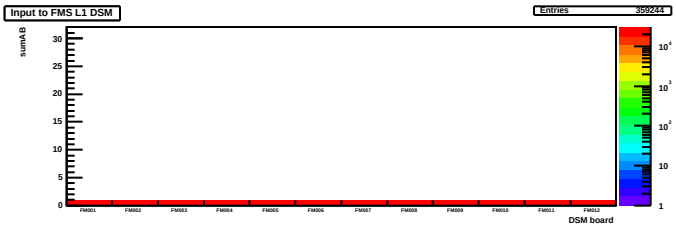

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

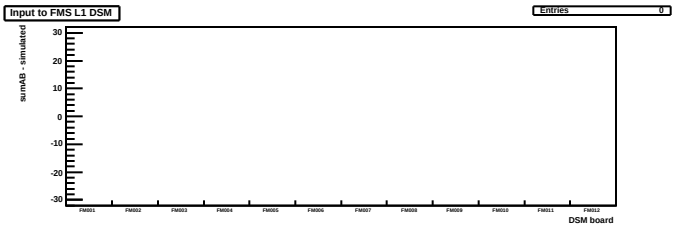

Input to FMS L1 DSM
Entries 359244
sumBC
DSM board

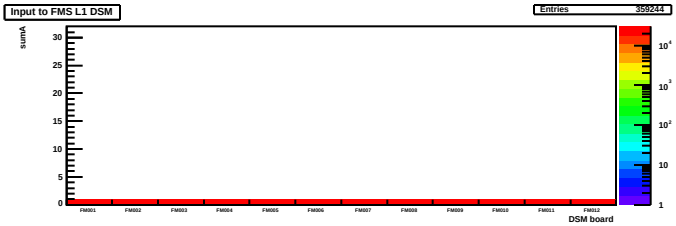

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

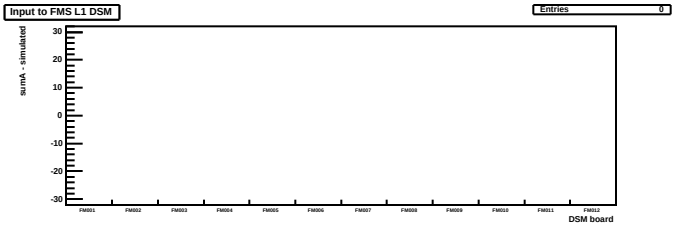

Input to FMS L1 DSM
Entries 359244
sumB
DSM board

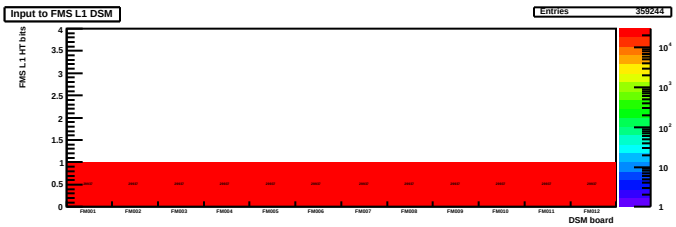

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

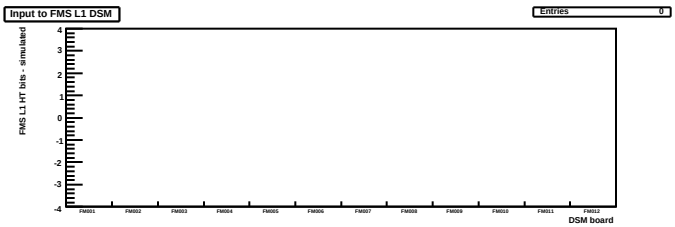

Input to FMS L1 DSM
Entries 359244
sumAB
DSM board

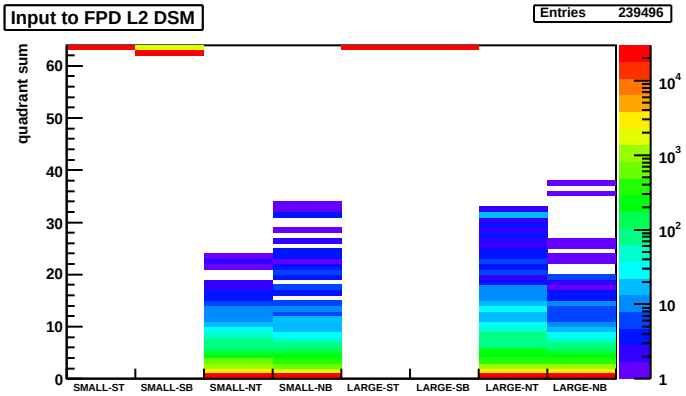

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

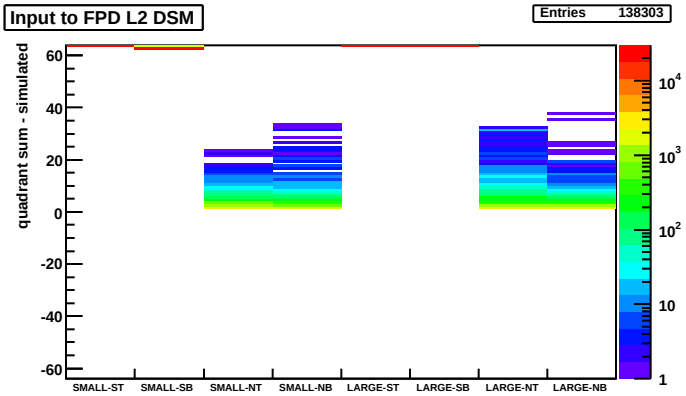

Input to FMS L1 DSM
Entries 359244
sumA
DSM board

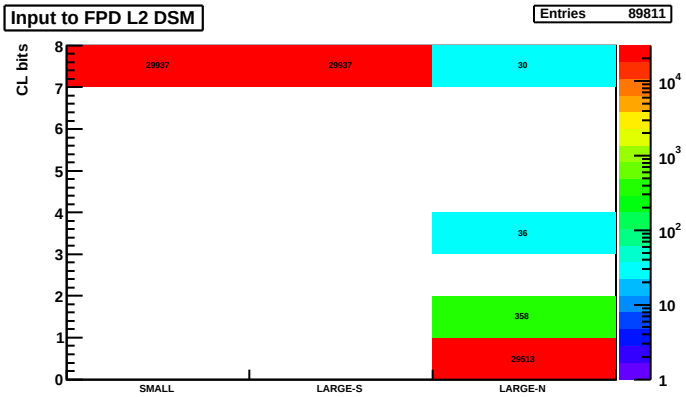

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

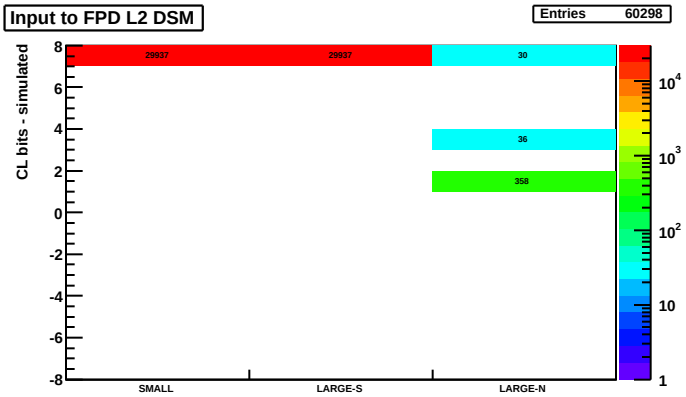

Input to FMS L1 DSM
Entries 359244
FMS L1 HT bits
DSM board

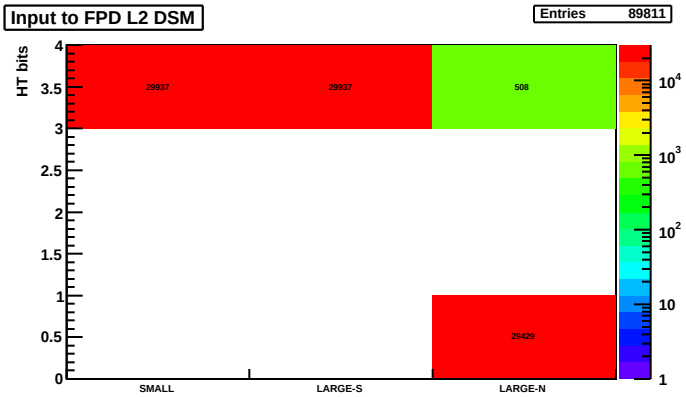

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

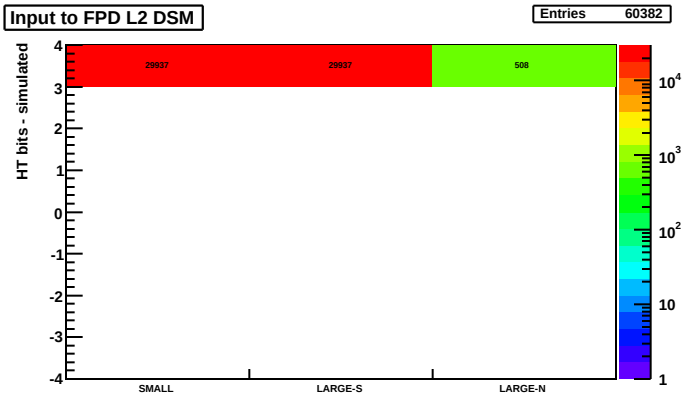

Input to FPD L2 DSM
Entries 239496
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

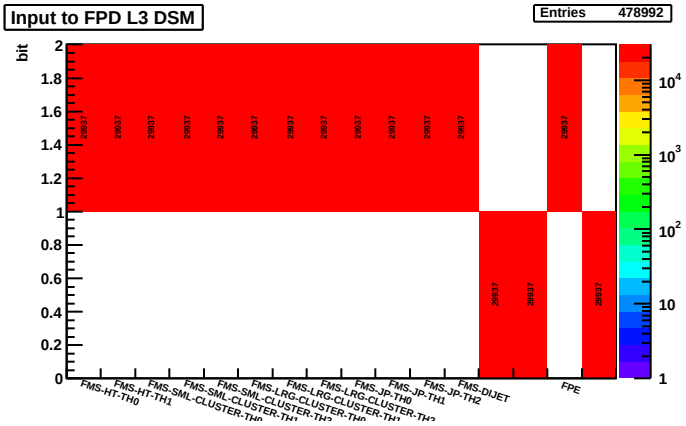

Input to FPD L2 DSM
Entries 138303
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

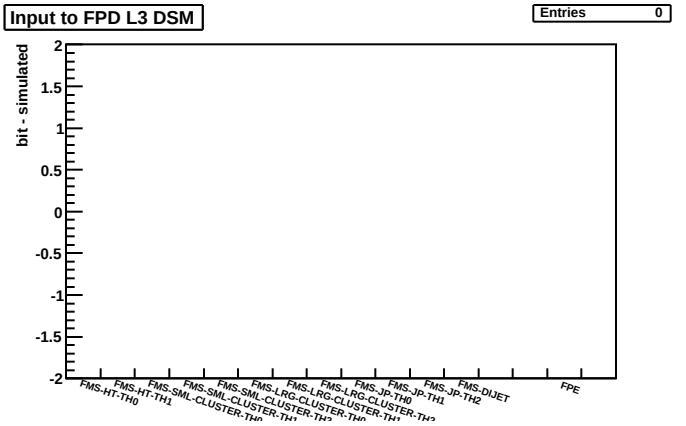

Input to FPD L2 DSM
Entries 89811
CL bits
29937
29937
30
36
358
29513
SMALL
LARGE-S
LARGE-N

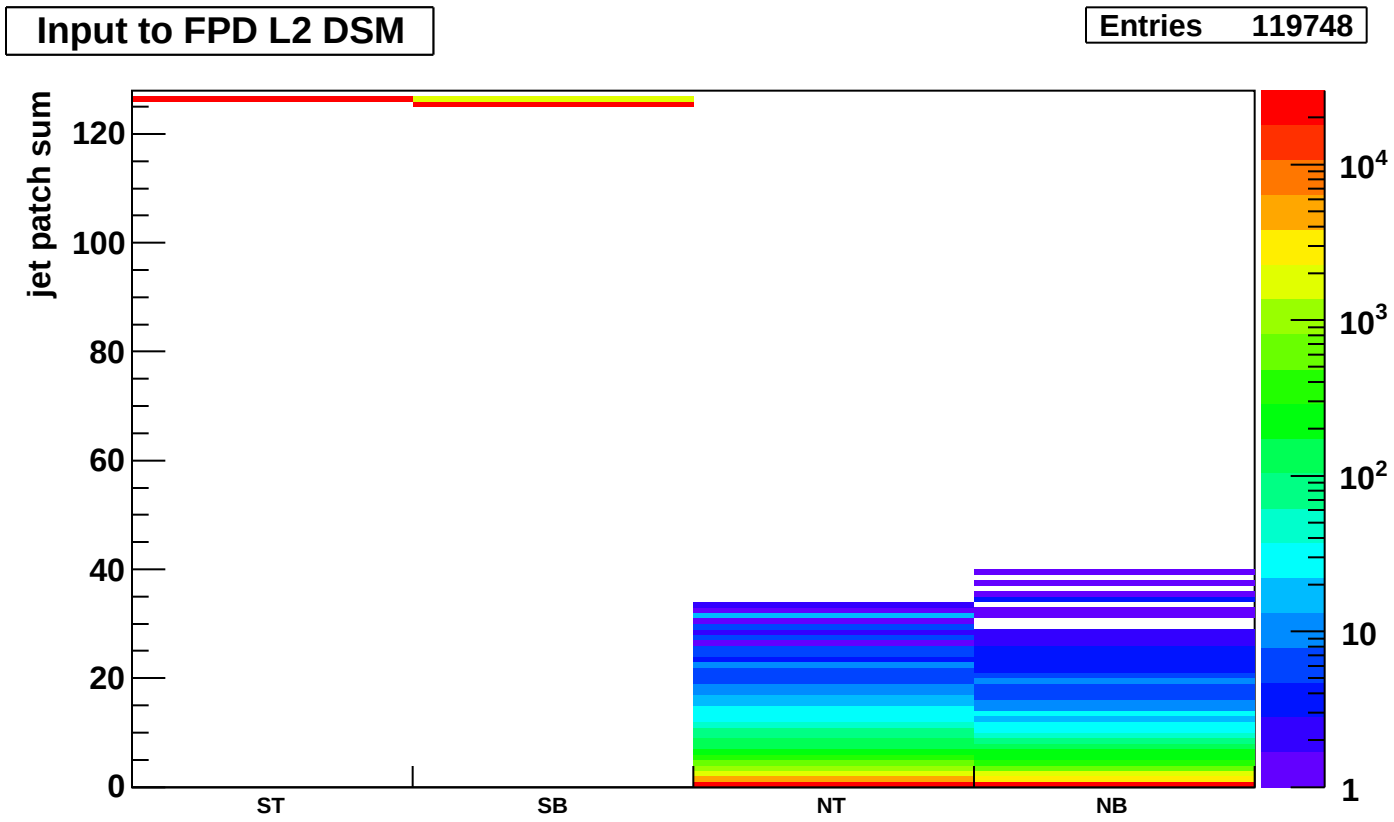

Input to FPD L2 DSM
Entries 60298
CL bits - simulated
29937
29937
30
36
358
SMALL
LARGE-S
LARGE-N

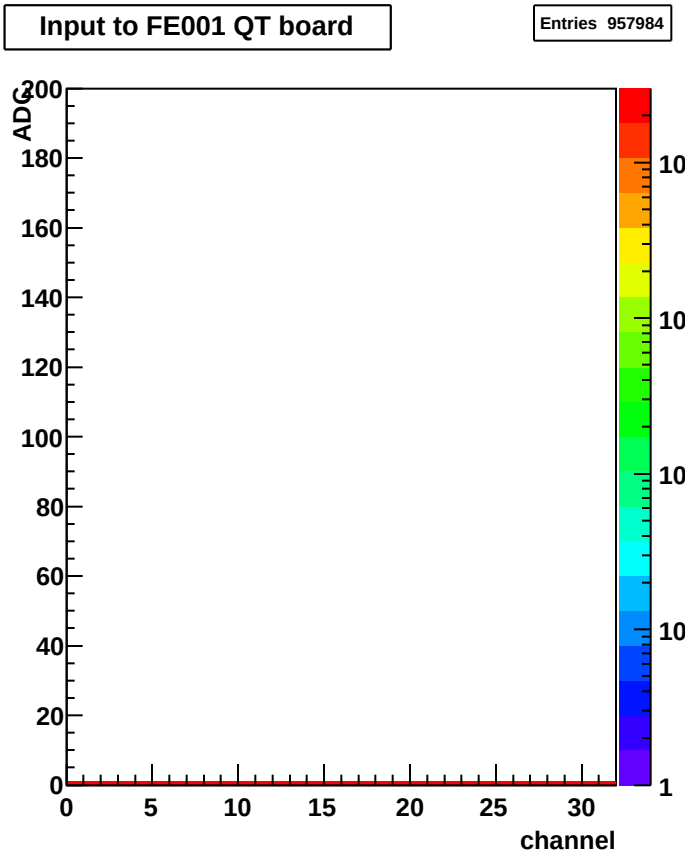

Input to FPD L2 DSM
Entries 89811
HT bits
29937
29937
508
29429
SMALL
LARGE-S
LARGE-N

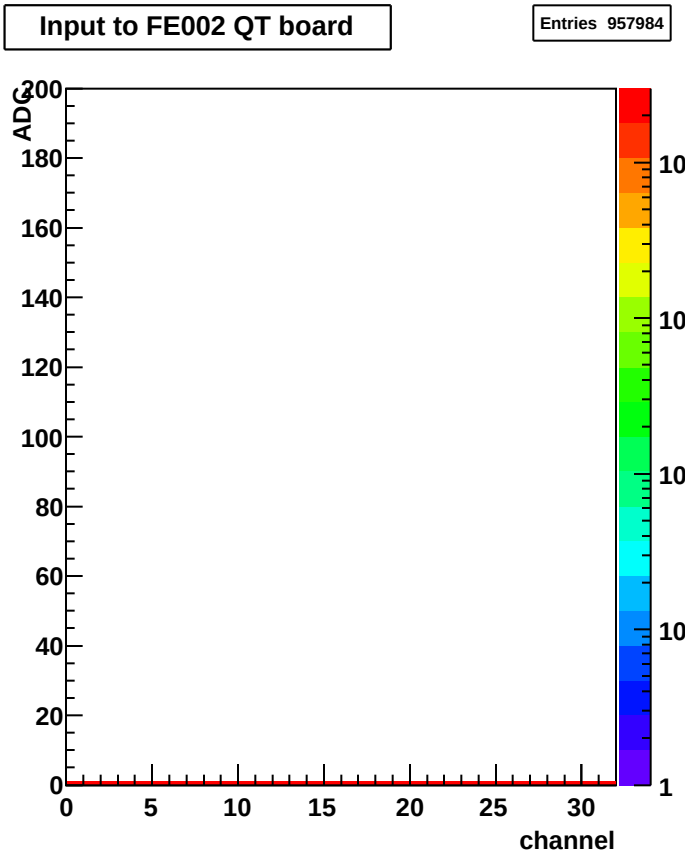

Input to FPD L2 DSM
Entries 60382
HT bits - simulated
29937
29937
508
SMALL
LARGE-S
LARGE-N

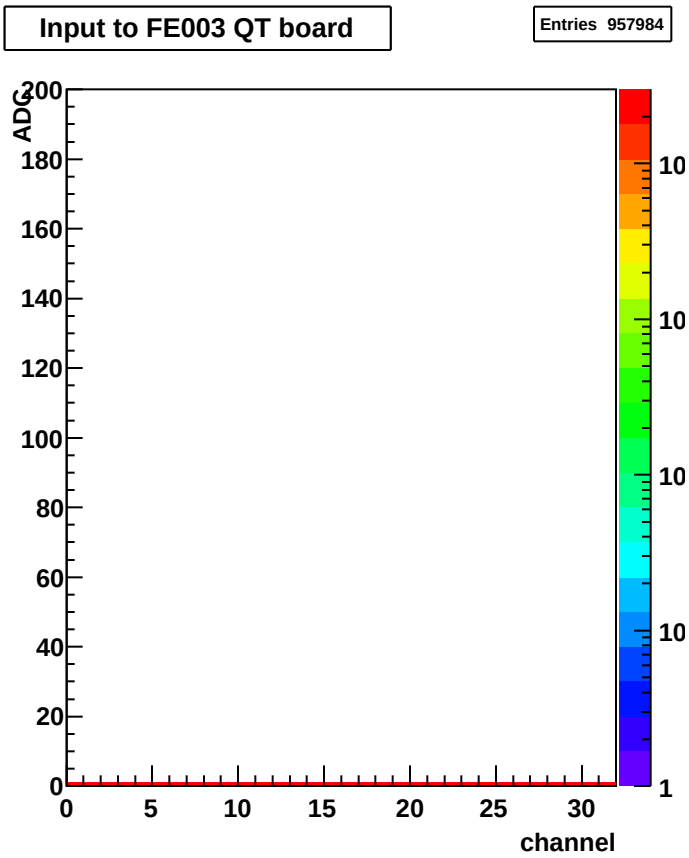

Input to FPD L3 DSM
Entries 478992
bit
29937
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

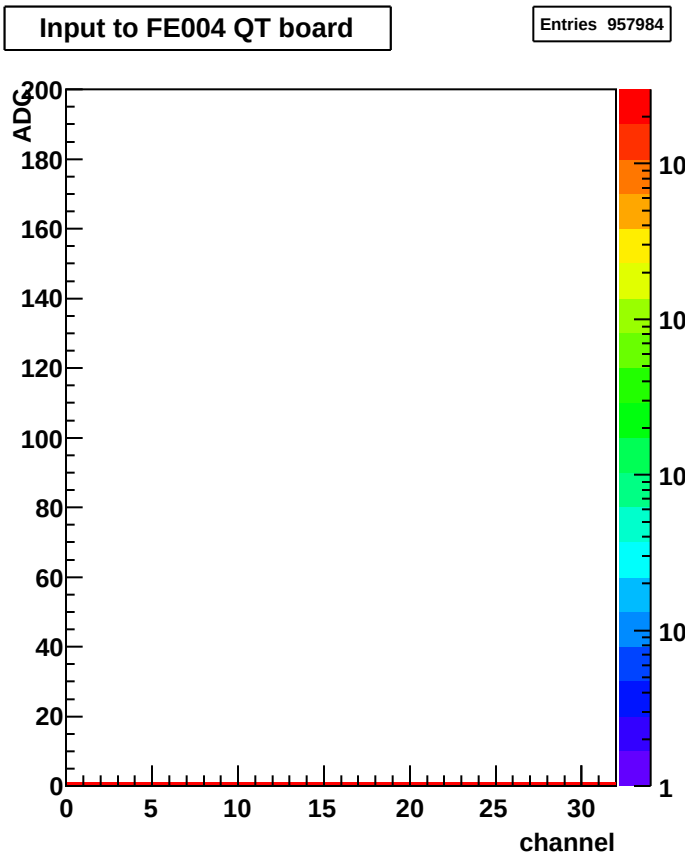

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

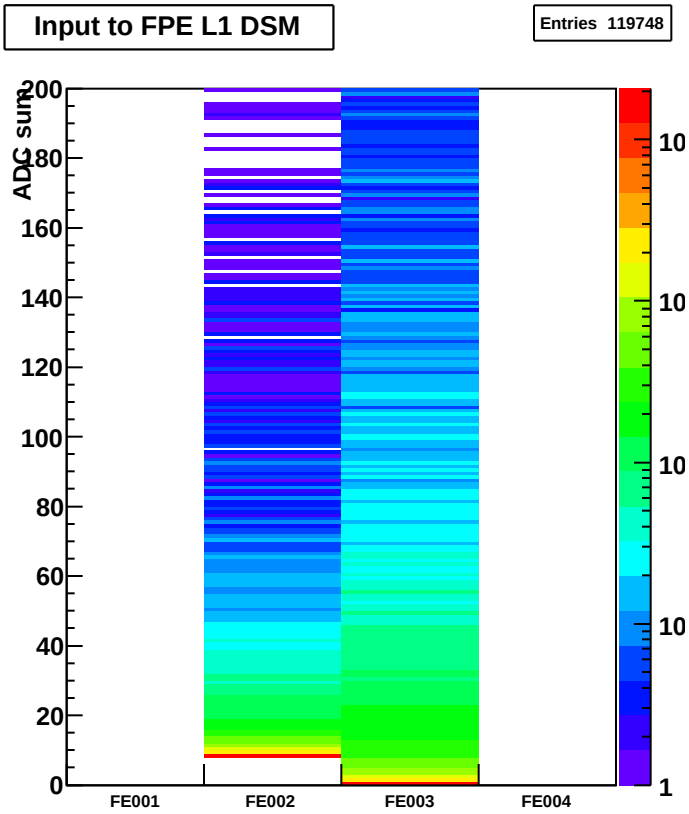

Input to FPD L2 DSM
Entries 119748
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

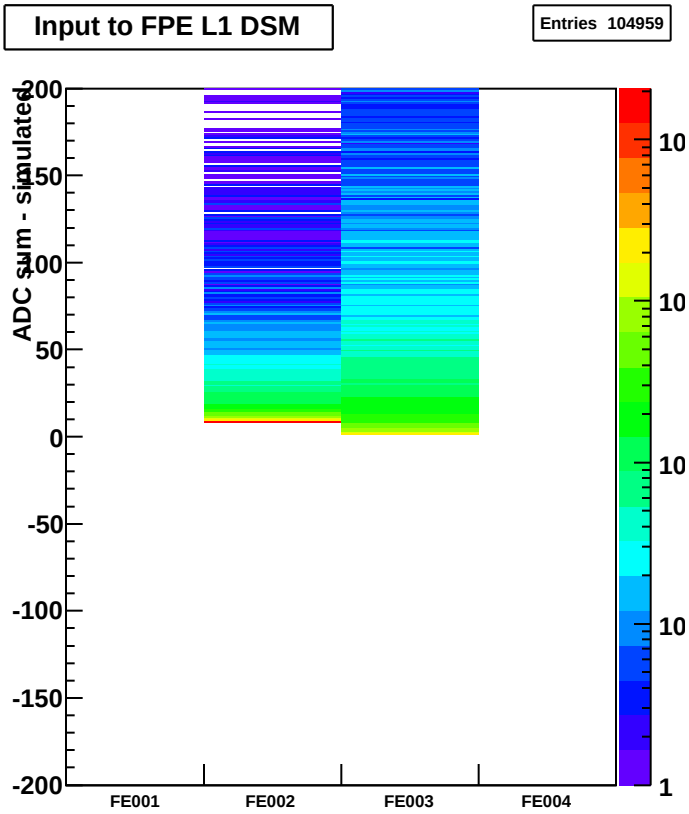

Input to FE001 QT board
Entries 957984
ADC
channel

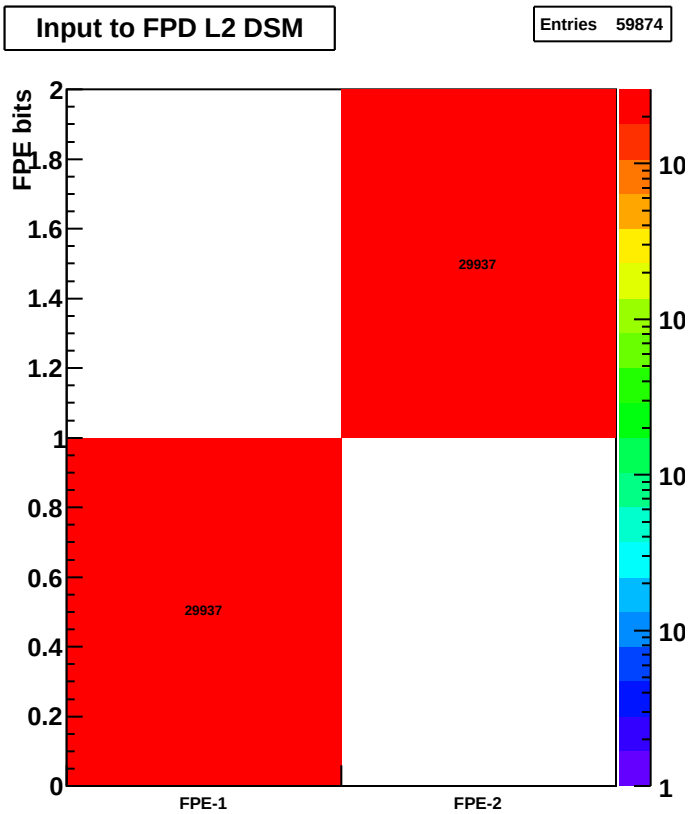

Input to FE002 QT board
Entries 957984
ADC
channel

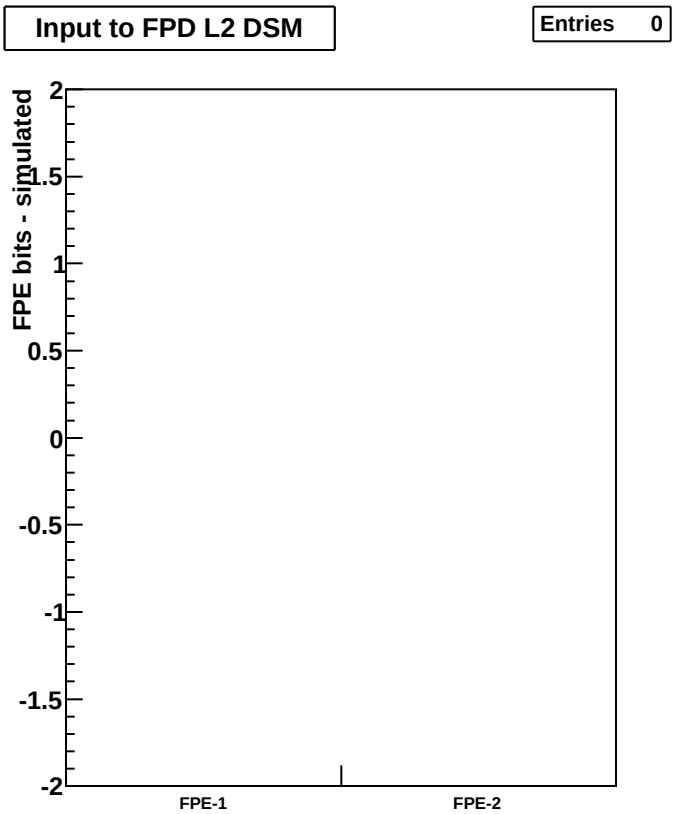

Input to FE003 QT board
Entries 957984
ADC
channel


Input to FE004 QT board
Entries 957984
ADC
channel


Input to FPE L1 DSM
Entries 119748
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 104959
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 59874
FPE bits
29937
29937
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2