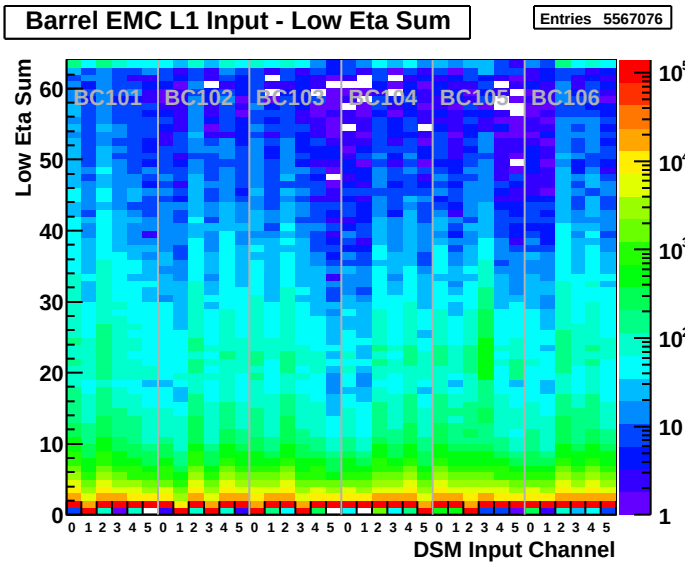
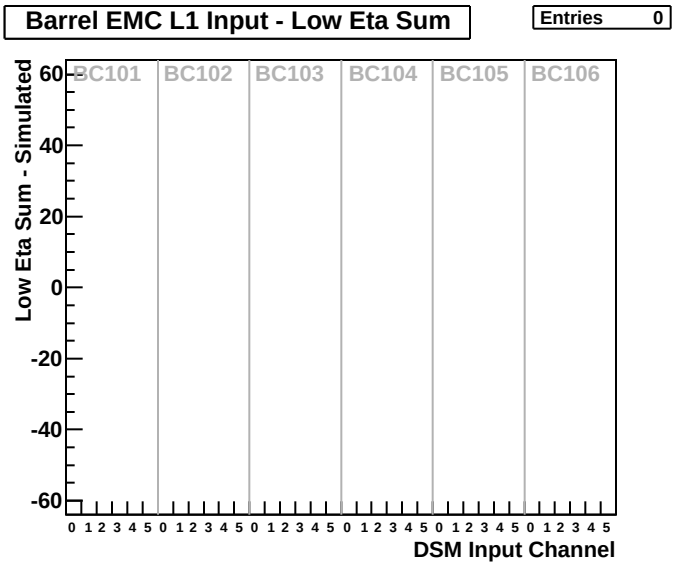
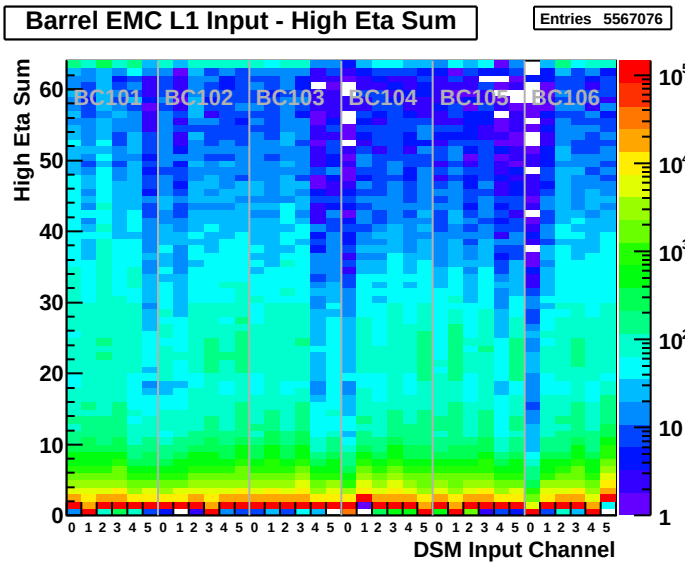
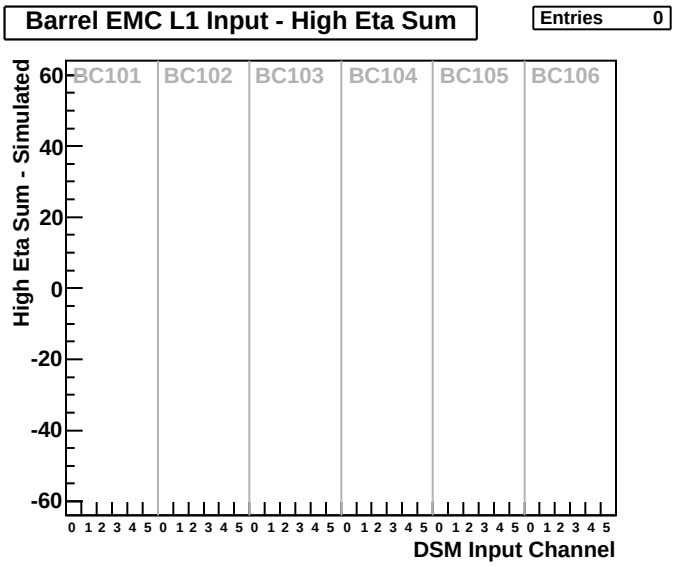
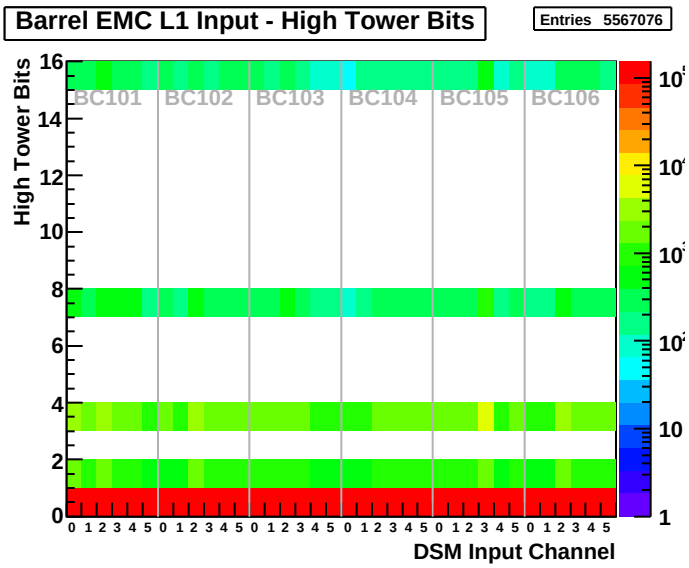
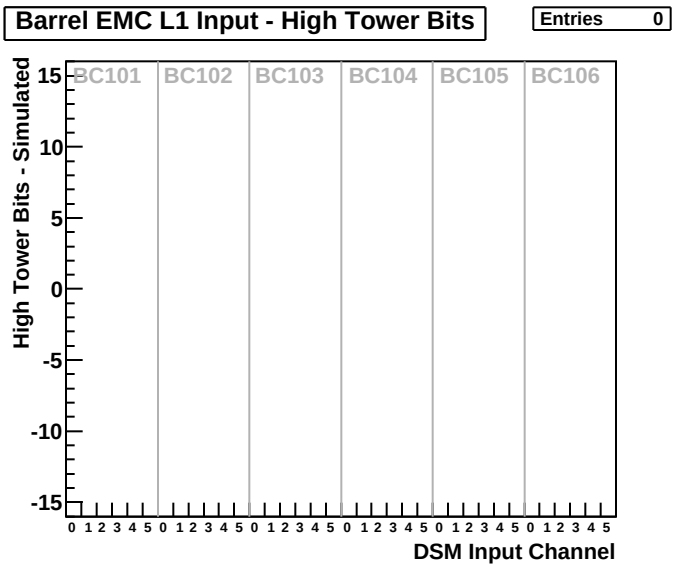

Barrel EMC L1 Input - Low Eta Sum
Entries 5567076
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

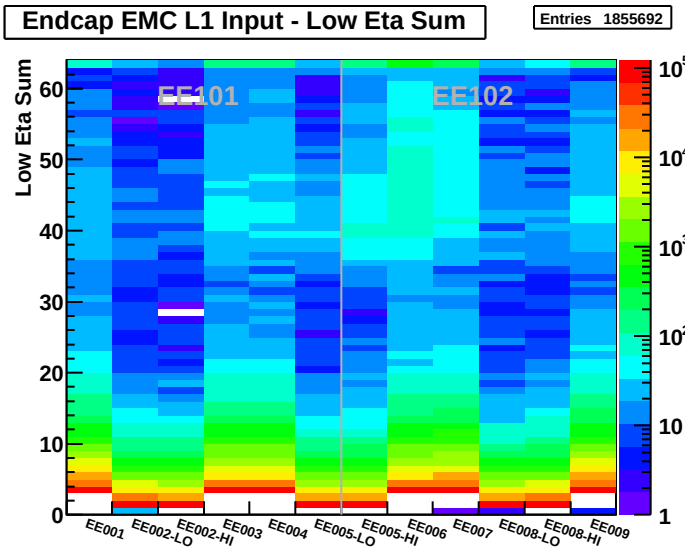

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

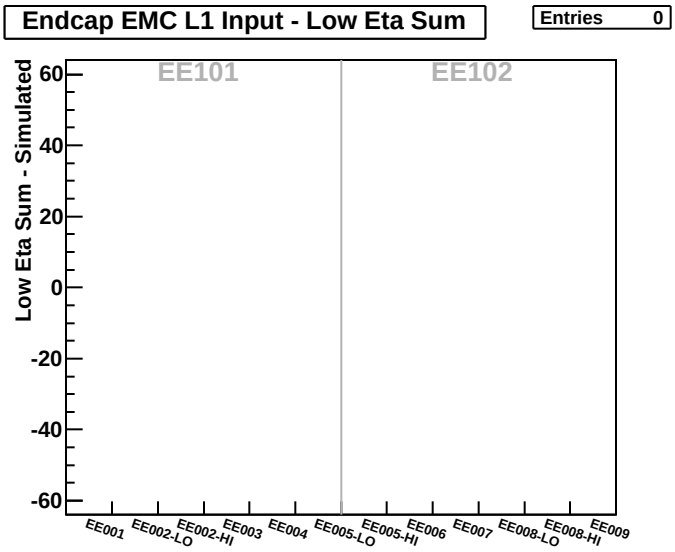

Barrel EMC L1 Input - High Eta Sum
Entries 5567076
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

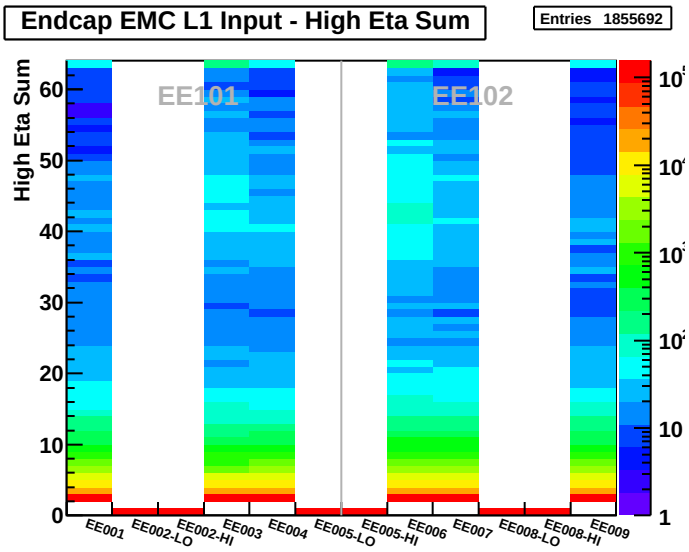

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

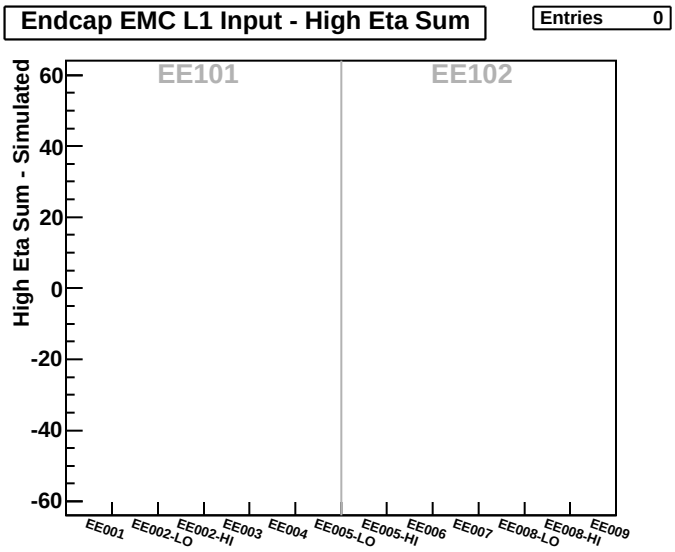

Barrel EMC L1 Input - High Tower Bits
Entries 5567076
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

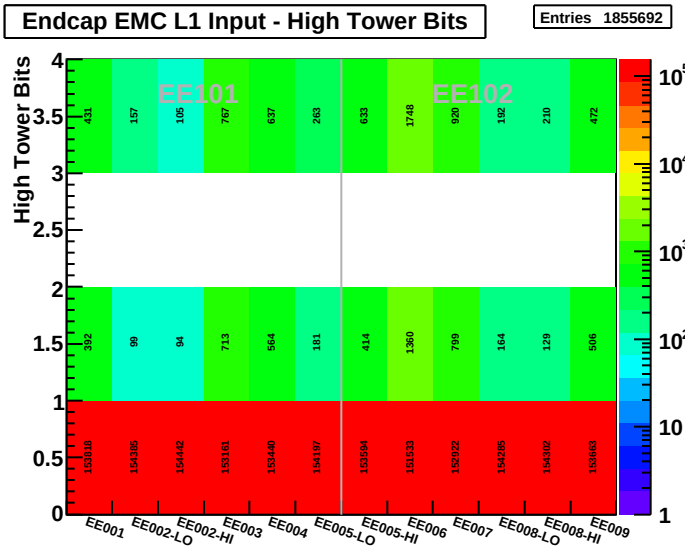

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

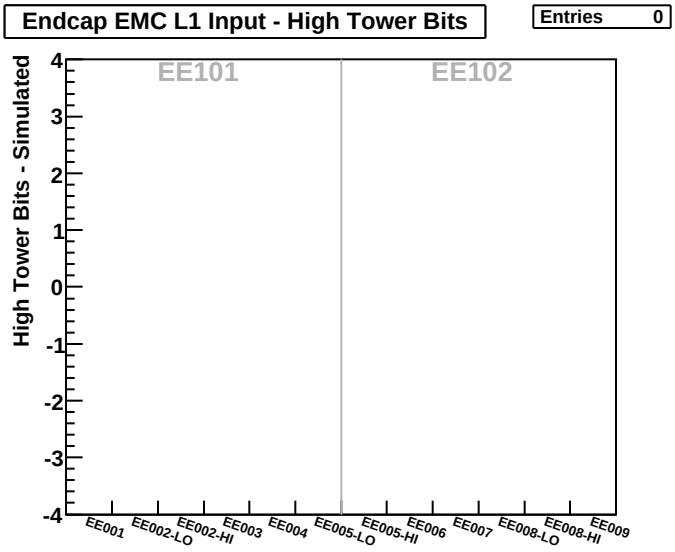

Endcap EMC L1 Input - Low Eta Sum
Entries 1855692
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

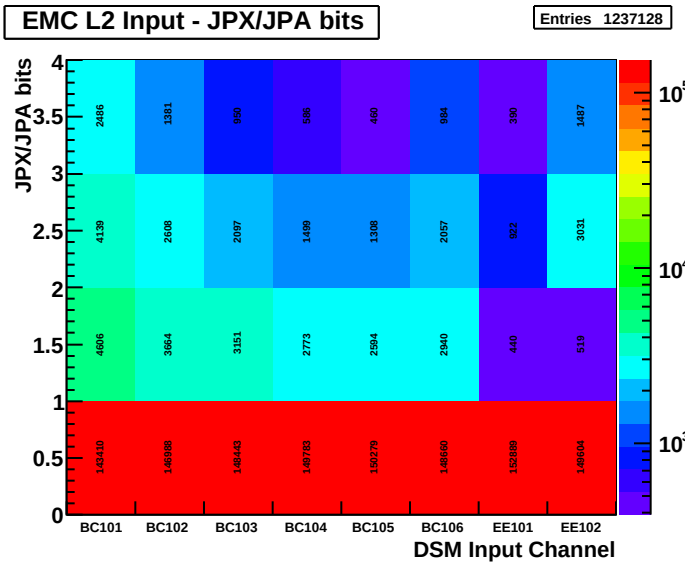

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

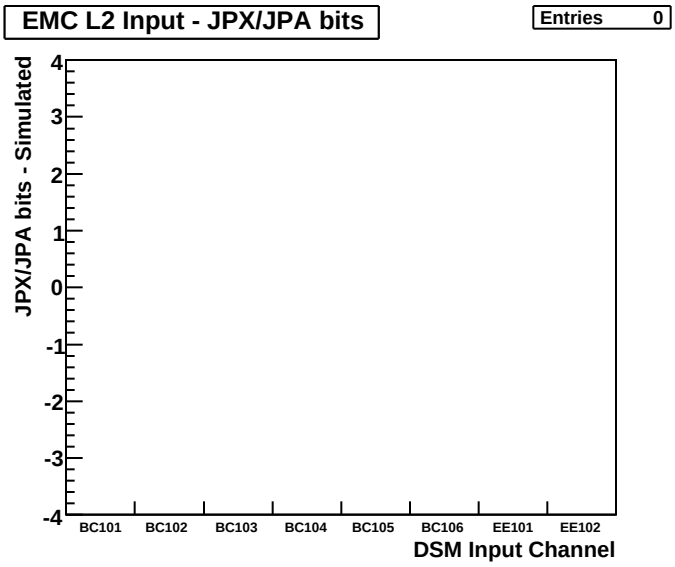

Endcap EMC L1 Input - High Eta Sum
Entries 1855692
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

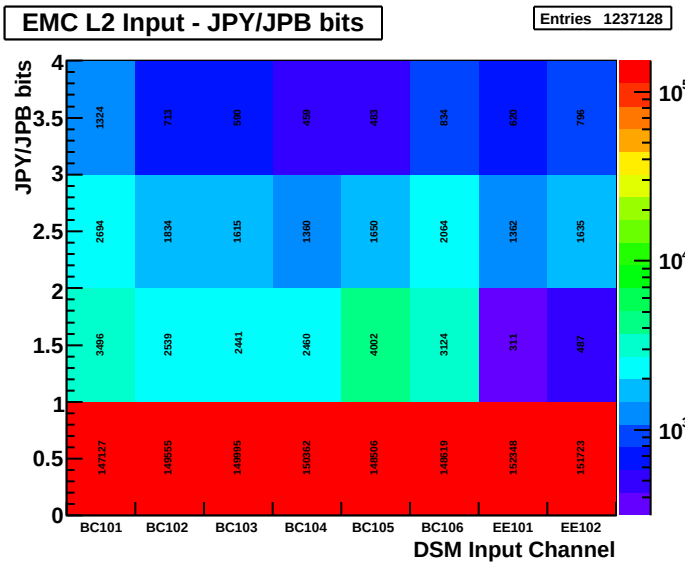

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

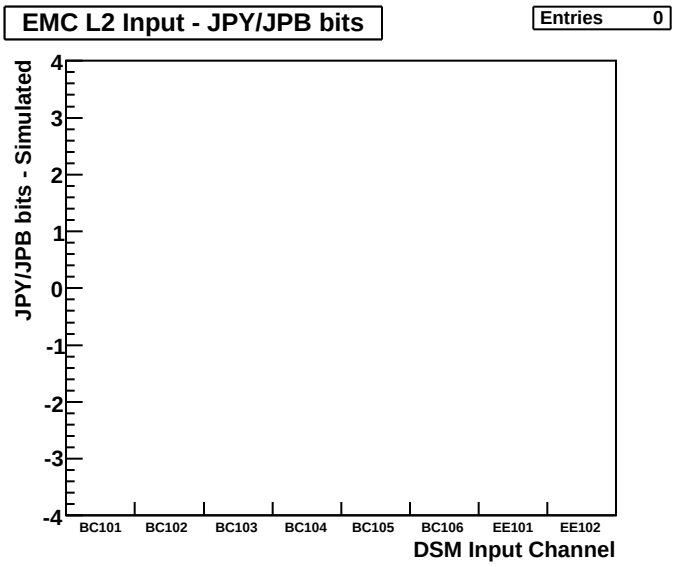

Endcap EMC L1 Input - High Tower Bits
Entries 1855692
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

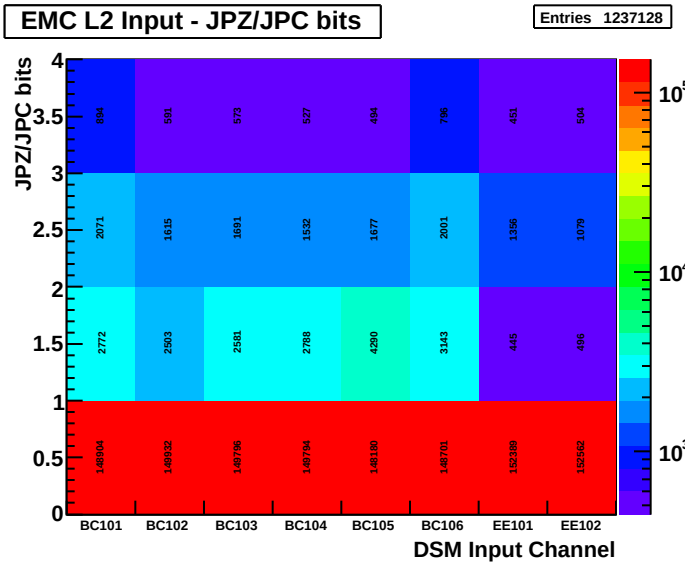

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

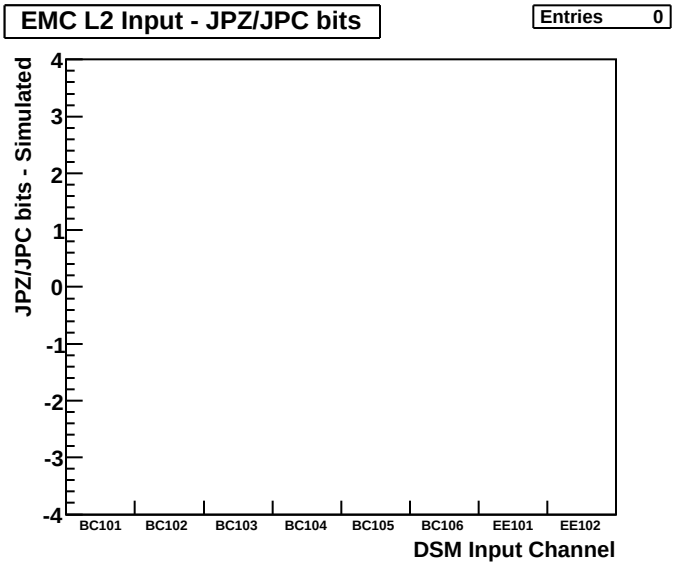

EMC L2 Input - JPX/JPA bits
Entries 1237128
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2486 1381 950 586 460 984 390 1487
4139 2608 2097 1499 1308 2057 922 3031
4606 3664 3151 2773 2594 2940 440 519
143410 146988 148443 149783 150279 148660 152889 149604

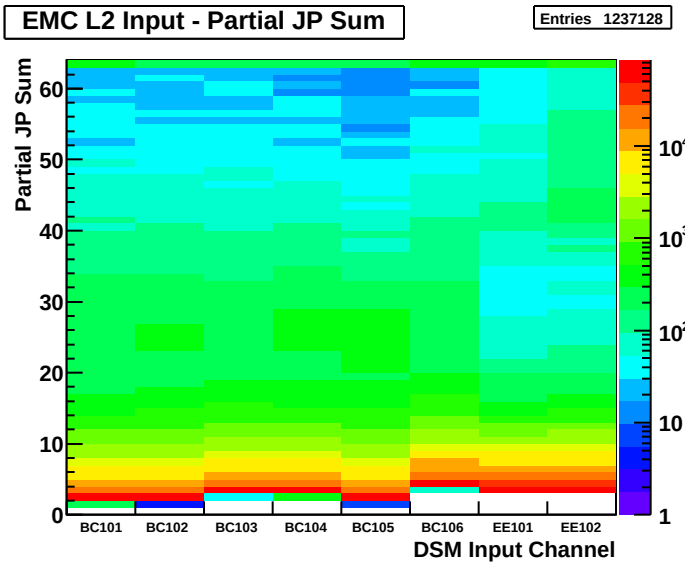

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

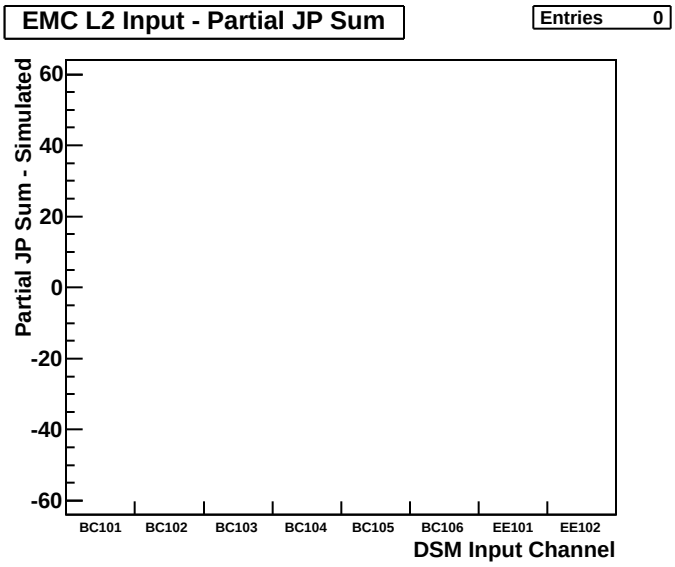

EMC L2 Input - JPY/JPB bits
Entries 1237128
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1324 713 590 459 483 834 620 796
2694 1834 1615 1360 1650 2064 1362 1635
3486 2539 2441 2460 4002 3124 311 437
147127 148555 148995 150362 148506 148619 152348 151723

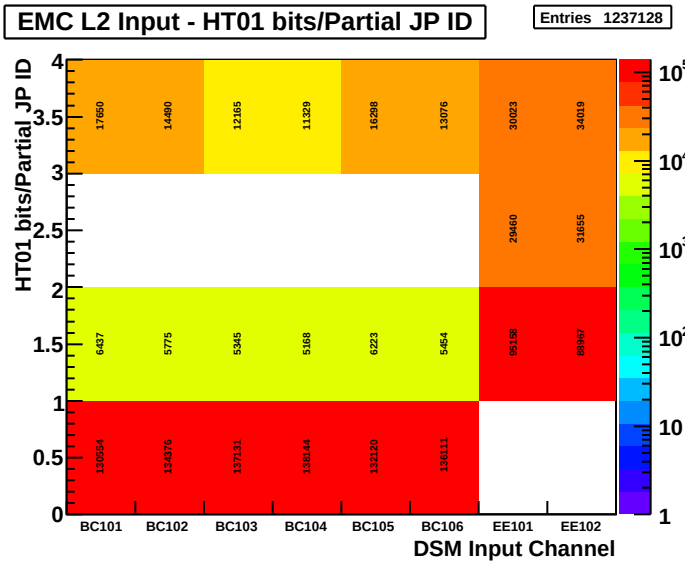

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

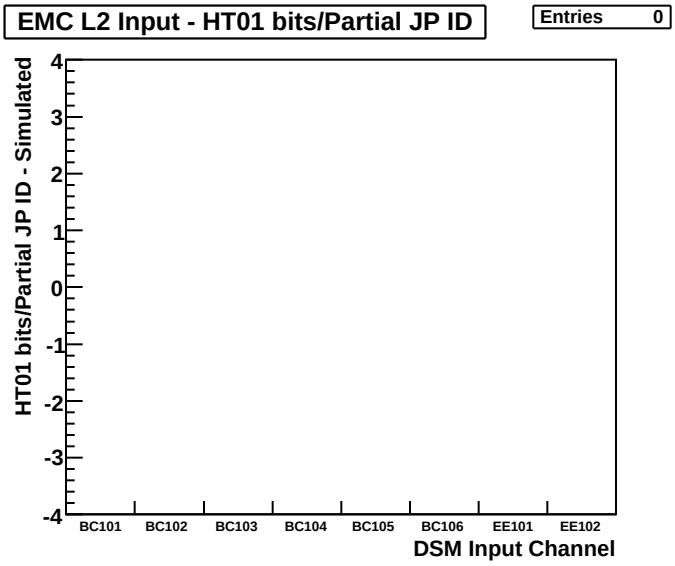

EMC L2 Input - JPZ/JPC bits
Entries 1237128
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
834 591 573 527 494 796 451 504
2071 1615 1691 1532 1677 2001 1356 1079
2772 2903 2581 2788 4290 3143 445 496
148904 149322 149796 149794 148180 148701 152389 152562

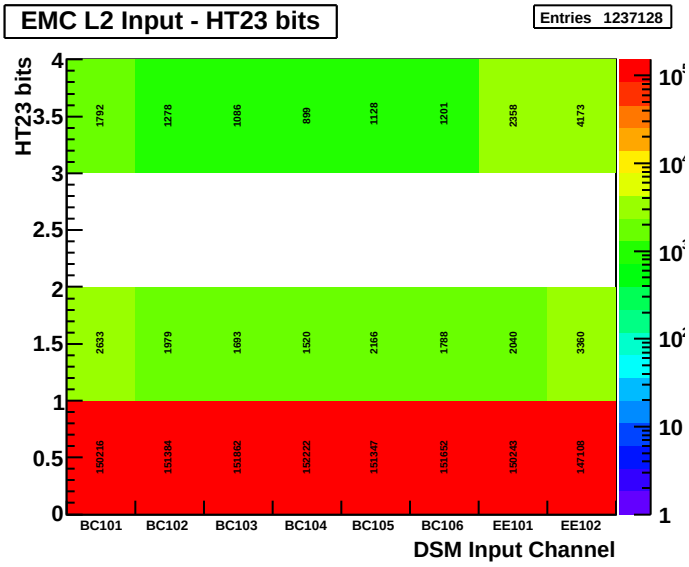

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

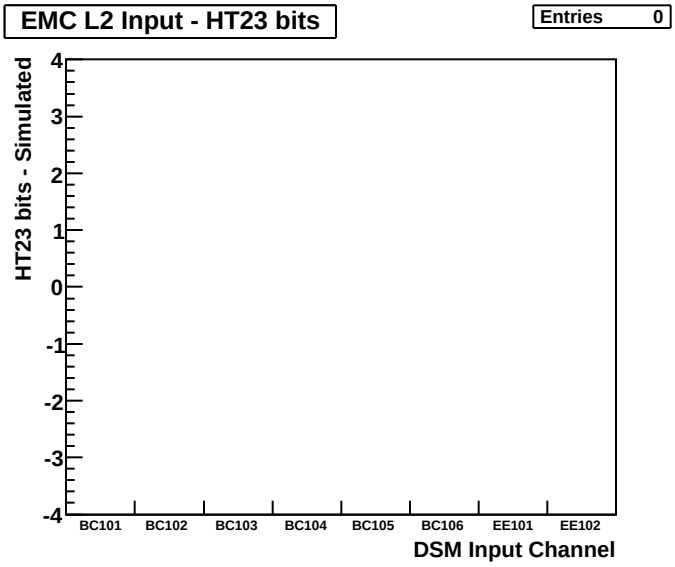

EMC L2 Input - Partial JP Sum
Entries 1237128
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

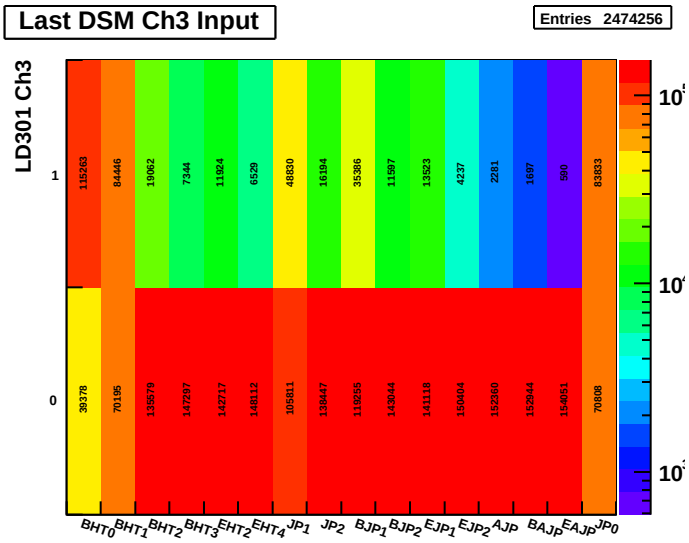

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

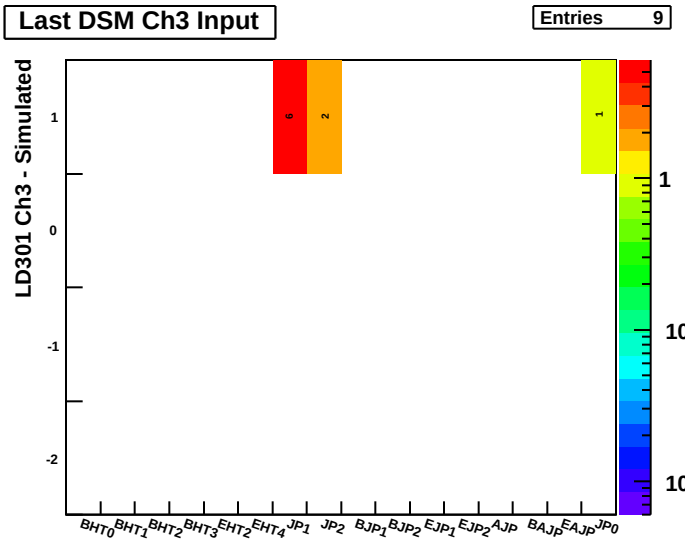

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1237128
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

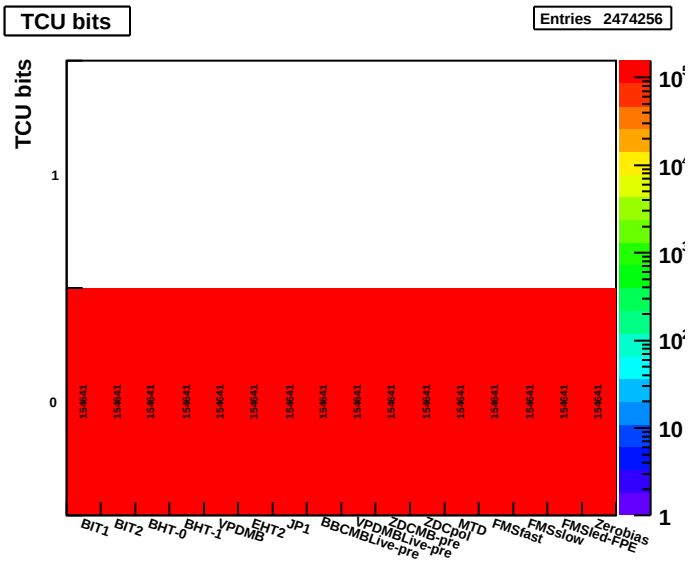

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

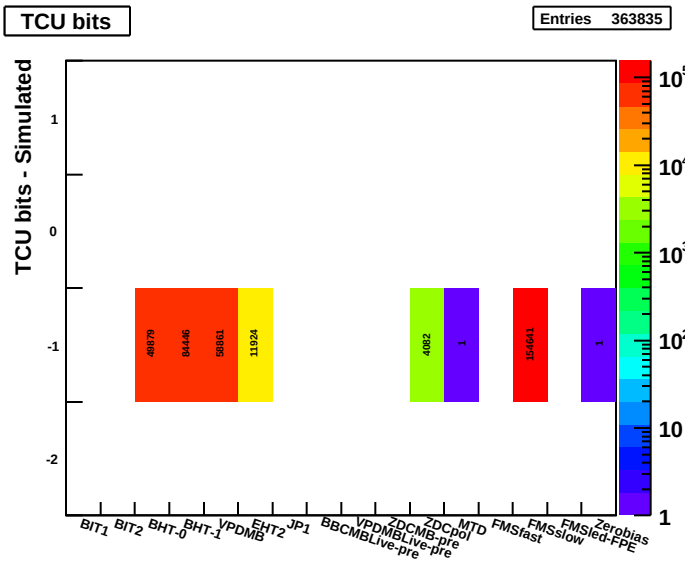

EMC L2 Input - HT23 bits
Entries 1237128
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

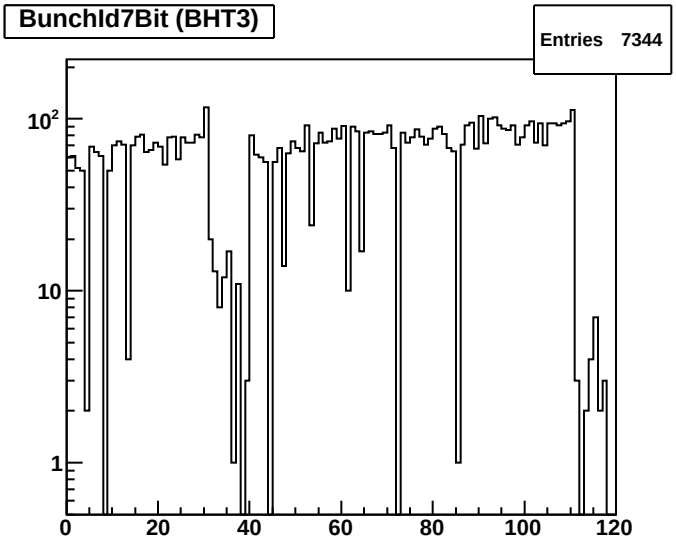

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

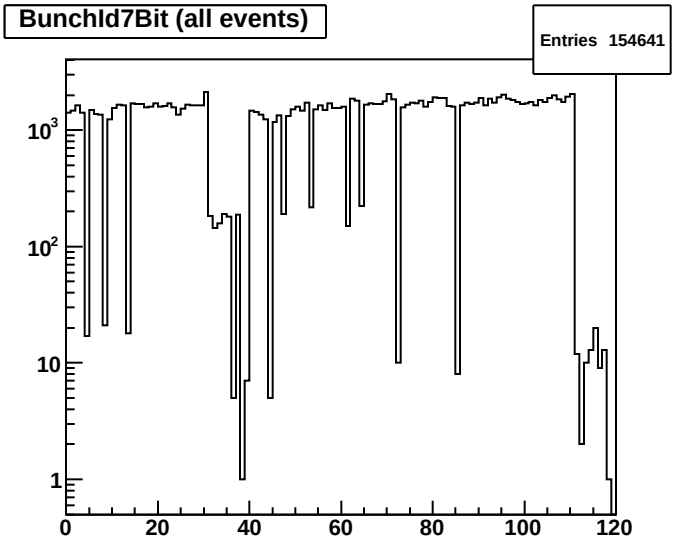

Last DSM Ch3 Input
Entries 2474256
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

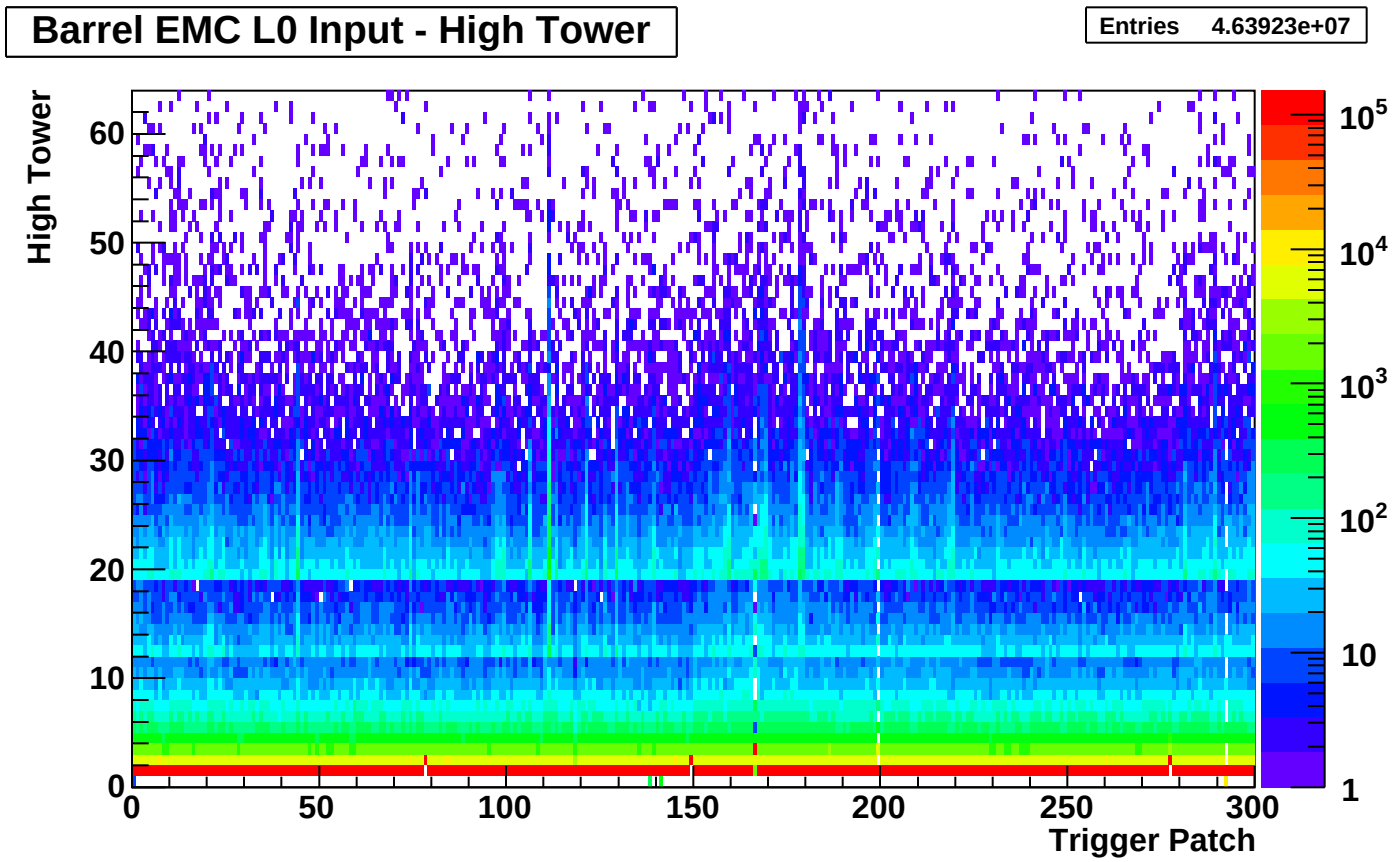

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

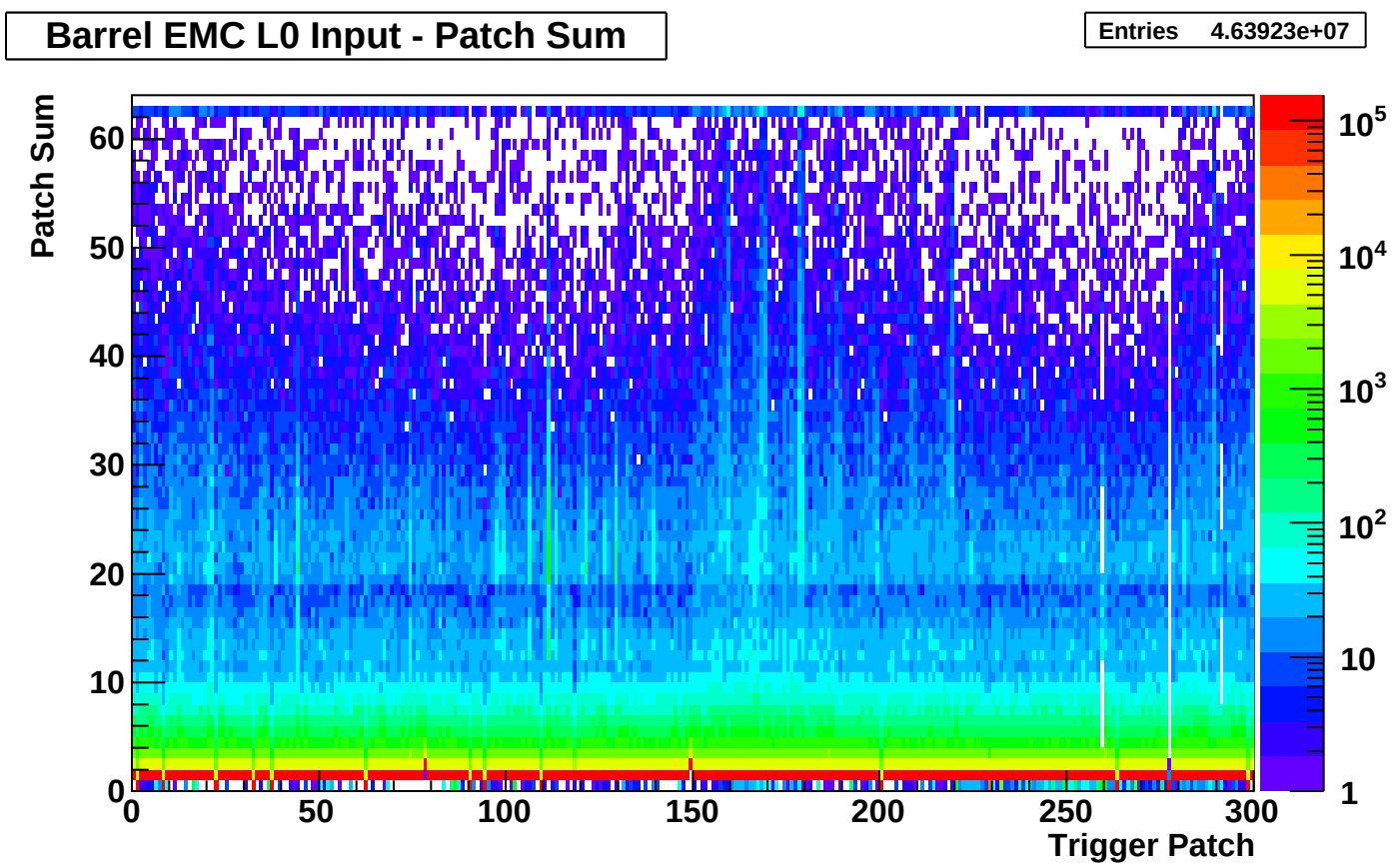

TCU bits
Entries 2474256
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

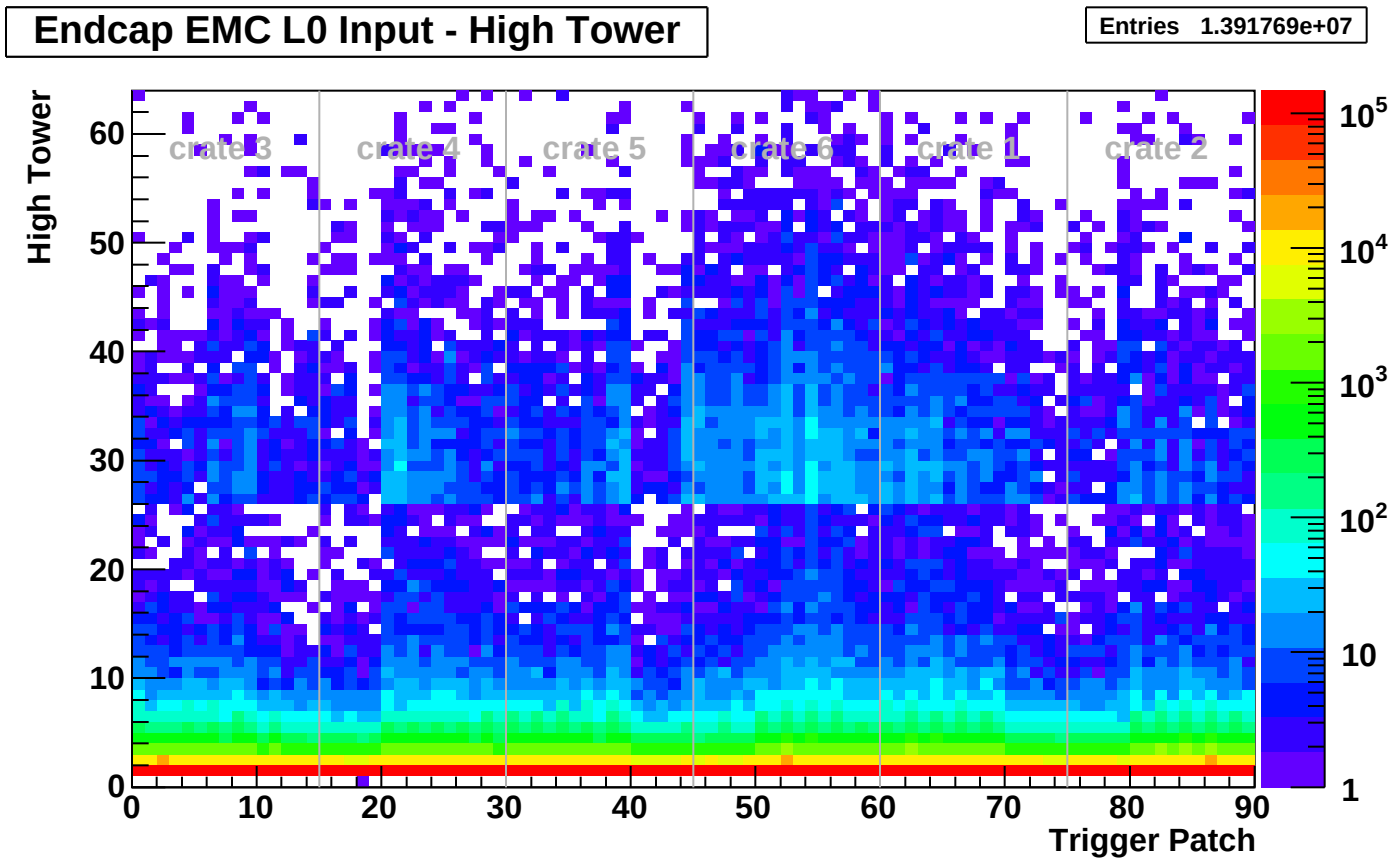

TCU bits
Entries 363835
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

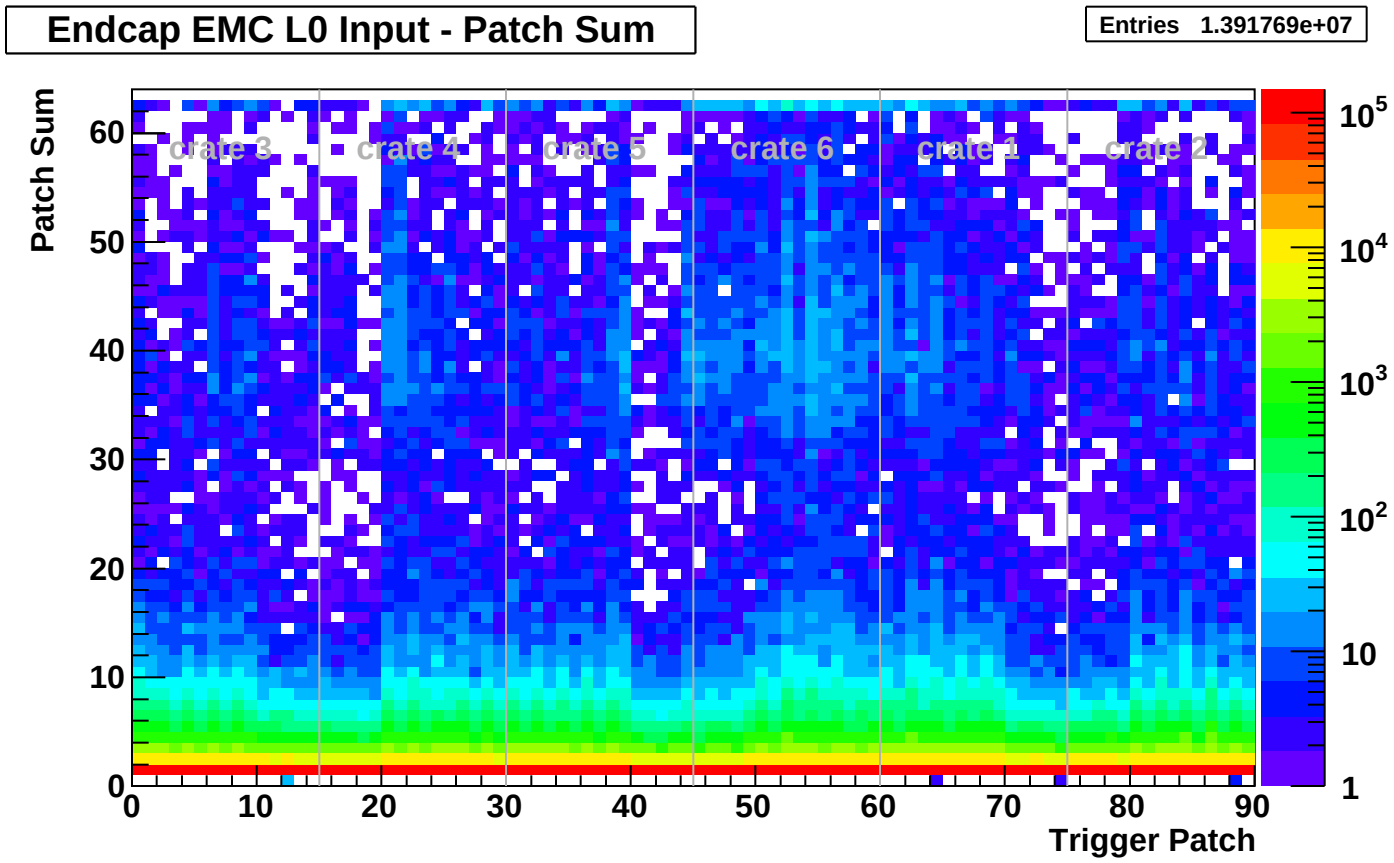

BunchId7Bit (BHT3)
Entries 7344

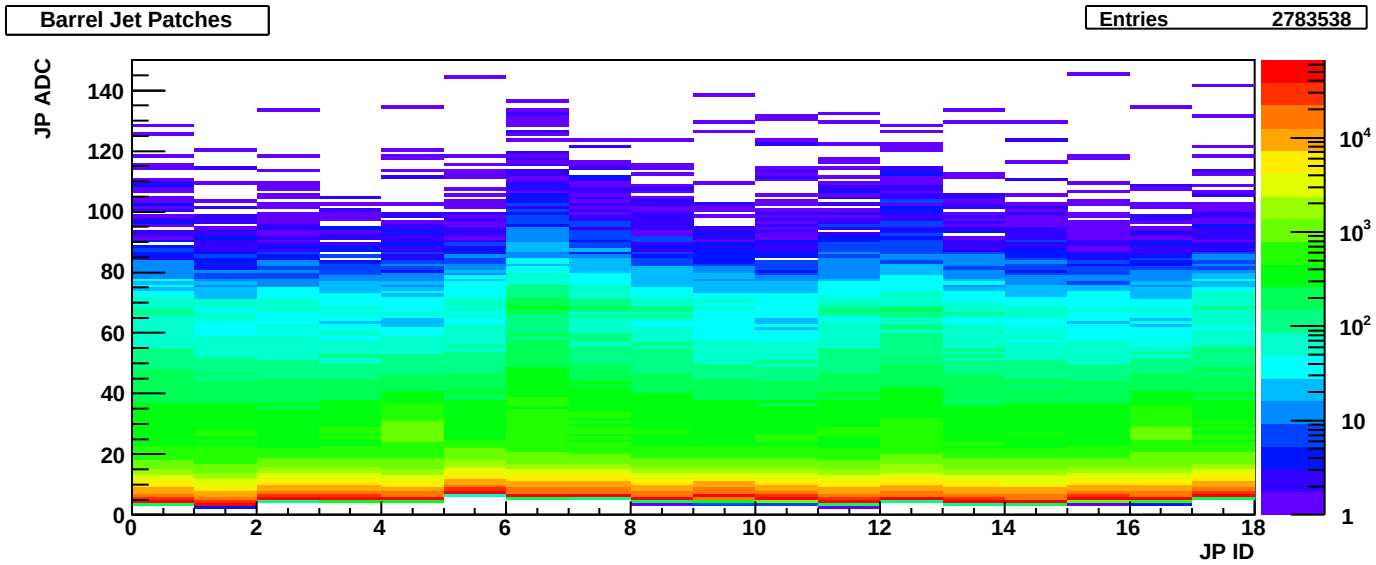

BunchId7Bit (all events)
Entries 154641

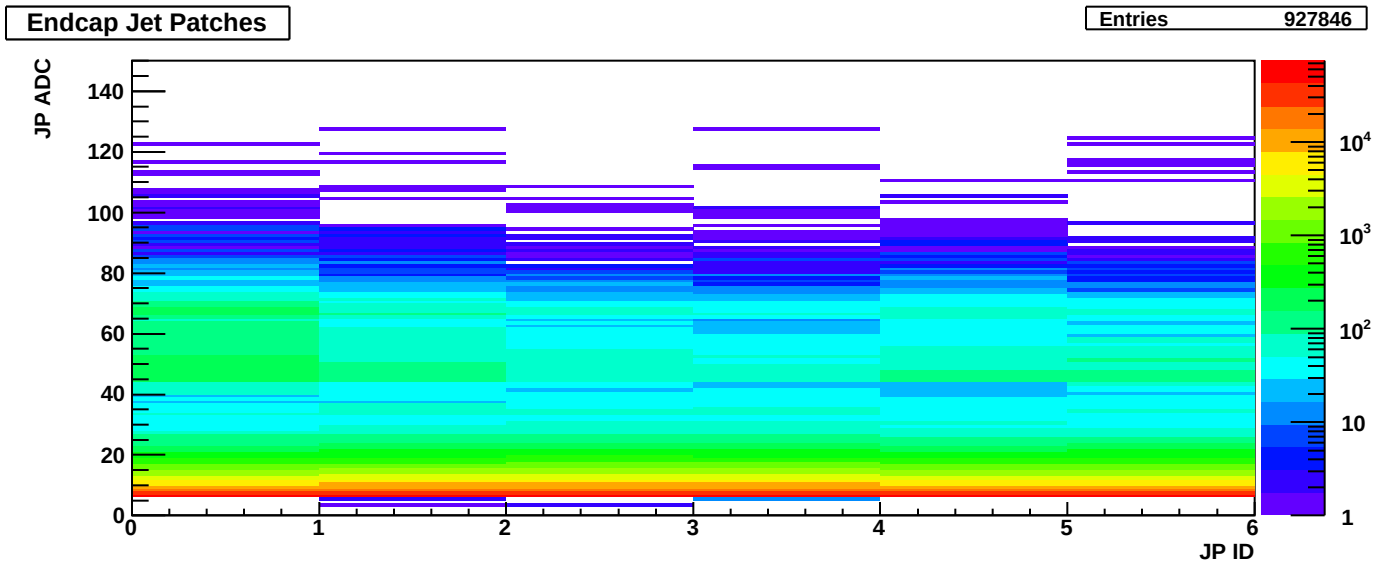

Barrel EMC L0 Input - High Tower
Entries 4.63923e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

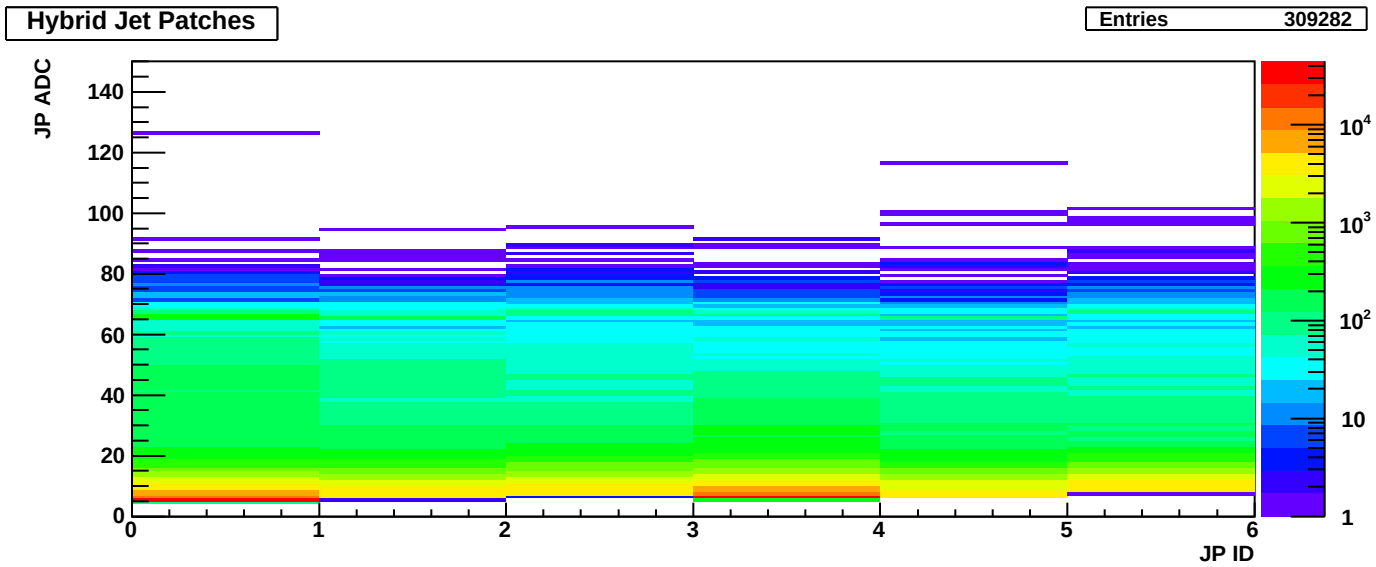

Barrel EMC L0 Input - Patch Sum
Entries 4.63923e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

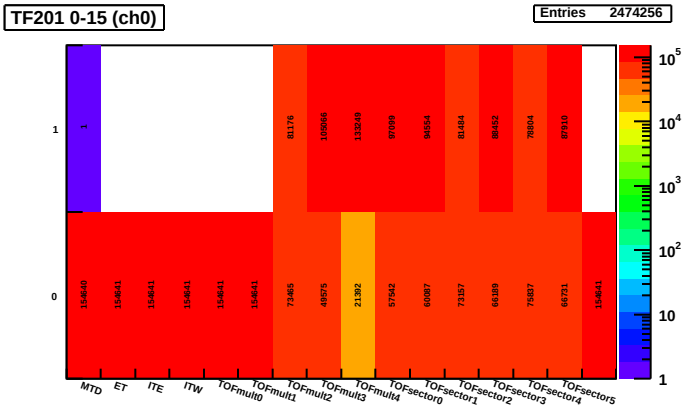

Endcap EMC L0 Input - High Tower
Entries 1.391769e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

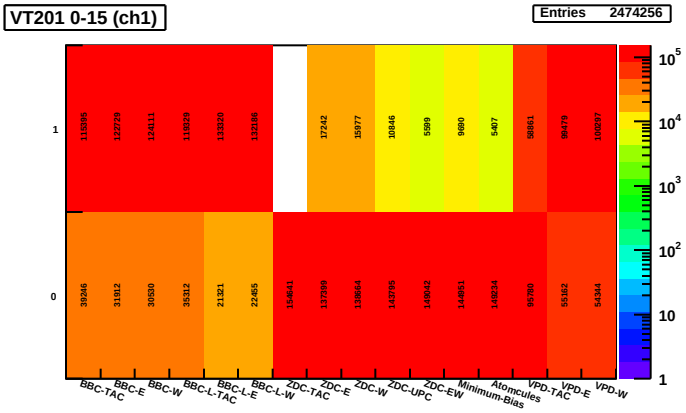

Endcap EMC L0 Input - Patch Sum
Entries 1.391769e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

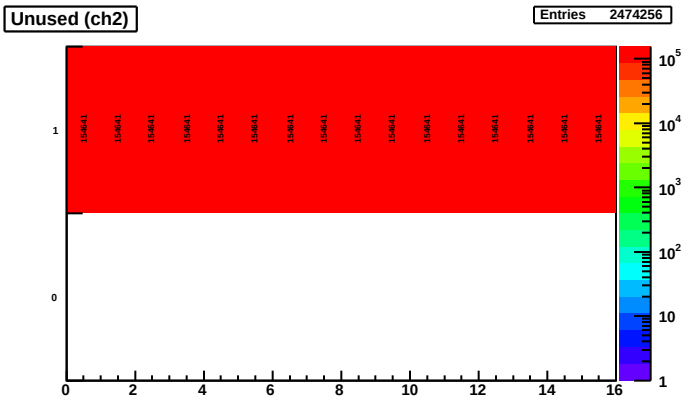

Barrel Jet Patches
Entries 2783538
JP ADC
JP ID

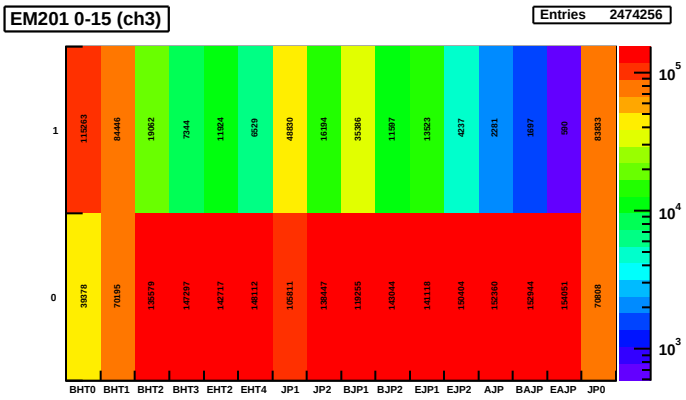

Endcap Jet Patches
Entries 927846
JP ADC
JP ID

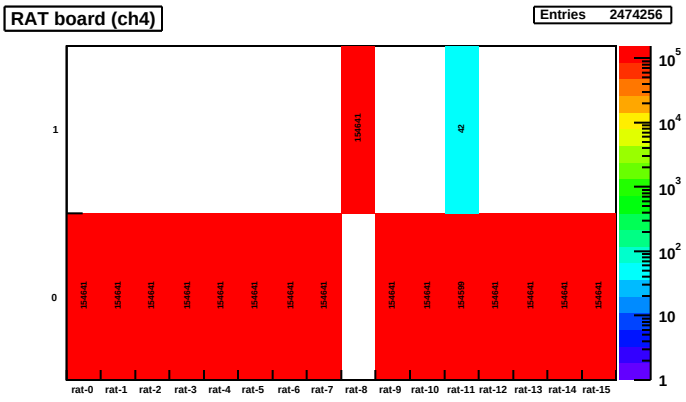

Hybrid Jet Patches
Entries 309282
JP ADC
JP ID

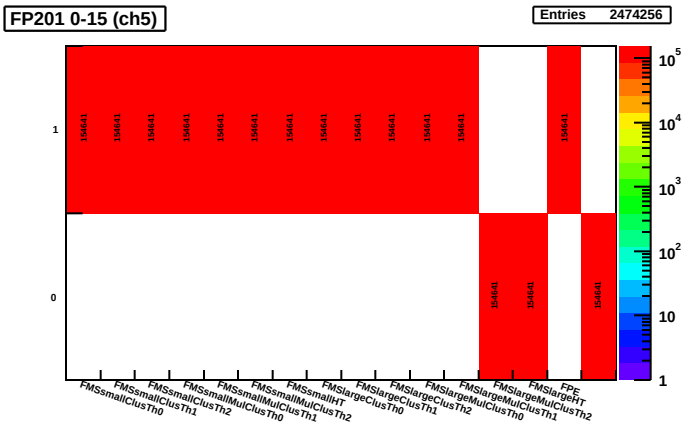

TF201 0-15 (ch0)
Entries 2474256
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

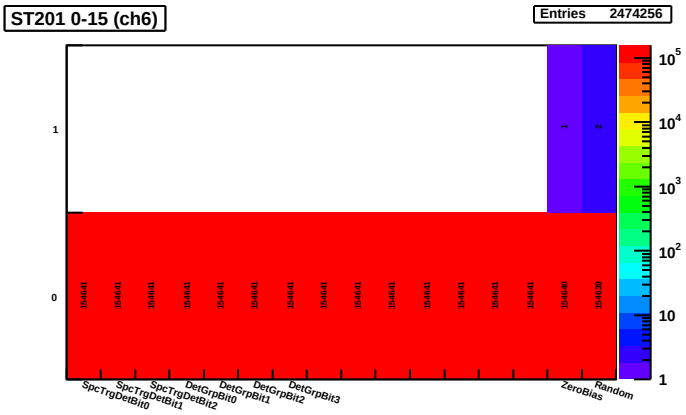

VT201 0-15 (ch1)
Entries 2474256
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

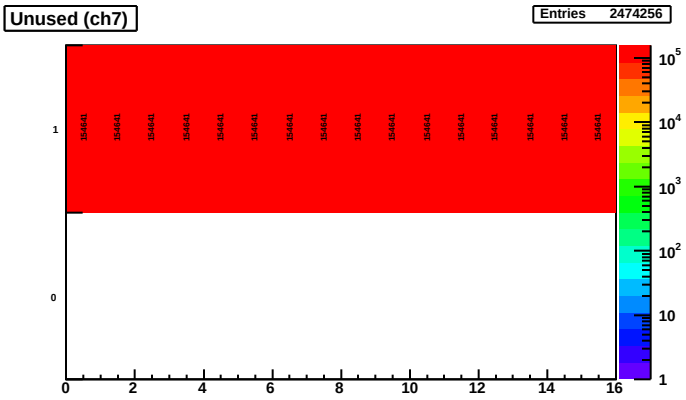

Unused (ch2)
Entries 2474256

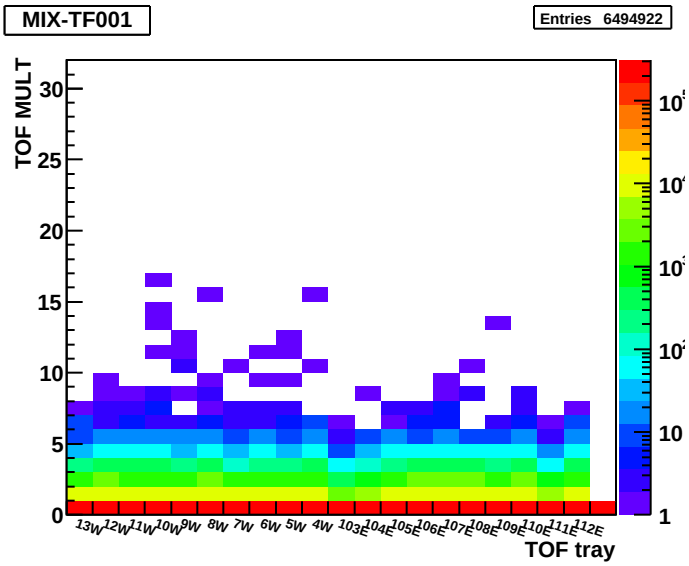

EM201 0-15 (ch3)
Entries 2474256
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

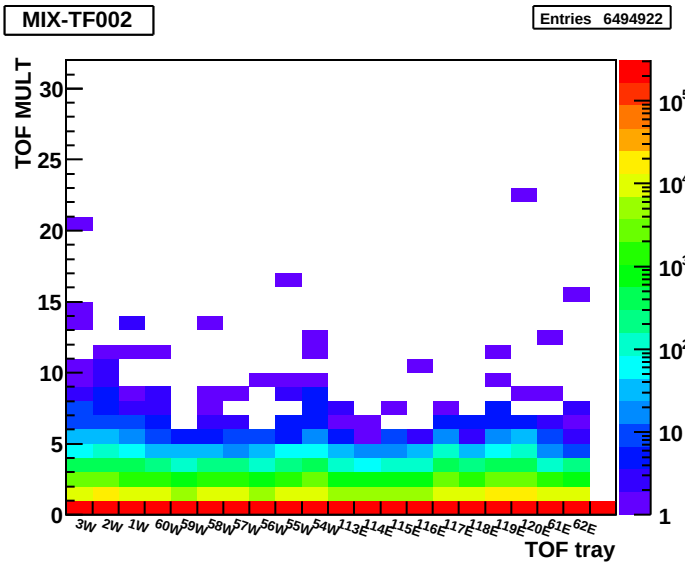

RAT board (ch4)
Entries 2474256
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

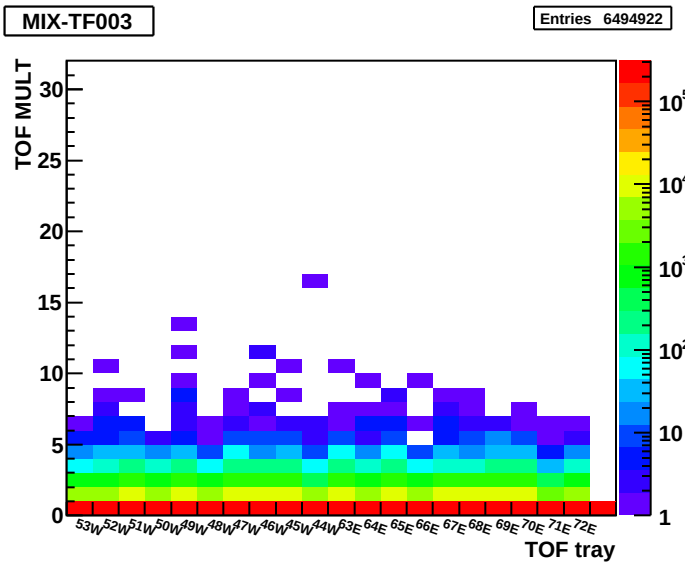

FP201 0-15 (ch5)
Entries 2474256
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

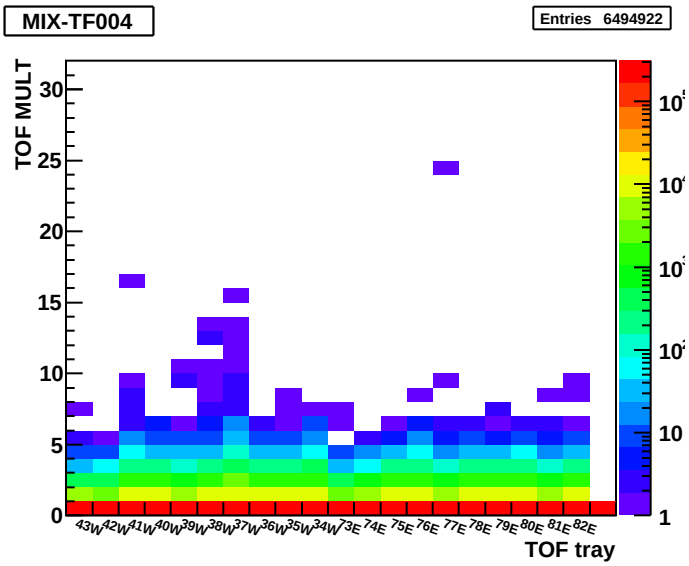

ST201 0-15 (ch6)
Entries 2474256
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

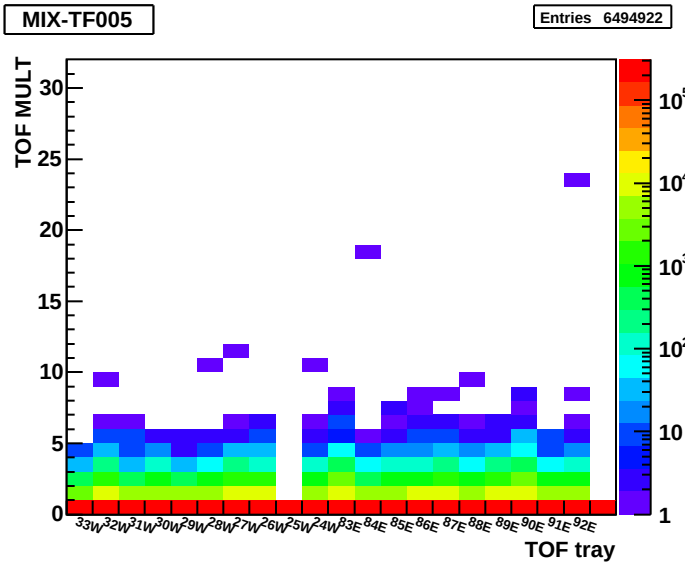

Unused (ch7)
Entries 2474256

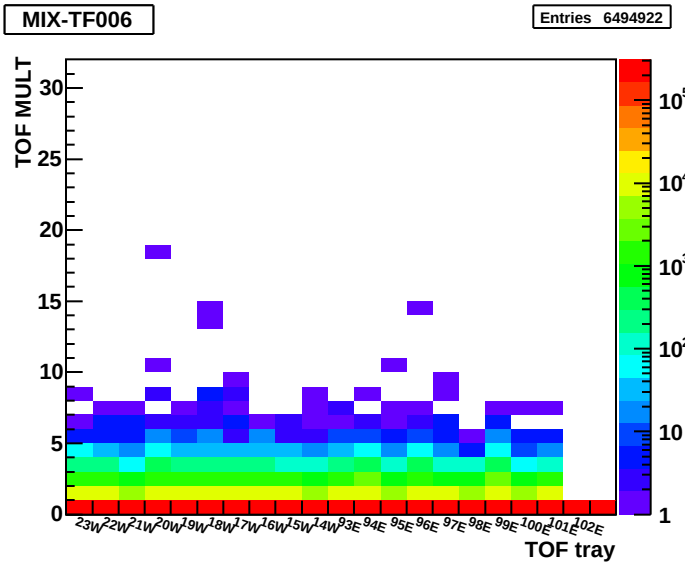

MIX-TF001
Entries 6494922
TOF MULT
TOF tray

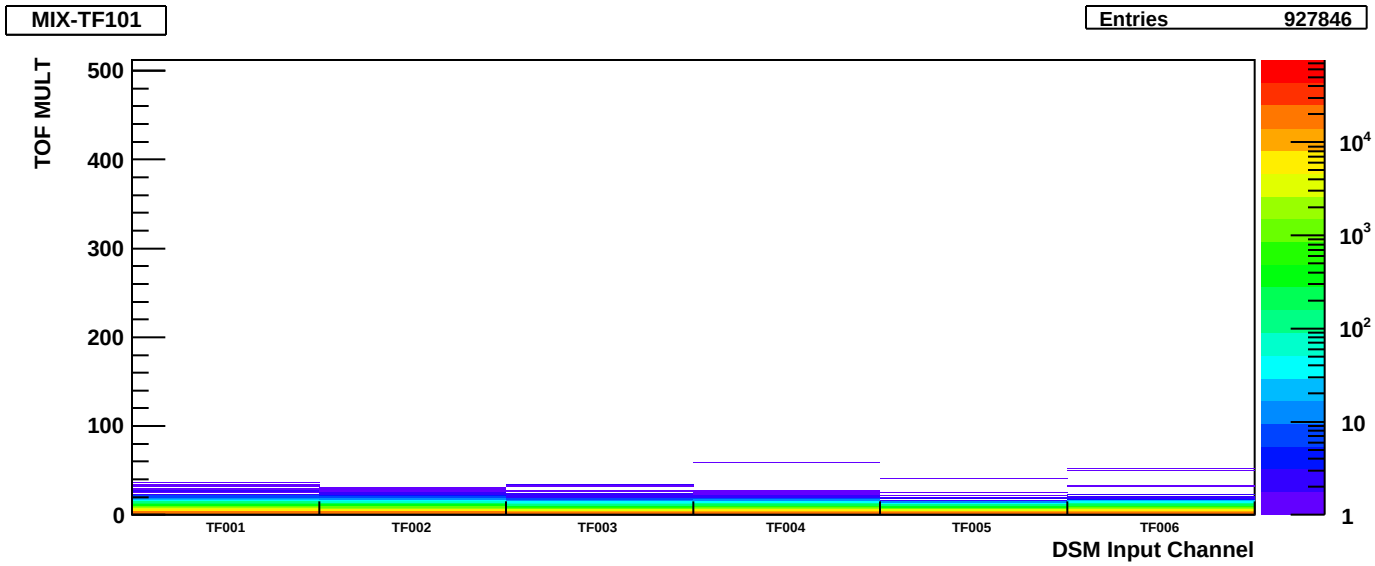

MIX-TF002
Entries 6494922
TOF MULT
TOF tray

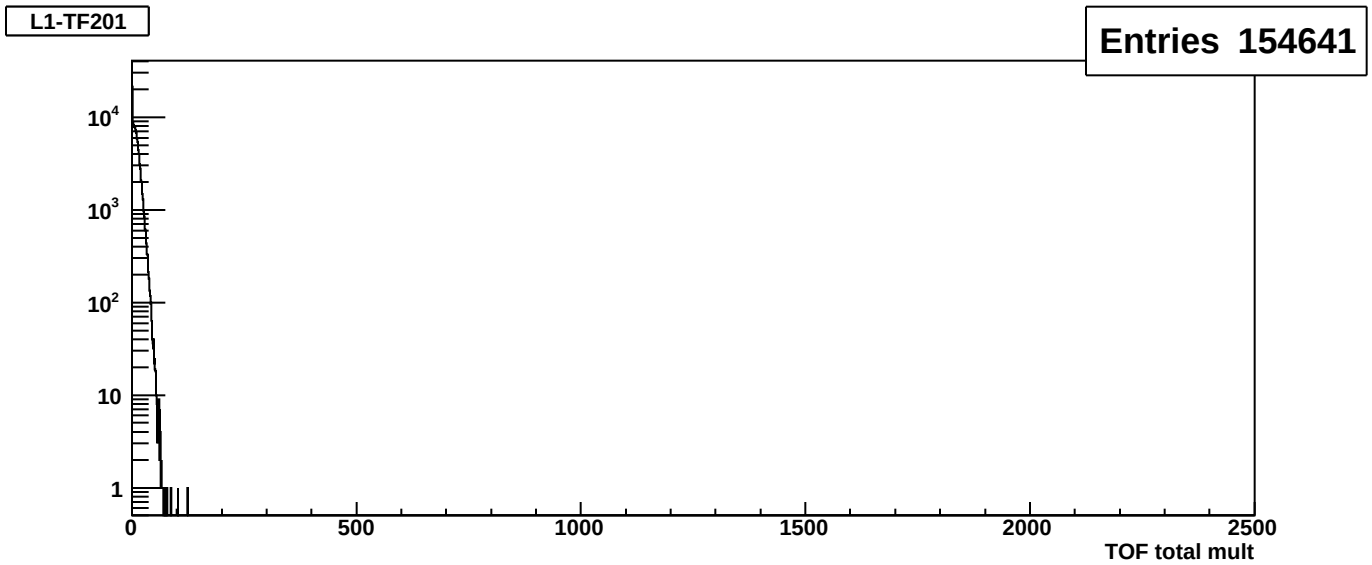

MIX-TF003
Entries 6494922
TOF MULT
TOF tray

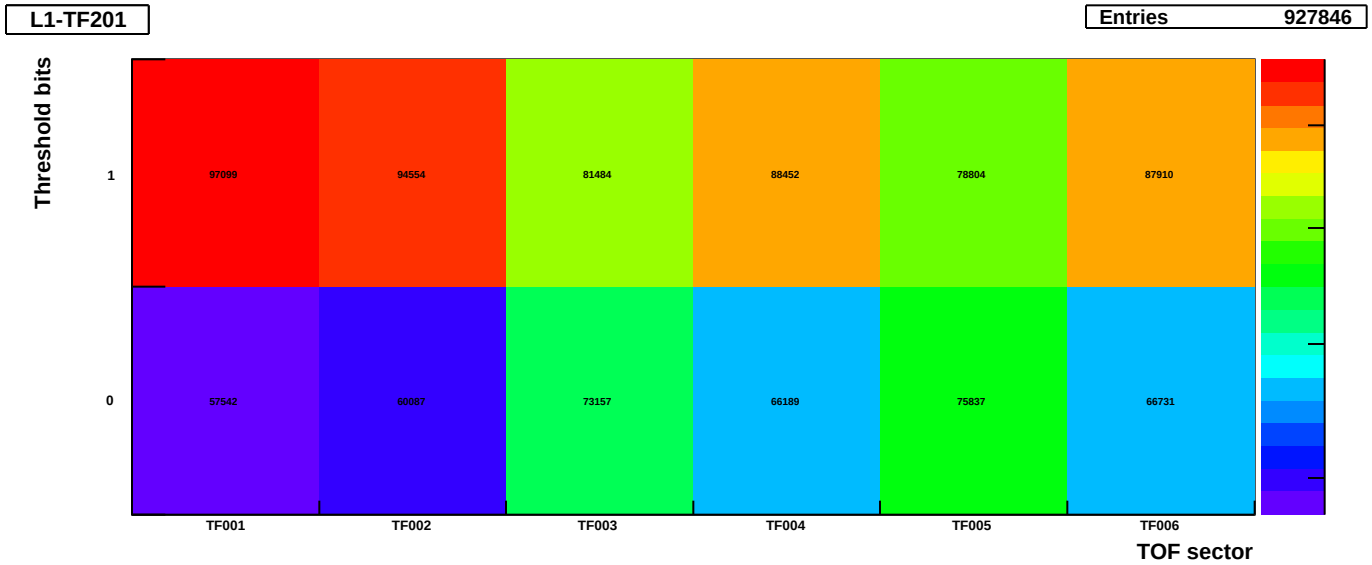

MIX-TF004
Entries 6494922
TOF MULT
TOF tray

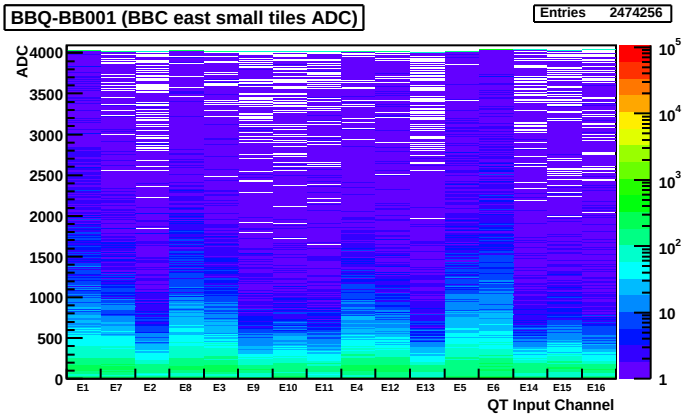

MIX-TF005
Entries 6494922
TOF MULT
TOF tray

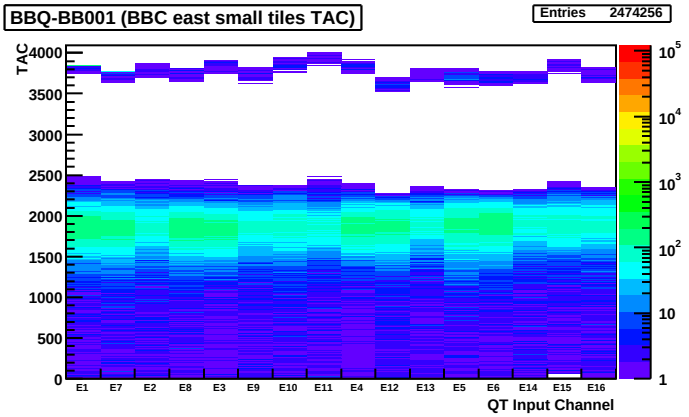

MIX-TF006
Entries 6494922
TOF MULT
TOF tray

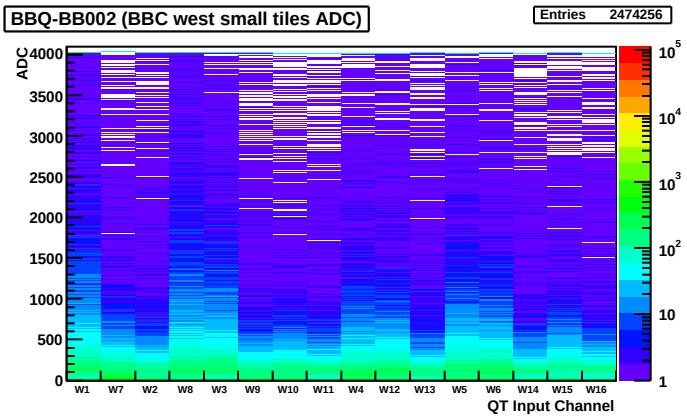

MIX-TF101
Entries 927846
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

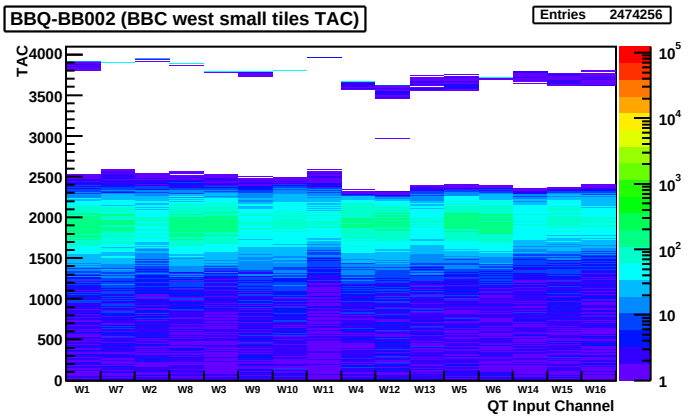

L1-TF201
Entries 154641
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

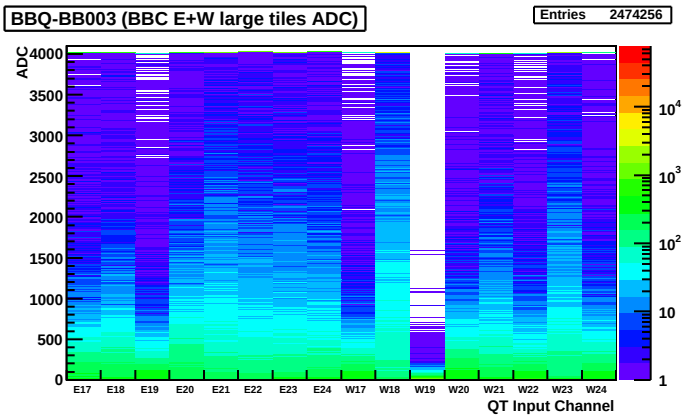

L1-TF201
Entries 927846
Threshold bits
1
0
97099
94554
81484
88452
78804
87910
57542
60087
73157
66189
75837
66731
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

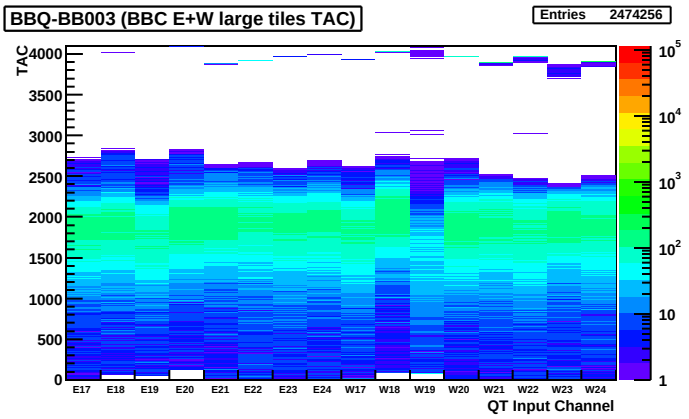

BBQ-BB001 (BBC east small tiles ADC)
Entries 2474256
ADC
QT Input Channel

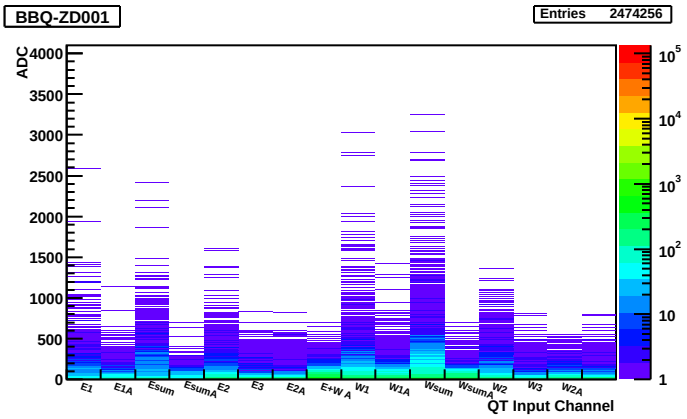

BBQ-BB001 (BBC east small tiles TAC)
Entries 2474256
TAC
QT Input Channel

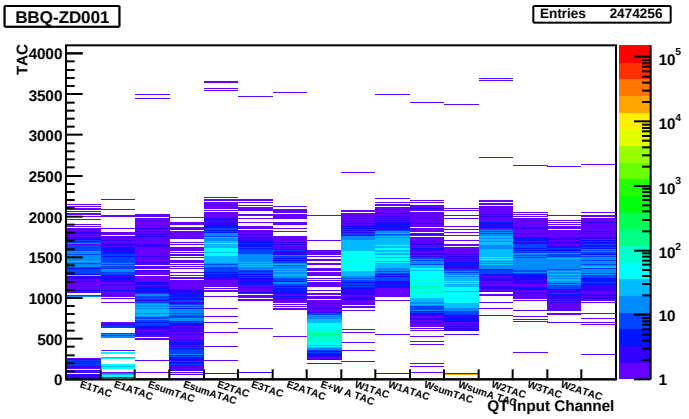

BBQ-BB002 (BBC west small tiles ADC)
Entries 2474256
ADC
QT Input Channel

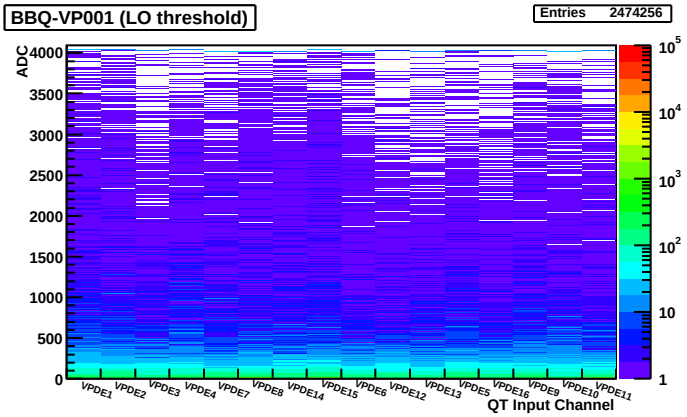

BBQ-BB002 (BBC west small tiles TAC)
Entries 2474256
TAC
QT Input Channel

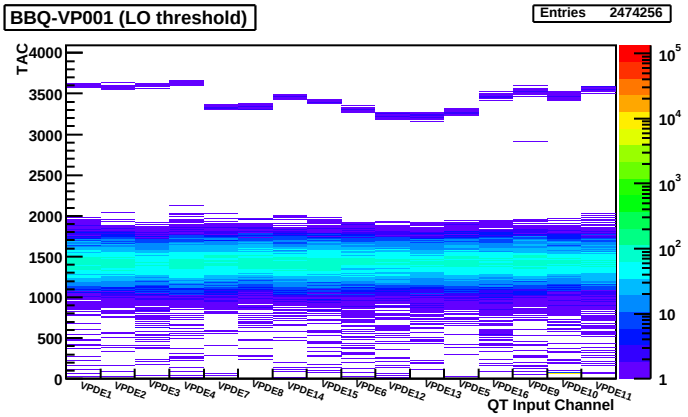

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2474256
ADC
QT Input Channel

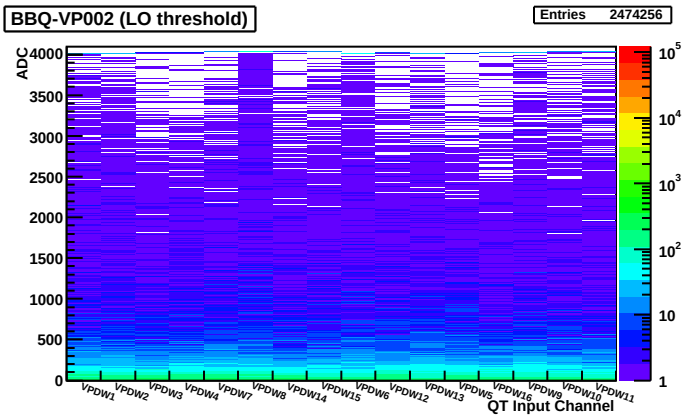

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2474256
TAC
QT Input Channel

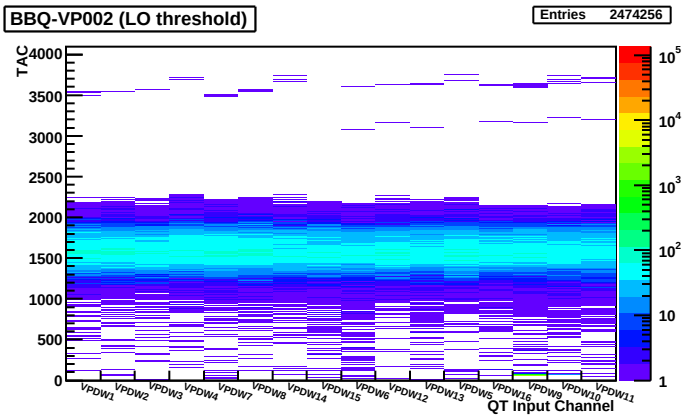

BBQ-ZD001
Entries 2474256
ADC
QT Input Channel

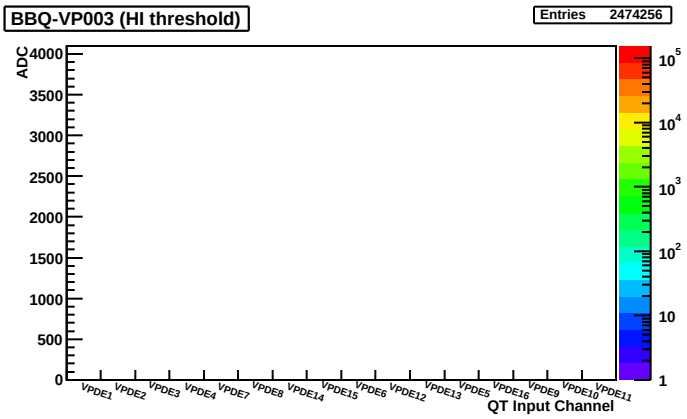

BBQ-ZD001
Entries 2474256
TAC
QT Input Channel

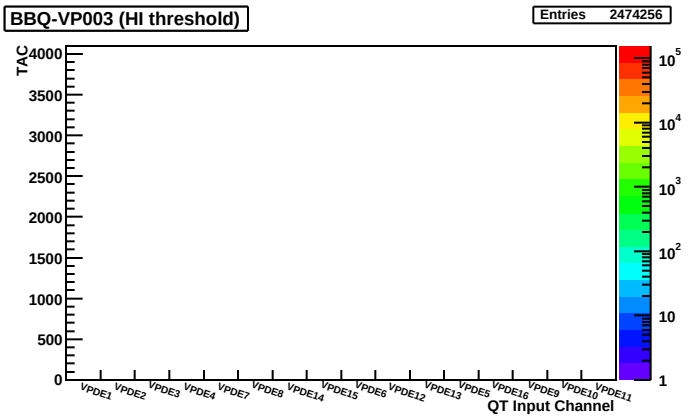

BBQ-VP001 (LO threshold)
Entries 2474256
ADC
QT Input Channel

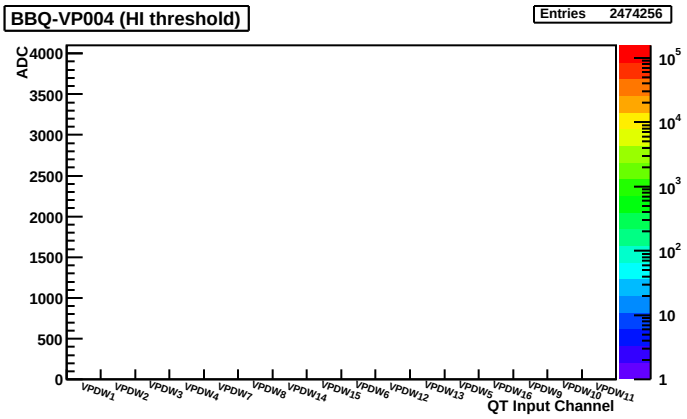

BBQ-VP001 (LO threshold)
Entries 2474256
TAC
QT Input Channel

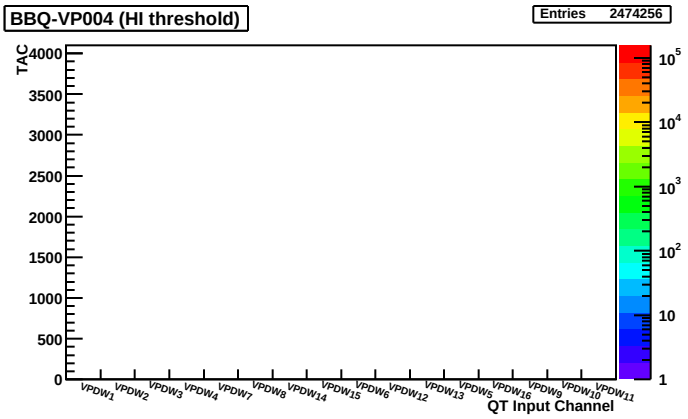

BBQ-VP002 (LO threshold)
Entries 2474256
ADC
QT Input Channel

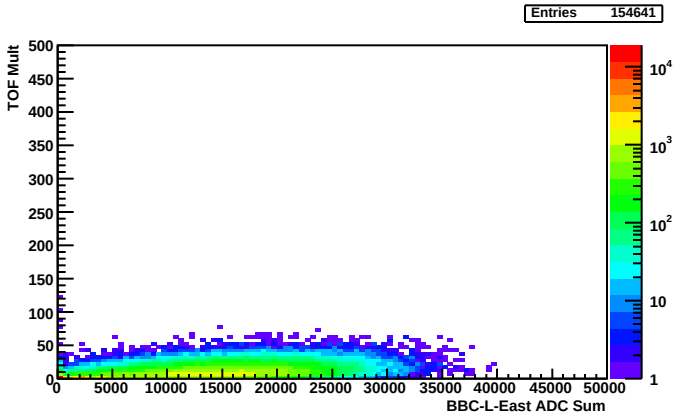

BBQ-VP002 (LO threshold)
Entries 2474256
TAC
QT Input Channel

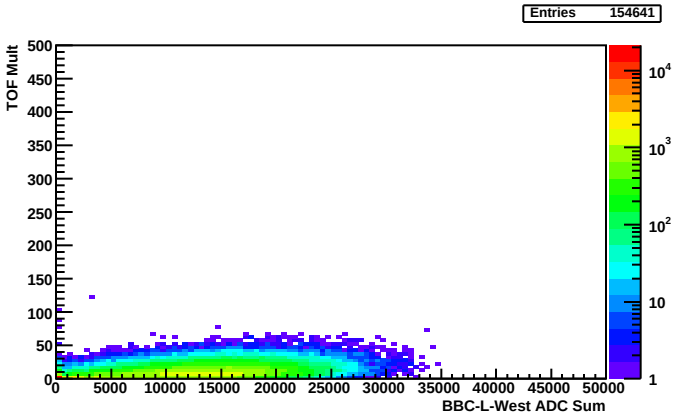

BBQ-VP003 (HI threshold)
Entries 2474256
ADC
QT Input Channel

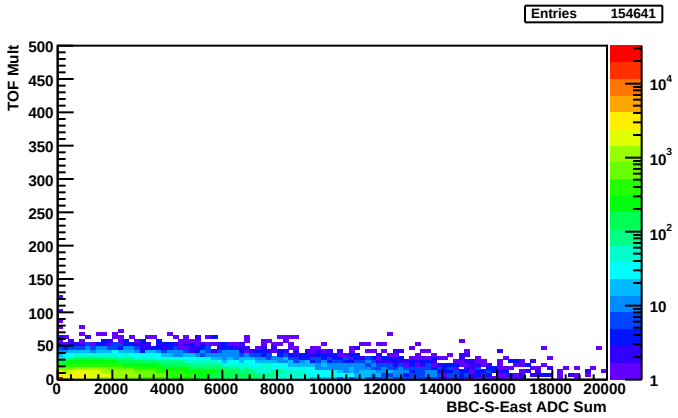

BBQ-VP003 (HI threshold)
Entries 2474256
TAC
QT Input Channel

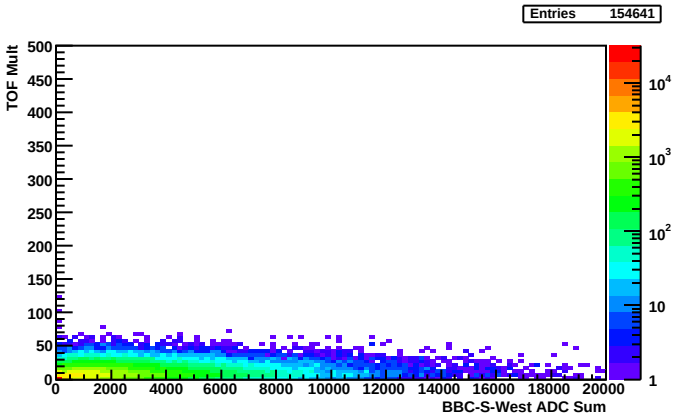

BBQ-VP004 (HI threshold)
Entries 2474256
ADC
QT Input Channel

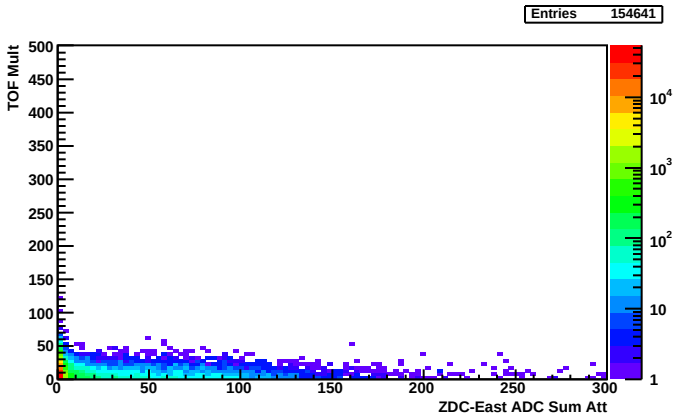

BBQ-VP004 (HI threshold)
Entries 2474256
TAC
QT Input Channel

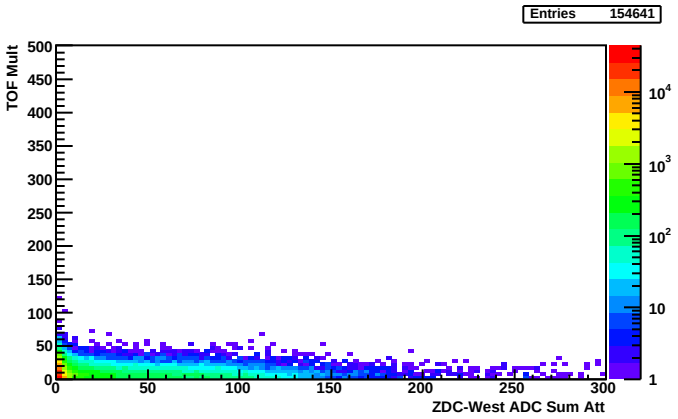

Entries 154641
TOF Mult
BBC-L-East ADC Sum

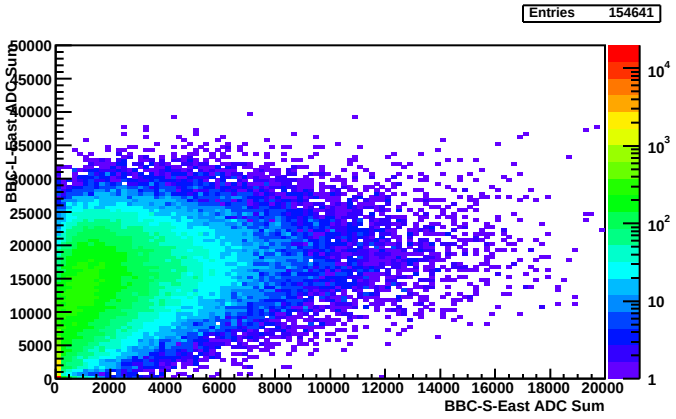

Entries 154641
TOF Mult
BBC-L-West ADC Sum

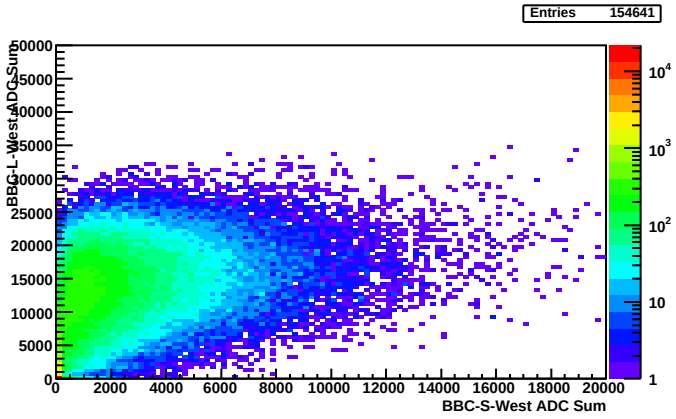

Entries 154641
TOF Mult
BBC-S-East ADC Sum

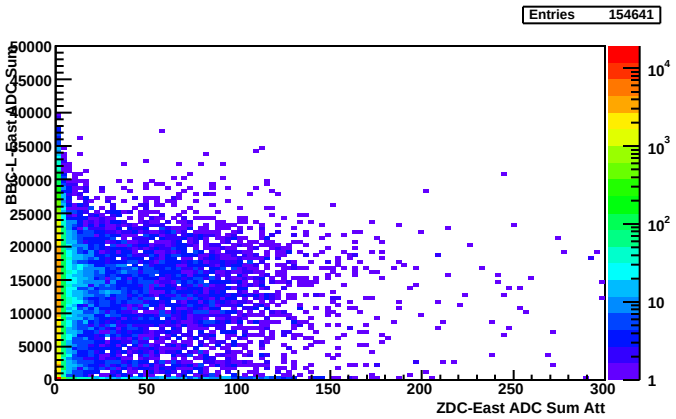

Entries 154641
TOF Mult
BBC-S-West ADC Sum

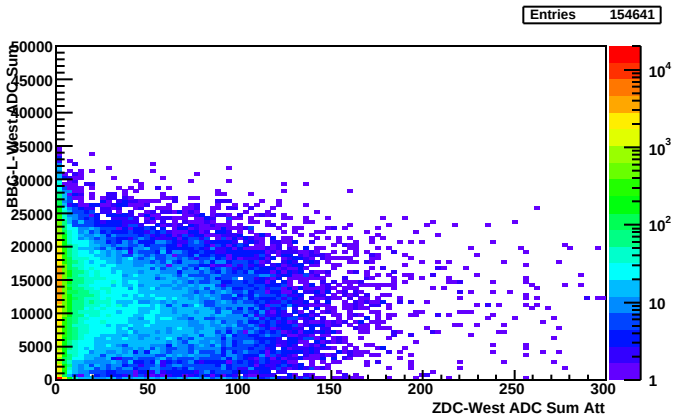

Entries 154641
TOF Mult
ZDC-East ADC Sum Att

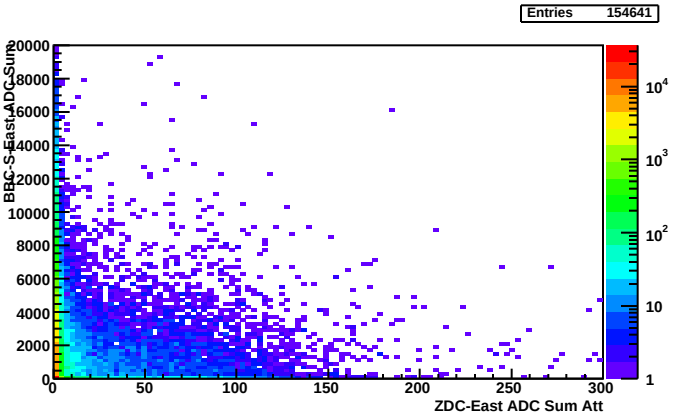

Entries 154641
TOF Mult
ZDC-West ADC Sum Att

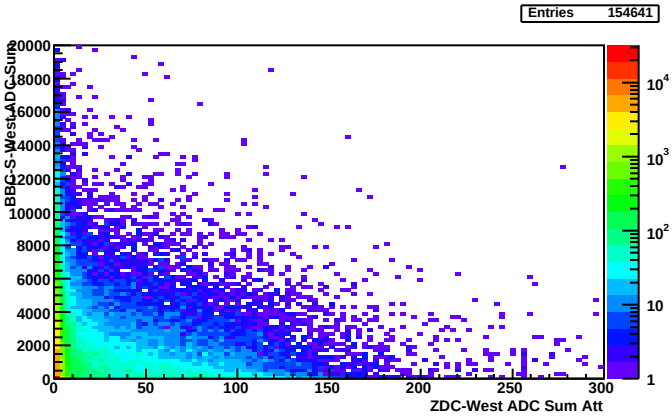

Entries 154641
BBC-L-East ADC Sum
BBC-S-East ADC Sum

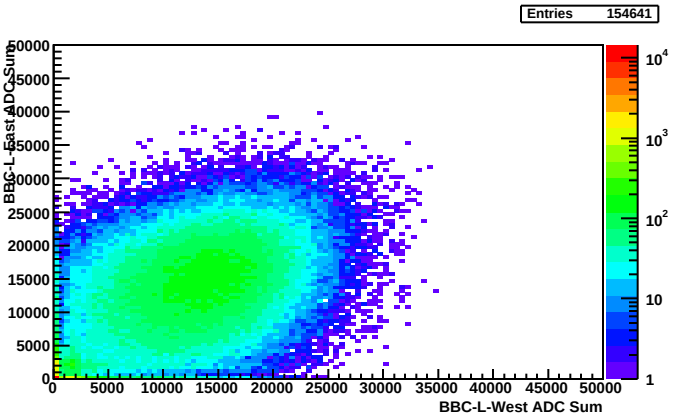

Entries 154641
BBC-L-West ADC Sum
BBC-S-West ADC Sum

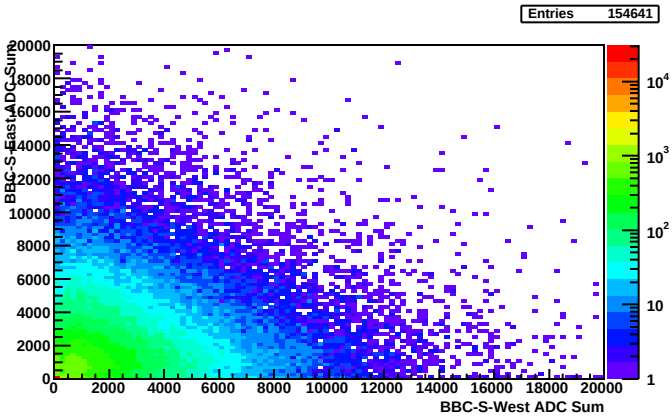

Entries 154641
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

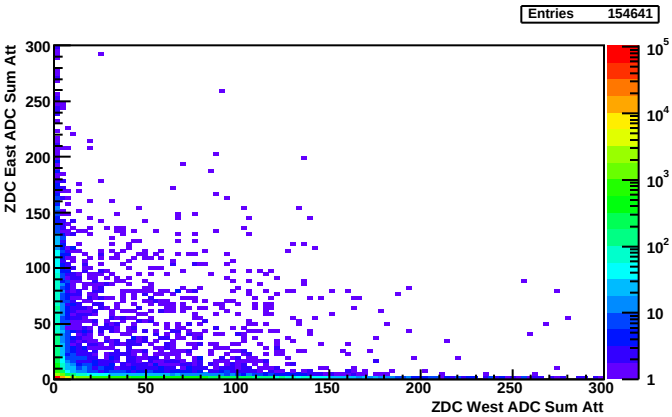

Entries 154641
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

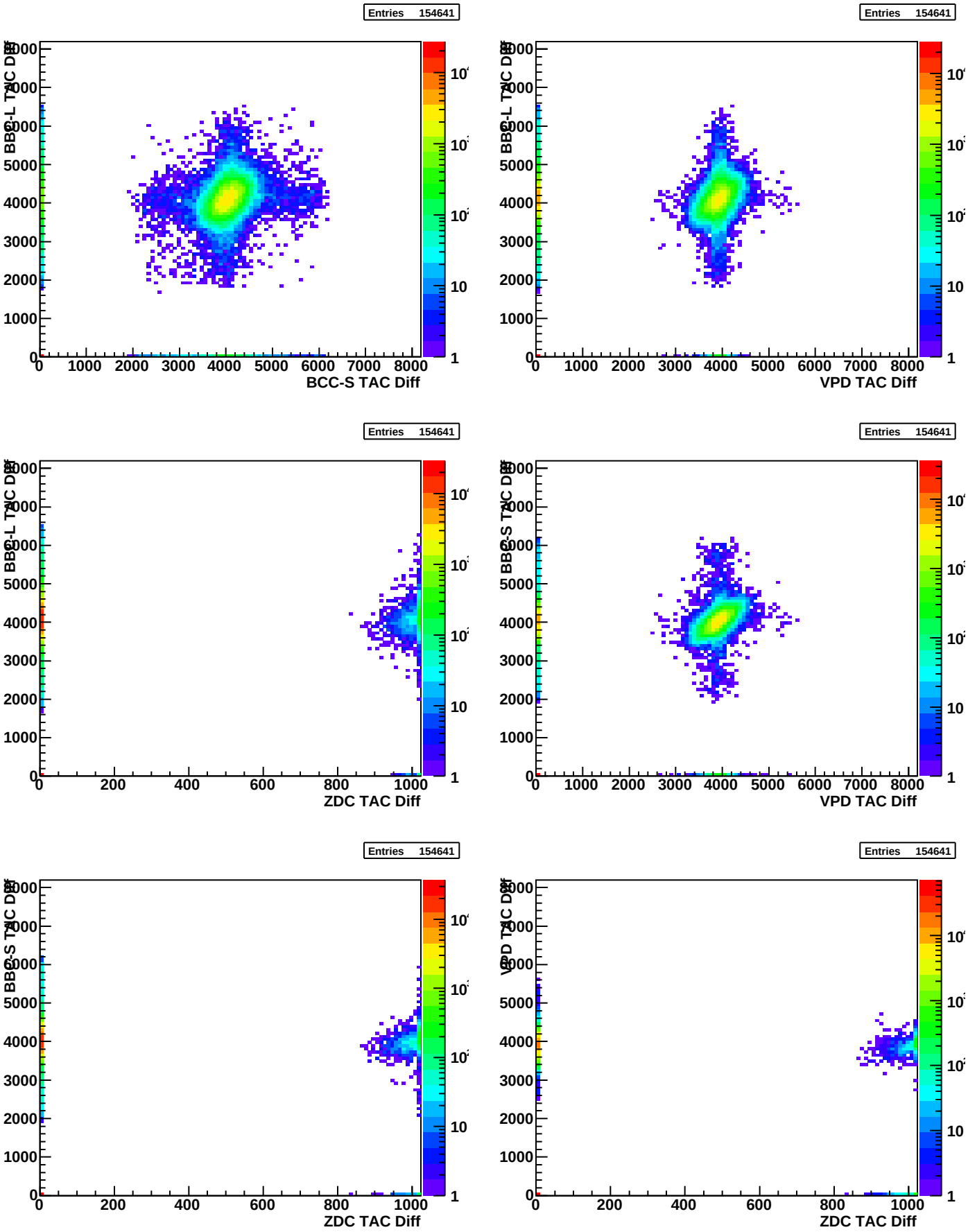

Entries 154641
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

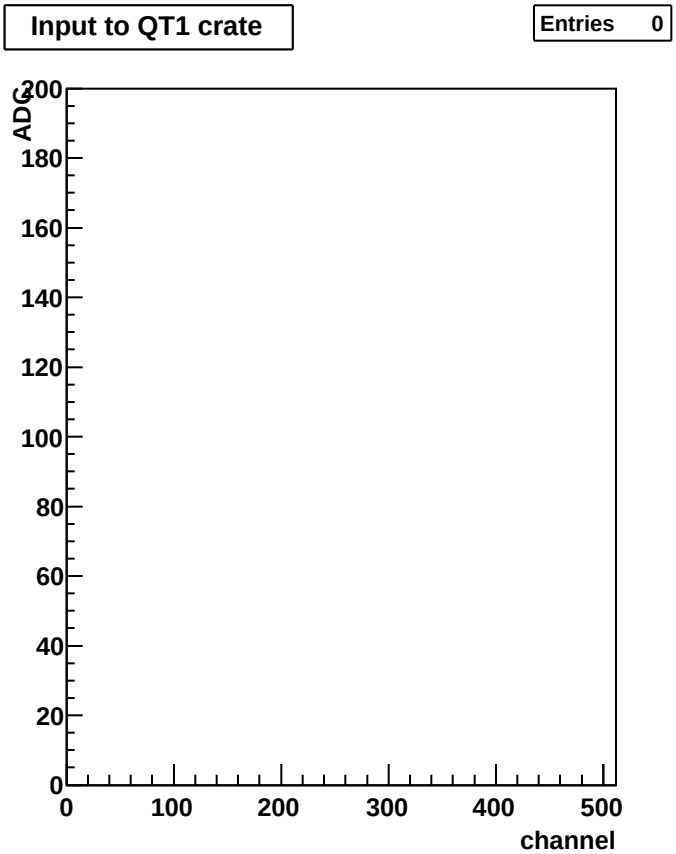

Entries 154641
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

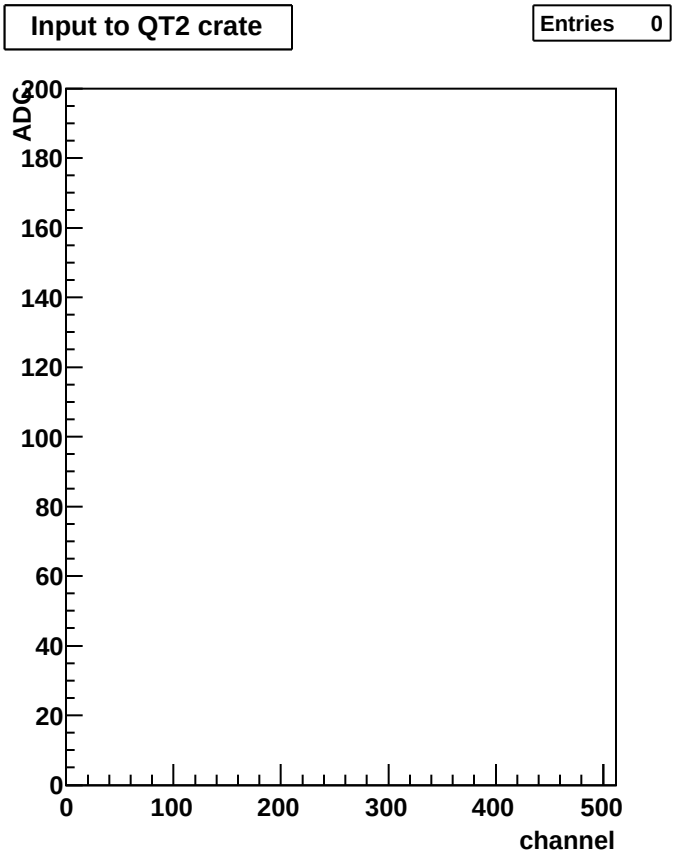

Entries 154641
BBC-L-East ADC Sum
BBC-L-West ADC Sum

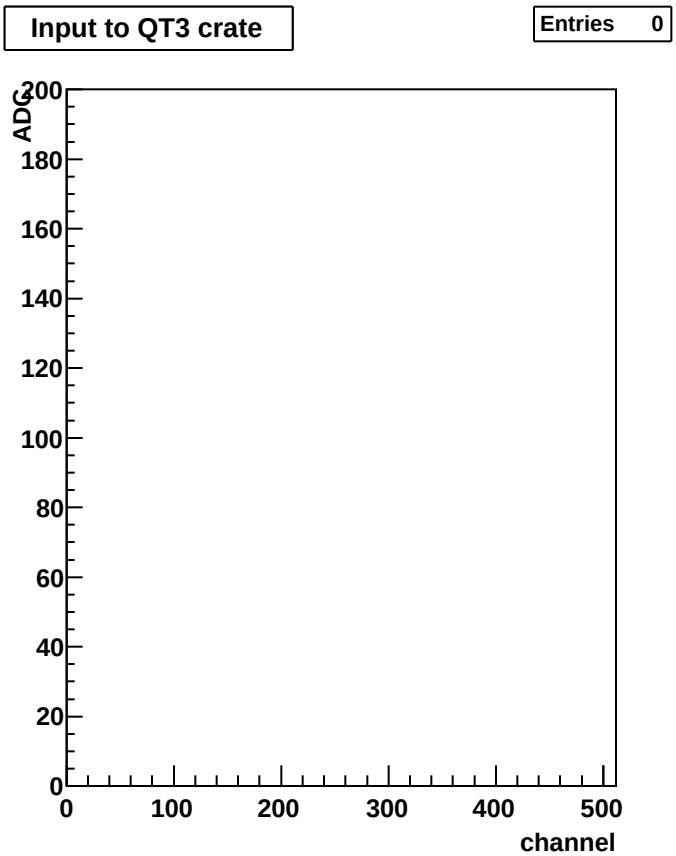

Entries 154641
BBC-S-East ADC Sum
BBC-S-West ADC Sum

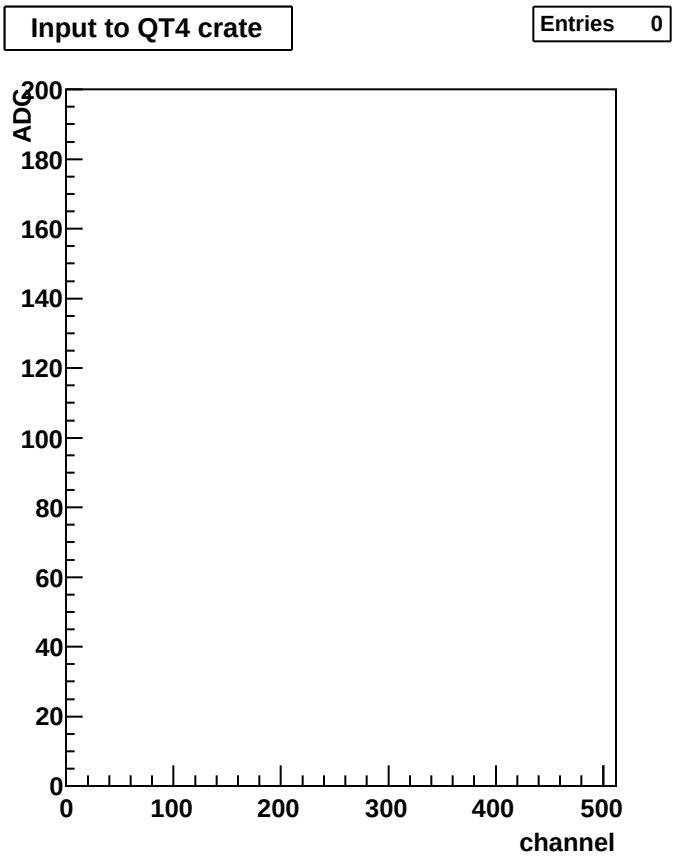

Entries 154641
ZDC East ADC Sum Att
ZDC West ADC Sum Att

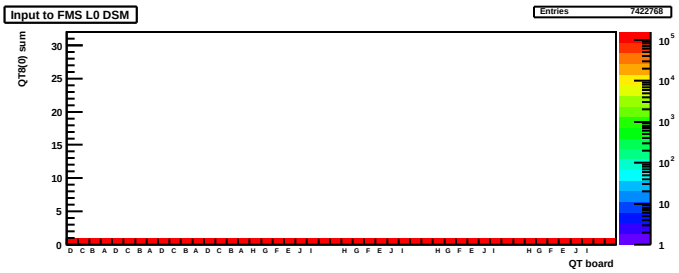

Entries 154641
BBC-L TAC Diff
BCC-S TAC Diff
Entries 154641
BBC-L TAC Diff
VPD TAC Diff
Entries 154641
BBC-L TAC Diff
ZDC TAC Diff
Entries 154641
BBC-S TAC Diff
VPD TAC Diff
Entries 154641
BBC-S TAC Diff
ZDC TAC Diff
Entries 154641
VPD TAC Diff
ZDC TAC Diff

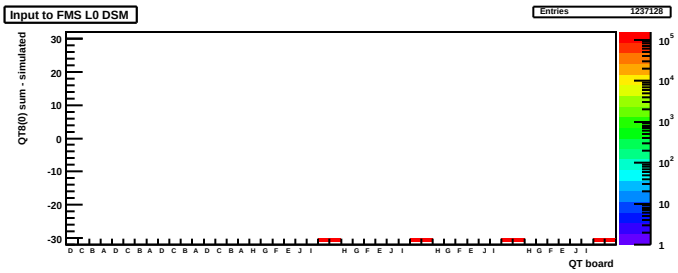

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

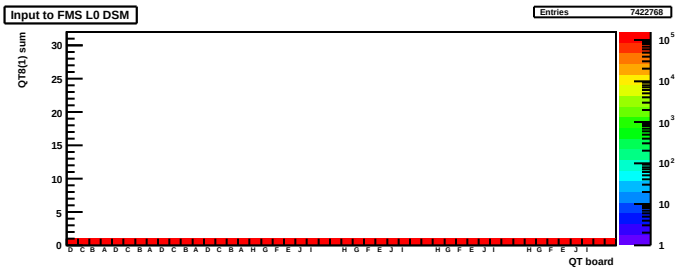

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

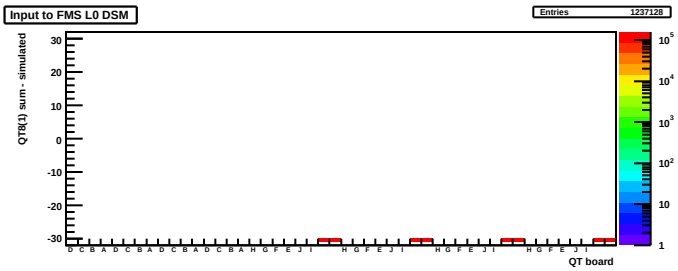

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

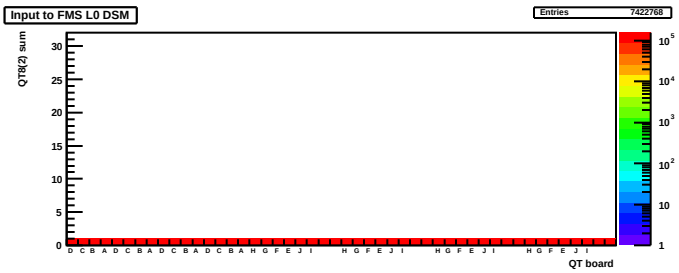

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

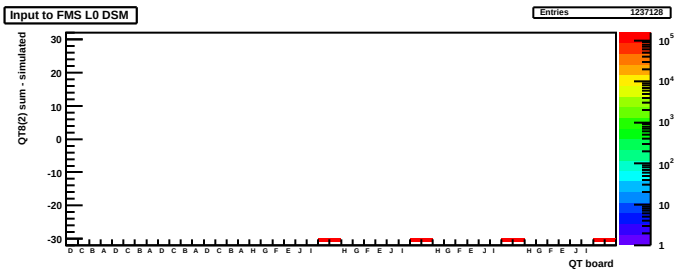

Input to FMS L0 DSM
Entries 7422768
QT8(0) sum
QT board

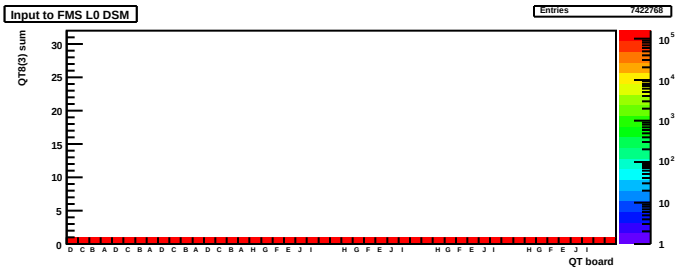

Input to FMS L0 DSM
Entries 1237128
QT8(0) sum - simulated
QT board

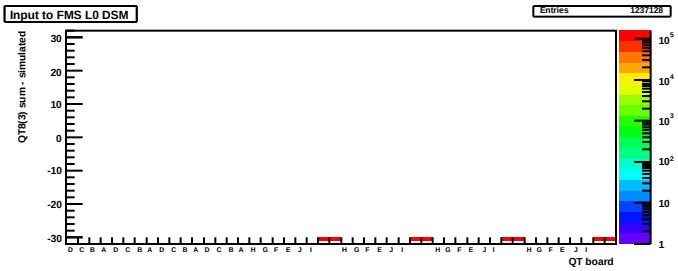

Input to FMS L0 DSM
Entries 7422768
QT8(1) sum
QT board

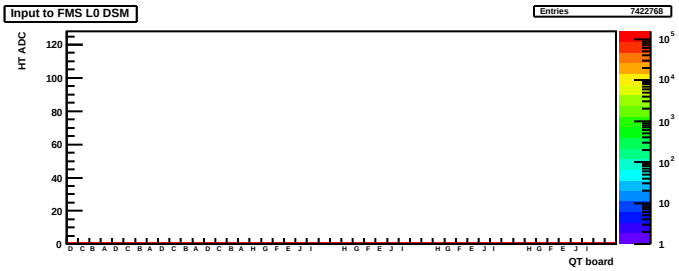

Input to FMS L0 DSM
Entries 1237128
QT8(1) sum - simulated
QT board

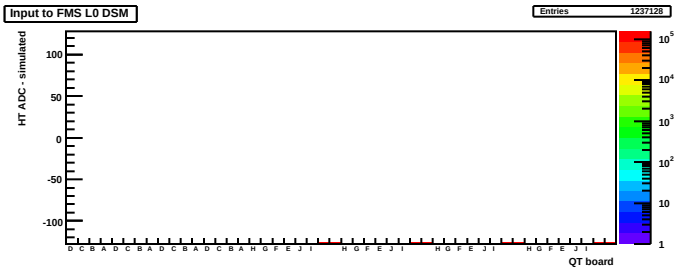

Input to FMS L0 DSM
Entries 7422768
QT8(2) sum
QT board

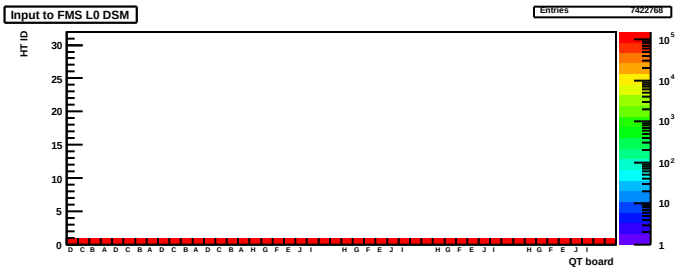

Input to FMS L0 DSM
Entries 1237128
QT8(2) sum - simulated
QT board

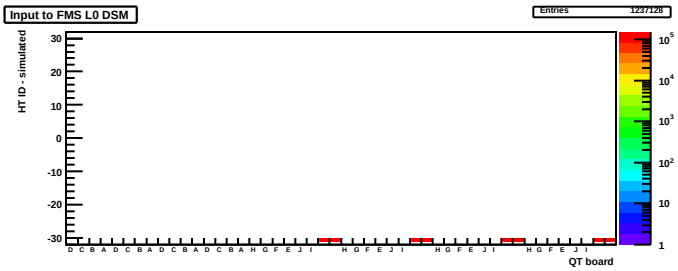

Input to FMS L0 DSM
Entries 7422768
QT8(3) sum
QT board

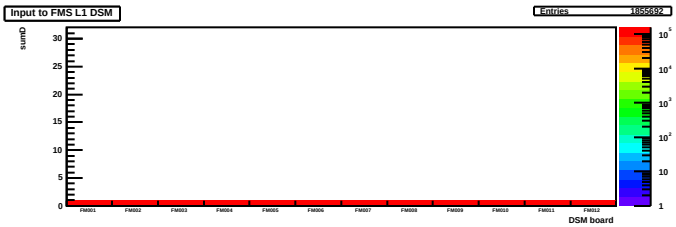

Input to FMS L0 DSM
Entries 1237128
QT8(3) sum - simulated
QT board

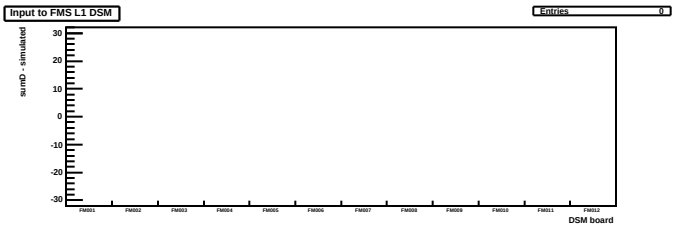

Input to FMS L0 DSM
Entries 7422768
HT ADC
QT board

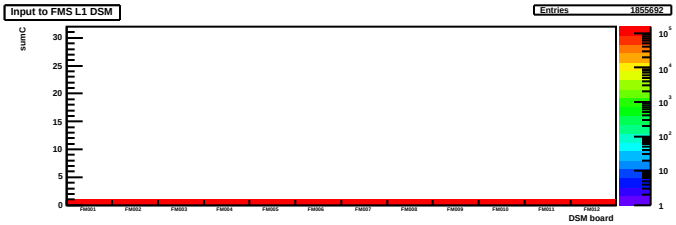

Input to FMS L0 DSM
Entries 1237128
HT ADC - simulated
QT board

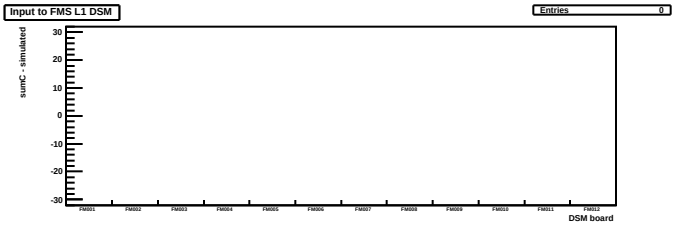

Input to FMS L0 DSM
Entries 7422768
HT ID
QT board

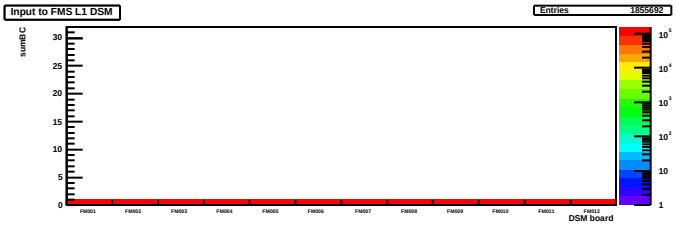

Input to FMS L0 DSM
Entries 1237128
HT ID - simulated
QT board

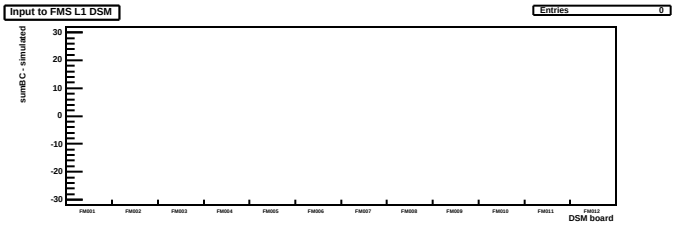

Input to FMS L1 DSM
Entries 1855692
sumD
DSM board

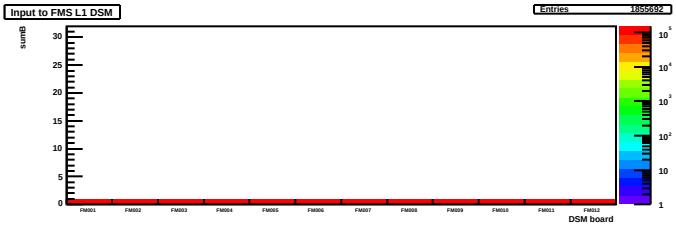

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

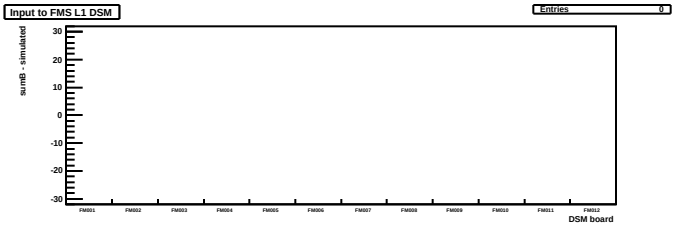

Input to FMS L1 DSM
Entries 1855692
sumC
DSM board

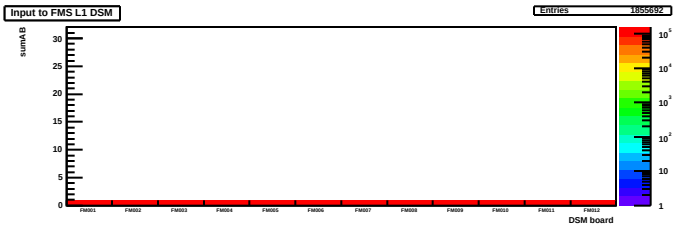

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

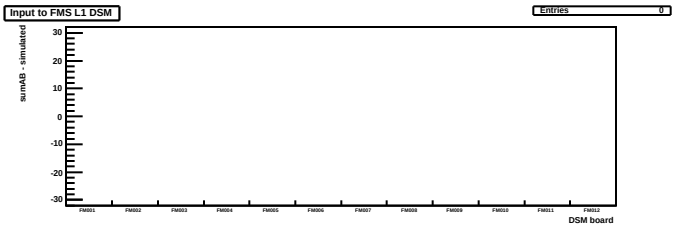

Input to FMS L1 DSM
Entries 1855692
sumBC
DSM board

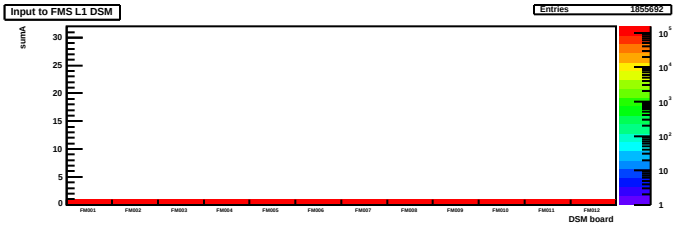

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

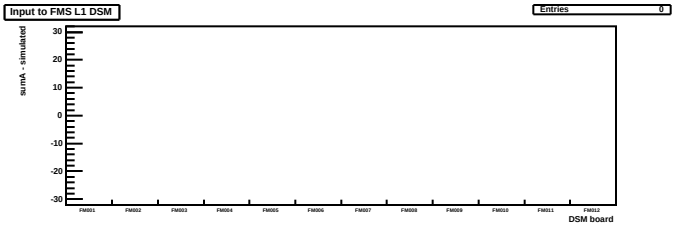

Input to FMS L1 DSM
Entries 1855692
sumB
DSM board

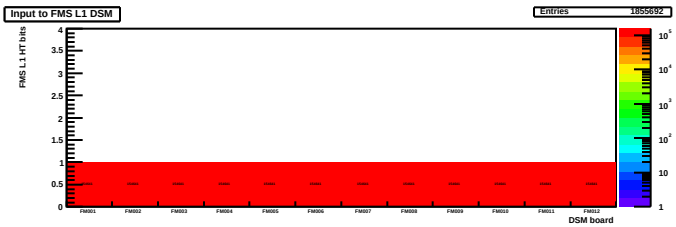

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

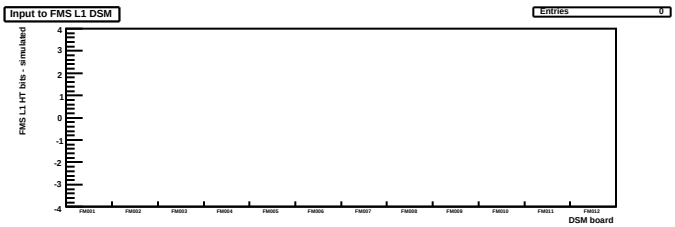

Input to FMS L1 DSM
Entries 1855692
sumAB
DSM board

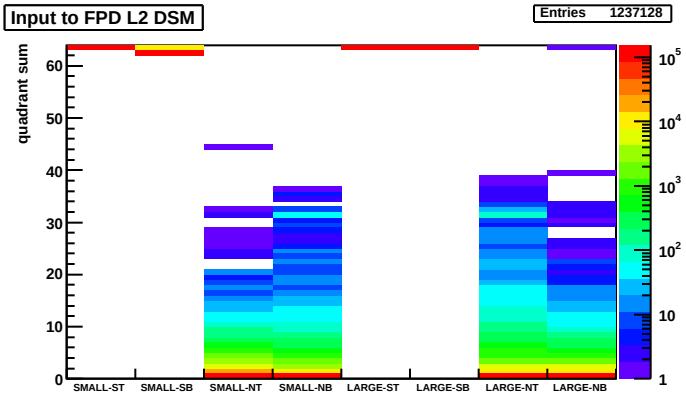

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

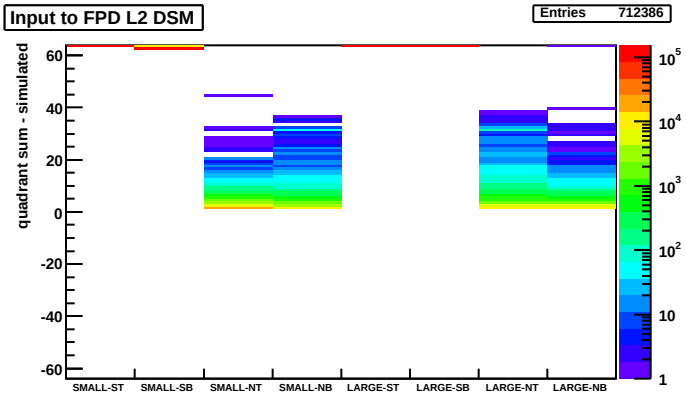

Input to FMS L1 DSM
Entries 1855692
sumA
DSM board

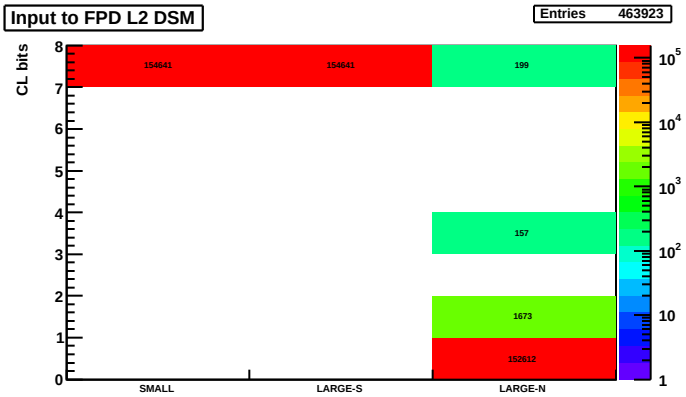

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

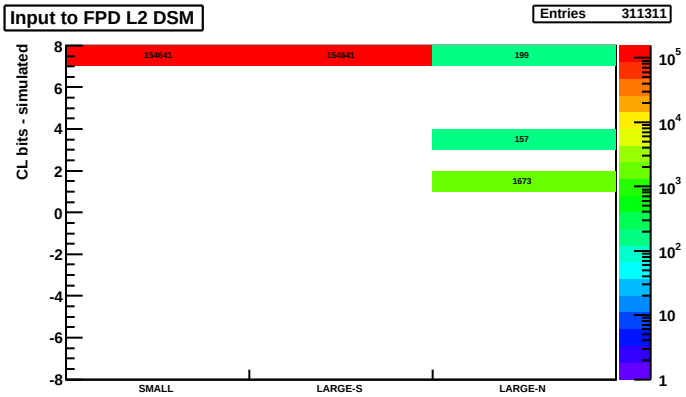

Input to FMS L1 DSM
Entries 1855692
FMS L1 HT bits
DSM board

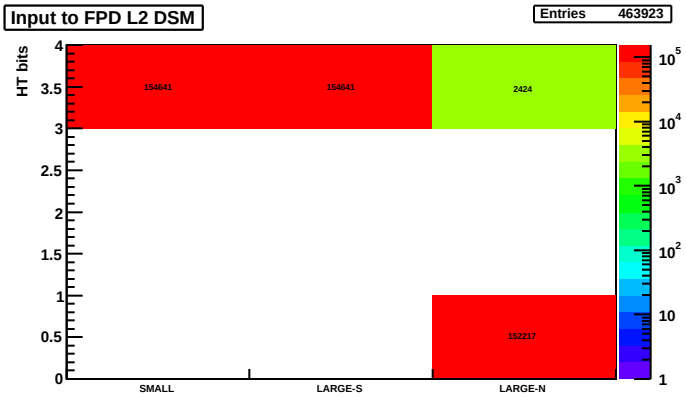

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

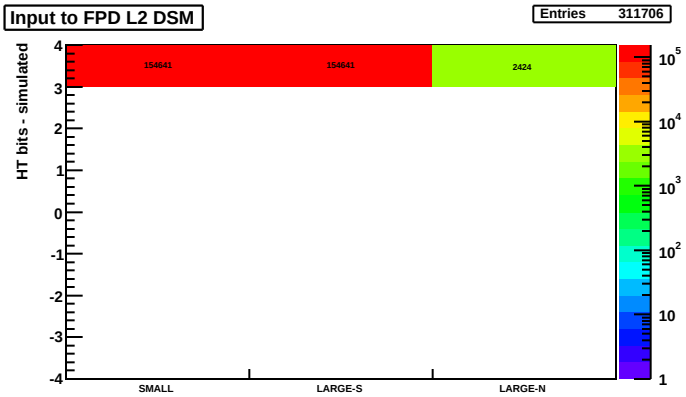

Input to FPD L2 DSM
Entries 1237128
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

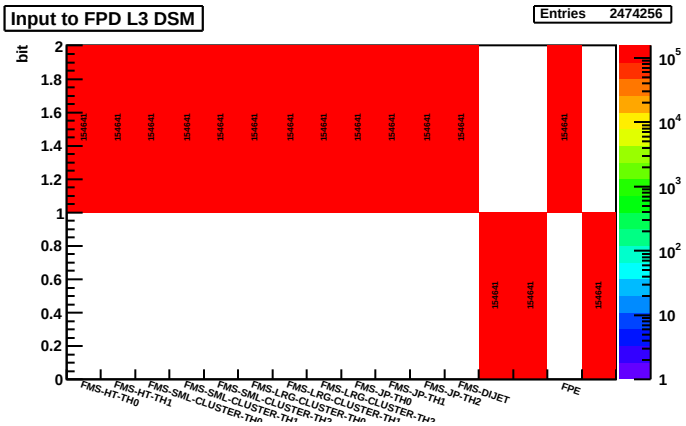

Input to FPD L2 DSM
Entries 712386
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

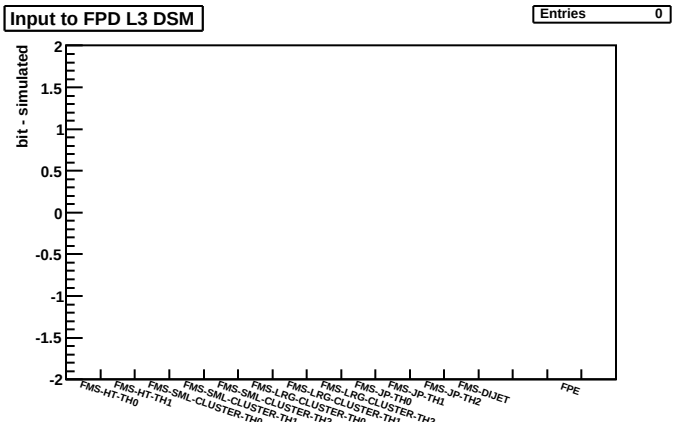

Input to FPD L2 DSM
Entries 463923
CL bits
154641
154641
199
157
1673
152612
SMALL
LARGE-S
LARGE-N

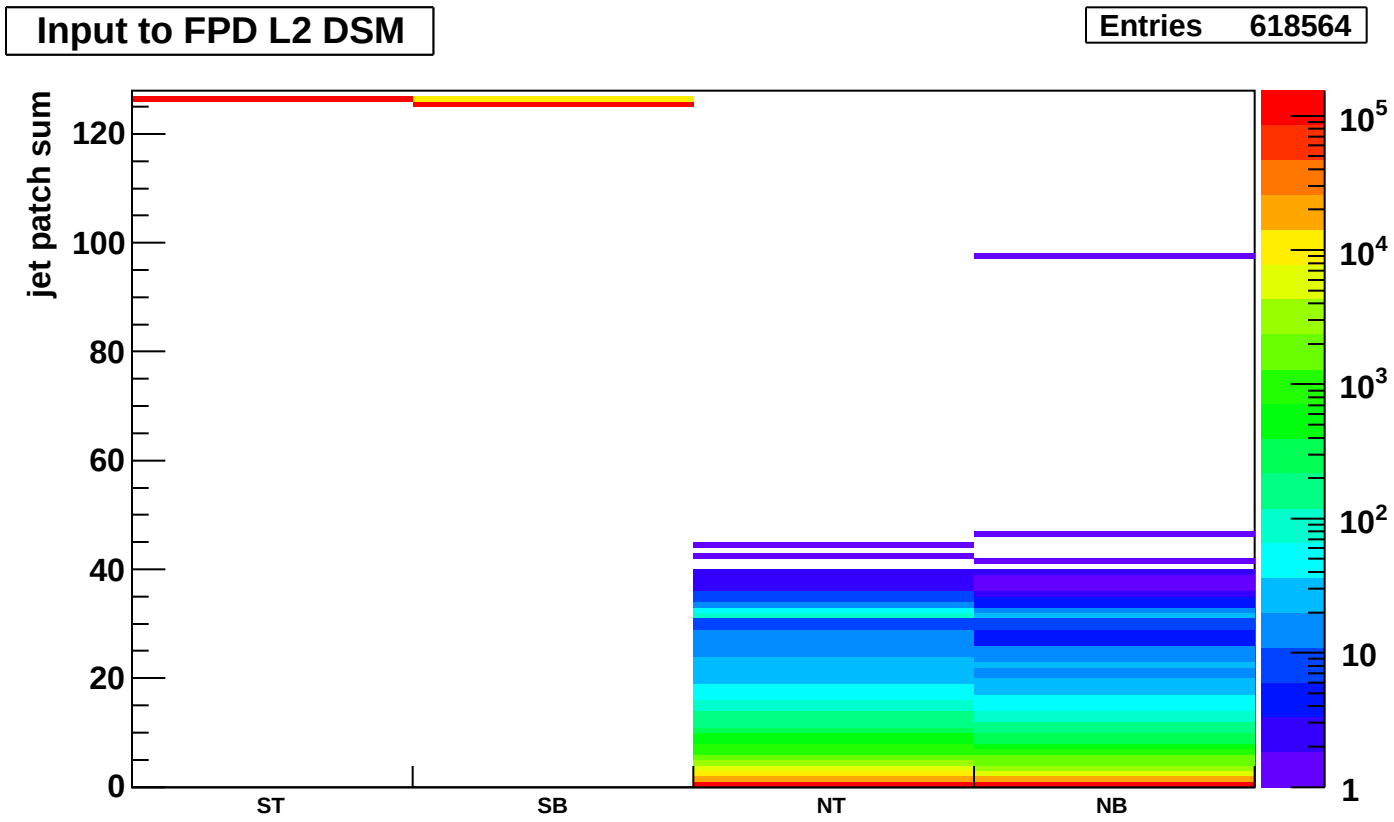

Input to FPD L2 DSM
Entries 311311
CL bits - simulated
154641
154641
199
157
1673
SMALL
LARGE-S
LARGE-N

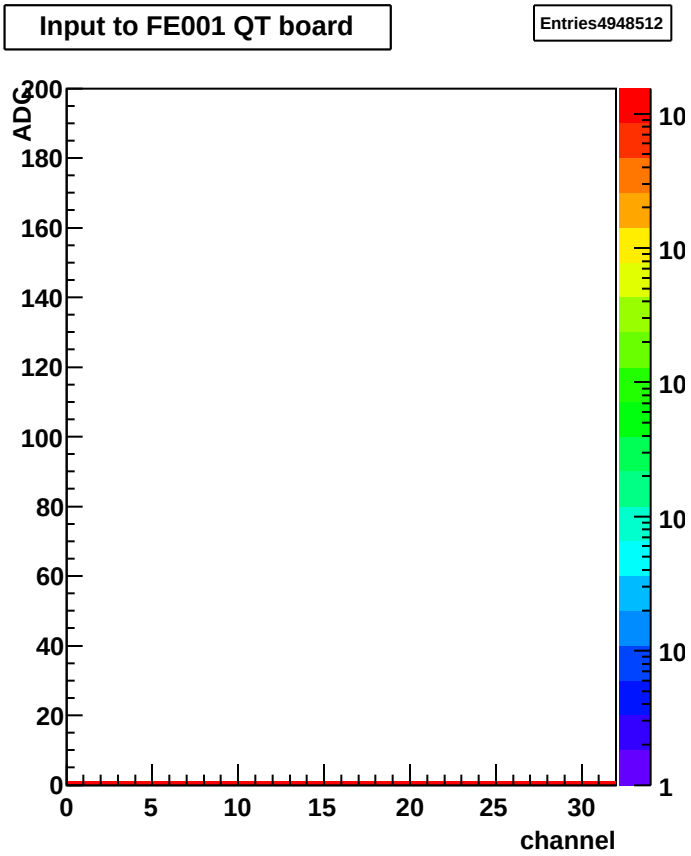

Input to FPD L2 DSM
Entries 463923
HT bits
154641
154641
2424
152217
SMALL
LARGE-S
LARGE-N

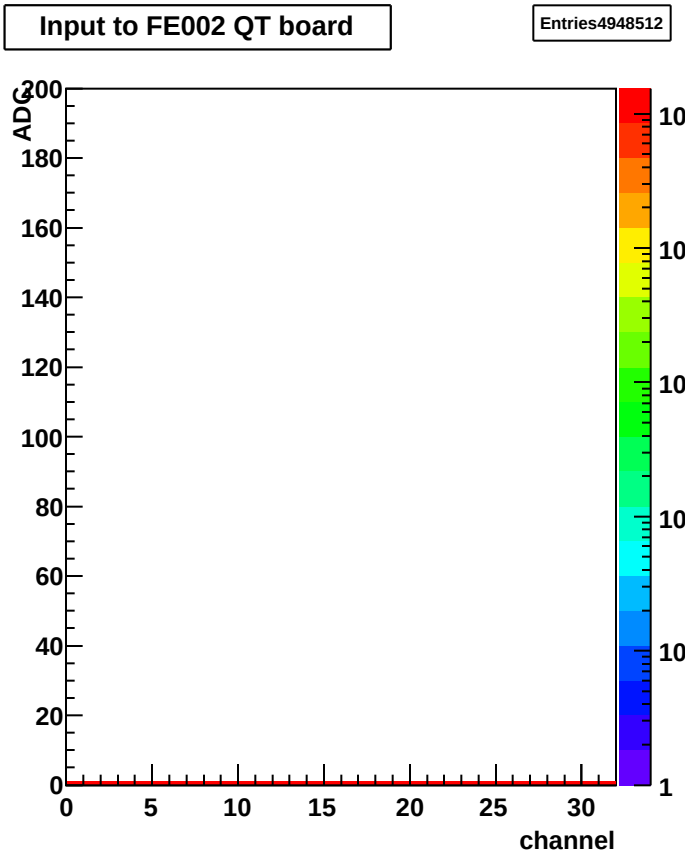

Input to FPD L2 DSM
Entries 311706
HT bits - simulated
154641
154641
2424
SMALL
LARGE-S
LARGE-N

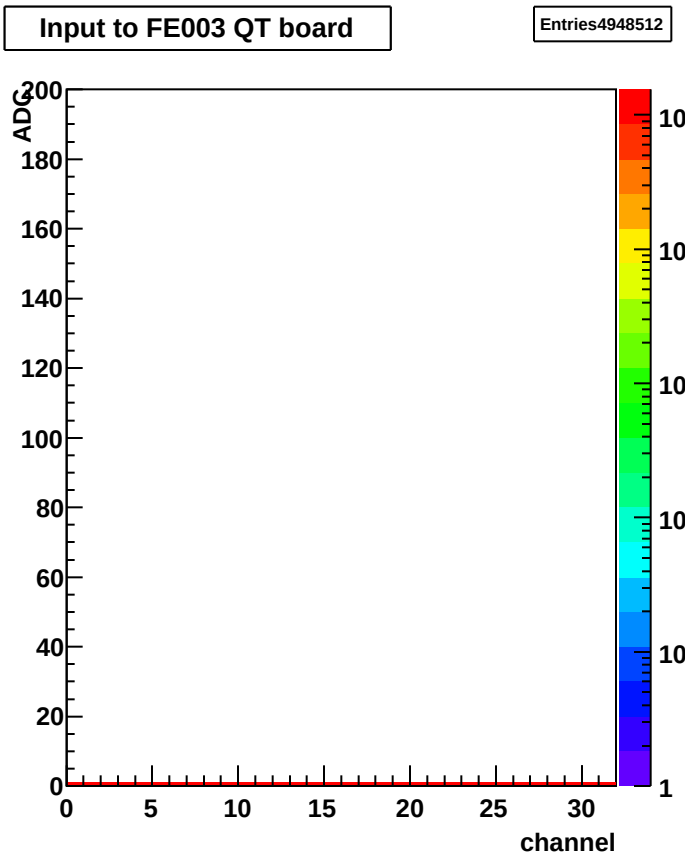

Input to FPD L3 DSM
Entries 2474256
bit
154641
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

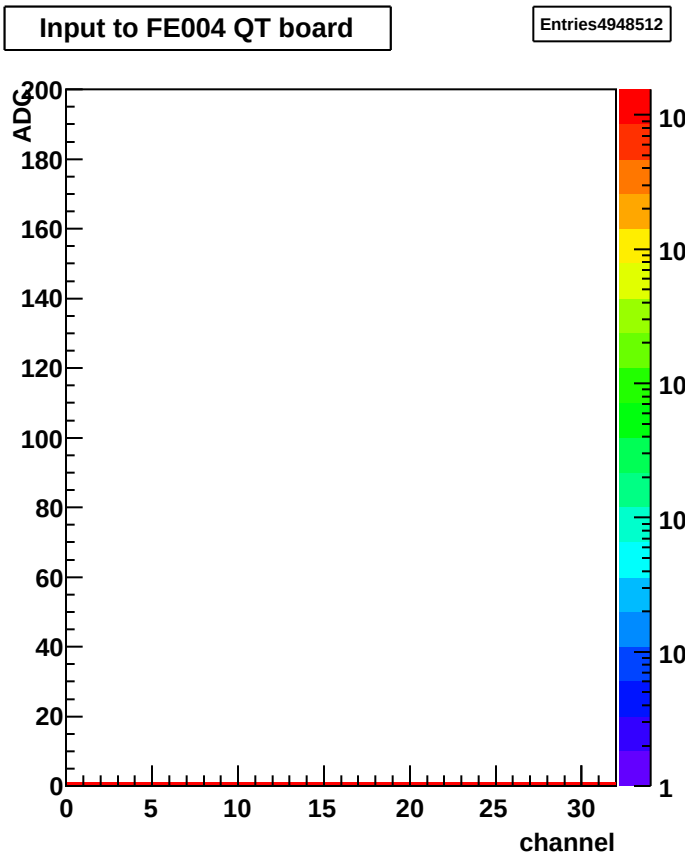

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

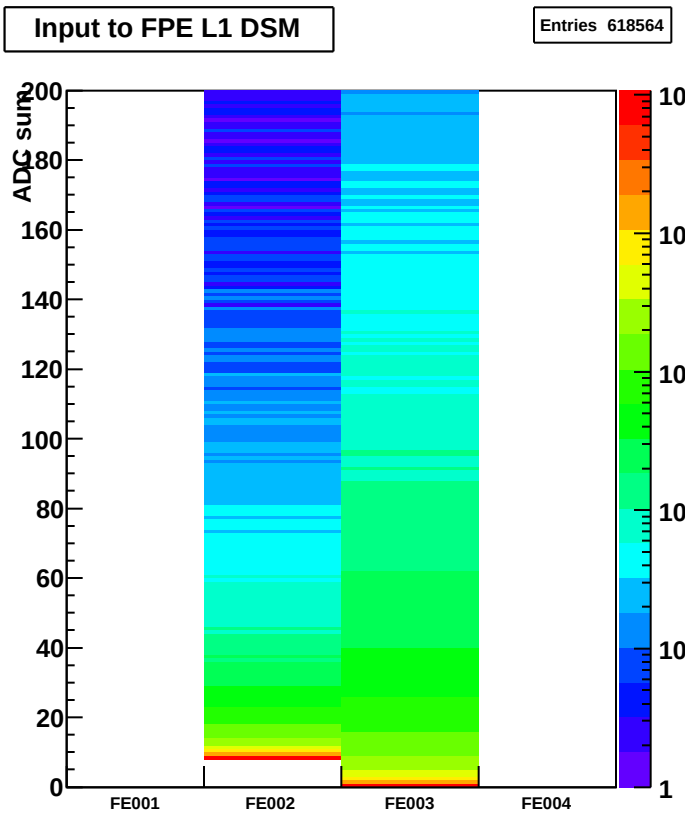

Input to FPD L2 DSM
Entries 618564
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

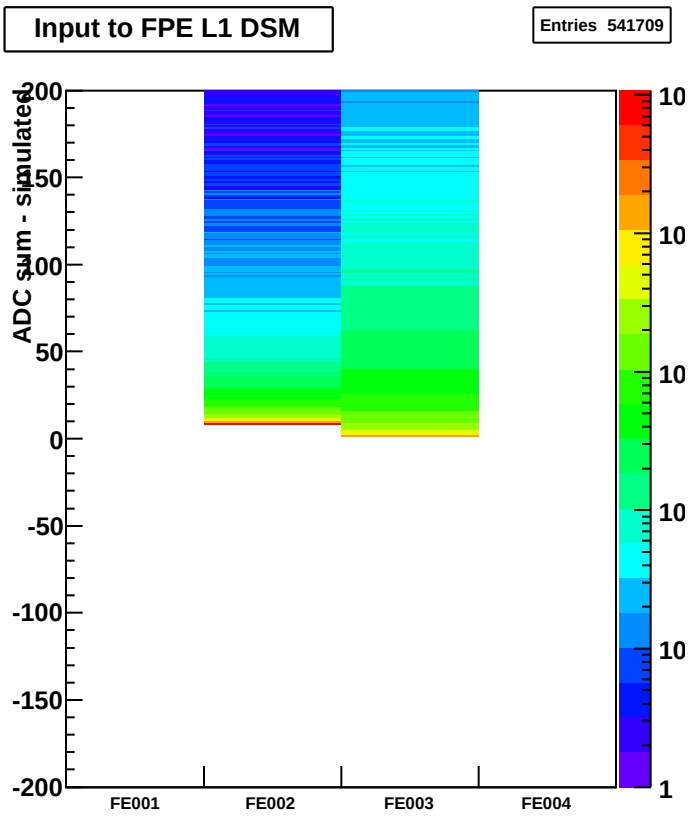

Input to FE001 QT board
Entries4948512
ADC
channel

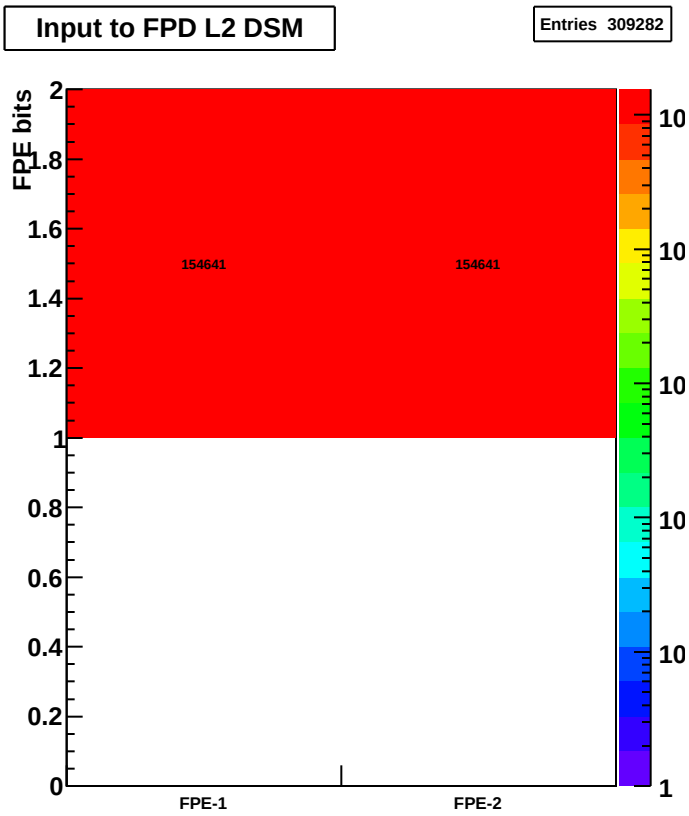

Input to FE002 QT board
Entries4948512
ADC
channel

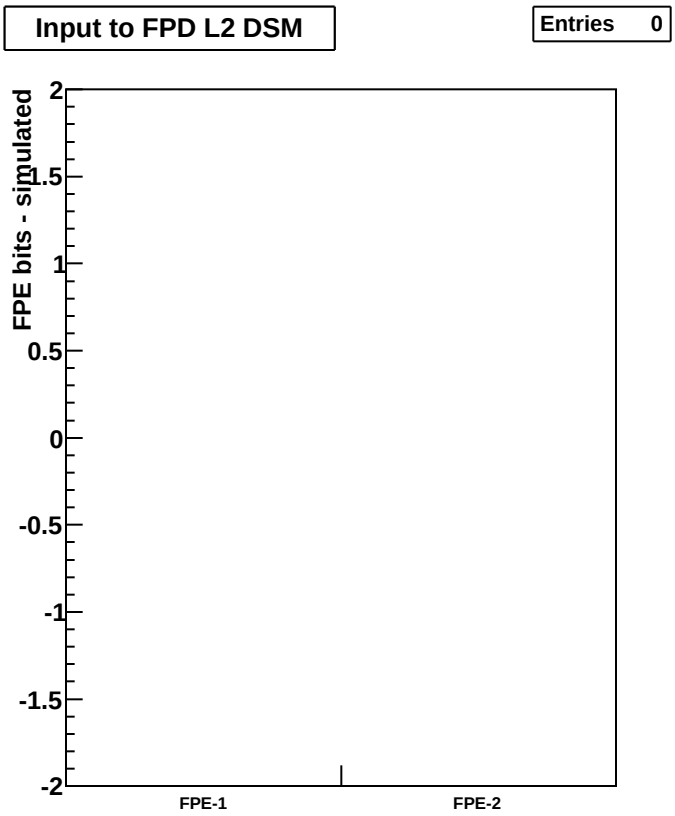

Input to FE003 QT board
Entries4948512
ADC
channel


Input to FE004 QT board
Entries4948512
ADC
channel


Input to FPE L1 DSM
Entries 618564
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 541709
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 309282
FPE bits
154641
154641
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2