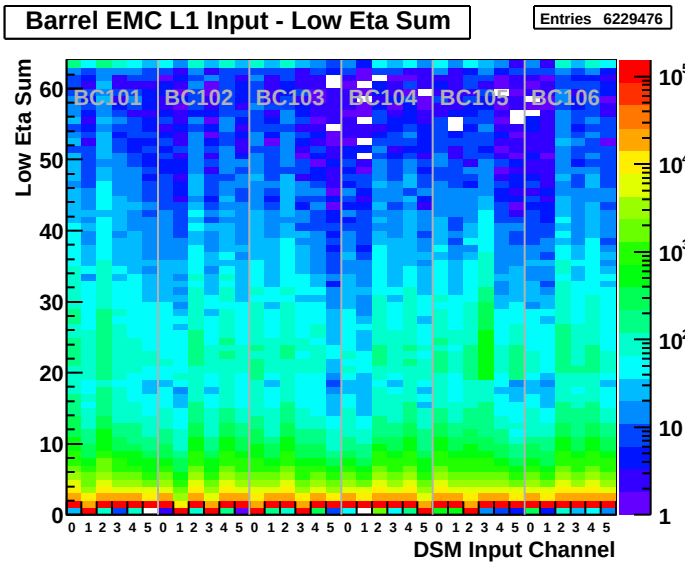
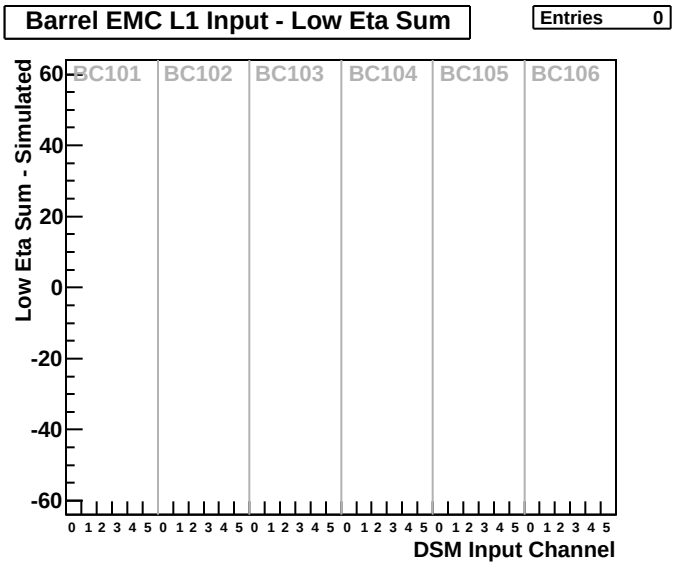
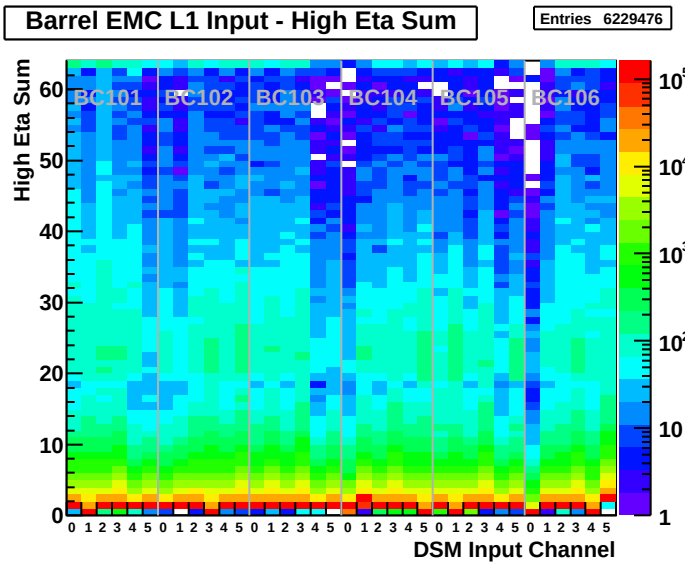
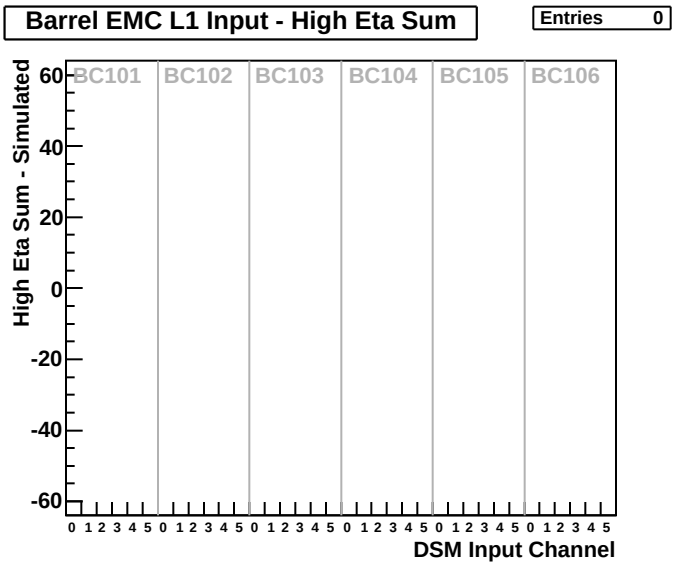
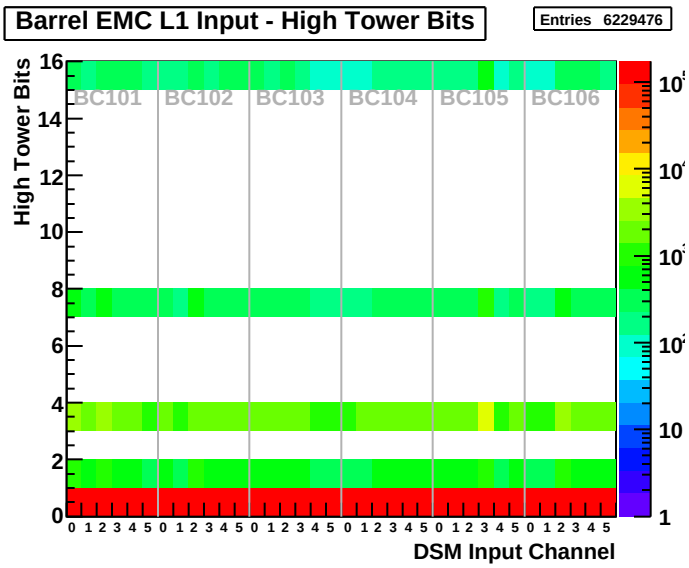
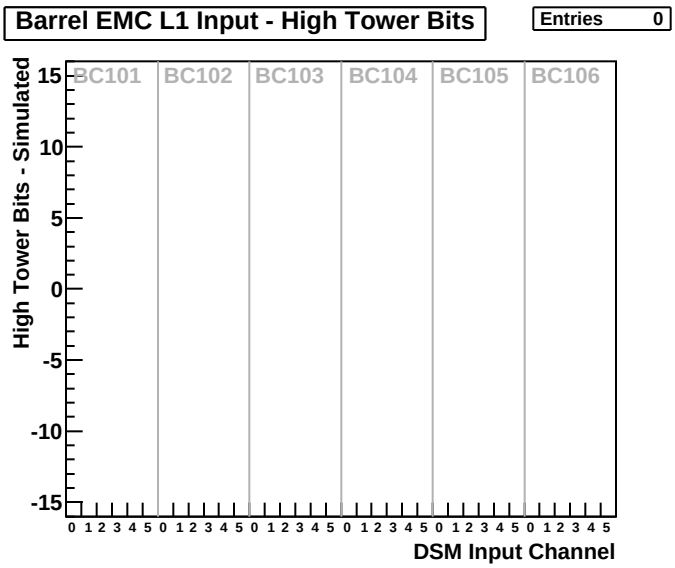

Barrel EMC L1 Input - Low Eta Sum
Entries 6229476
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

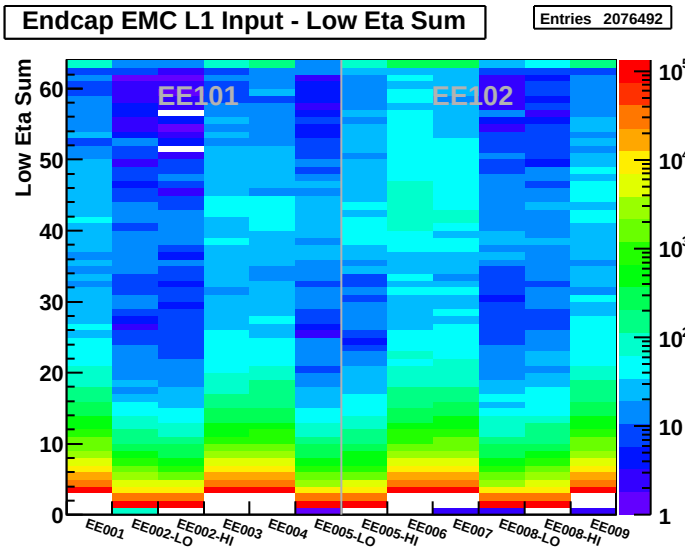

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

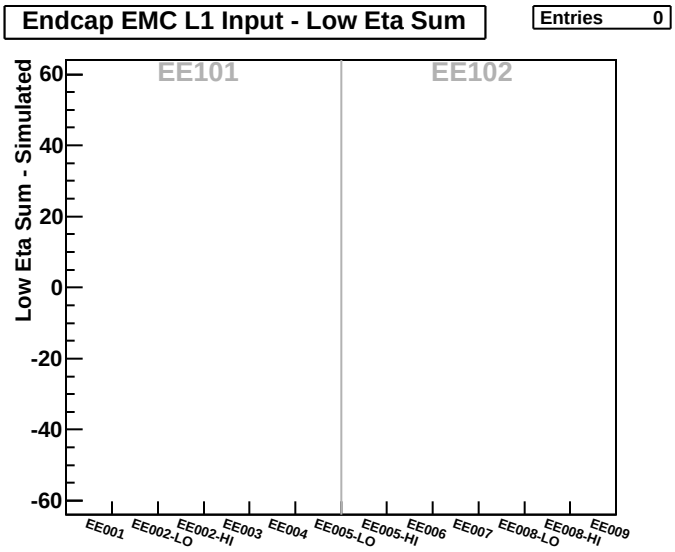

Barrel EMC L1 Input - High Eta Sum
Entries 6229476
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

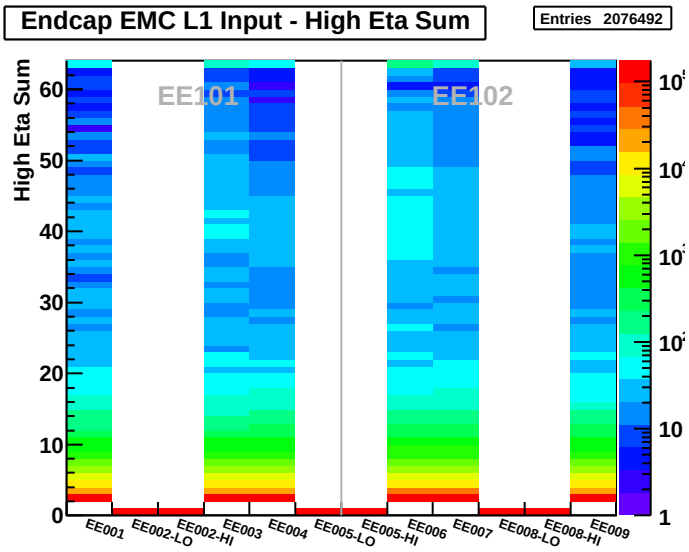

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

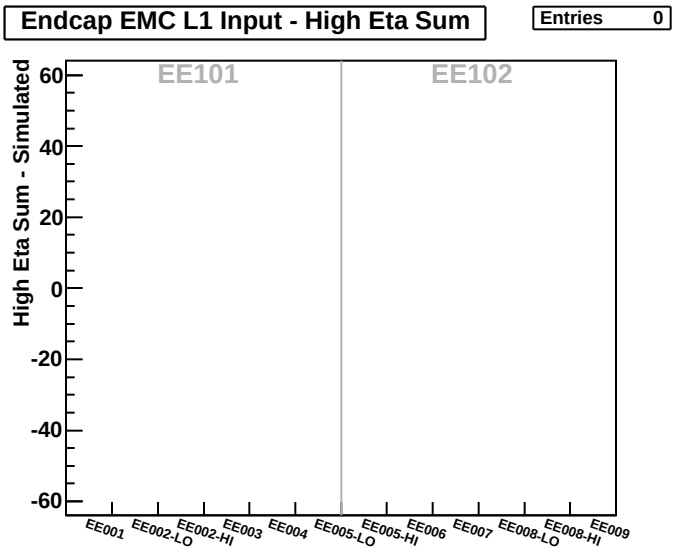

Barrel EMC L1 Input - High Tower Bits
Entries 6229476
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

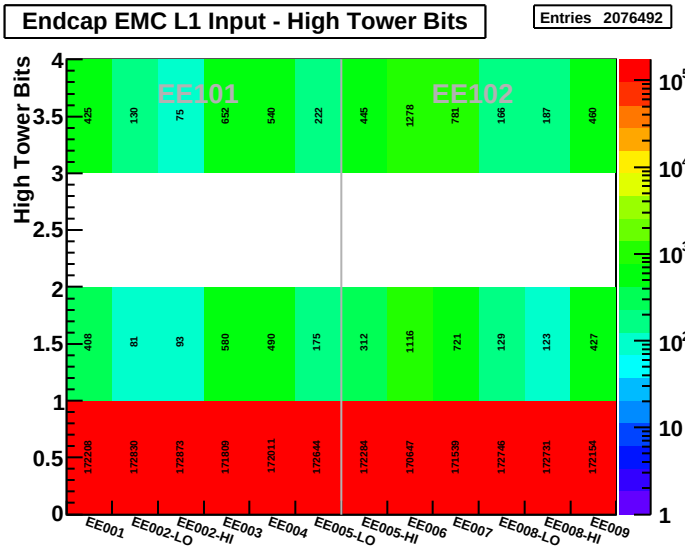

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

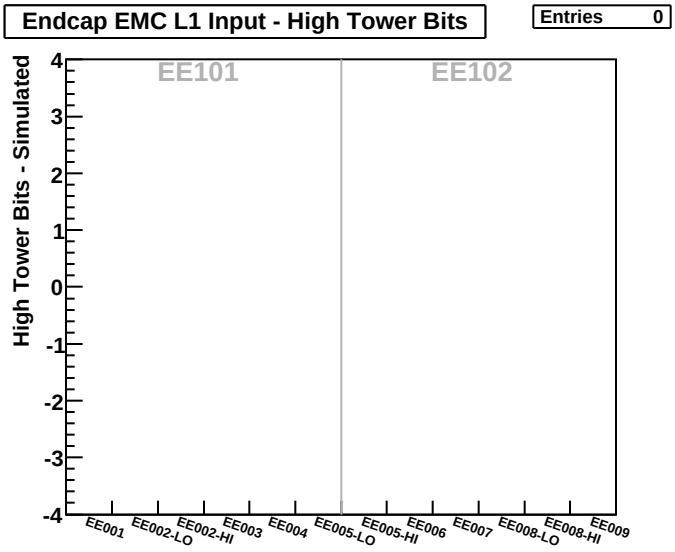

Endcap EMC L1 Input - Low Eta Sum
Entries 2076492
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

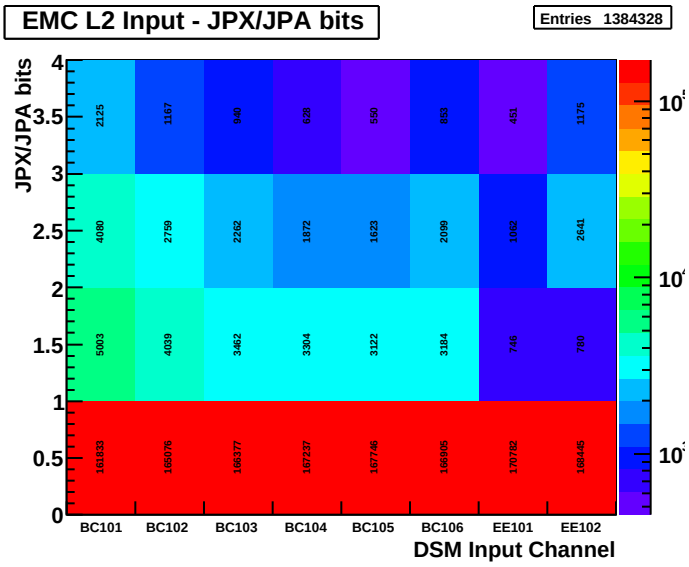

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

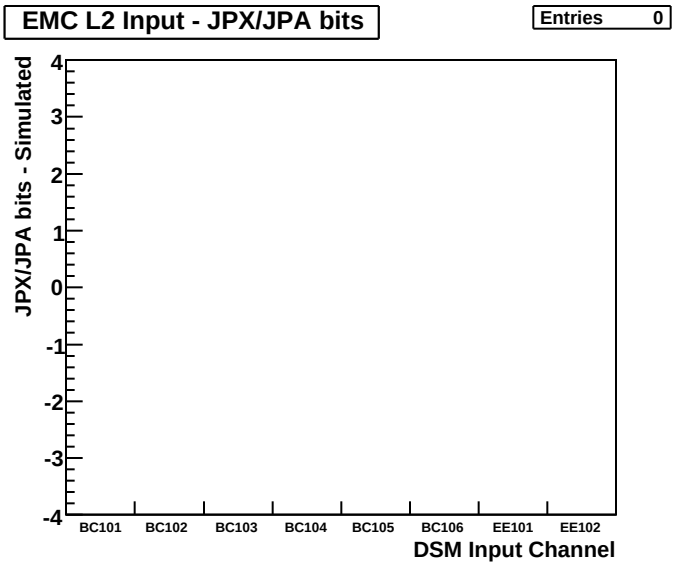

Endcap EMC L1 Input - High Eta Sum
Entries 2076492
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

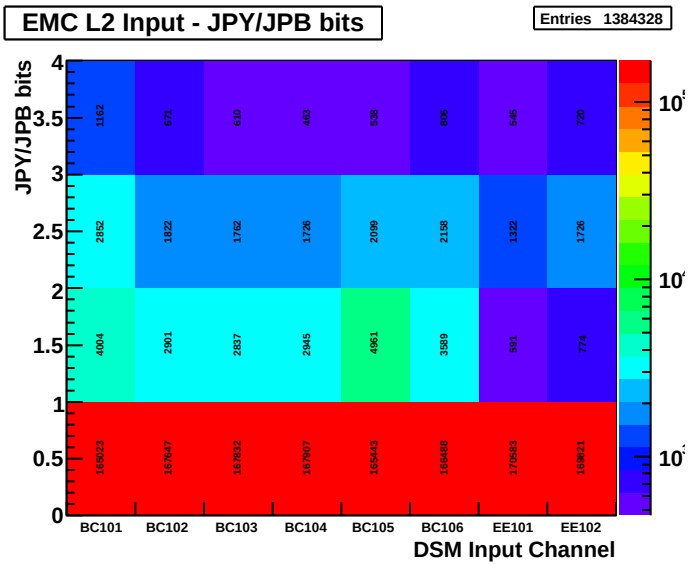

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

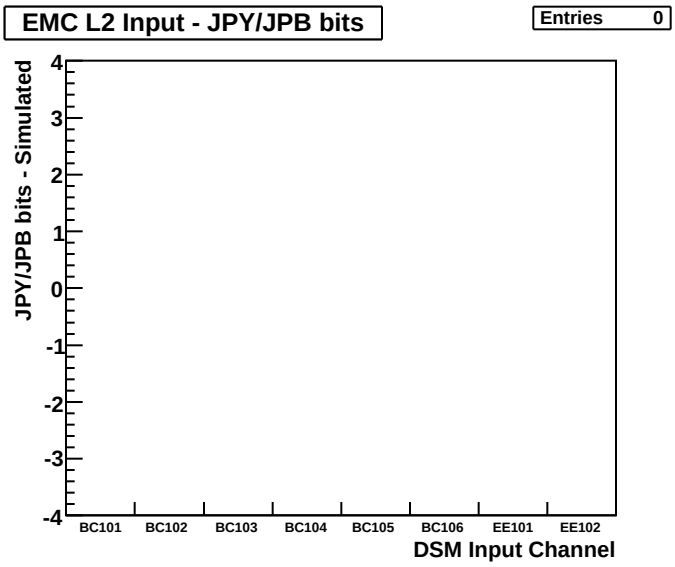

Endcap EMC L1 Input - High Tower Bits
Entries 2076492
High Tower Bits
EE101
EE102
425 130 75 652 540 222 445 1278 781 166 187 460
408 81 93 580 490 175 312 1116 721 129 123 427
172208 172830 172873 171809 172011 172644 172284 170547 171539 172746 172731 172154
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

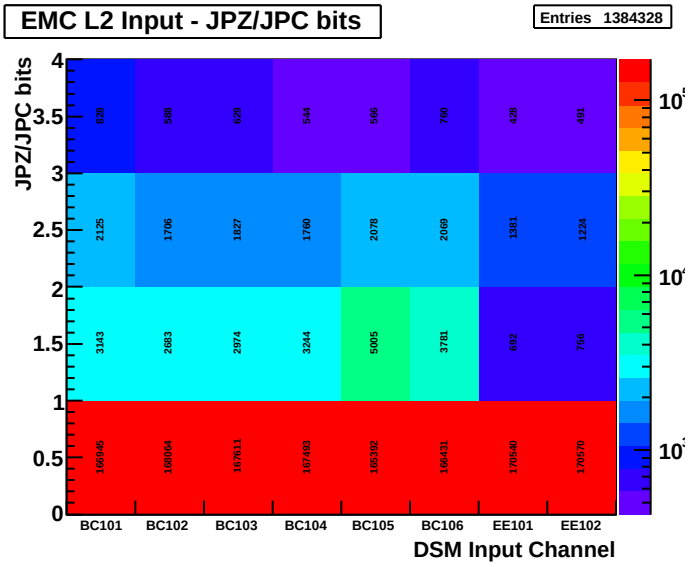

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

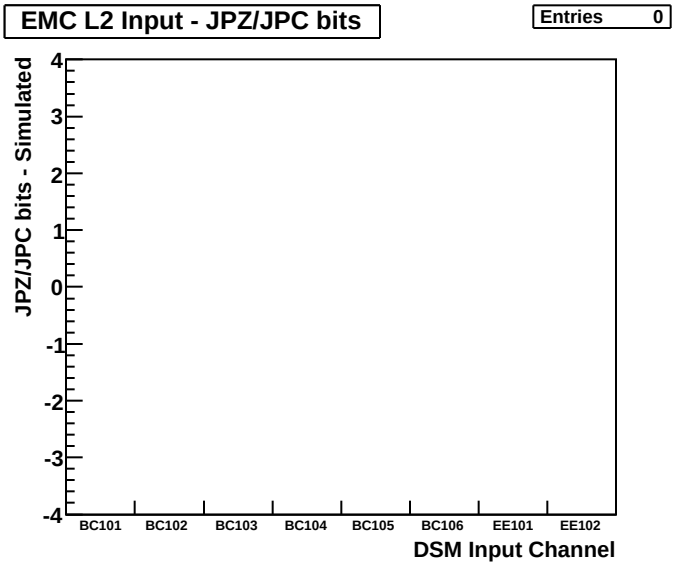

EMC L2 Input - JPX/JPA bits
Entries 1384328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2125 1167 940 628 550 853 451 1175
4080 2759 2262 1872 1623 2099 1062 2641
5003 4039 3462 3304 3122 3184 746 780
161833 165076 166377 167237 167746 166905 170752 168445

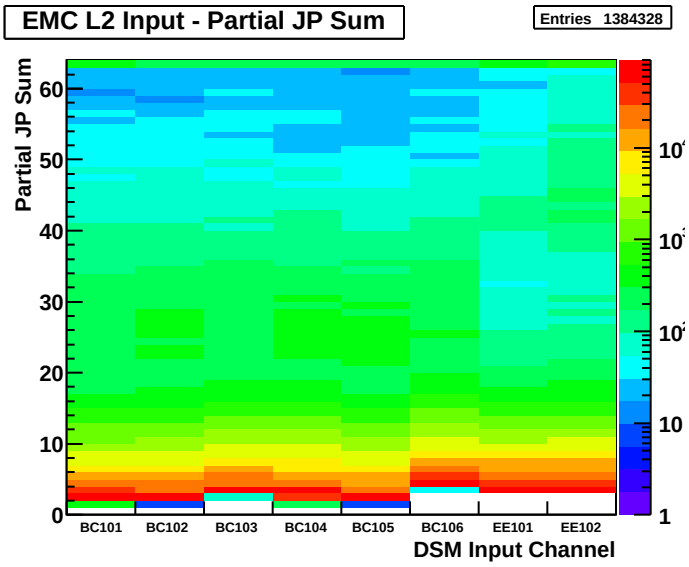

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

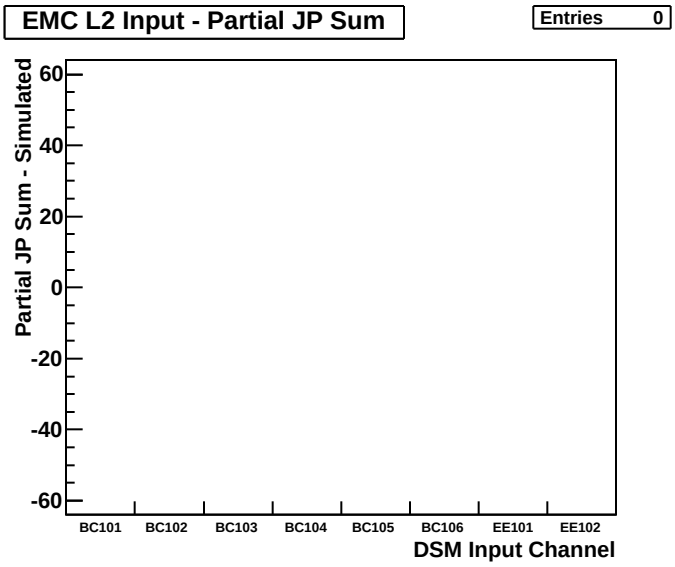

EMC L2 Input - JPY/JPB bits
Entries 1384328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1162 671 610 463 538 806 545 720
2852 1822 1762 1726 2099 2158 1322 1726
4004 2901 2837 2945 4961 3589 591 774
165023 167647 167832 167907 165443 166488 170583 169821

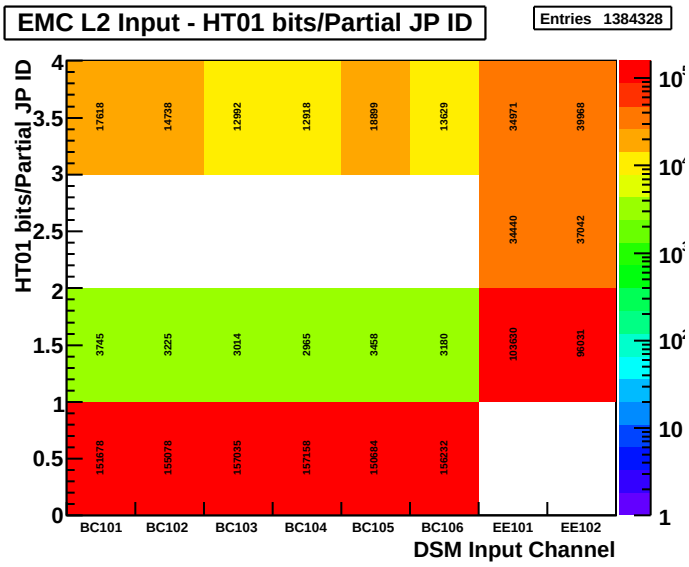

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

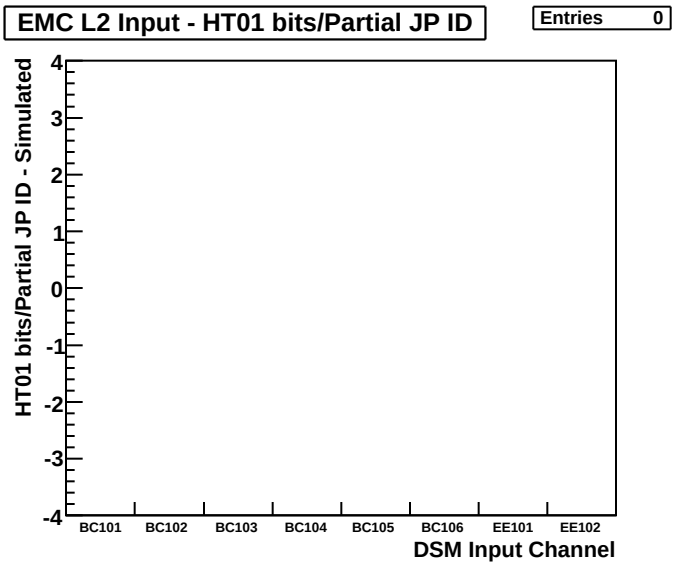

EMC L2 Input - JPZ/JPC bits
Entries 1384328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
828 588 629 544 566 760 428 491
2125 1706 1827 1760 2078 2069 1381 1224
3143 2683 2974 3244 5005 3781 692 756
166945 168064 167611 167493 165392 166431 170540 170570

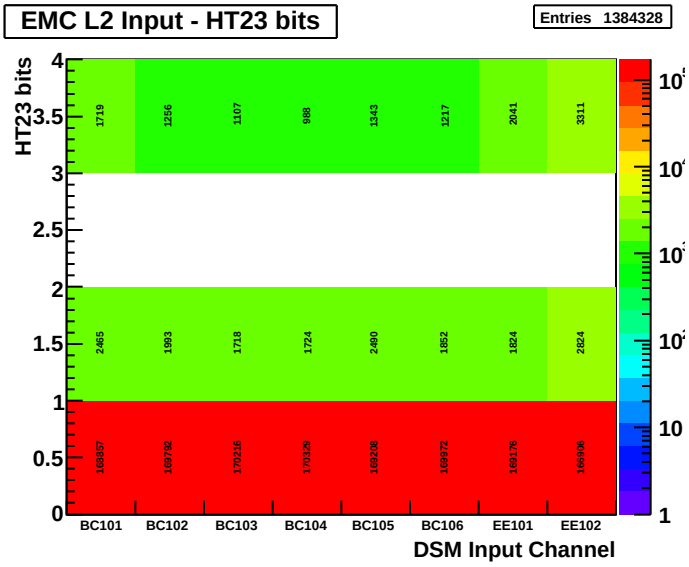

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

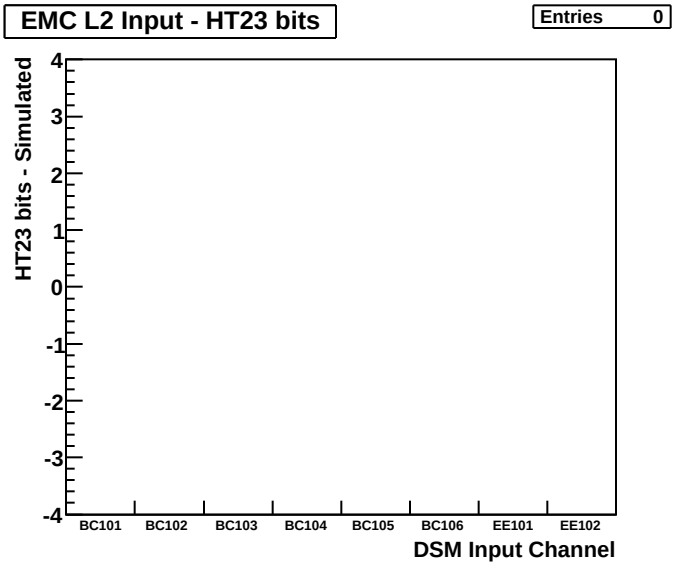

EMC L2 Input - Partial JP Sum
Entries 1384328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

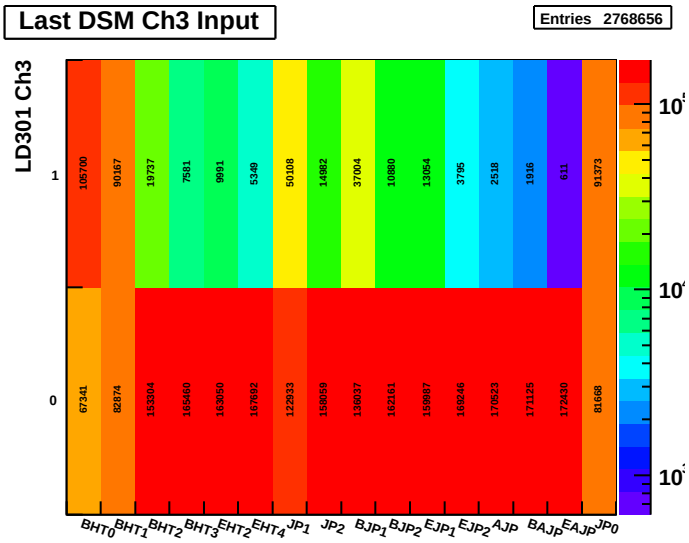

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

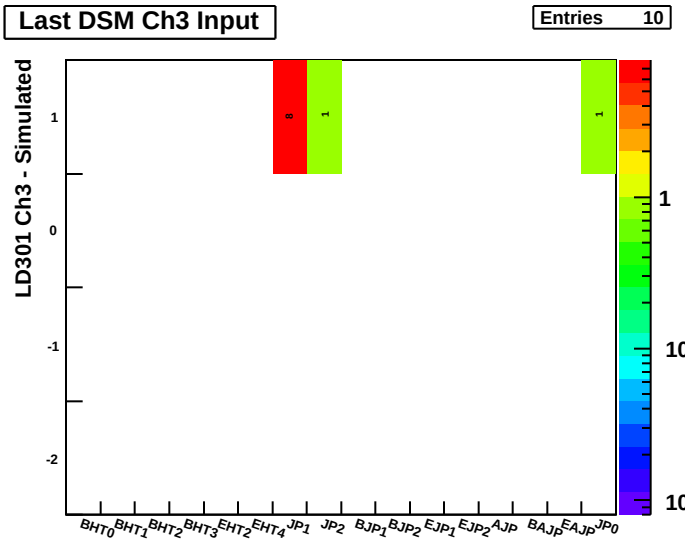

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1384328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

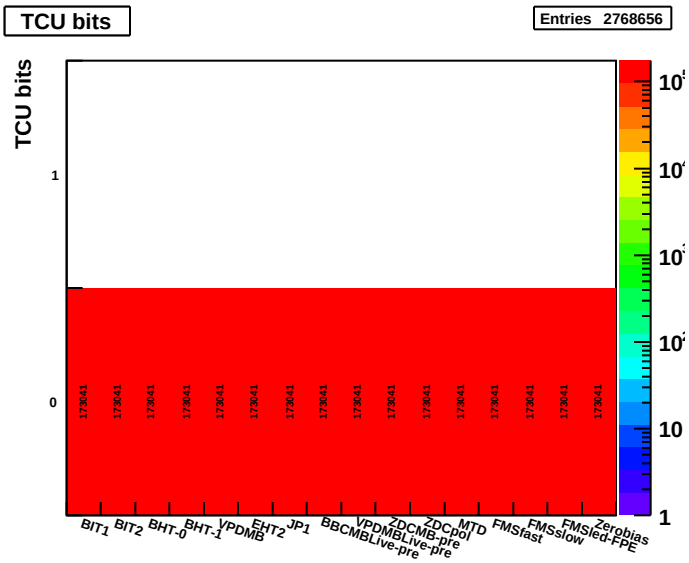

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

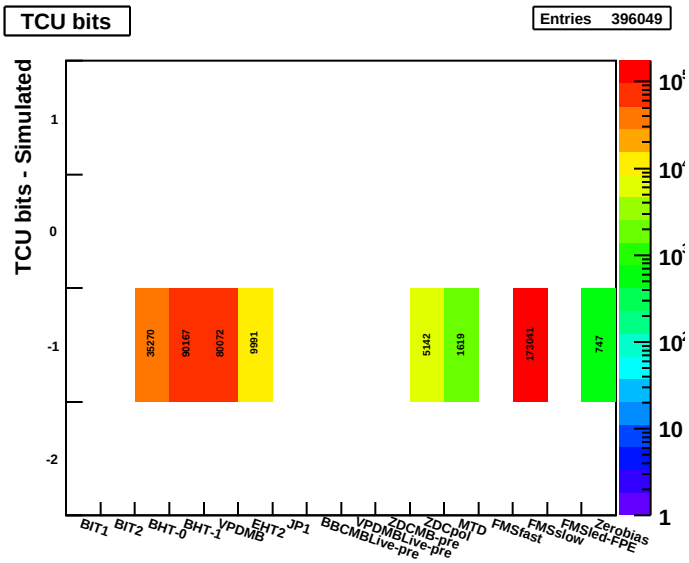

EMC L2 Input - HT23 bits
Entries 1384328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

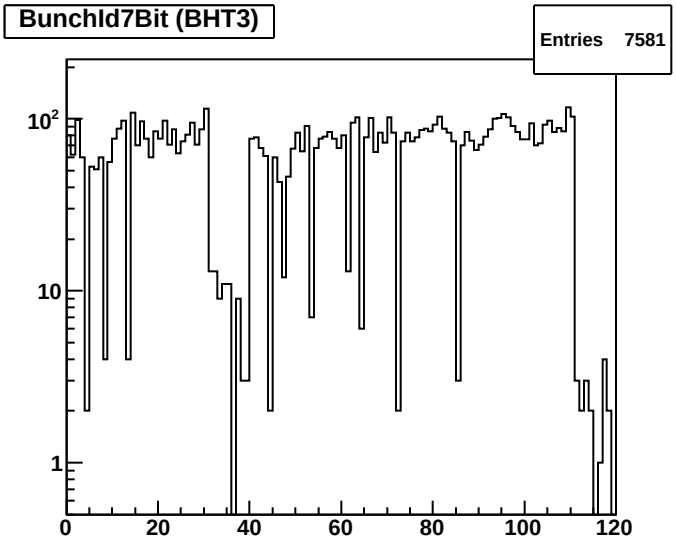

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

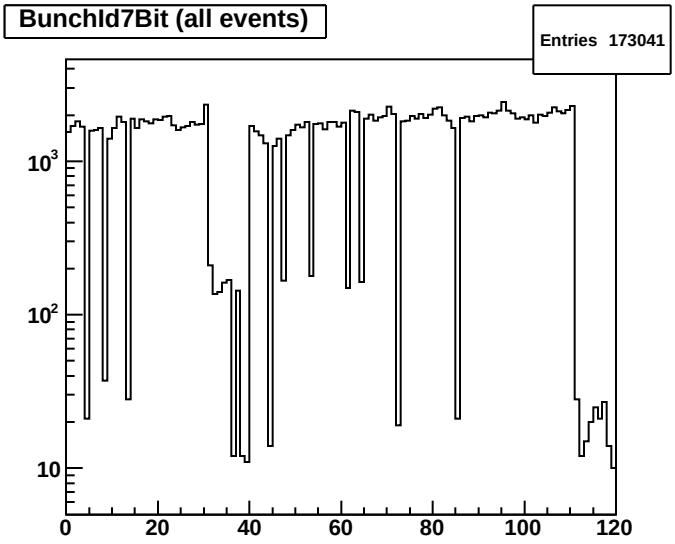

Last DSM Ch3 Input
Entries 2768656
LD301 Ch3
105700 90167 19737 7581 9991 5349 50108 14982 37004 10880 13054 3795 2518 1916 611 91373
67341 82874 153304 165460 163050 167692 122933 158059 136037 162161 159987 169246 170523 171125 172430 81668
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

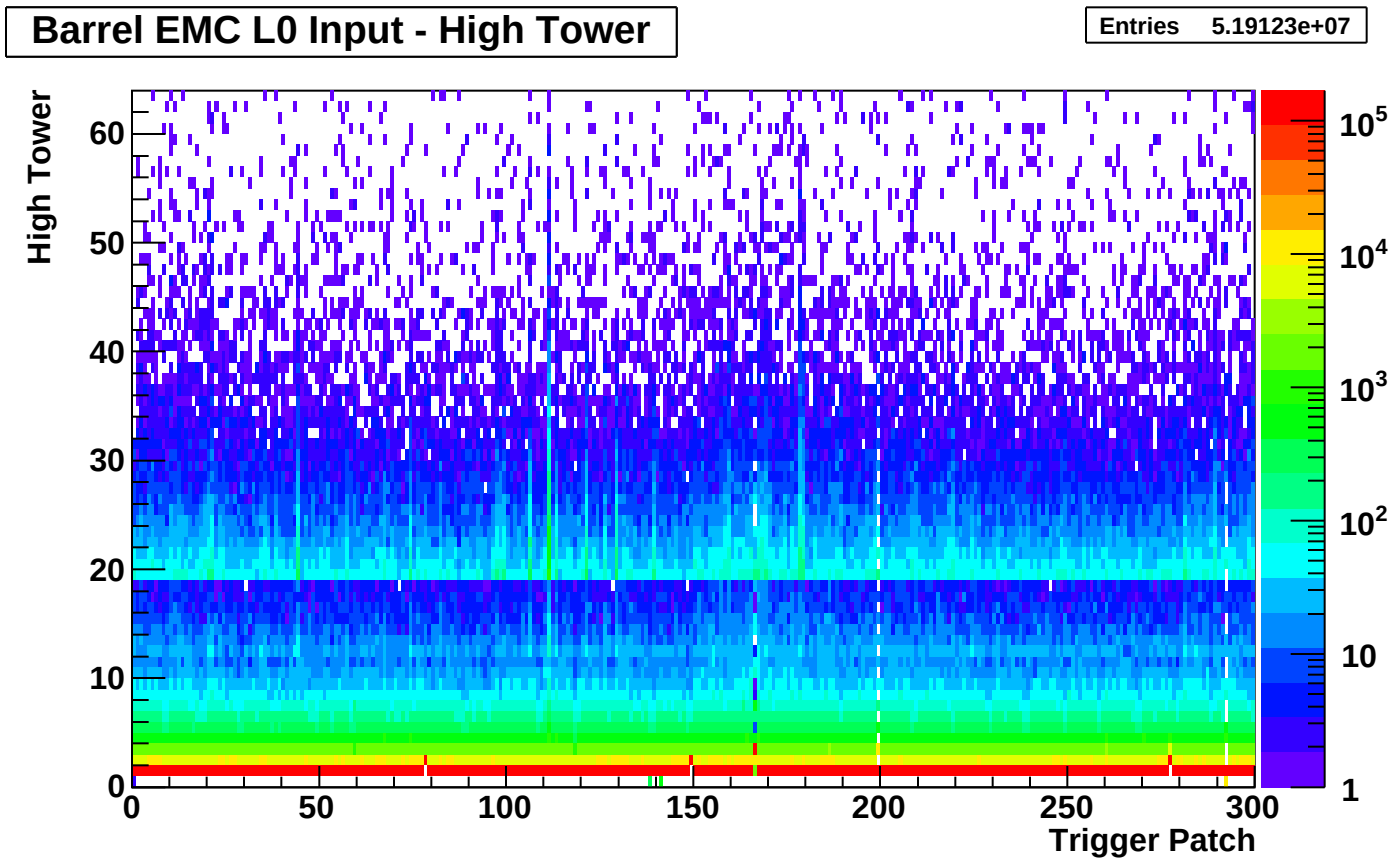

Last DSM Ch3 Input
Entries 10
LD301 Ch3 - Simulated
8 1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

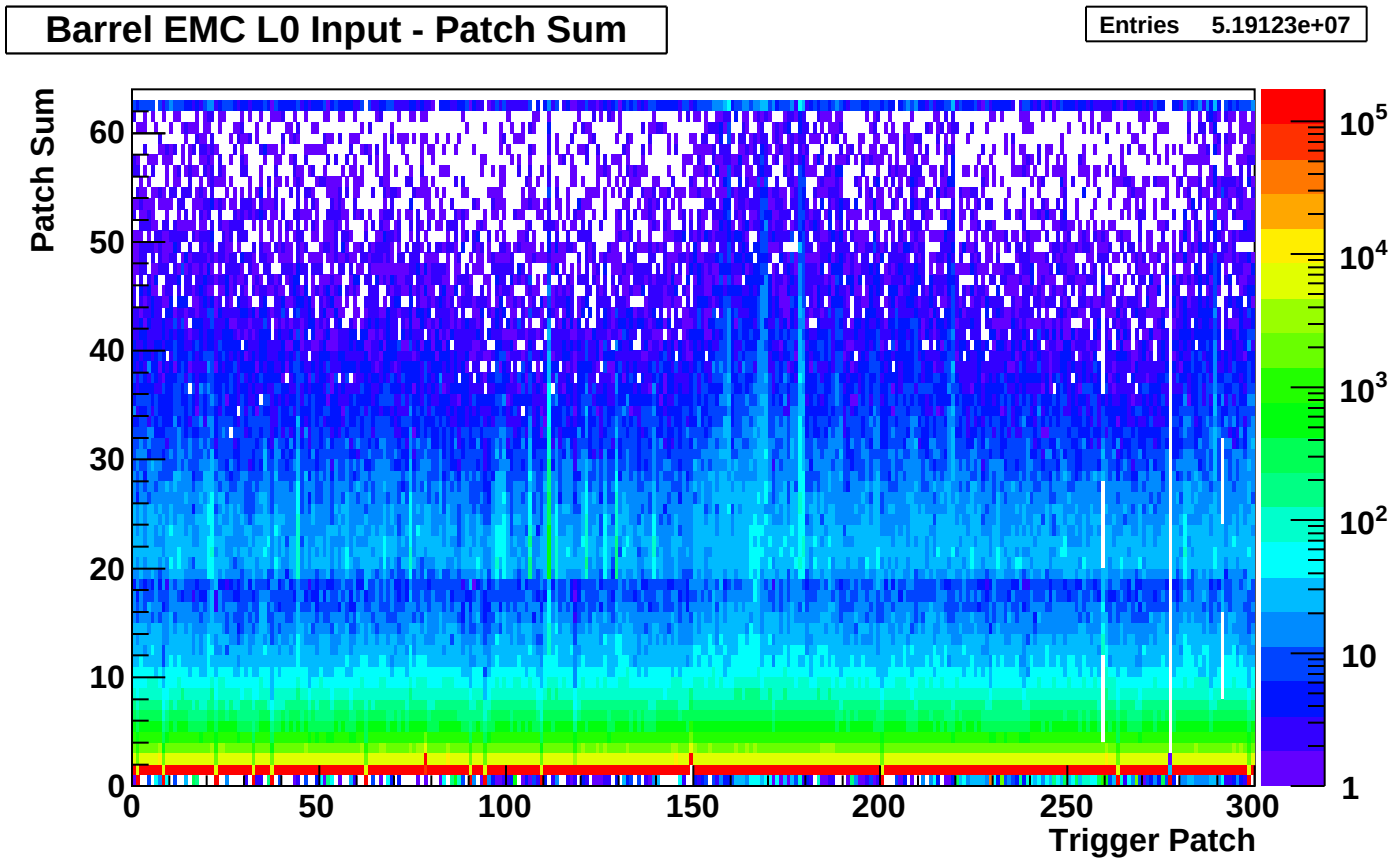

TCU bits
Entries 2768656
TCU bits
173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041 173041
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

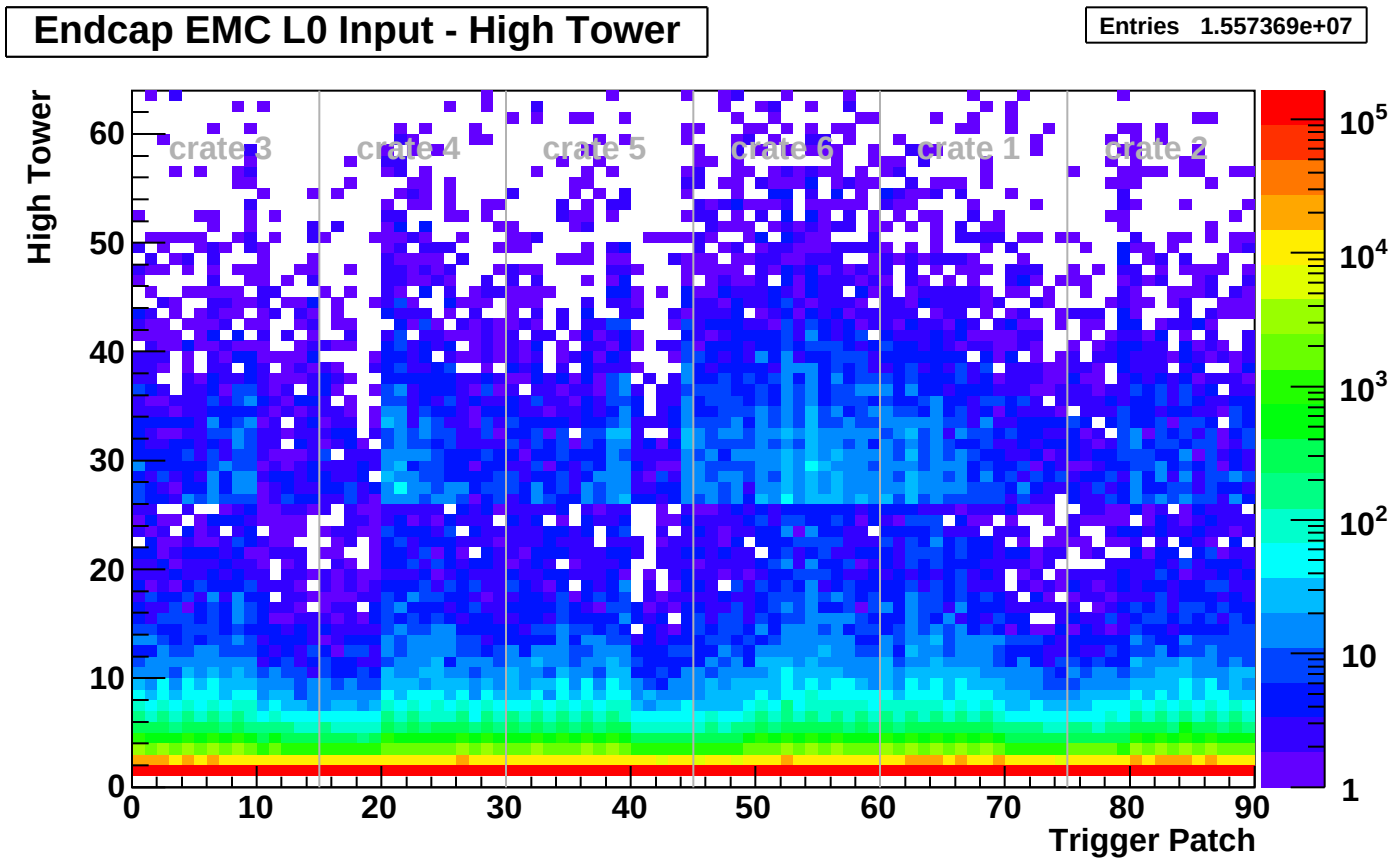

TCU bits
Entries 396049
TCU bits - Simulated
35270 90167 80072 9991 5142 1619 173041 747
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

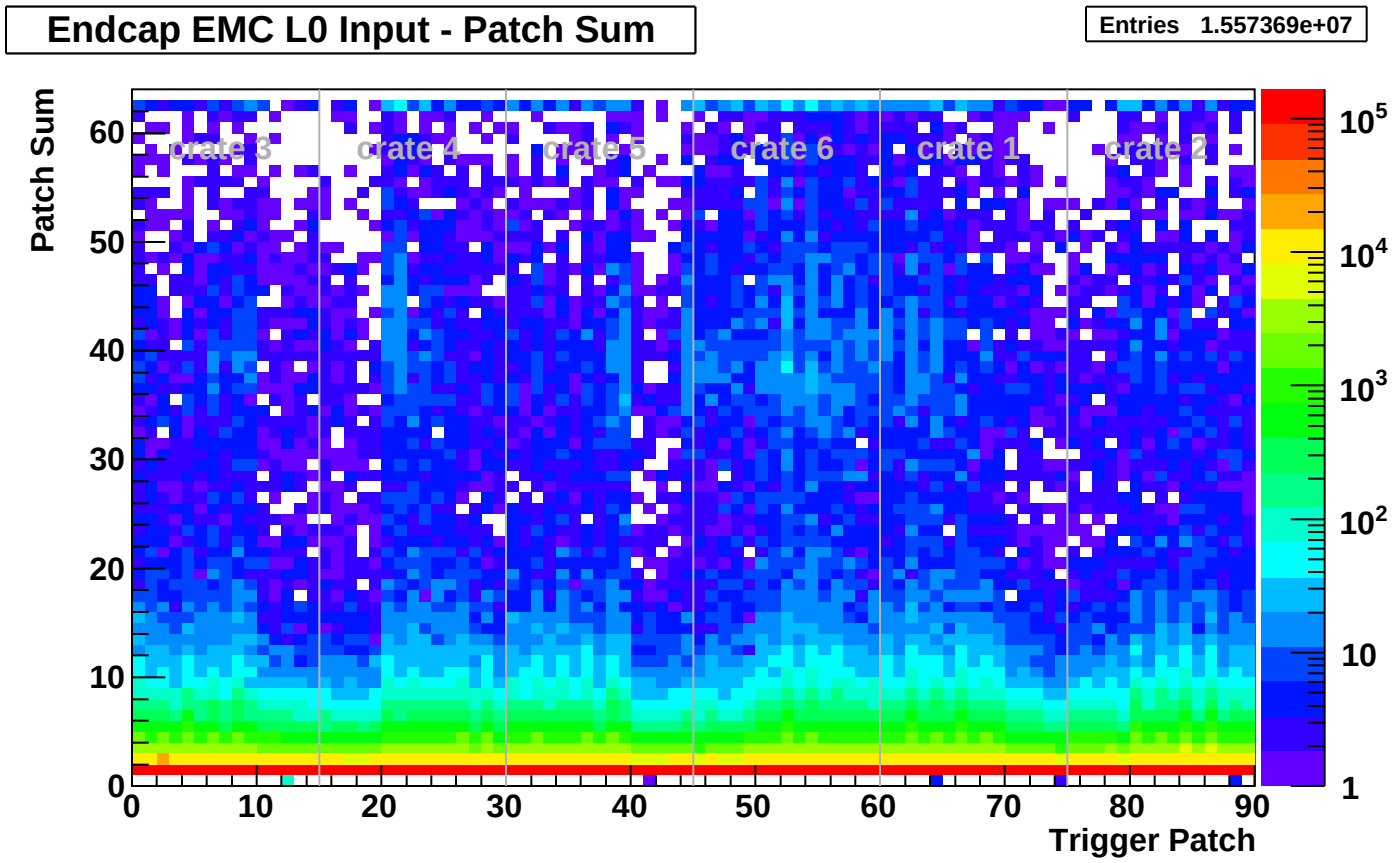

BunchId7Bit (BHT3)
Entries 7581

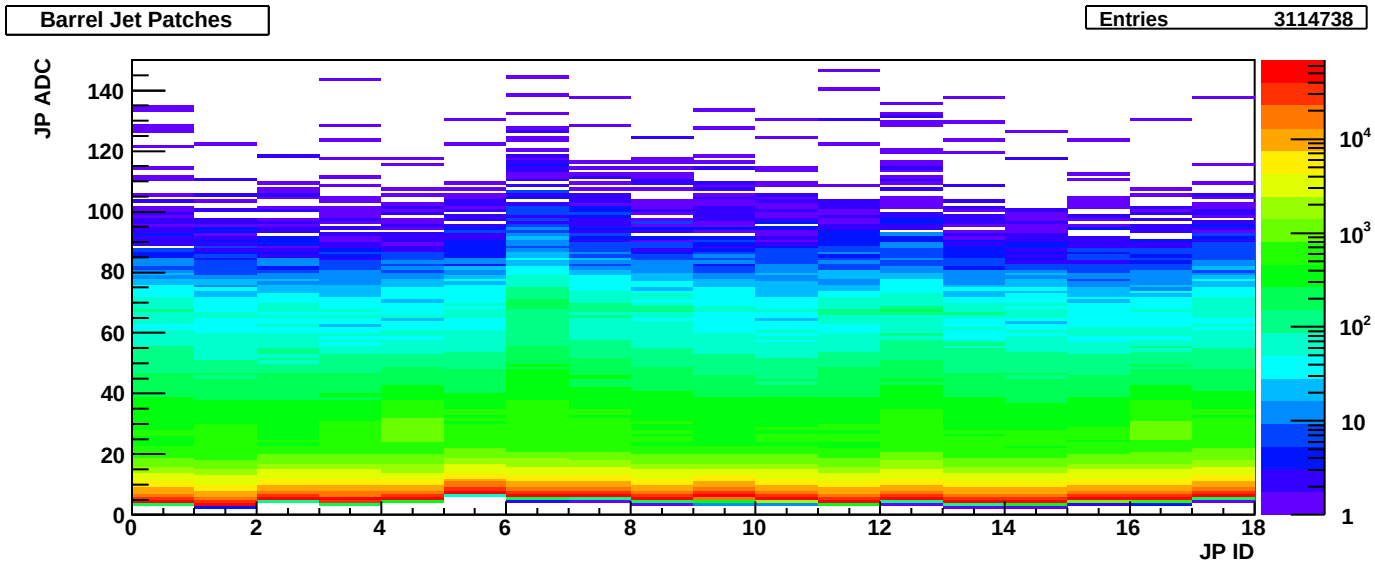

BunchId7Bit (all events)
Entries 173041

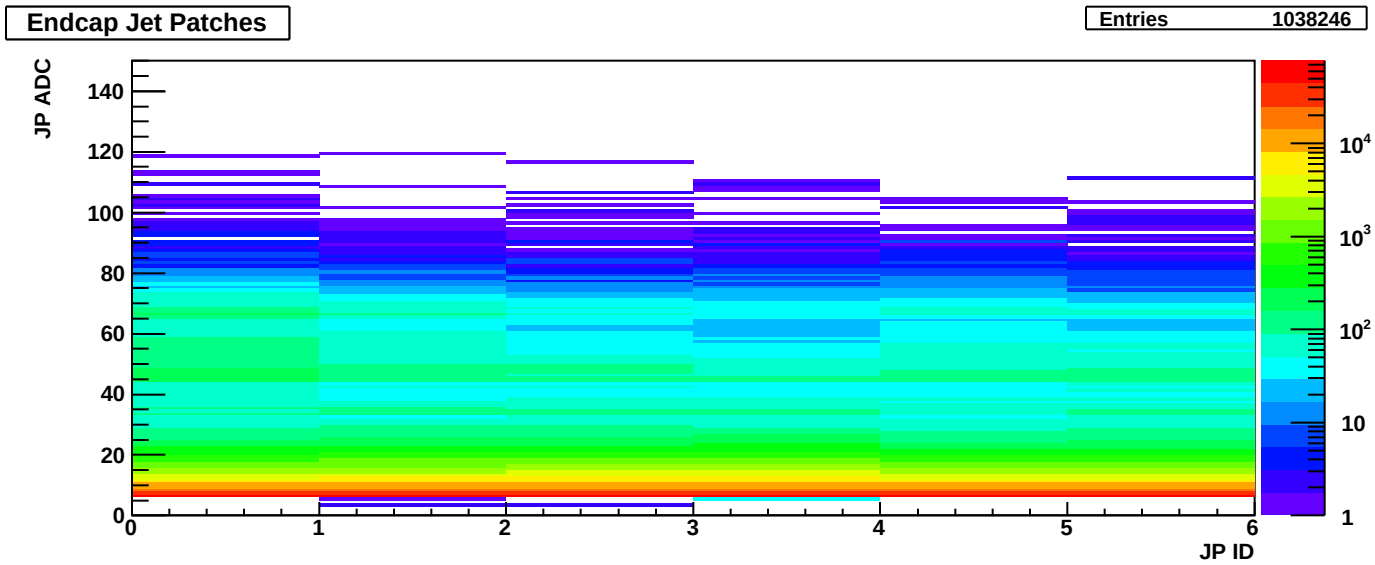

Barrel EMC L0 Input - High Tower
Entries 5.19123e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

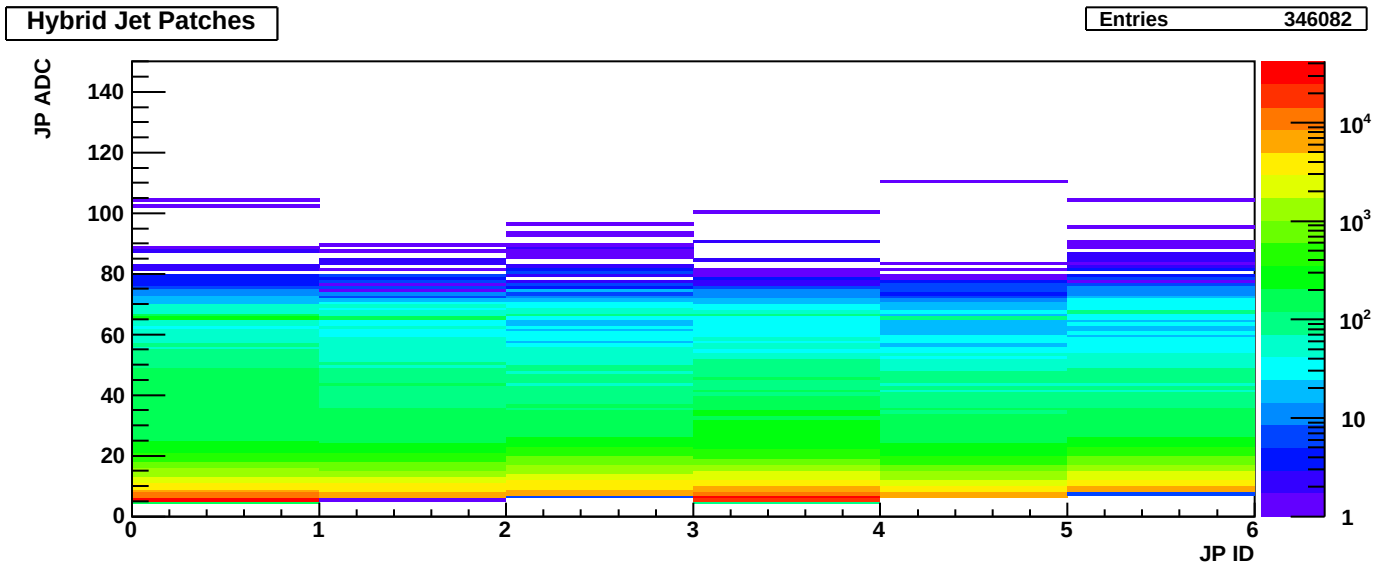

Barrel EMC L0 Input - Patch Sum
Entries 5.19123e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

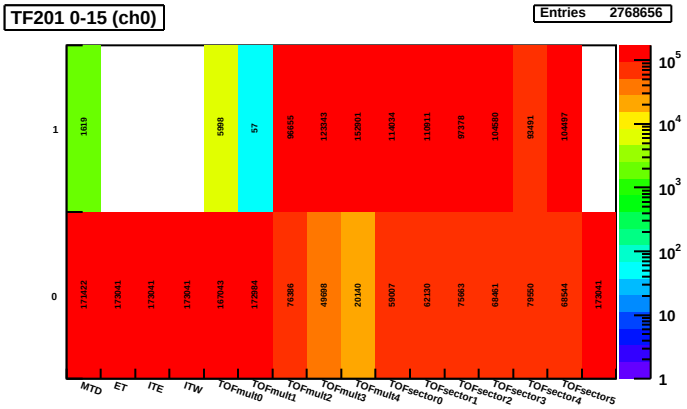

Endcap EMC L0 Input - High Tower
Entries 1.557369e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

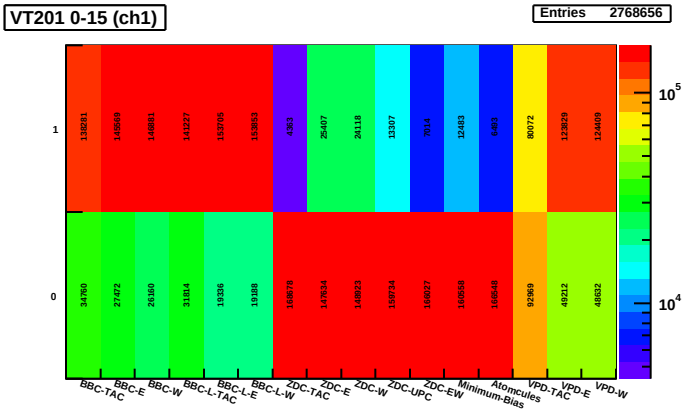

Endcap EMC L0 Input - Patch Sum
Entries 1.557369e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

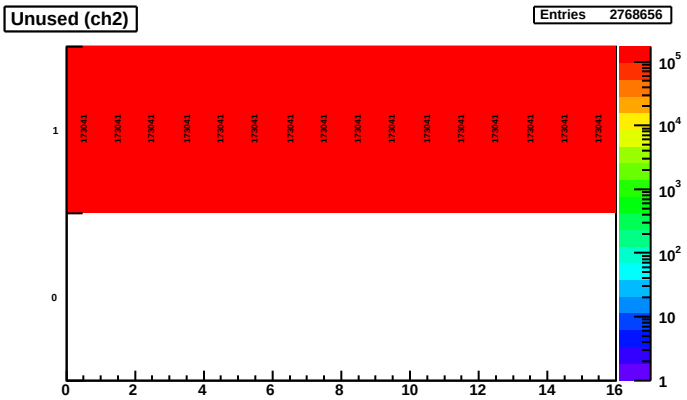

Barrel Jet Patches
Entries 3114738
JP ADC
JP ID

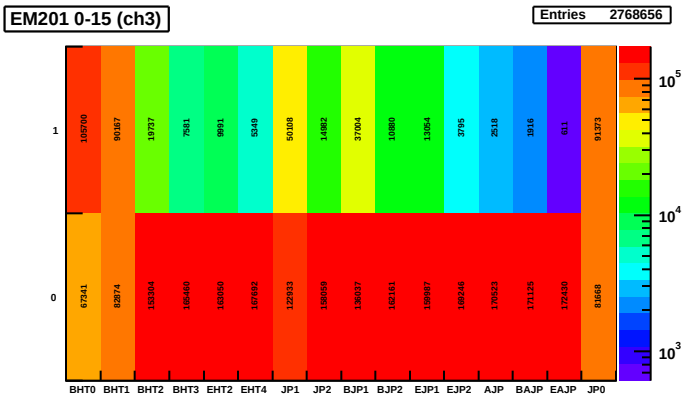

Endcap Jet Patches
Entries 1038246
JP ADC
JP ID

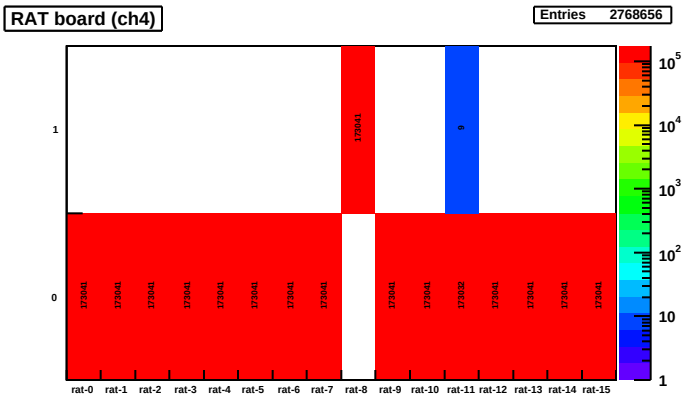

Hybrid Jet Patches
Entries 346082
JP ADC
JP ID

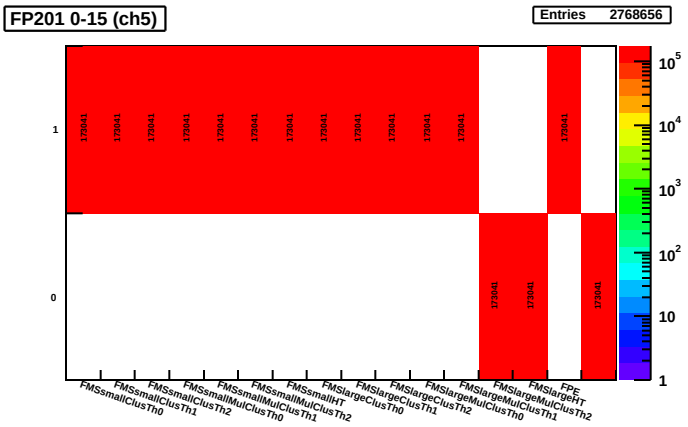

TF201 0-15 (ch0)
Entries 2768656
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

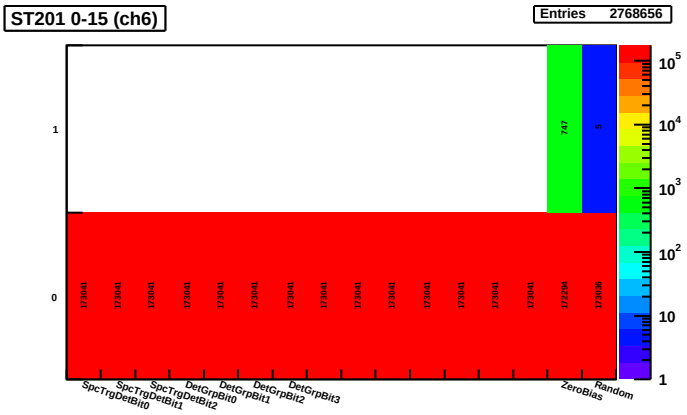

VT201 0-15 (ch1)
Entries 2768656
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

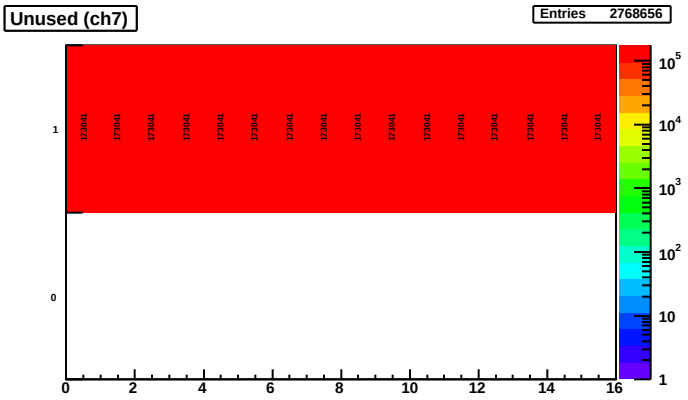

Unused (ch2)
Entries 2768656

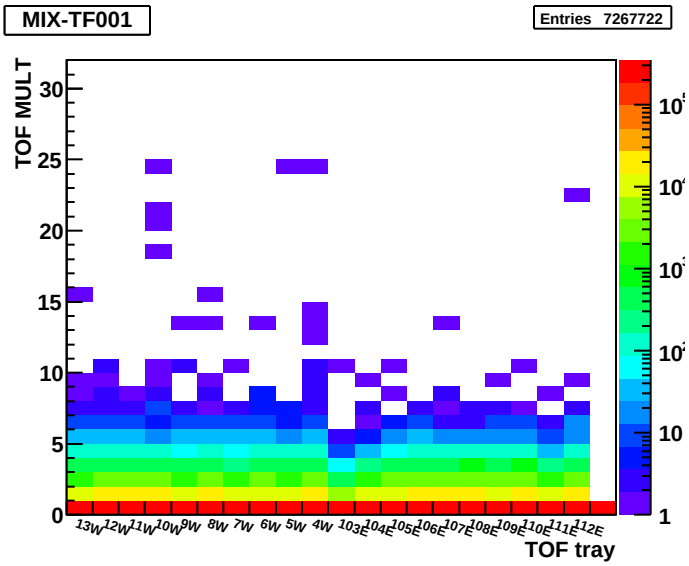

EM201 0-15 (ch3)
Entries 2768656
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

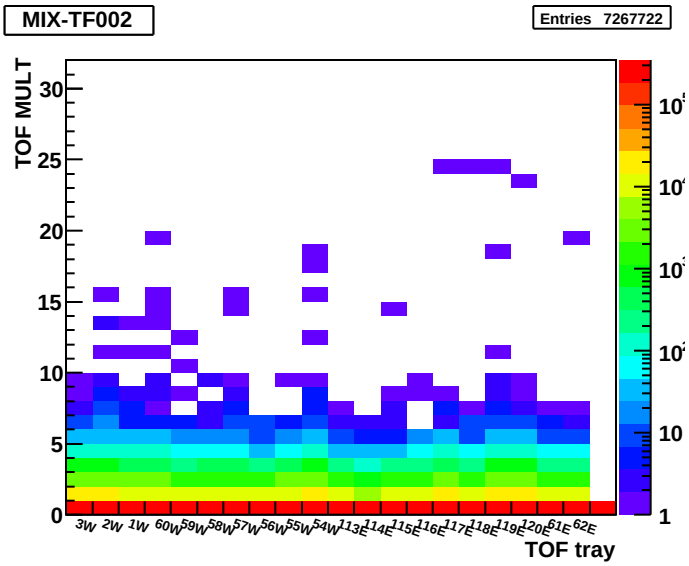

RAT board (ch4)
Entries 2768656
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

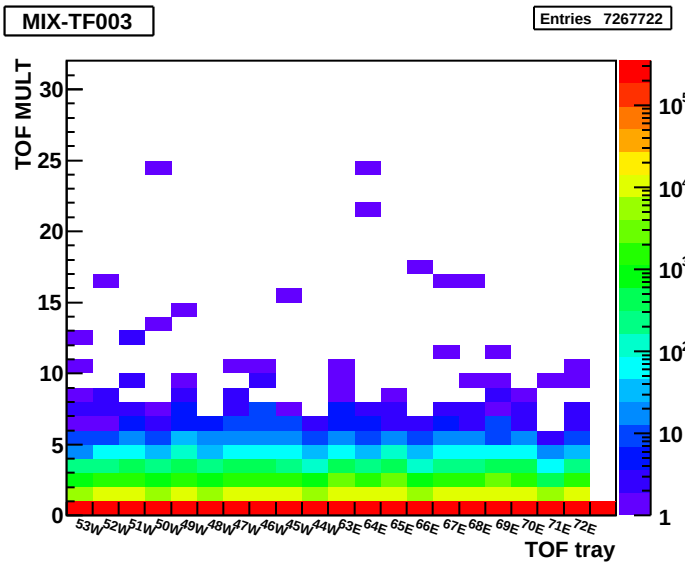

FP201 0-15 (ch5)
Entries 2768656
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

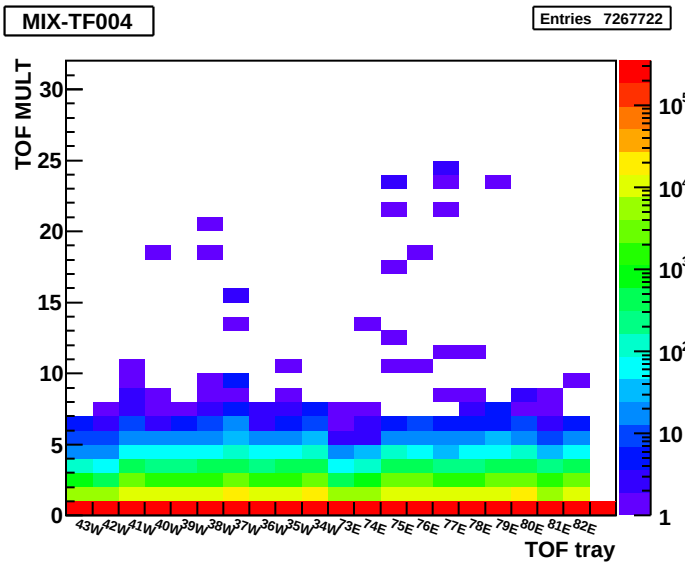

ST201 0-15 (ch6)
Entries 2768656
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

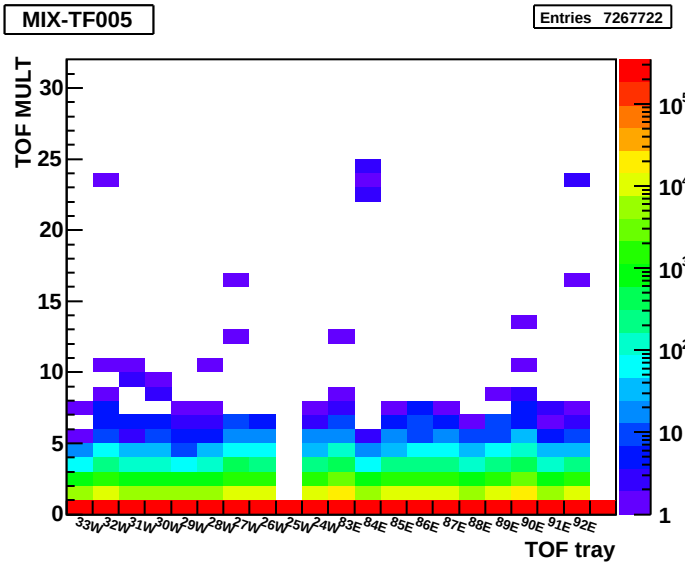

Unused (ch7)
Entries 2768656

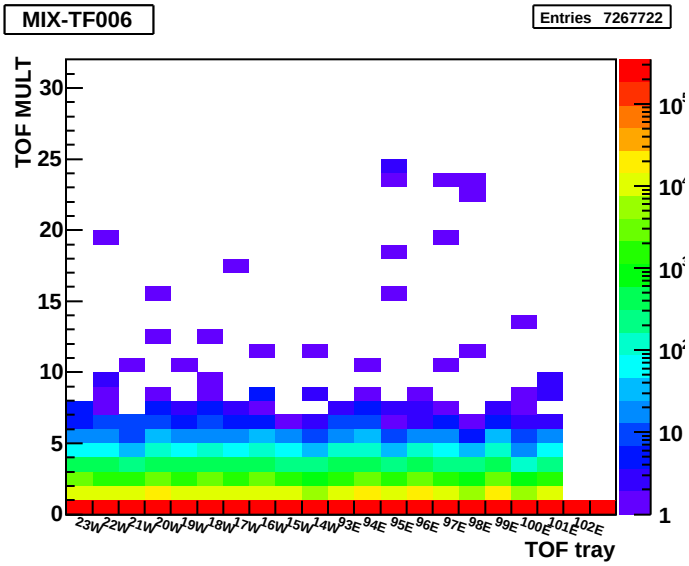

MIX-TF001
Entries 7267722
TOF MULT
TOF tray

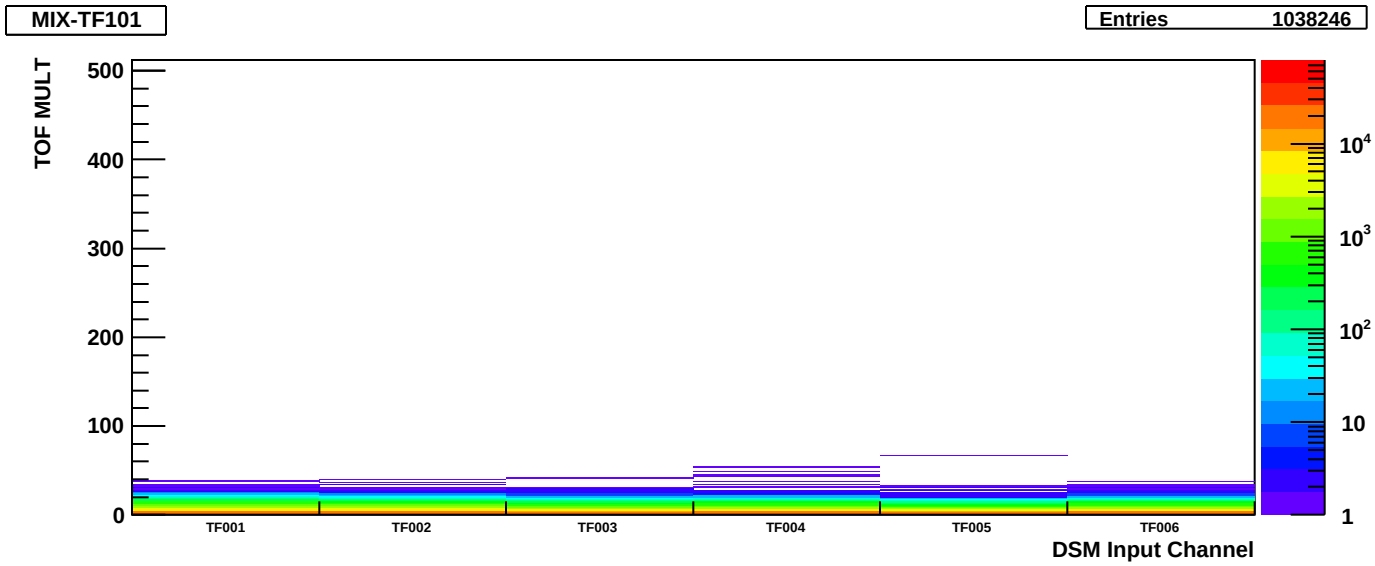

MIX-TF002
Entries 7267722
TOF MULT
TOF tray

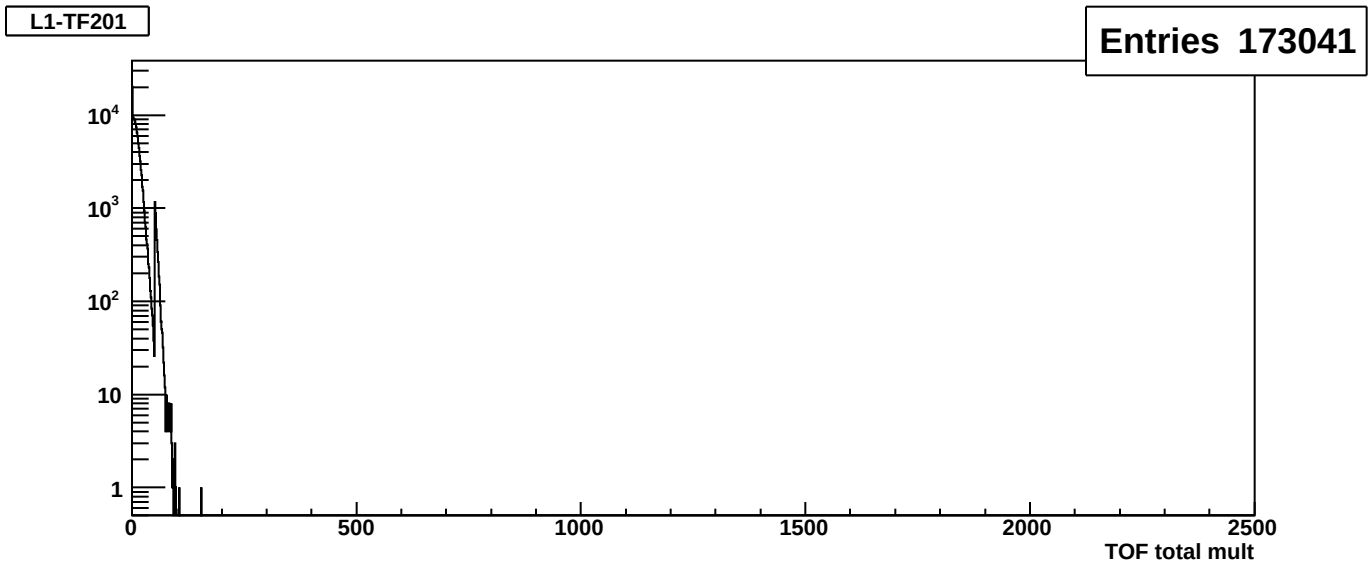

MIX-TF003
Entries 7267722
TOF MULT
TOF tray

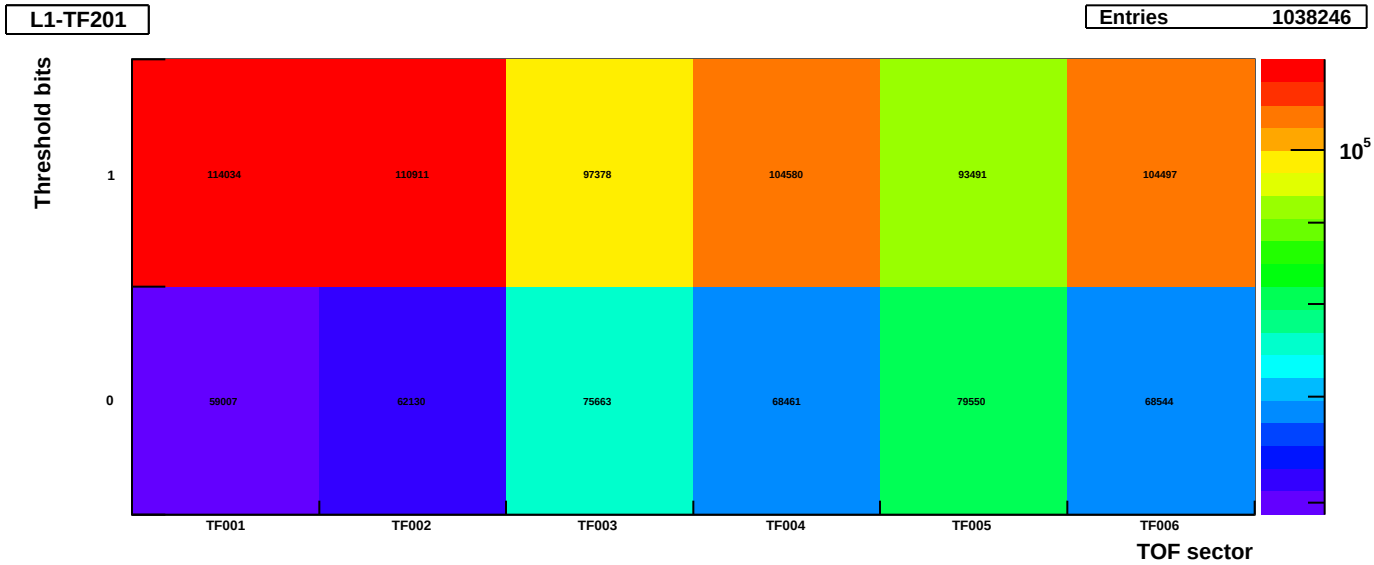

MIX-TF004
Entries 7267722
TOF MULT
TOF tray

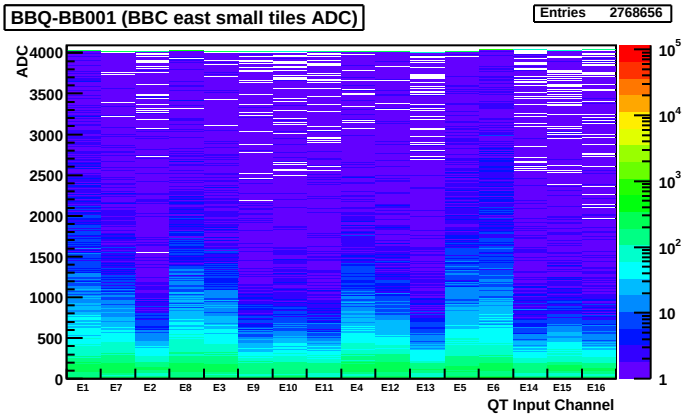

MIX-TF005
Entries 7267722
TOF MULT
TOF tray

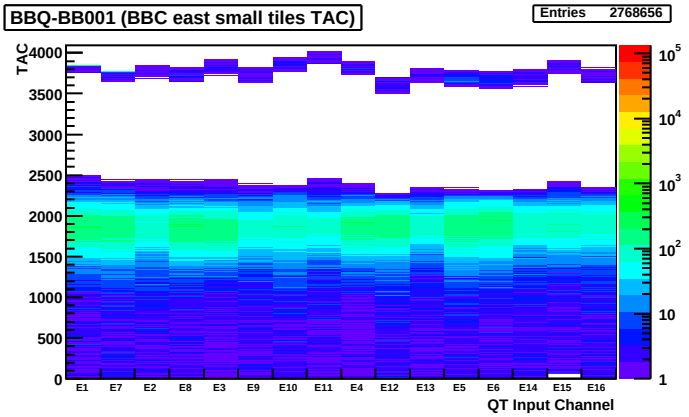

MIX-TF006
Entries 7267722
TOF MULT
TOF tray

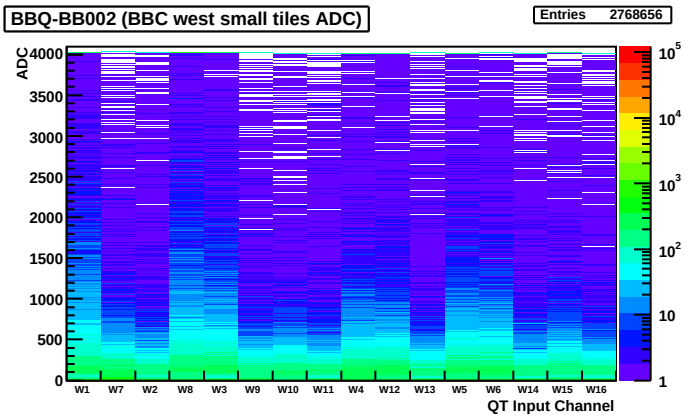

MIX-TF101
Entries 1038246
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

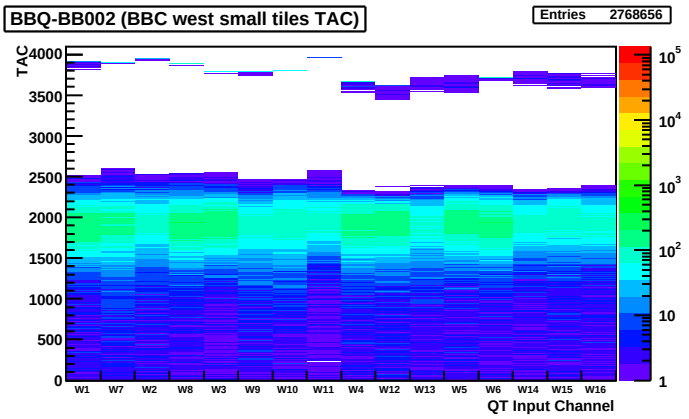

L1-TF201
Entries 173041
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

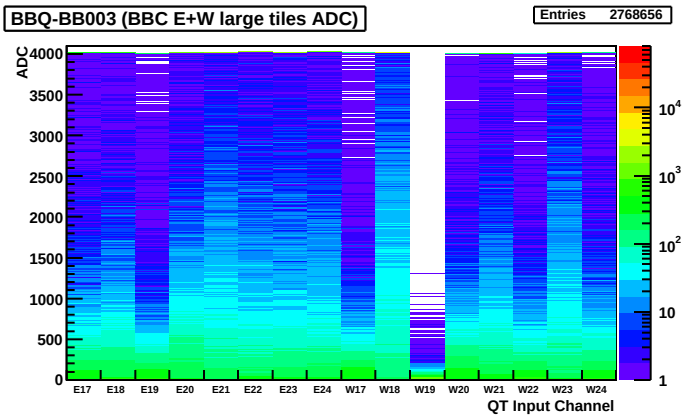

L1-TF201
Entries 1038246
Threshold bits
1
0
114034
110911
97378
104580
93491
104497
59007
62130
75663
68461
79550
68544
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

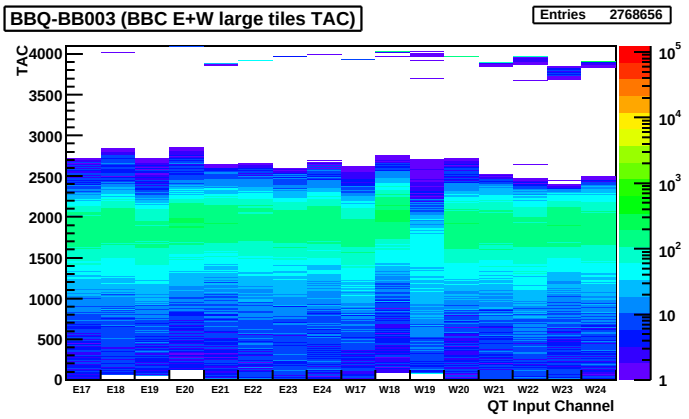

BBQ-BB001 (BBC east small tiles ADC)
Entries 2768656
ADC
QT Input Channel

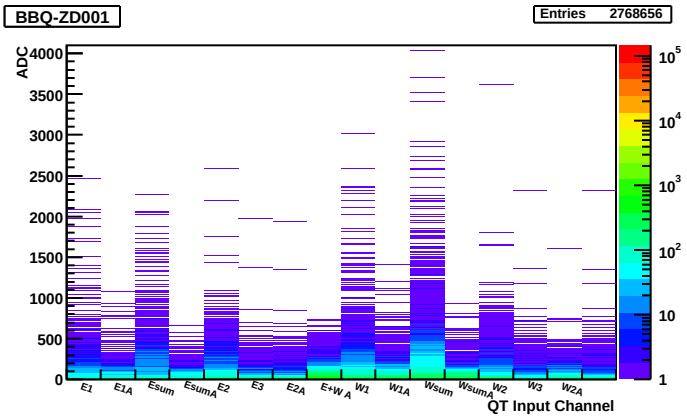

BBQ-BB001 (BBC east small tiles TAC)
Entries 2768656
TAC
QT Input Channel

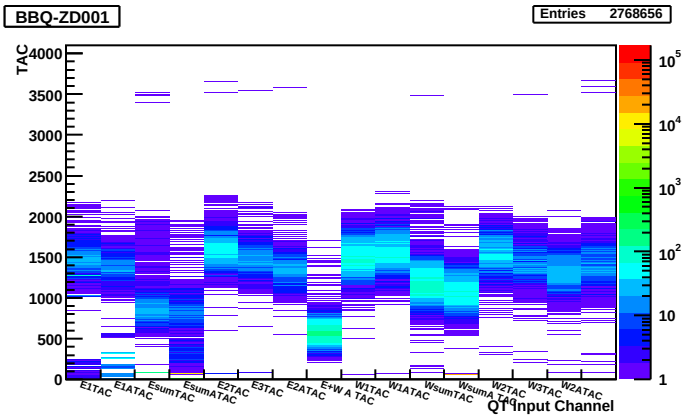

BBQ-BB002 (BBC west small tiles ADC)
Entries 2768656
ADC
QT Input Channel

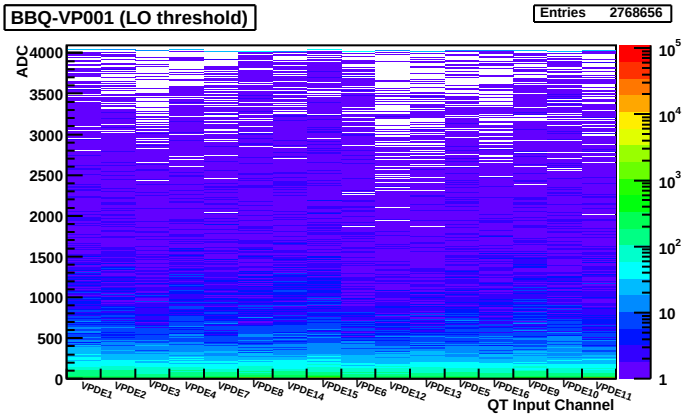

BBQ-BB002 (BBC west small tiles TAC)
Entries 2768656
TAC
QT Input Channel

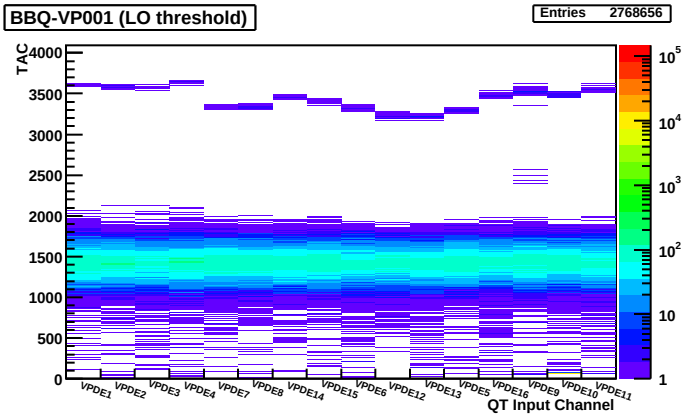

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2768656
ADC
QT Input Channel

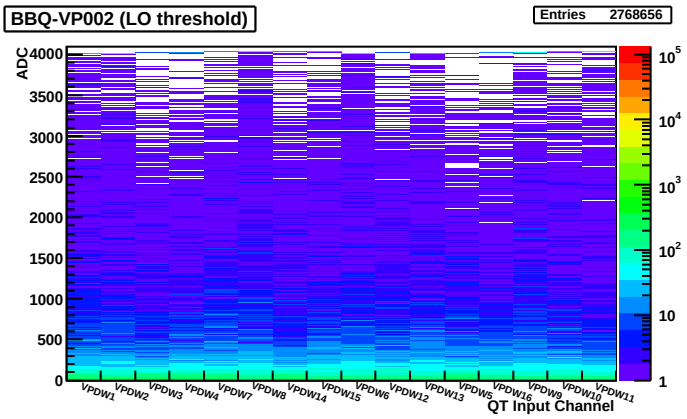

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2768656
TAC
QT Input Channel

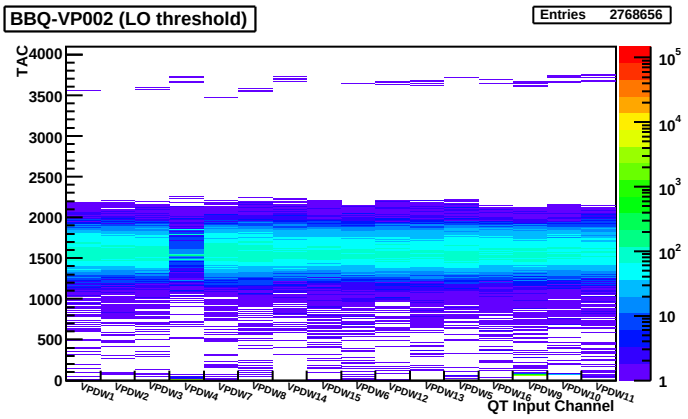

BBQ-ZD001
Entries 2768656
ADC
QT Input Channel

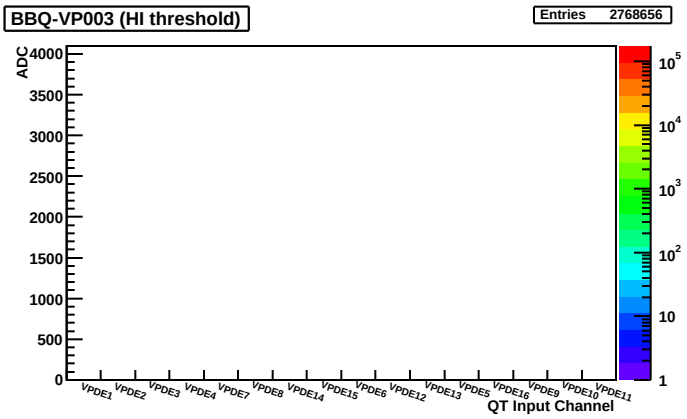

BBQ-ZD001
Entries 2768656
TAC
QT Input Channel

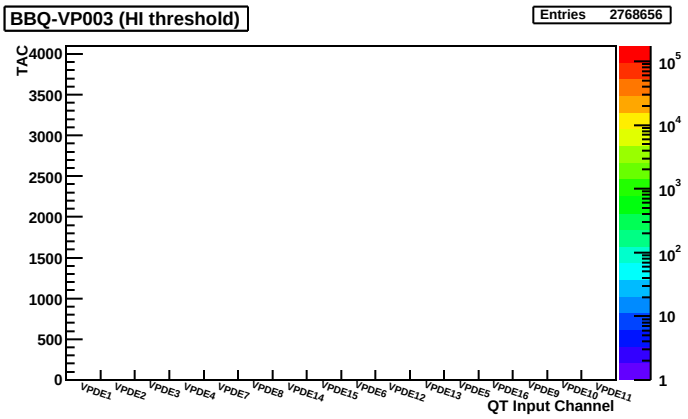

BBQ-VP001 (LO threshold)
Entries 2768656
ADC
QT Input Channel

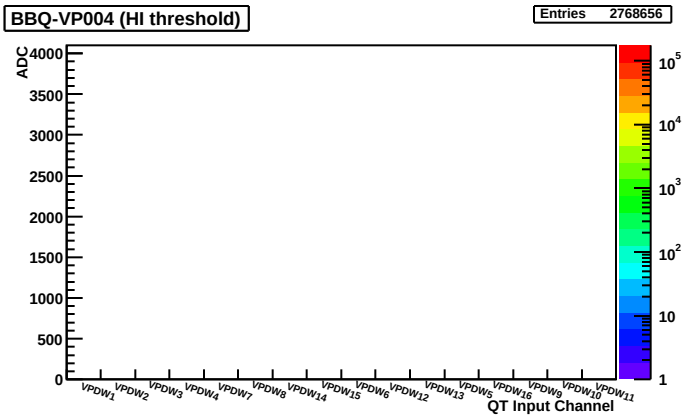

BBQ-VP001 (LO threshold)
Entries 2768656
TAC
QT Input Channel

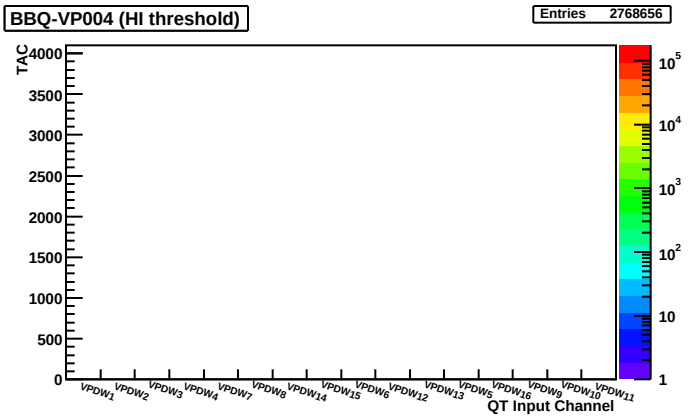

BBQ-VP002 (LO threshold)
Entries 2768656
ADC
QT Input Channel

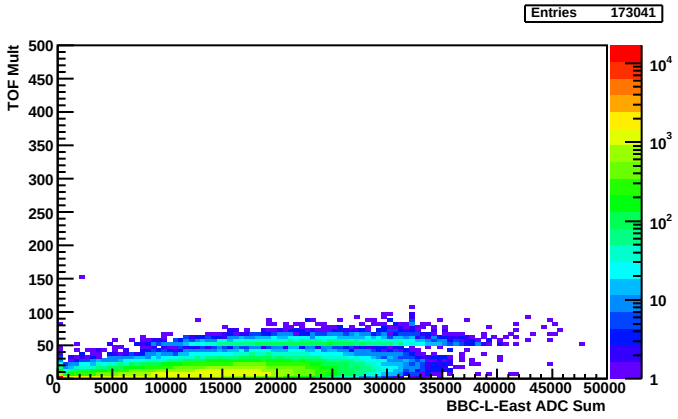

BBQ-VP002 (LO threshold)
Entries 2768656
TAC
QT Input Channel

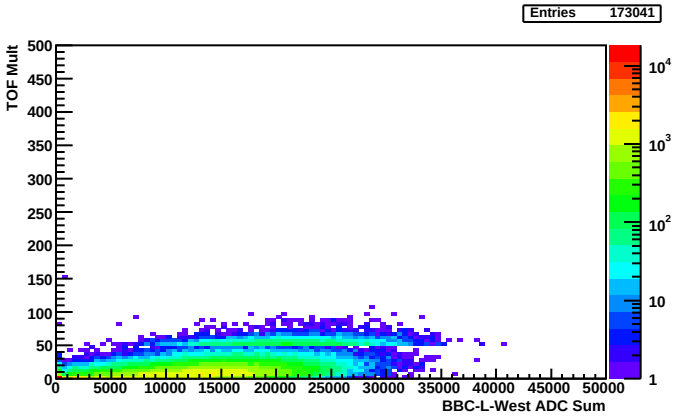

BBQ-VP003 (HI threshold)
Entries 2768656
ADC
QT Input Channel

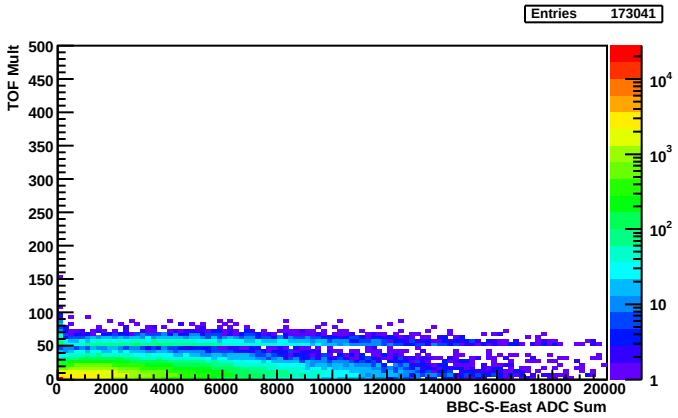

BBQ-VP003 (HI threshold)
Entries 2768656
TAC
QT Input Channel

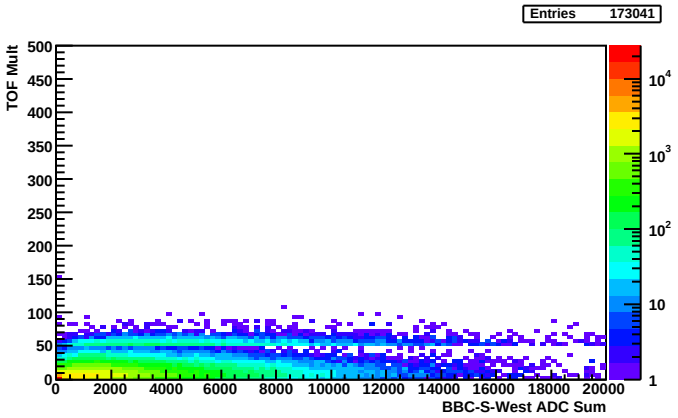

BBQ-VP004 (HI threshold)
Entries 2768656
ADC
QT Input Channel

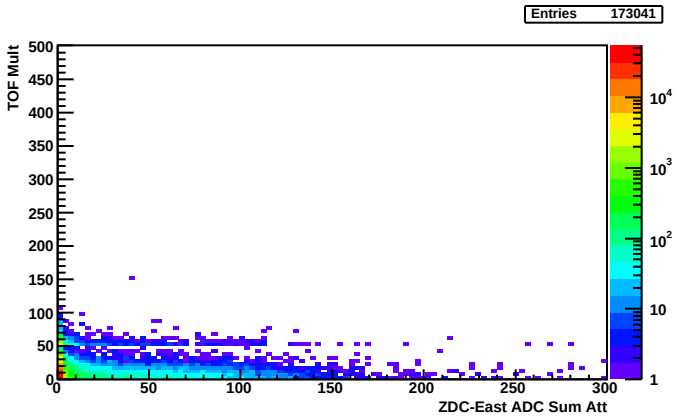

BBQ-VP004 (HI threshold)
Entries 2768656
TAC
QT Input Channel

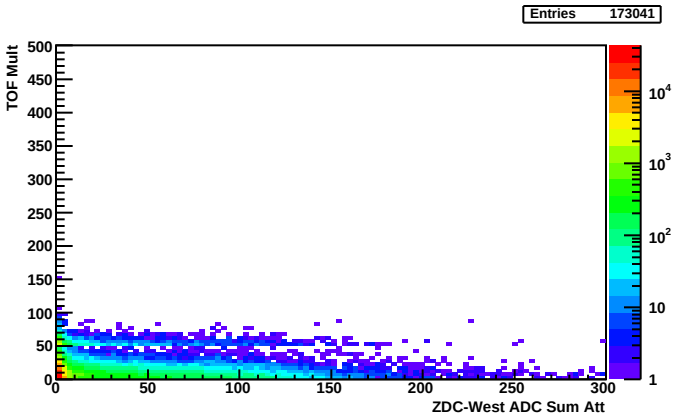

Entries 173041
TOF Mult
BBC-L-East ADC Sum

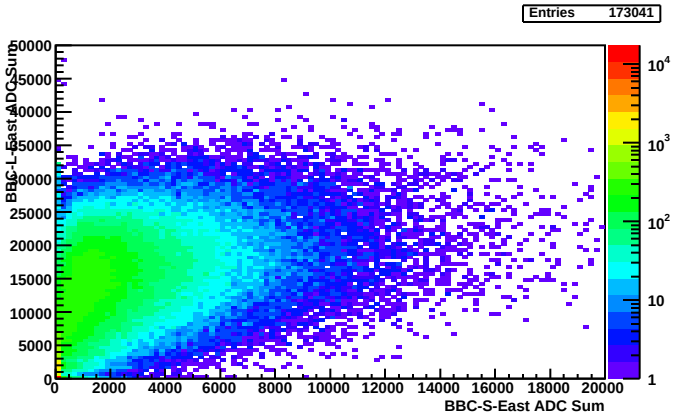

Entries 173041
TOF Mult
BBC-L-West ADC Sum

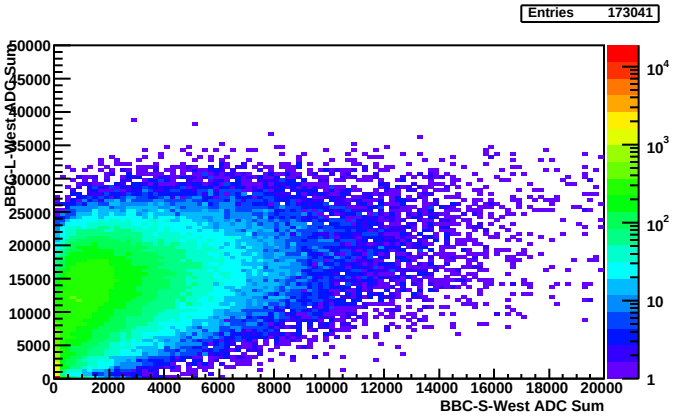

Entries 173041
TOF Mult
BBC-S-East ADC Sum

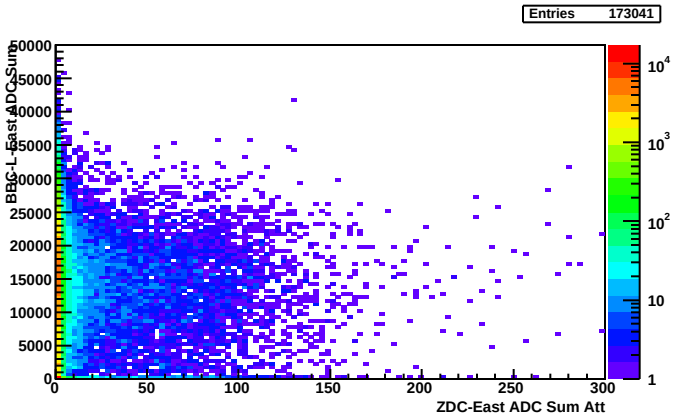

Entries 173041
TOF Mult
BBC-S-West ADC Sum

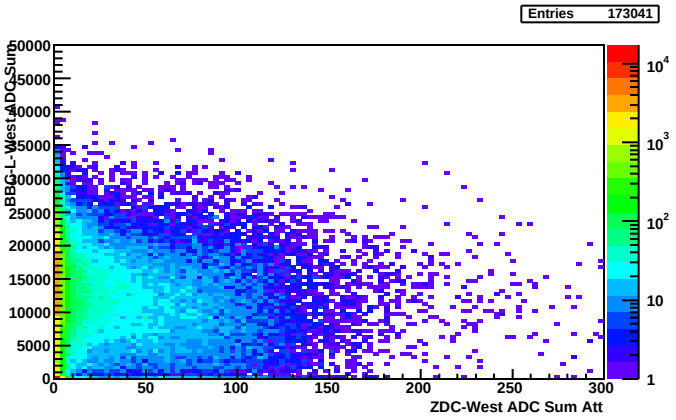

Entries 173041
TOF Mult
ZDC-East ADC Sum Att

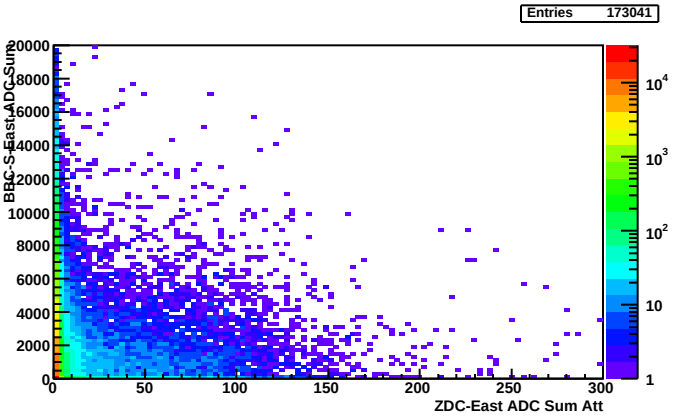

Entries 173041
TOF Mult
ZDC-West ADC Sum Att

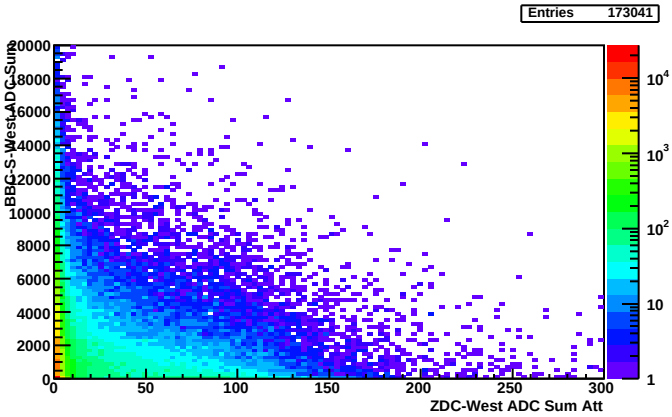

Entries 173041
BBC-L-East ADC Sum
BBC-S-East ADC Sum

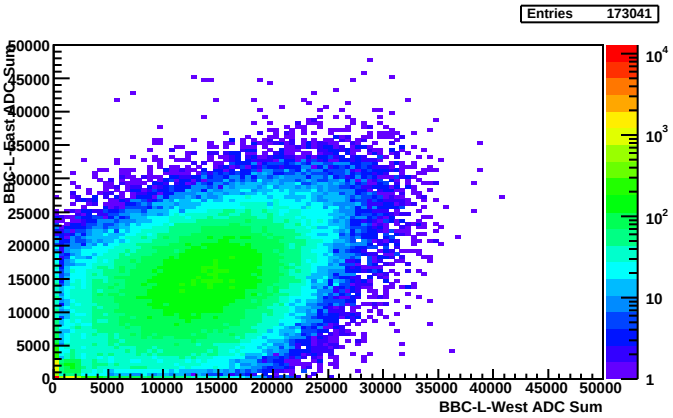

Entries 173041
BBC-L-West ADC Sum
BBC-S-West ADC Sum

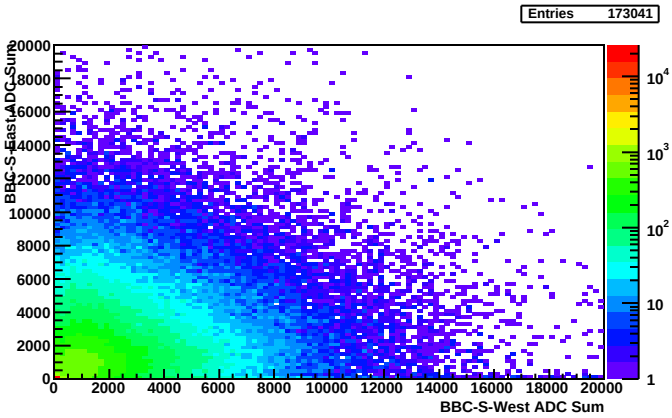

Entries 173041
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

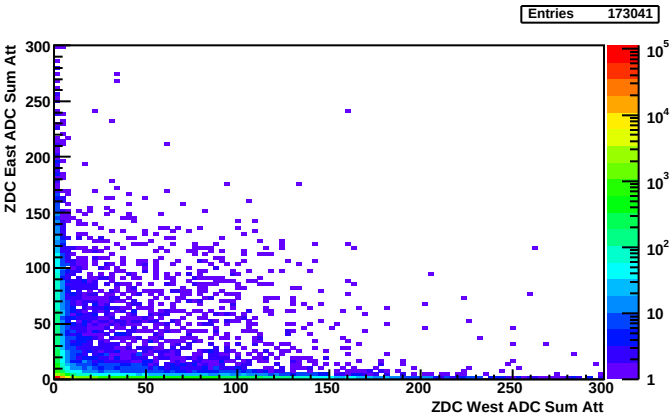

Entries 173041
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

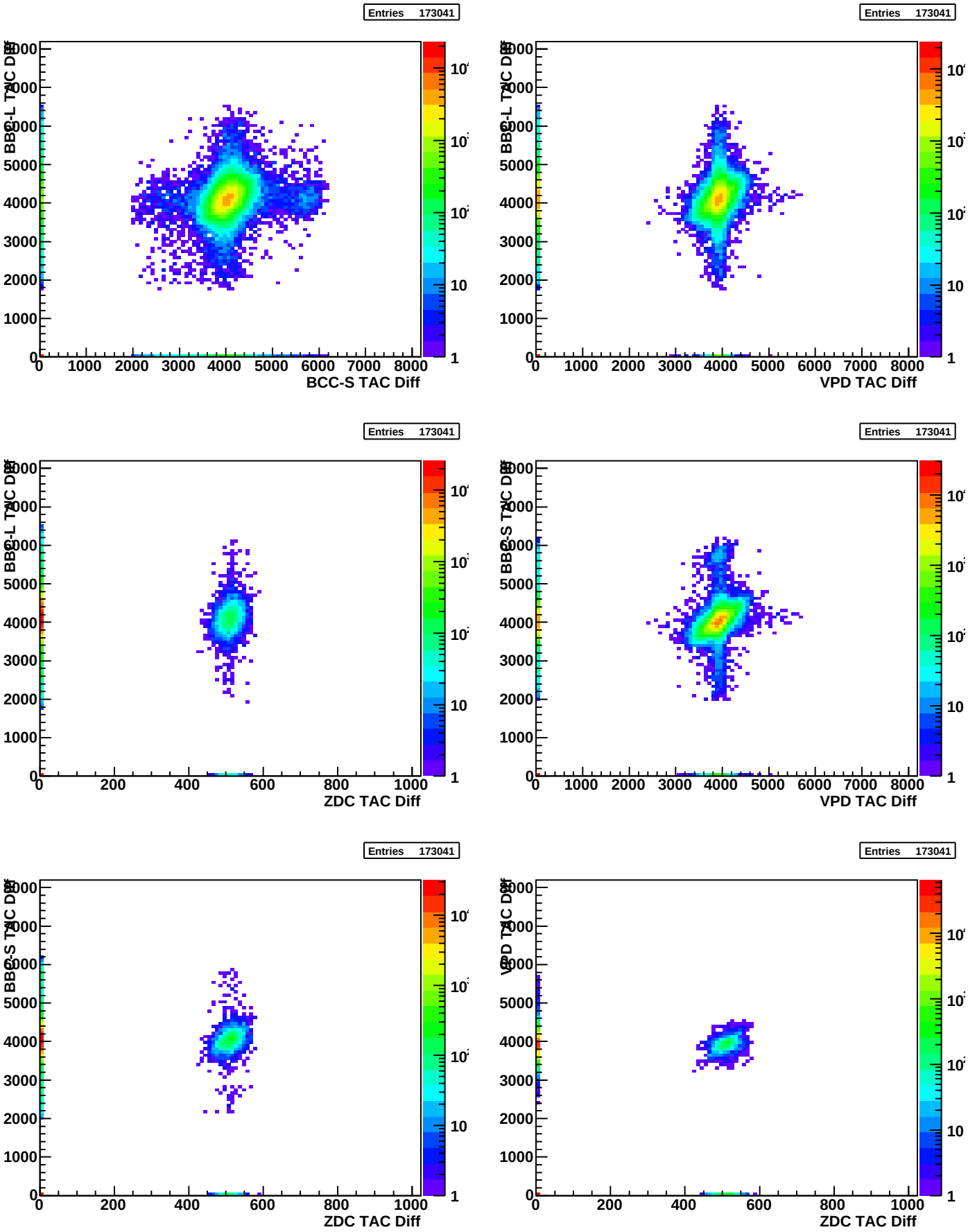

Entries 173041
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

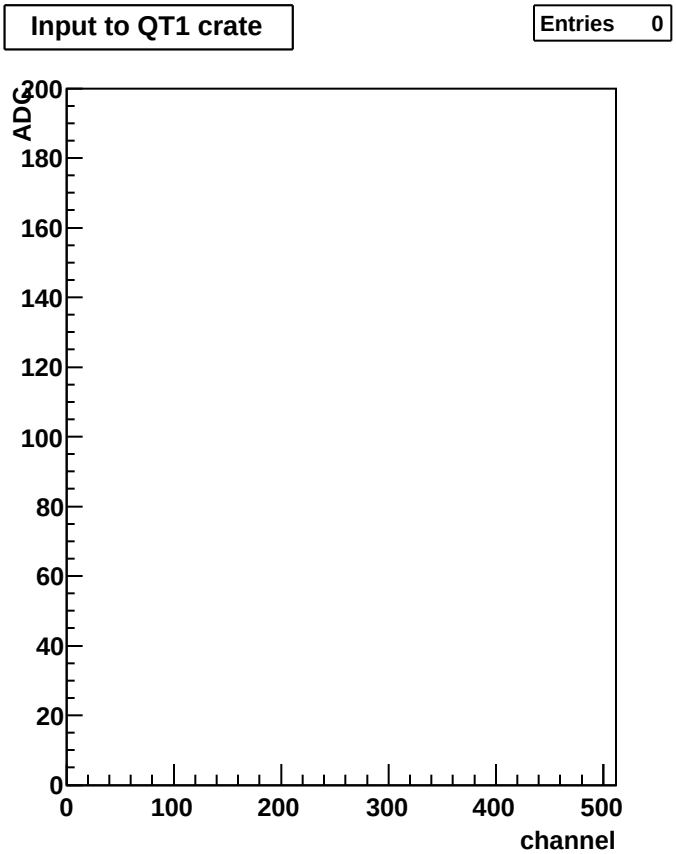

Entries 173041
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

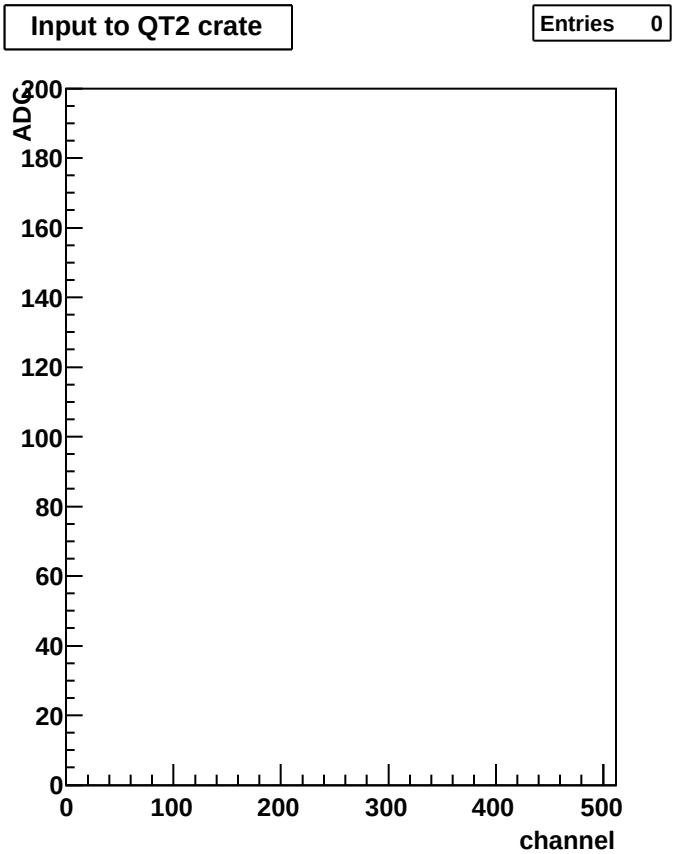

Entries 173041
BBC-L-East ADC Sum
BBC-L-West ADC Sum

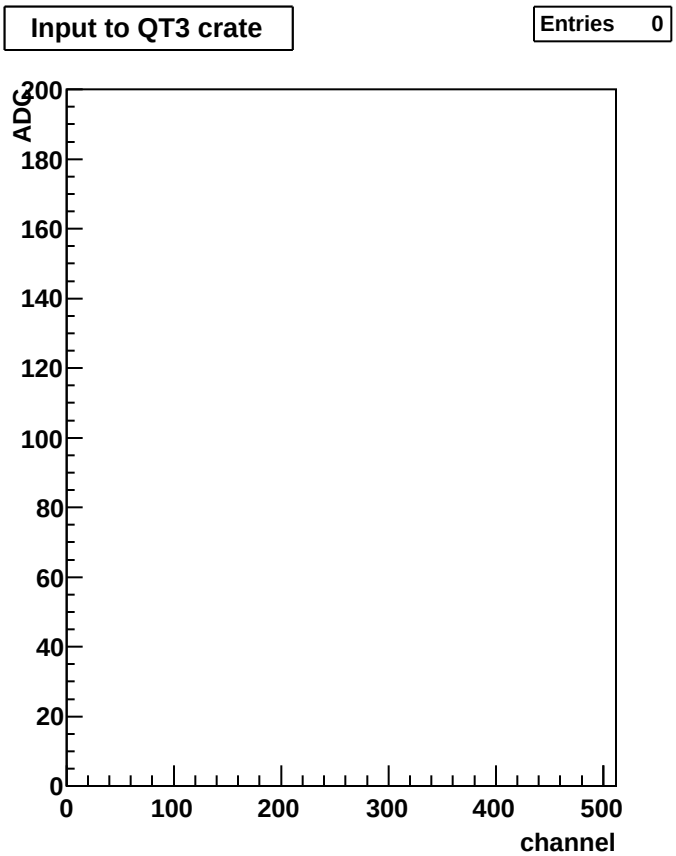

Entries 173041
BBC-S-East ADC Sum
BBC-S-West ADC Sum

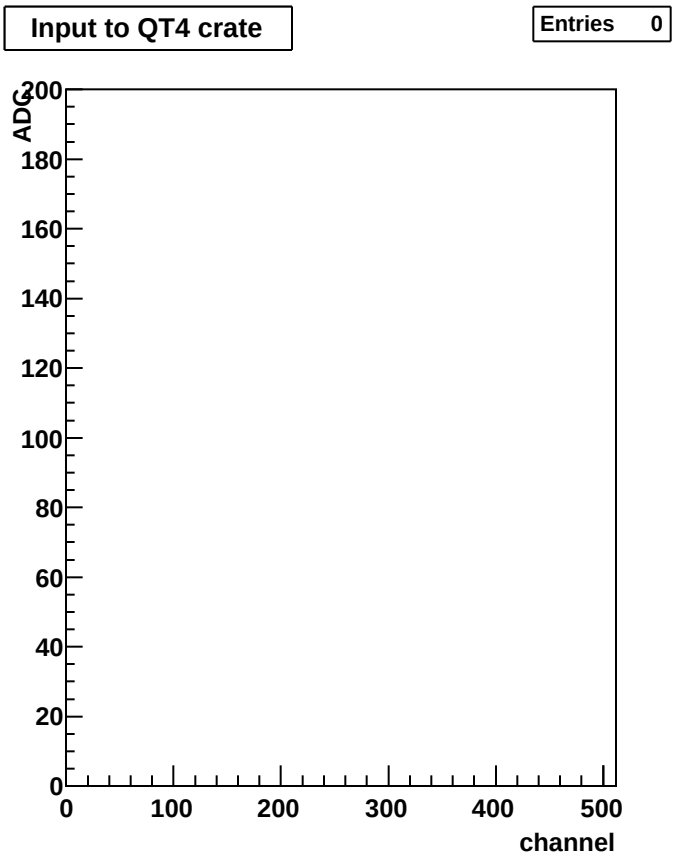

Entries 173041
ZDC East ADC Sum Att
ZDC West ADC Sum Att

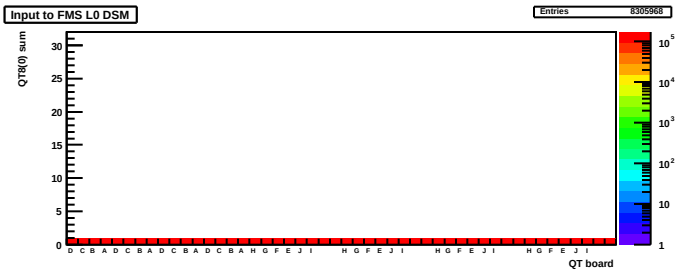

Entries 173041
BBC-L TAC Diff
BCC-S TAC Diff
Entries 173041
BBC-L TAC Diff
VPD TAC Diff
Entries 173041
BBC-L TAC Diff
ZDC TAC Diff
Entries 173041
BBC-S TAC Diff
VPD TAC Diff
Entries 173041
BBC-S TAC Diff
ZDC TAC Diff
Entries 173041
VPD TAC Diff
ZDC TAC Diff

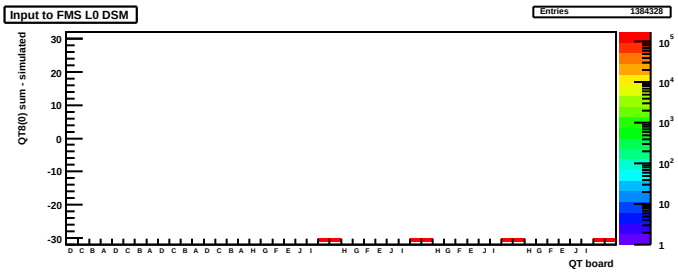

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

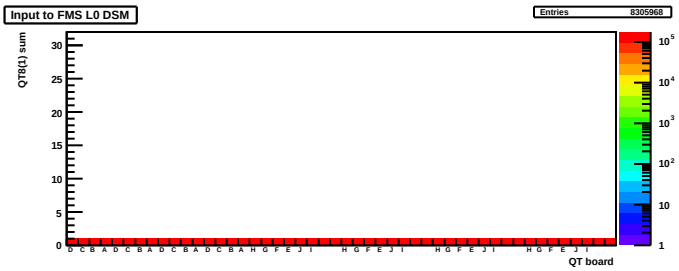

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

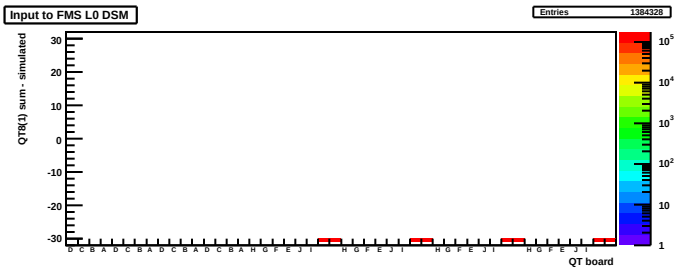

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

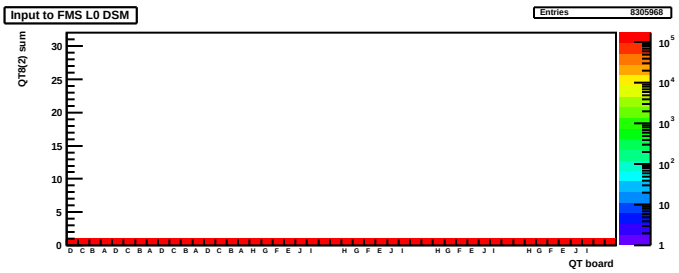

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

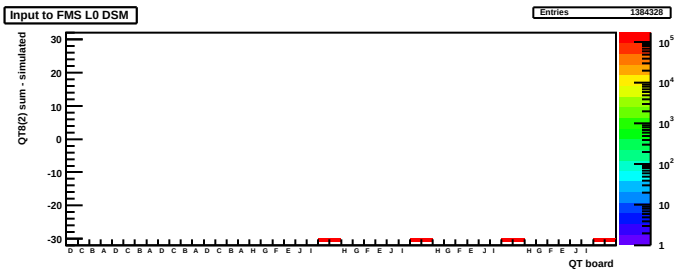

Input to FMS L0 DSM
Entries 8305968
QT8(0) sum
QT board

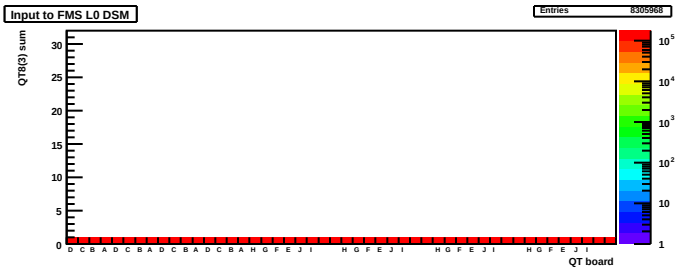

Input to FMS L0 DSM
Entries 1384328
QT8(0) sum - simulated
QT board

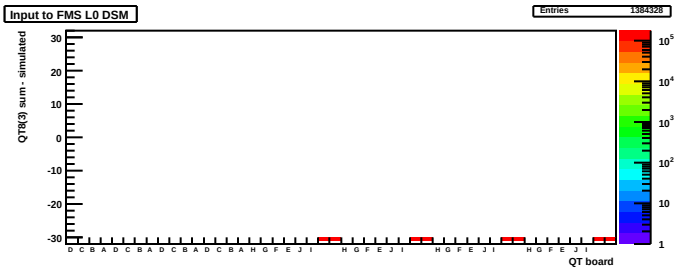

Input to FMS L0 DSM
Entries 8305968
QT8(1) sum
QT board

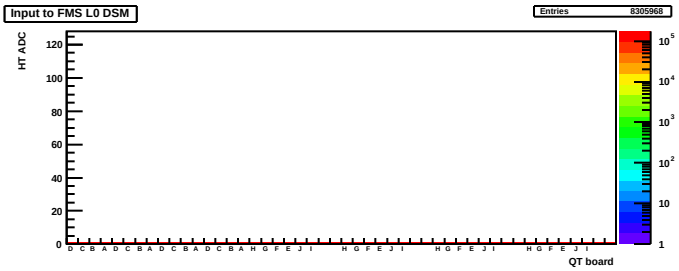

Input to FMS L0 DSM
Entries 1384328
QT8(1) sum - simulated
QT board

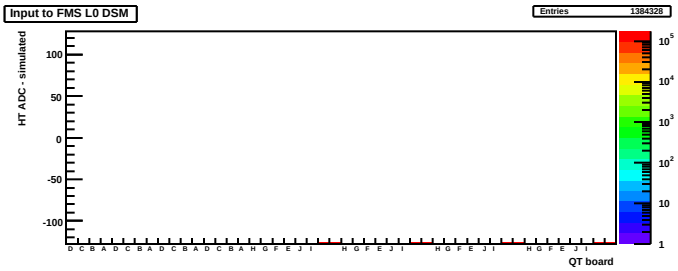

Input to FMS L0 DSM
Entries 8305968
QT8(2) sum
QT board

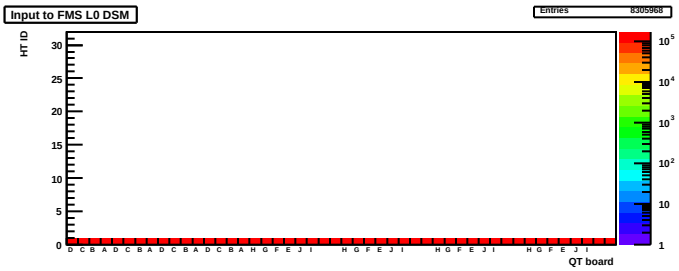

Input to FMS L0 DSM
Entries 1384328
QT8(2) sum - simulated
QT board

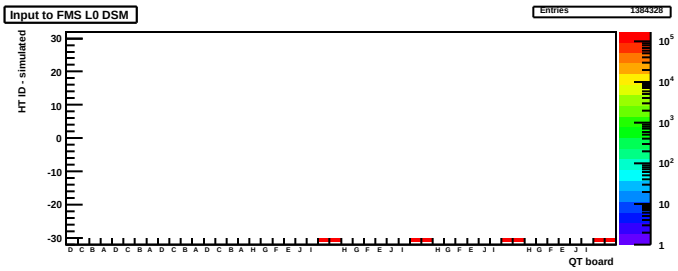

Input to FMS L0 DSM
Entries 8305968
QT8(3) sum
QT board

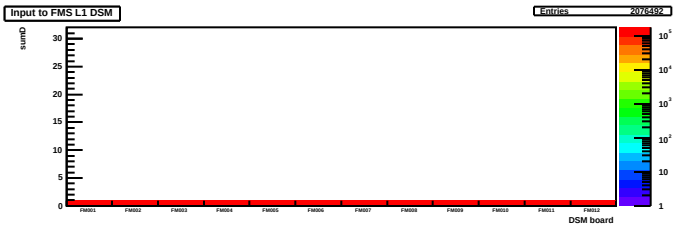

Input to FMS L0 DSM
Entries 1384328
QT8(3) sum - simulated
QT board

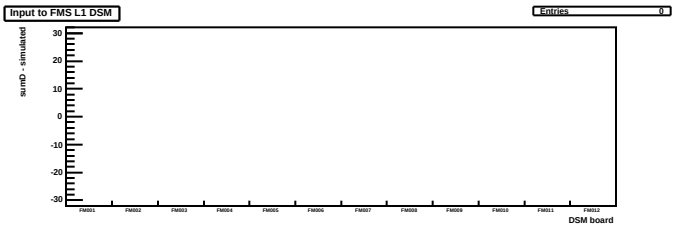

Input to FMS L0 DSM
Entries 8305968
HT ADC
QT board

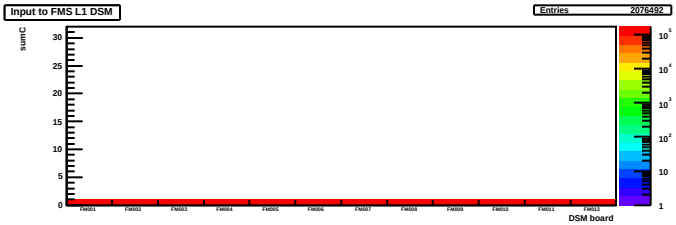

Input to FMS L0 DSM
Entries 1384328
HT ADC - simulated
QT board

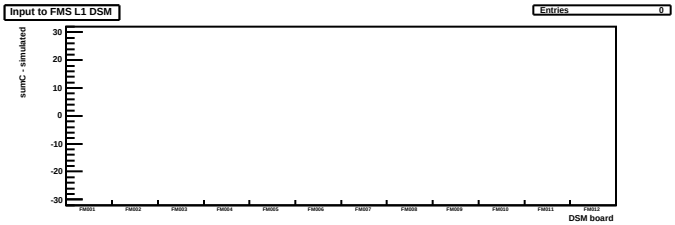

Input to FMS L0 DSM
Entries 8305968
HT ID
QT board

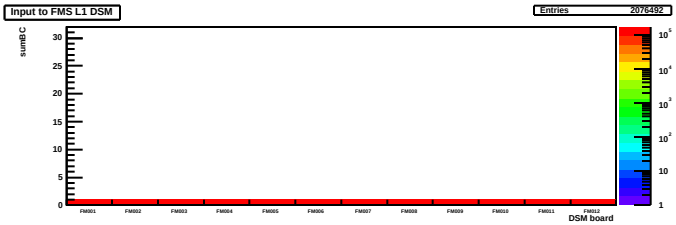

Input to FMS L0 DSM
Entries 1384328
HT ID - simulated
QT board

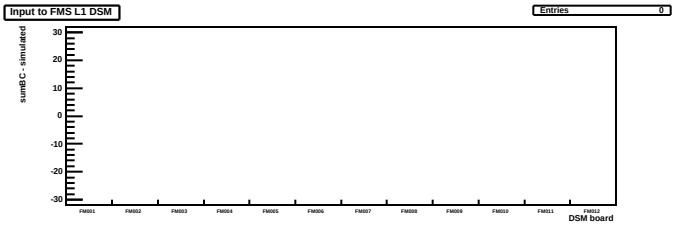

Input to FMS L1 DSM
Entries 2076492
sumD
DSM board

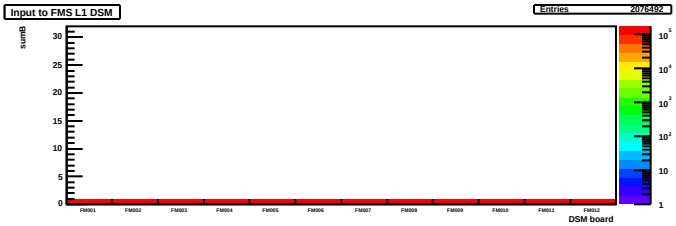

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

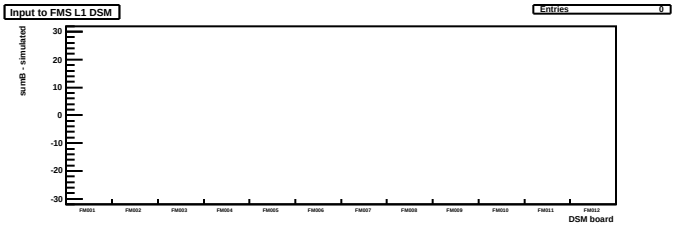

Input to FMS L1 DSM
Entries 2076492
sumC
DSM board

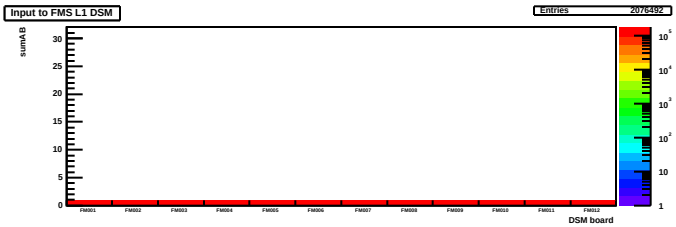

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

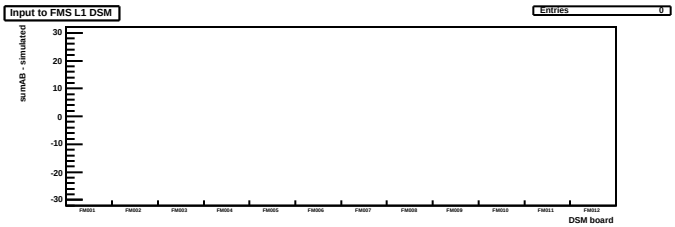

Input to FMS L1 DSM
Entries 2076492
sumBC
DSM board

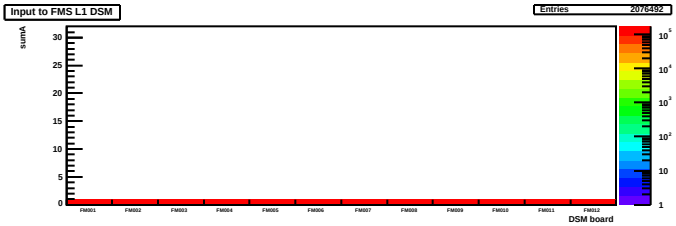

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

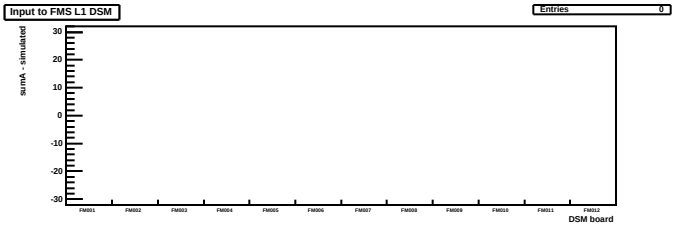

Input to FMS L1 DSM
Entries 2076492
sumB
DSM board

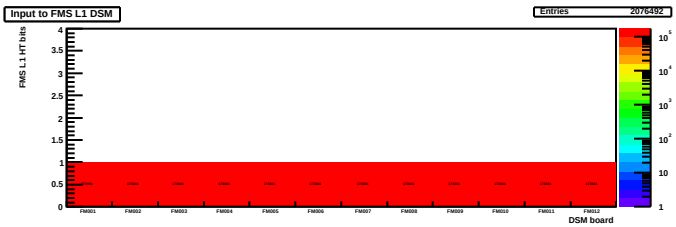

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

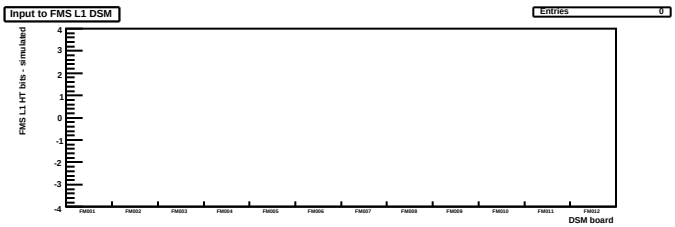

Input to FMS L1 DSM
Entries 2076492
sumAB
DSM board

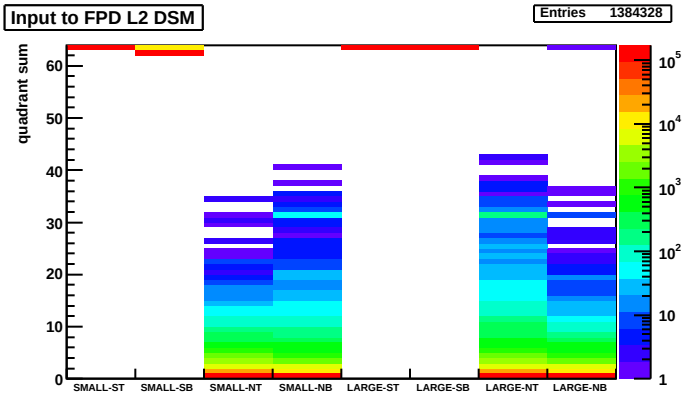

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

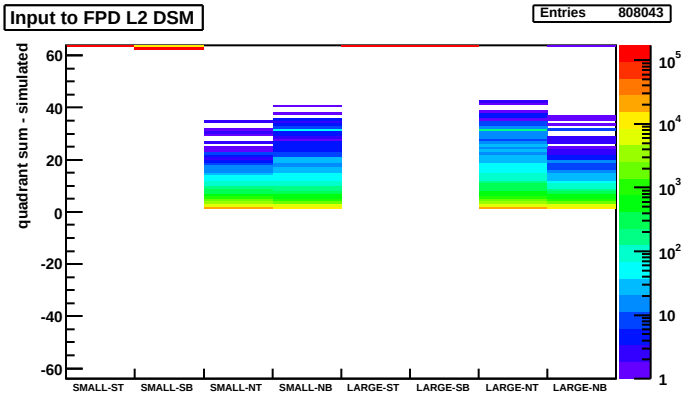

Input to FMS L1 DSM
Entries 2076492
sumA
DSM board

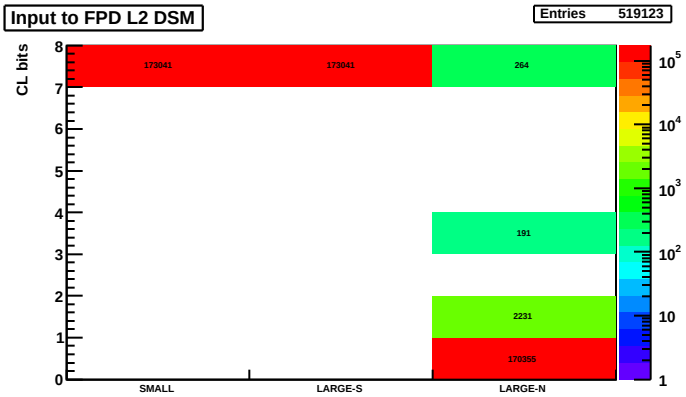

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

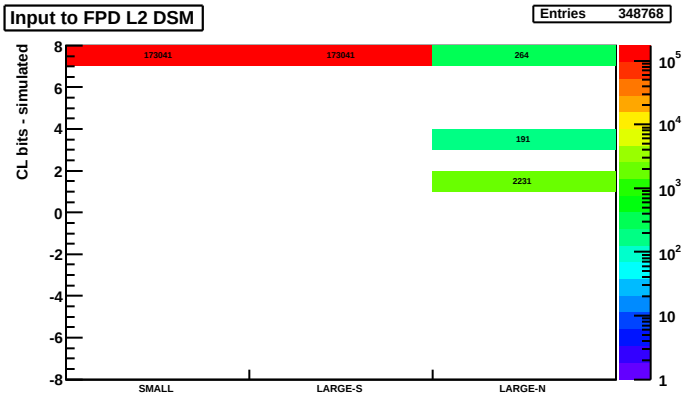

Input to FMS L1 DSM
Entries 2076492
FMS L1 HT bits
DSM board

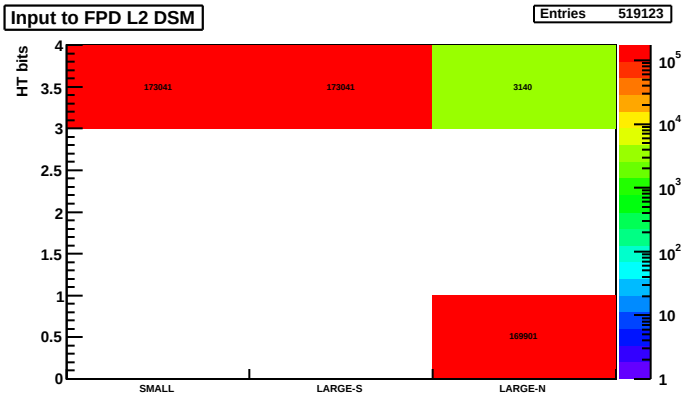

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

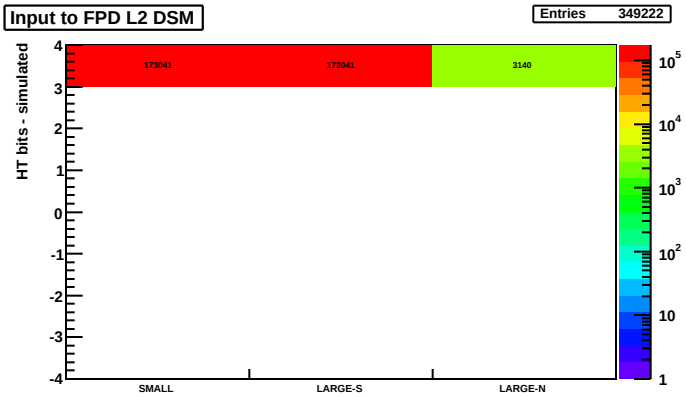

Input to FPD L2 DSM
Entries 1384328
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

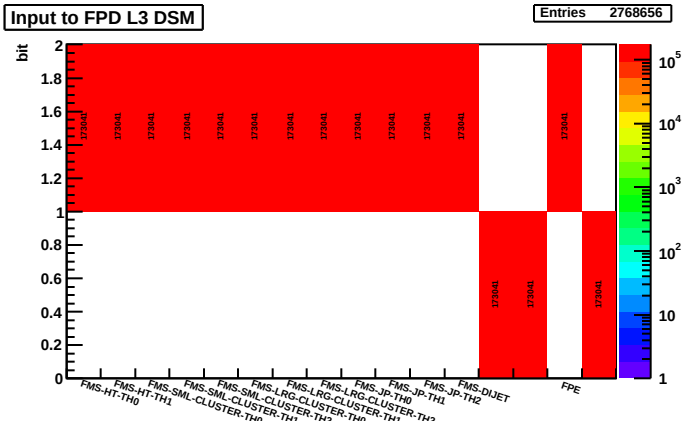

Input to FPD L2 DSM
Entries 808043
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

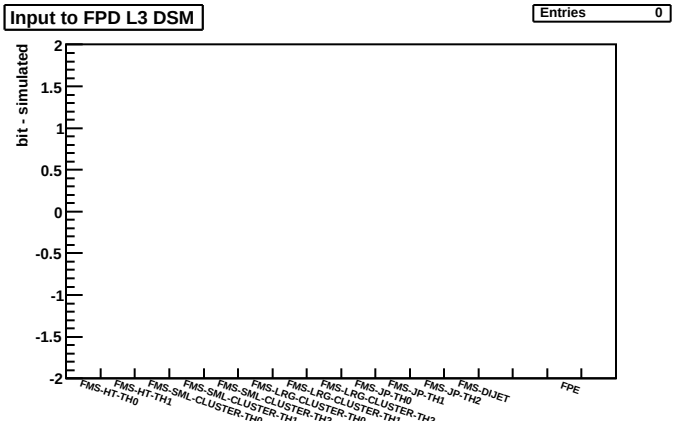

Input to FPD L2 DSM
Entries 519123
CL bits
173041
173041
264
191
2231
170355
SMALL
LARGE-S
LARGE-N

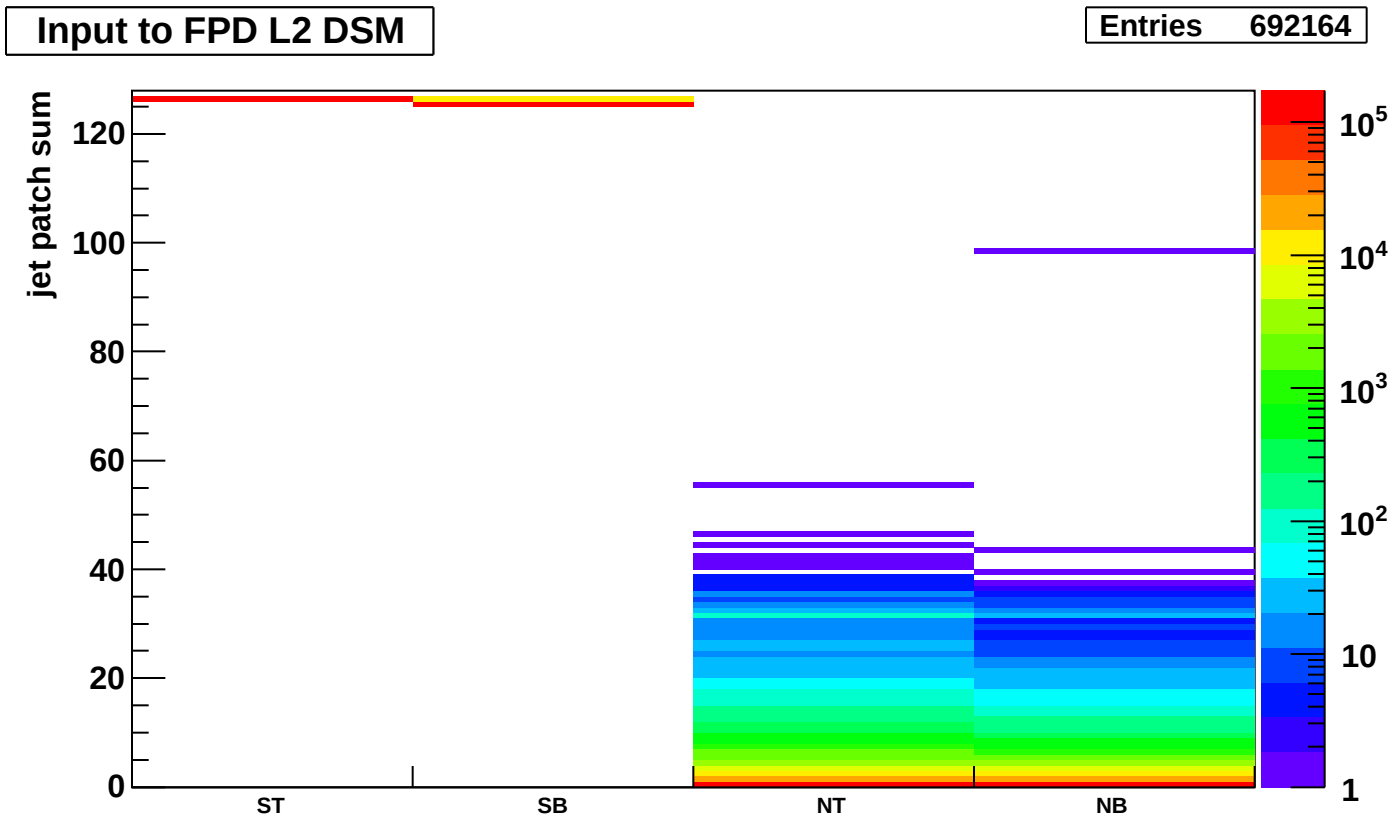

Input to FPD L2 DSM
Entries 348768
CL bits - simulated
173041
173041
264
191
2231
SMALL
LARGE-S
LARGE-N

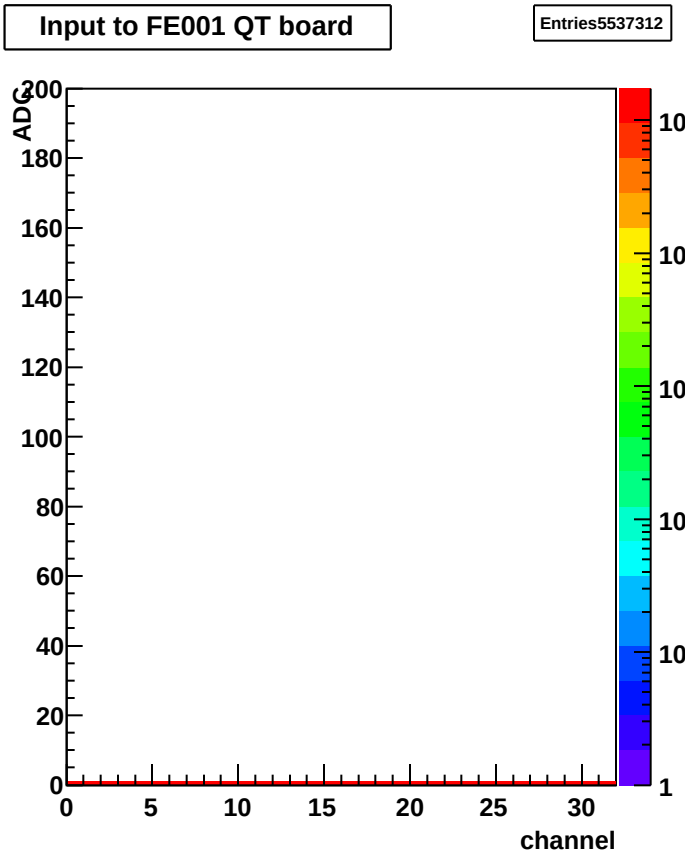

Input to FPD L2 DSM
Entries 519123
HT bits
173041
173041
3140
169901
SMALL
LARGE-S
LARGE-N

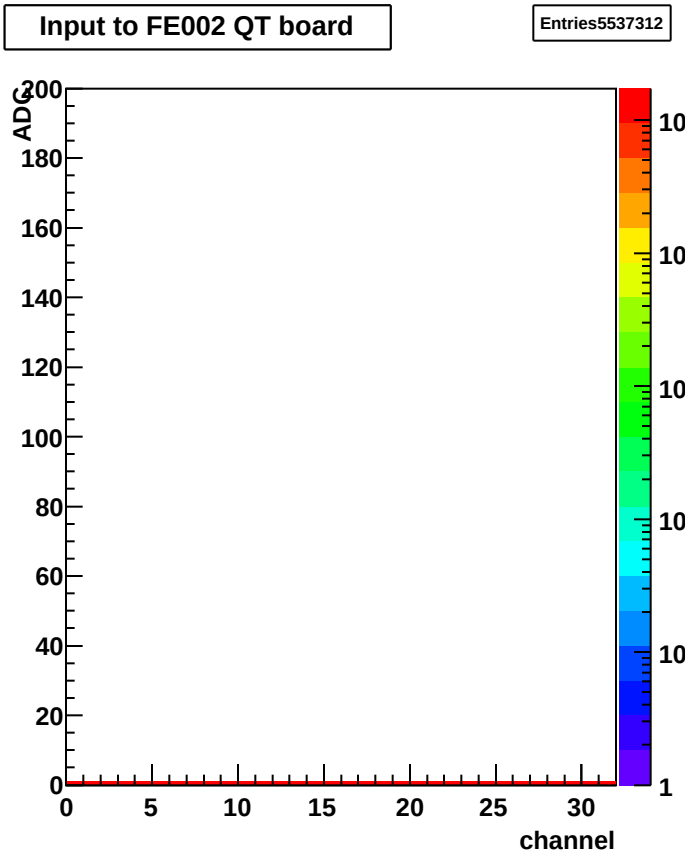

Input to FPD L2 DSM
Entries 349222
HT bits - simulated
173041
173041
3140
SMALL
LARGE-S
LARGE-N

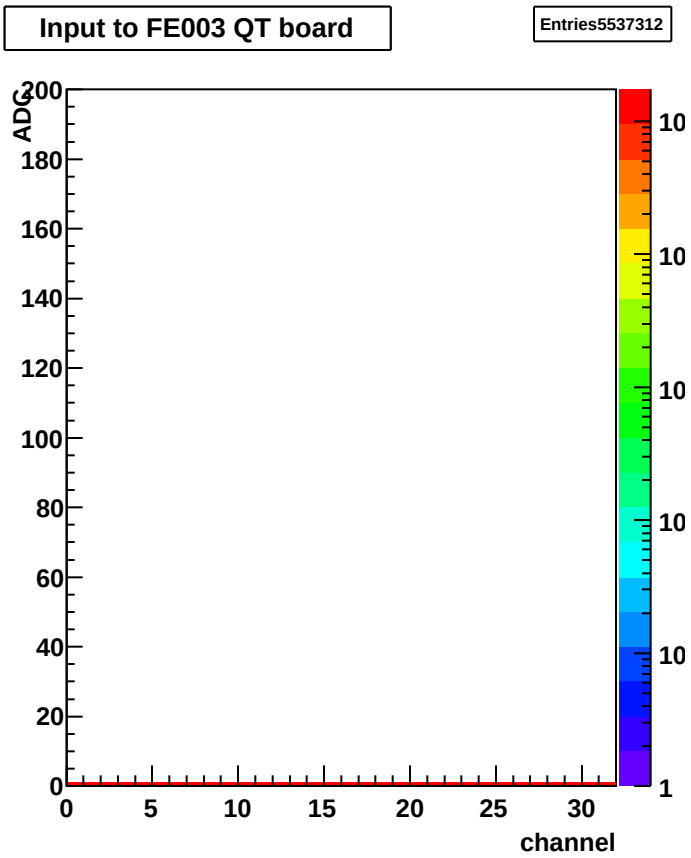

Input to FPD L3 DSM
Entries 2768656
bit
173041
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

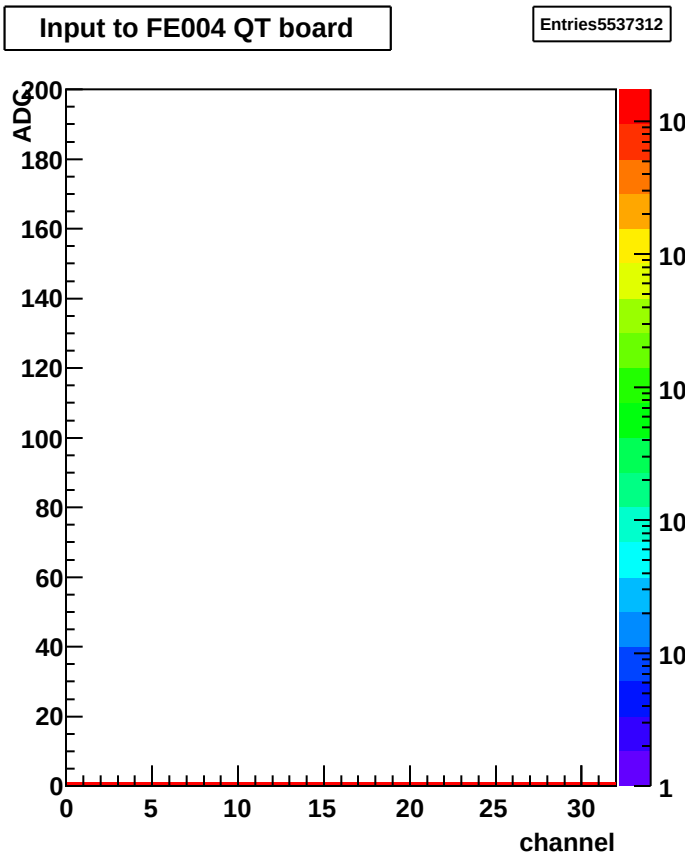

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

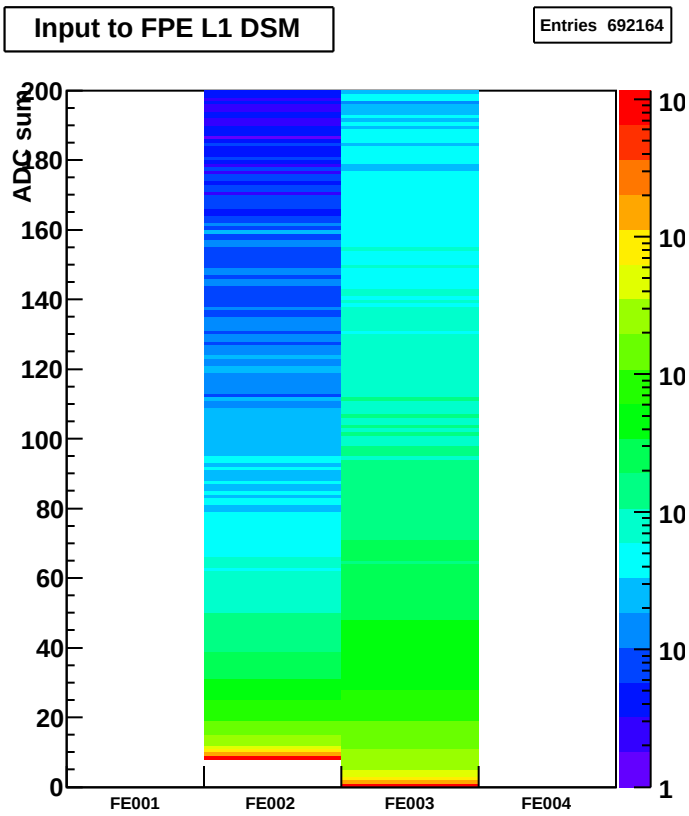

Input to FPD L2 DSM
Entries 692164
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

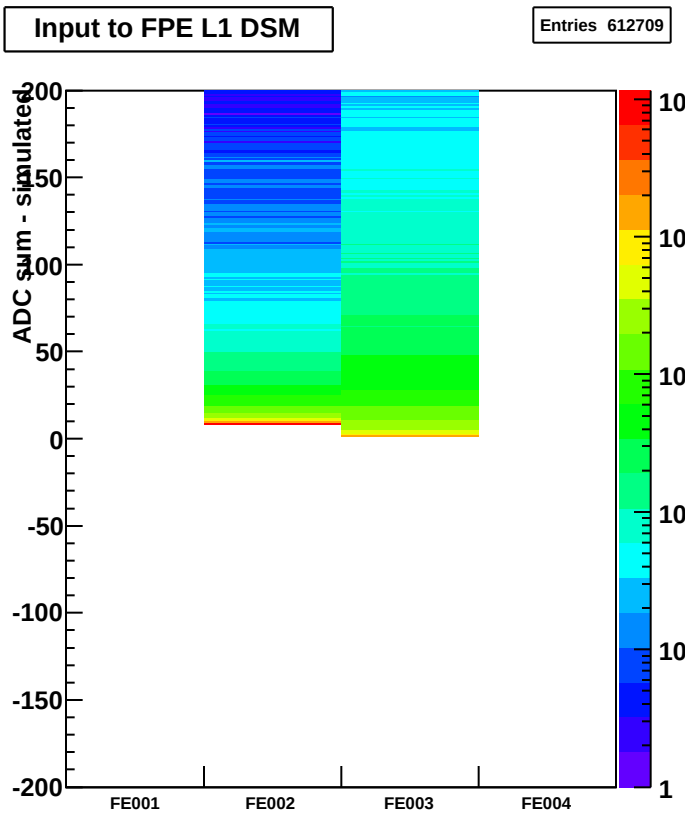

Input to FE001 QT board
Entries5537312
ADC
channel

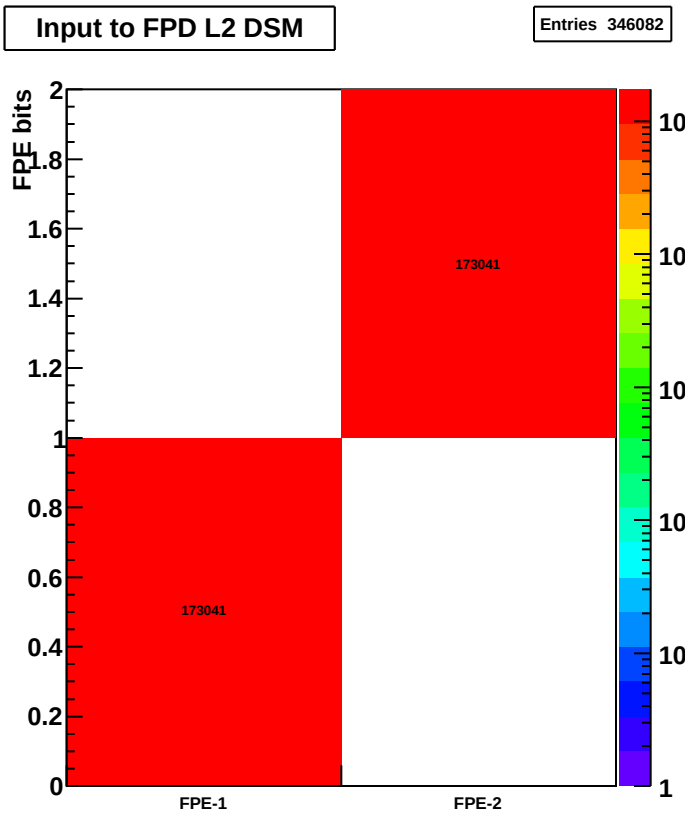

Input to FE002 QT board
Entries5537312
ADC
channel

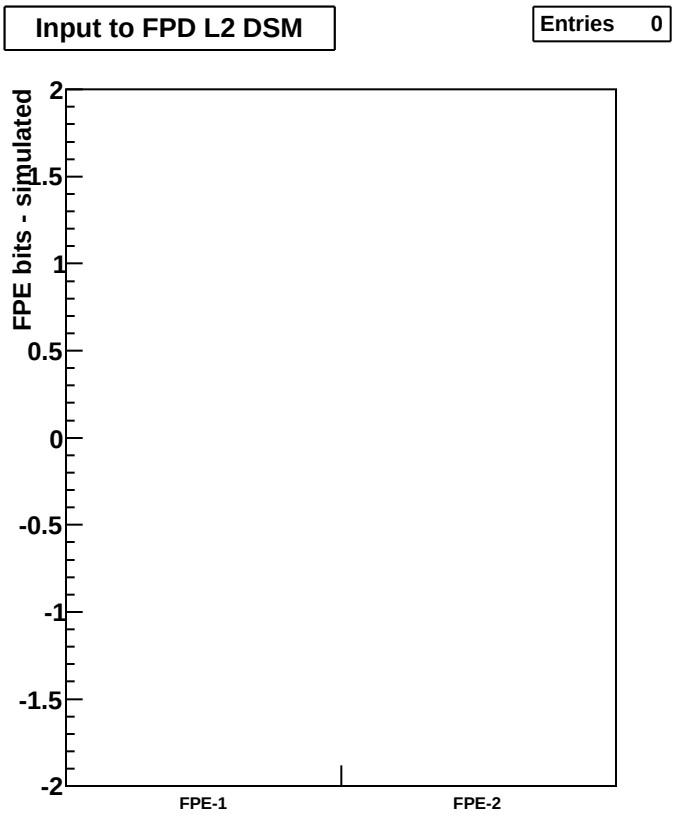

Input to FE003 QT board
Entries5537312
ADC
channel


Input to FE004 QT board
Entries5537312
ADC
channel


Input to FPE L1 DSM
Entries 692164
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 612709
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 346082
FPE bits
173041
173041
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2