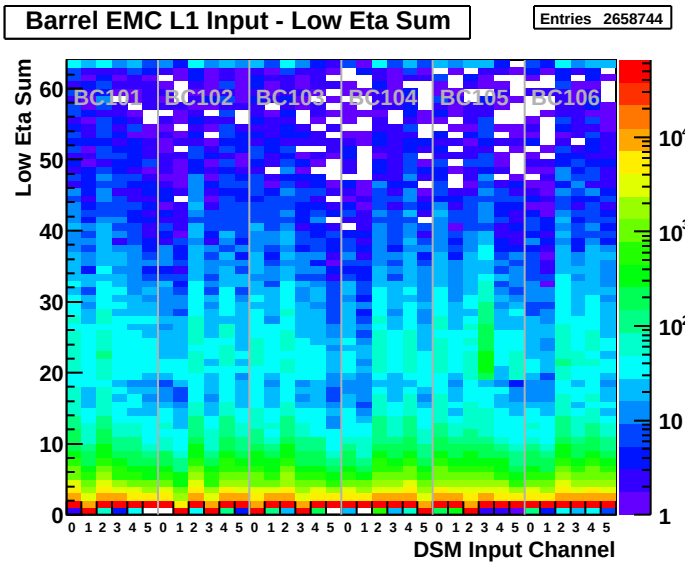
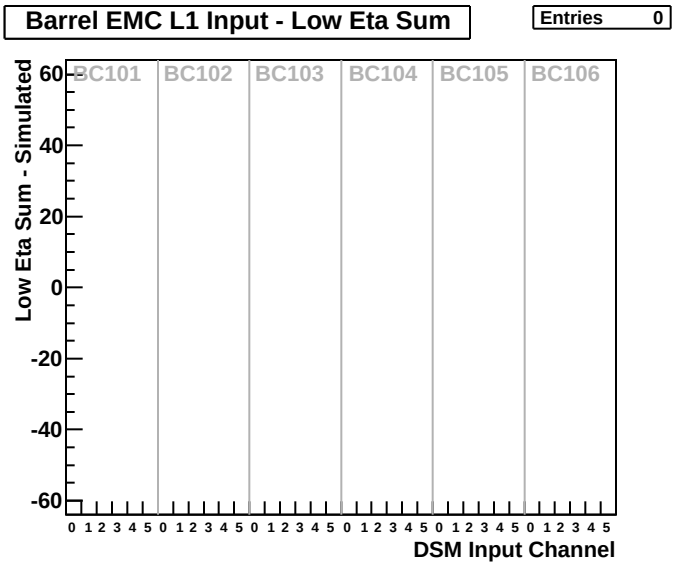
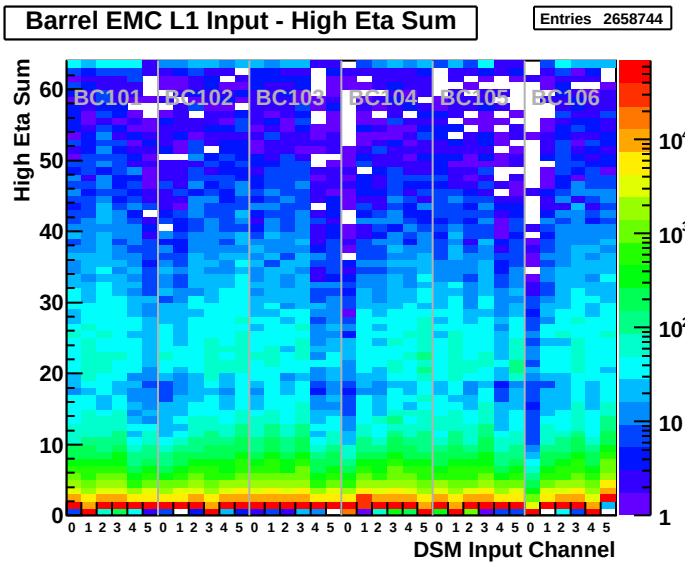
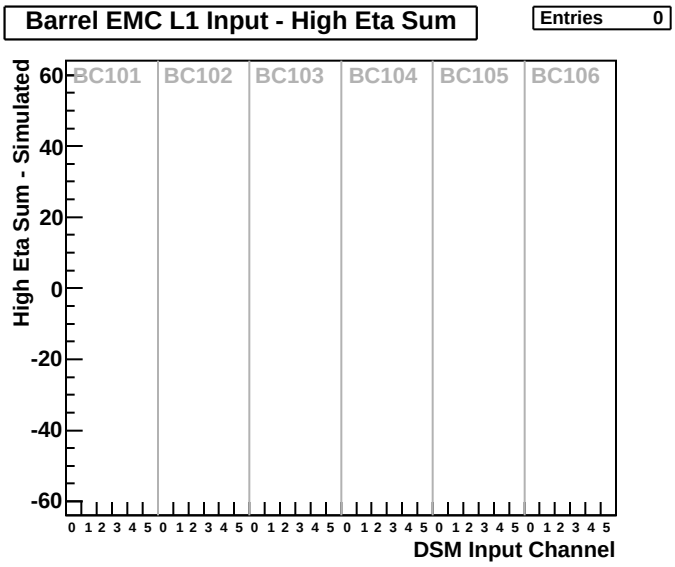
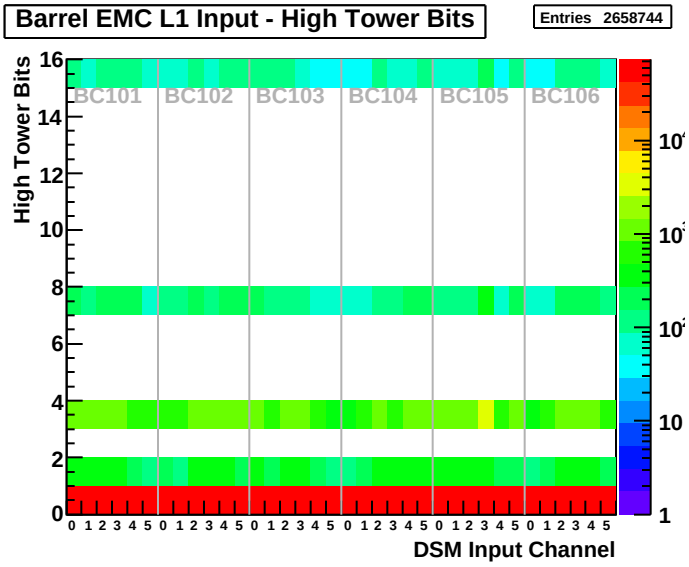
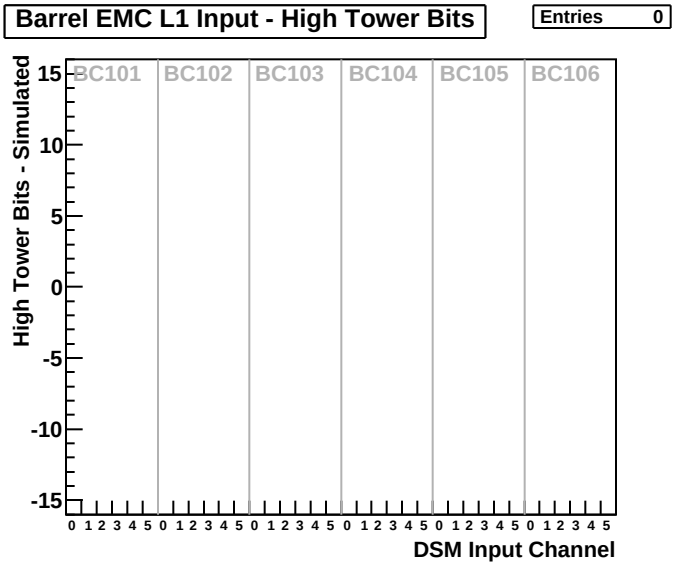

Barrel EMC L1 Input - Low Eta Sum
Entries 2658744
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

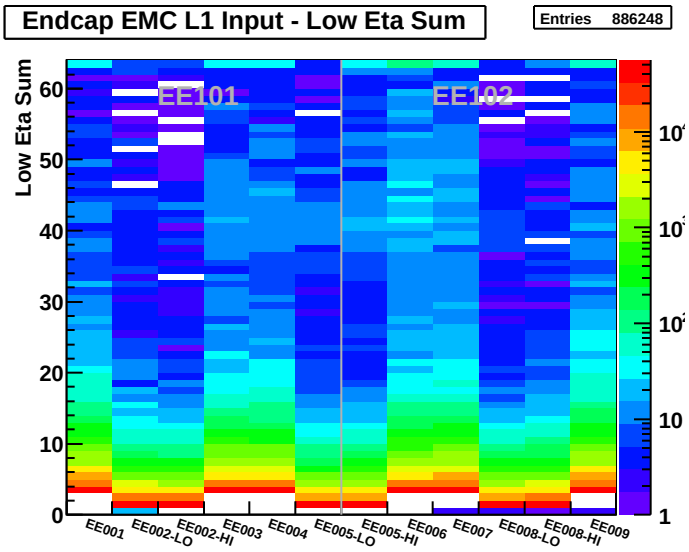

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

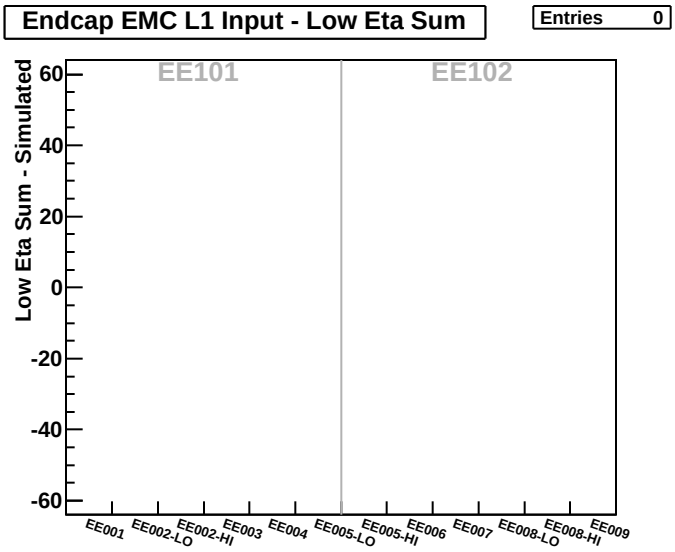

Barrel EMC L1 Input - High Eta Sum
Entries 2658744
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

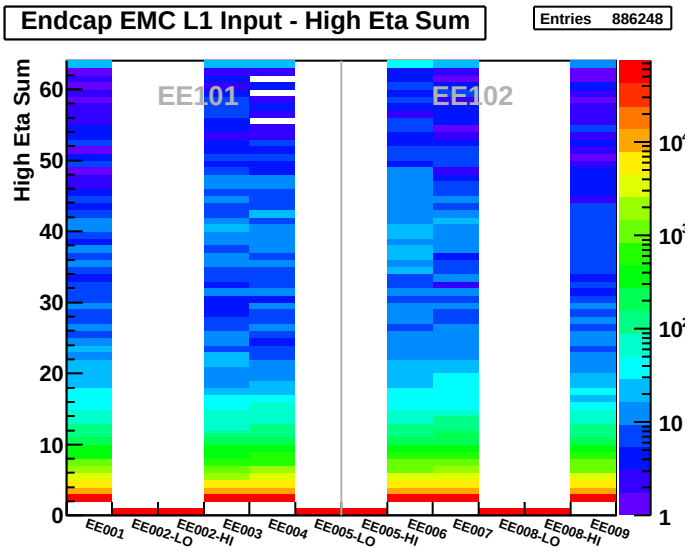

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

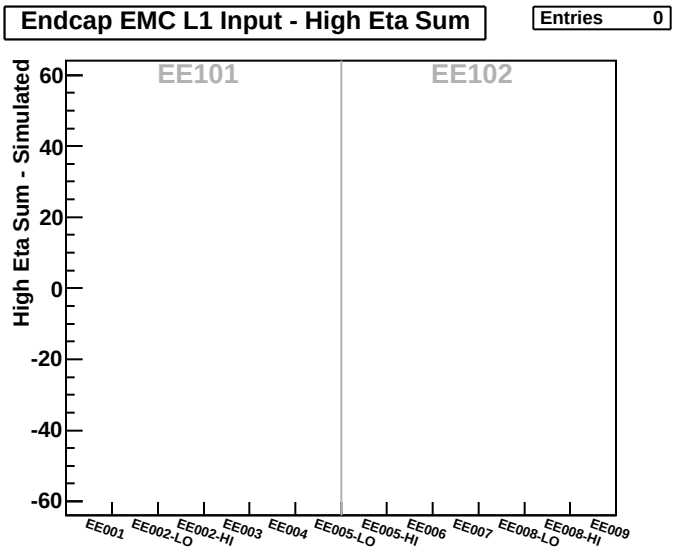

Barrel EMC L1 Input - High Tower Bits
Entries 2658744
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

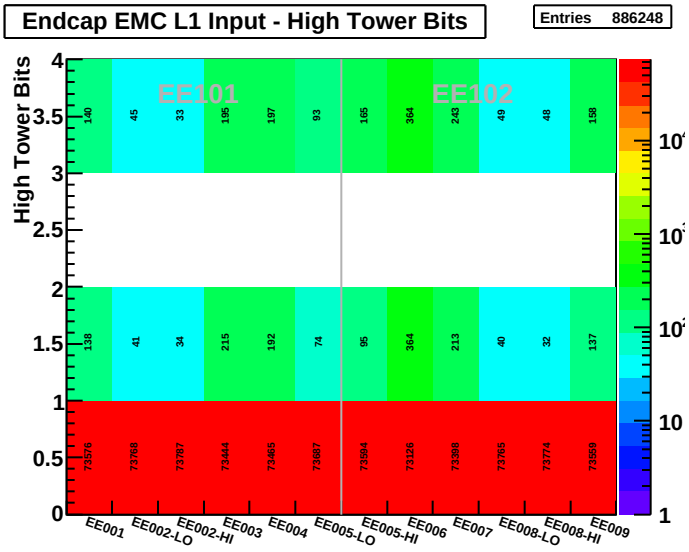

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

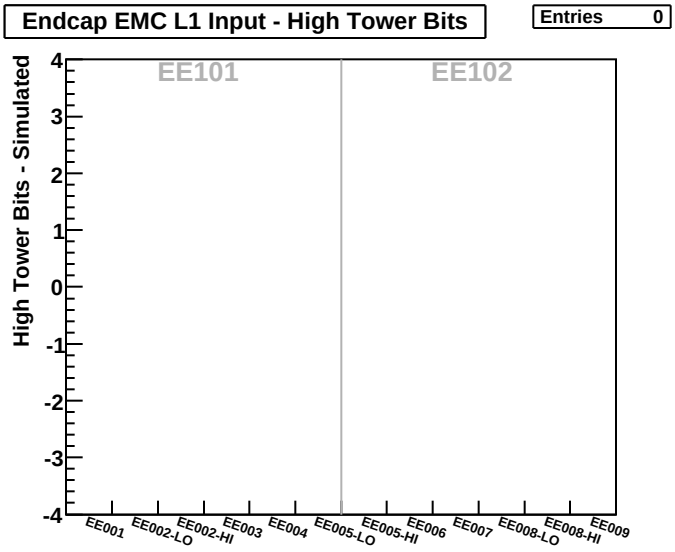

Endcap EMC L1 Input - Low Eta Sum
Entries 886248
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

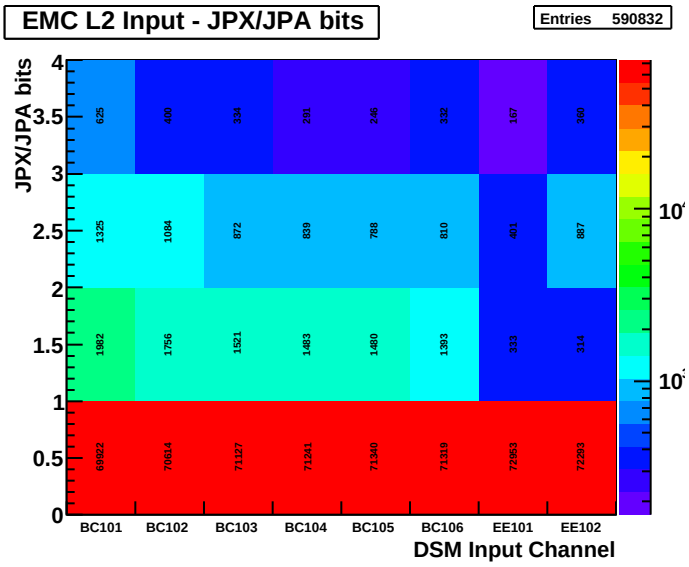

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

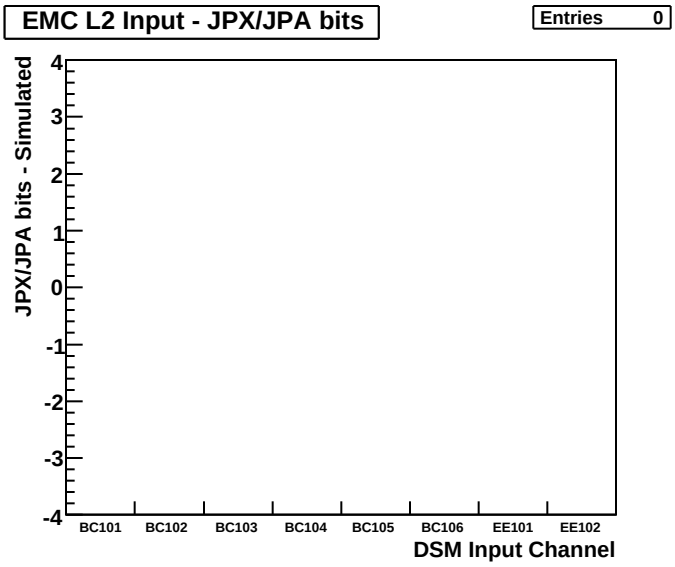

Endcap EMC L1 Input - High Eta Sum
Entries 886248
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

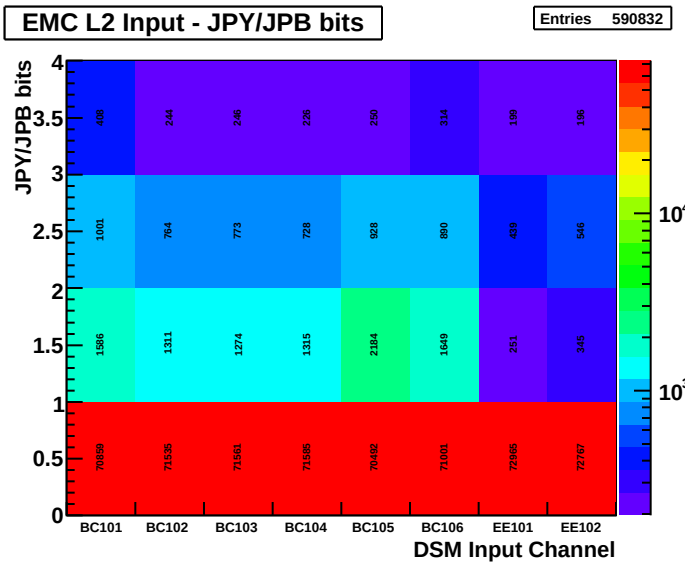

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

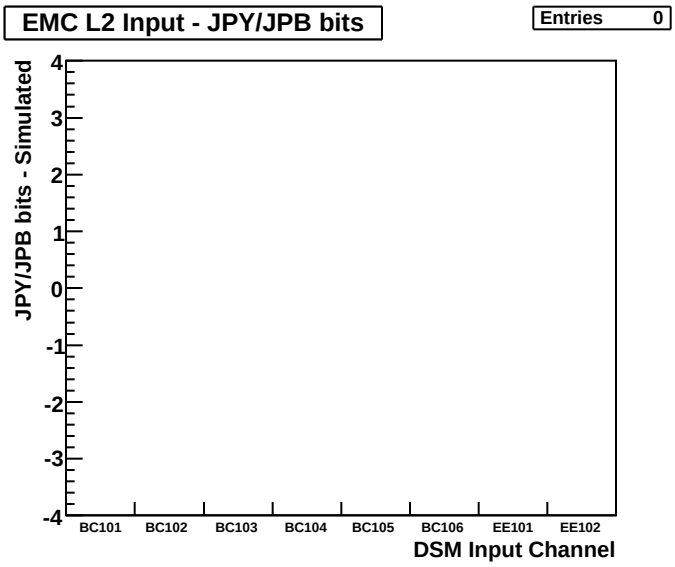

Endcap EMC L1 Input - High Tower Bits
Entries 886248
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

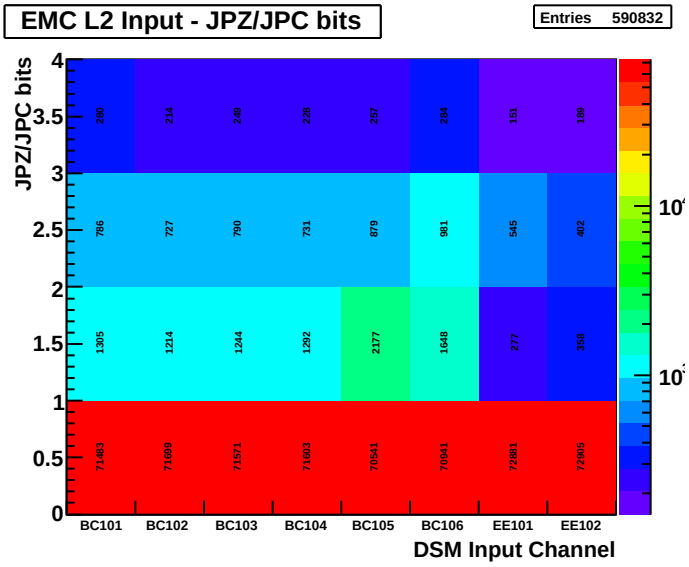

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

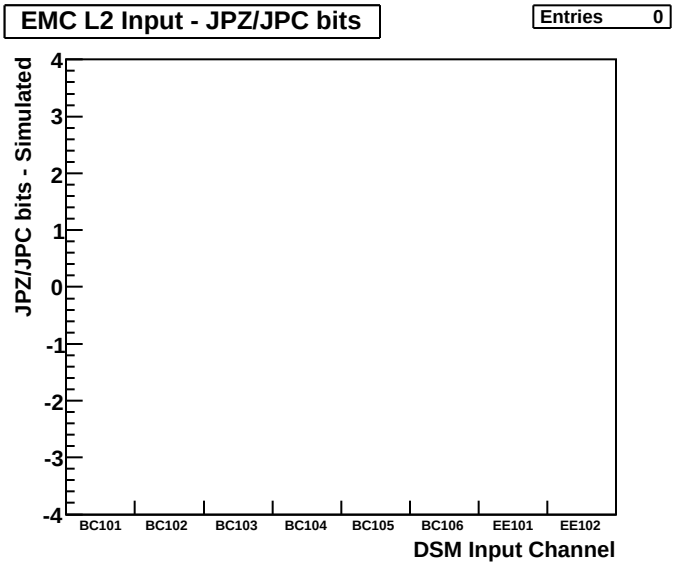

EMC L2 Input - JPX/JPA bits
Entries 590832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
625 400 334 291 246 332 167 360
1325 1084 872 839 788 810 401 887
1962 1756 1521 1483 1480 1393 333 314
69922 70614 71127 71241 71340 71319 72953 72293

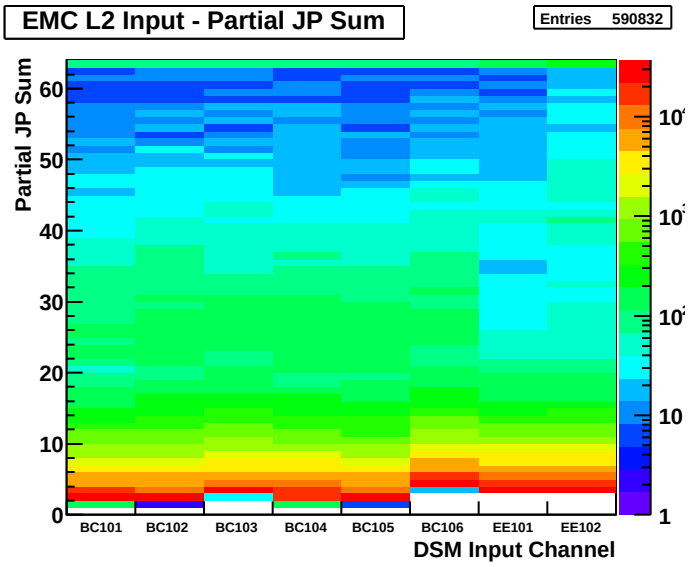

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

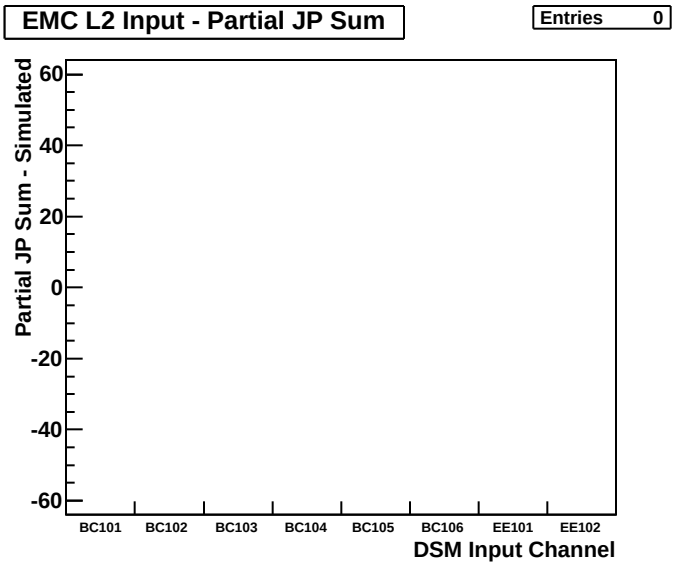

EMC L2 Input - JPY/JPB bits
Entries 590832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
406 244 246 226 250 314 199 196
1001 764 773 728 928 890 439 546
1586 1311 1274 1315 2184 1649 251 345
70859 71535 71561 71585 70492 71001 72965 72767

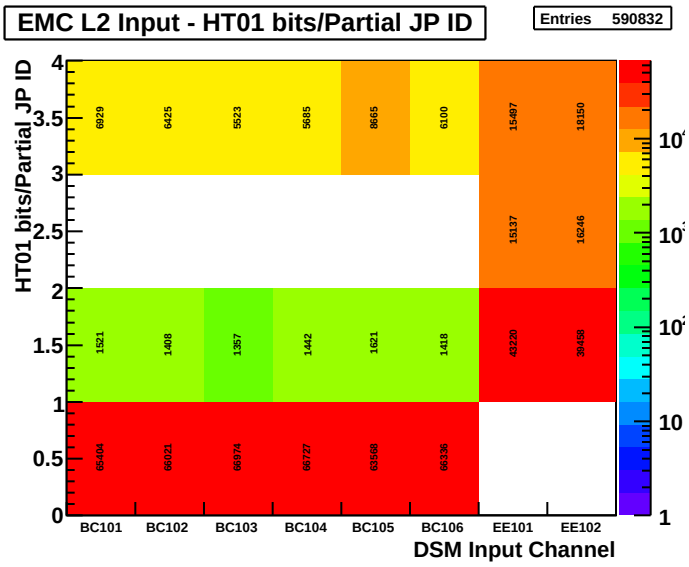

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

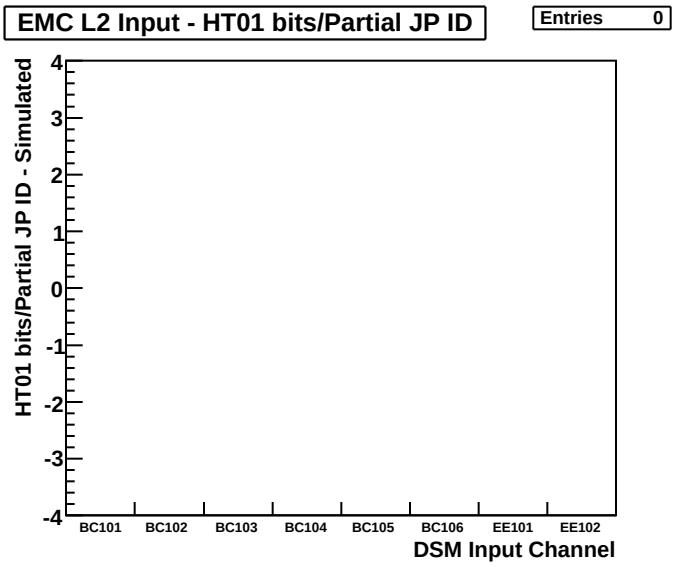

EMC L2 Input - JPZ/JPC bits
Entries 590832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
280 214 249 228 257 234 151 189
786 727 790 731 879 981 545 402
1305 1214 1244 1292 2177 1648 277 358
71483 71689 71571 71603 70541 70941 72881 72905

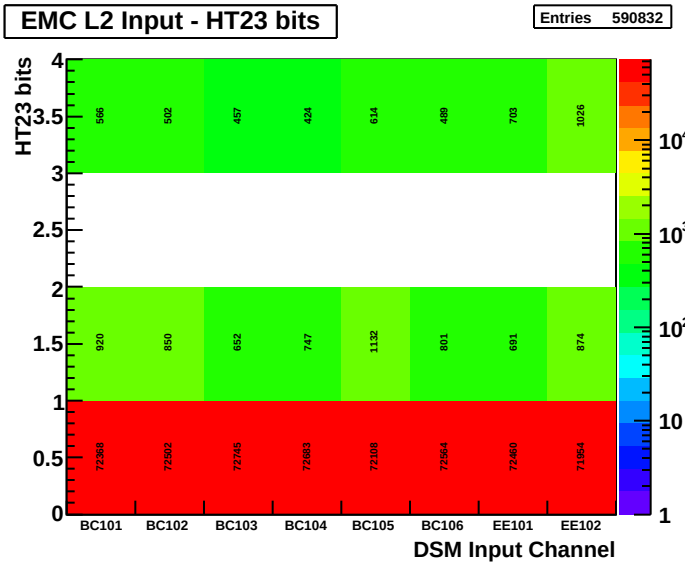

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

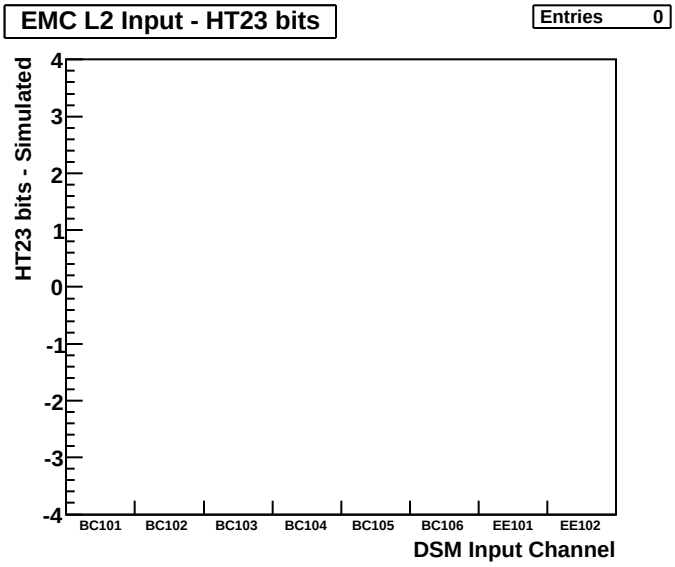

EMC L2 Input - Partial JP Sum
Entries 590832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

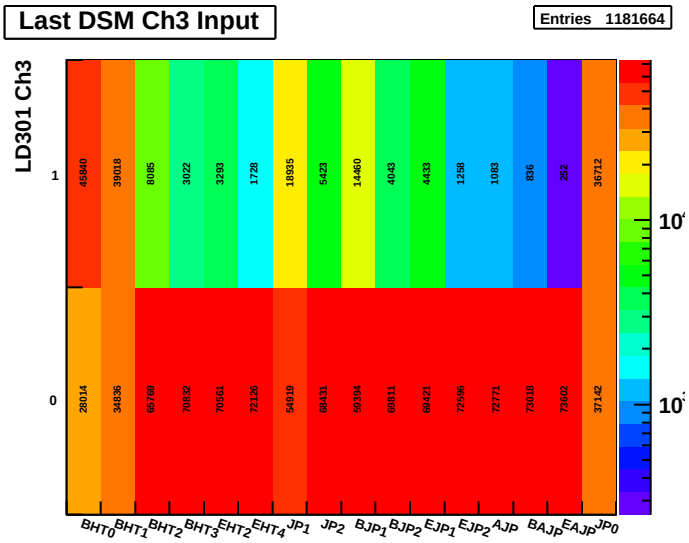

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

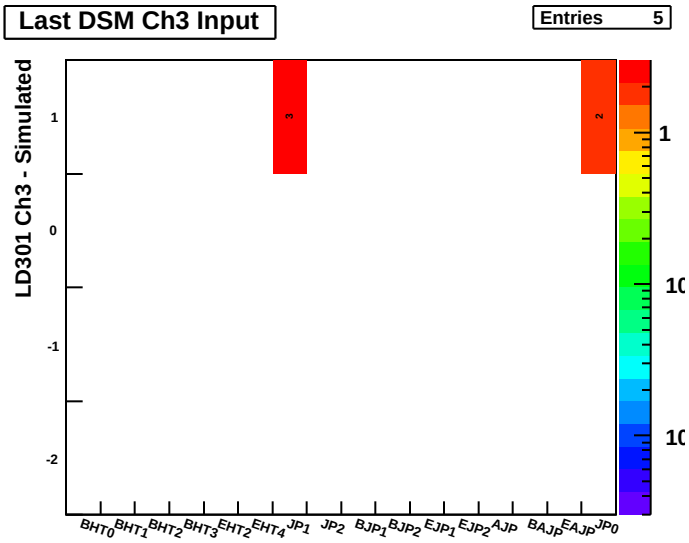

EMC L2 Input - HT01 bits/Partial JP ID
Entries 590832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

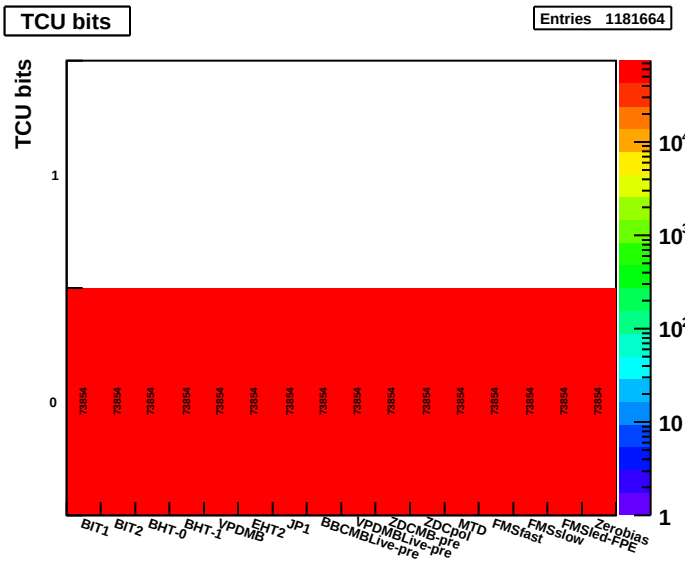

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

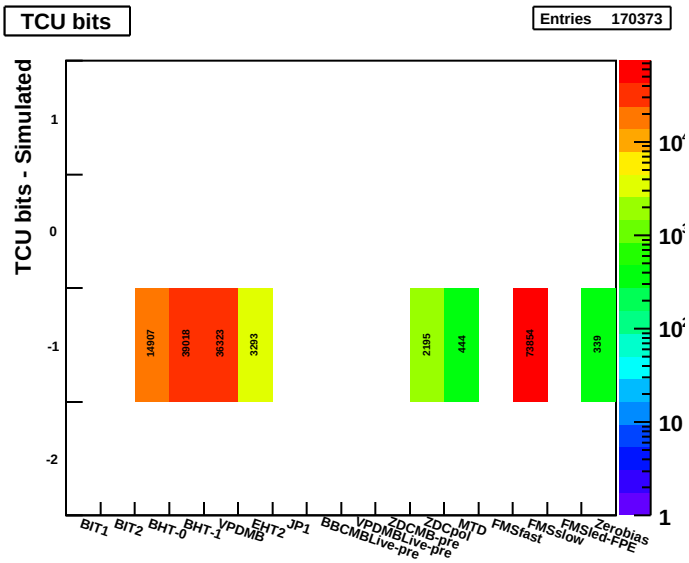

EMC L2 Input - HT23 bits
Entries 590832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

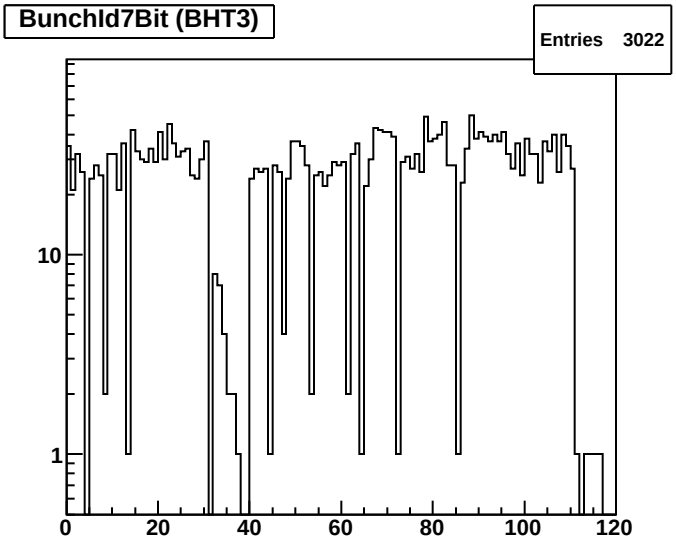

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

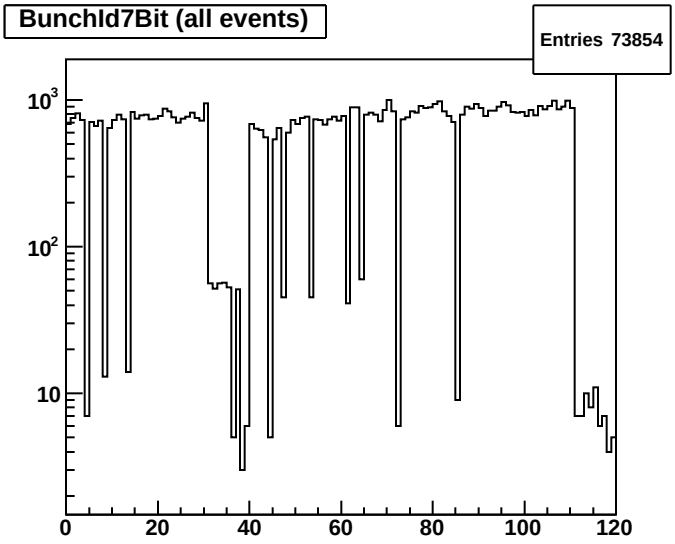

Last DSM Ch3 Input
Entries 1181664
LD301 Ch3
45840 39018 8085 3022 3293 1728 18935 5423 14460 4043 4433 1258 1083 836 252 36712
28014 34836 65769 70832 70561 72126 54919 68431 69394 68811 69421 72596 72771 73018 73602 37142
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

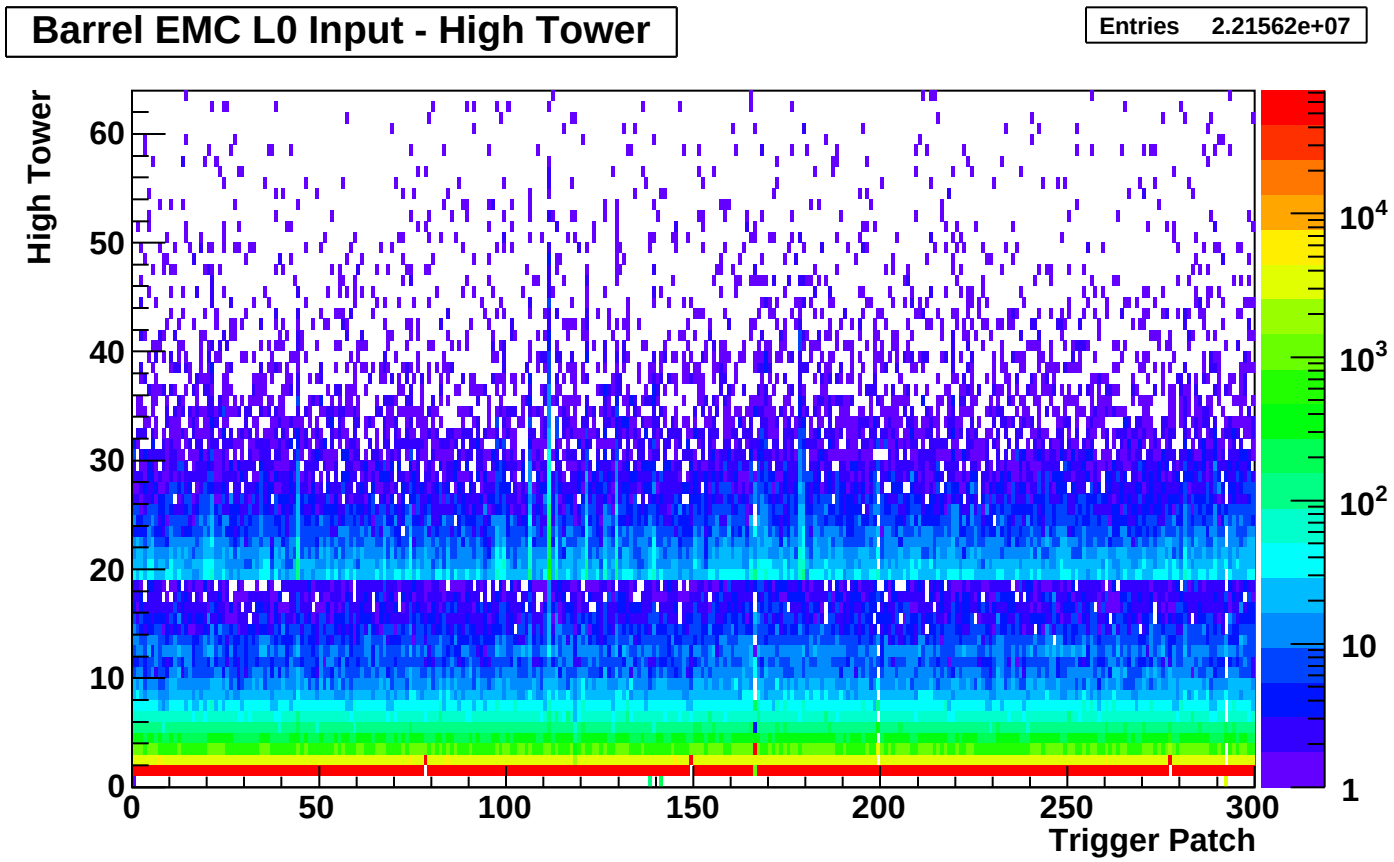

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
3
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

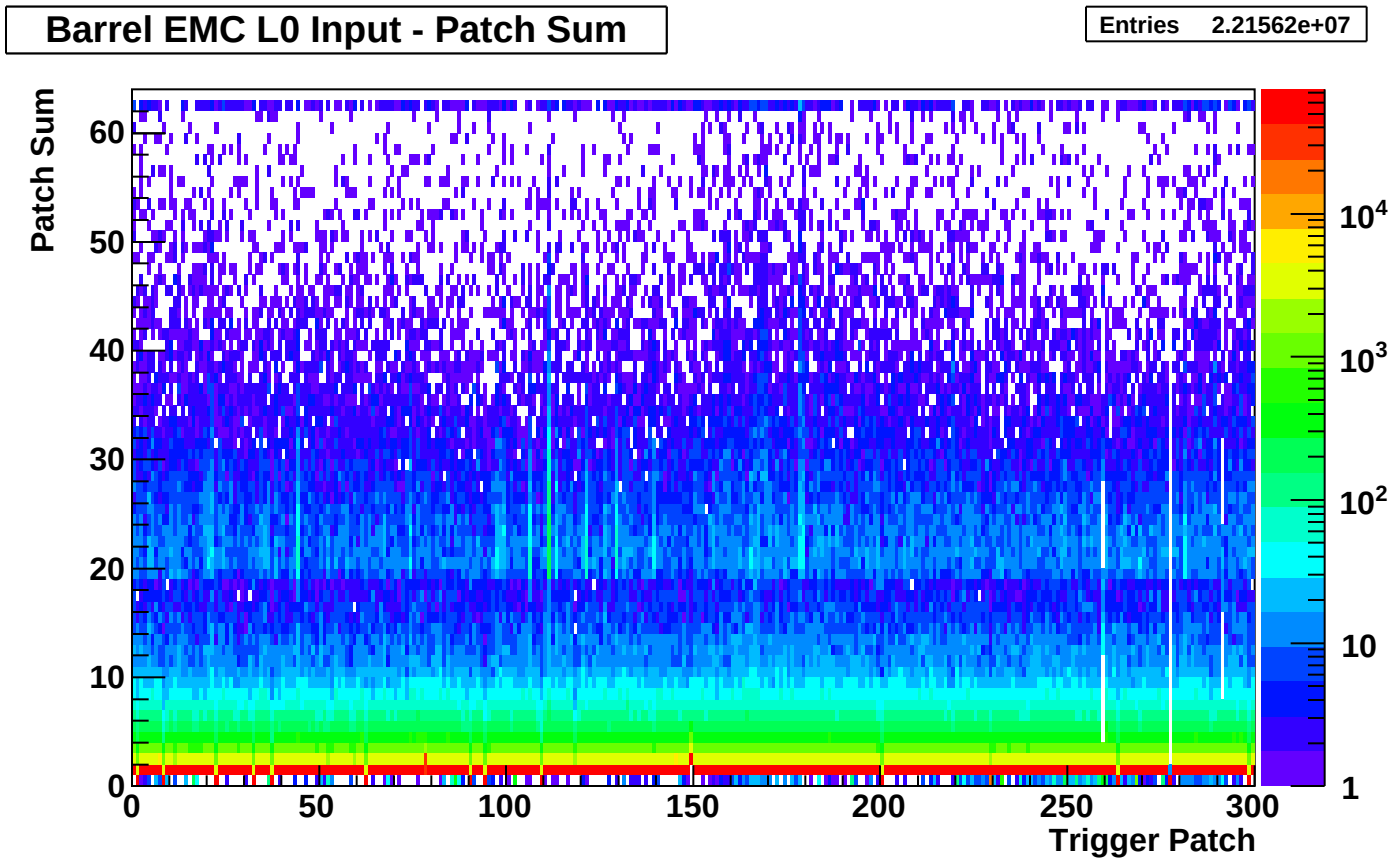

TCU bits
Entries 1181664
TCU bits
73854
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

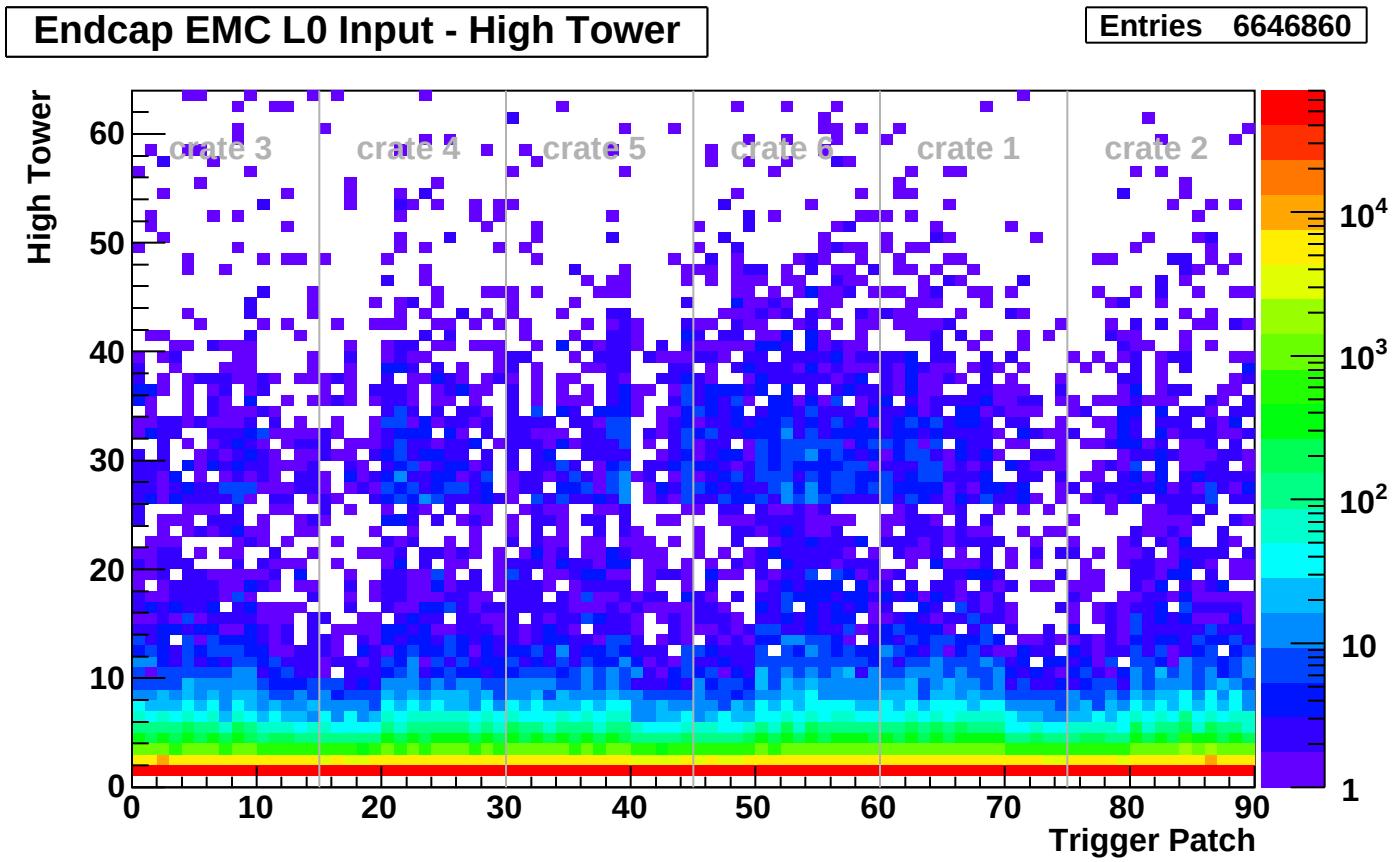

TCU bits
Entries 170373
TCU bits - Simulated
14907 39018 36323 3293 2195 444 73854 339
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

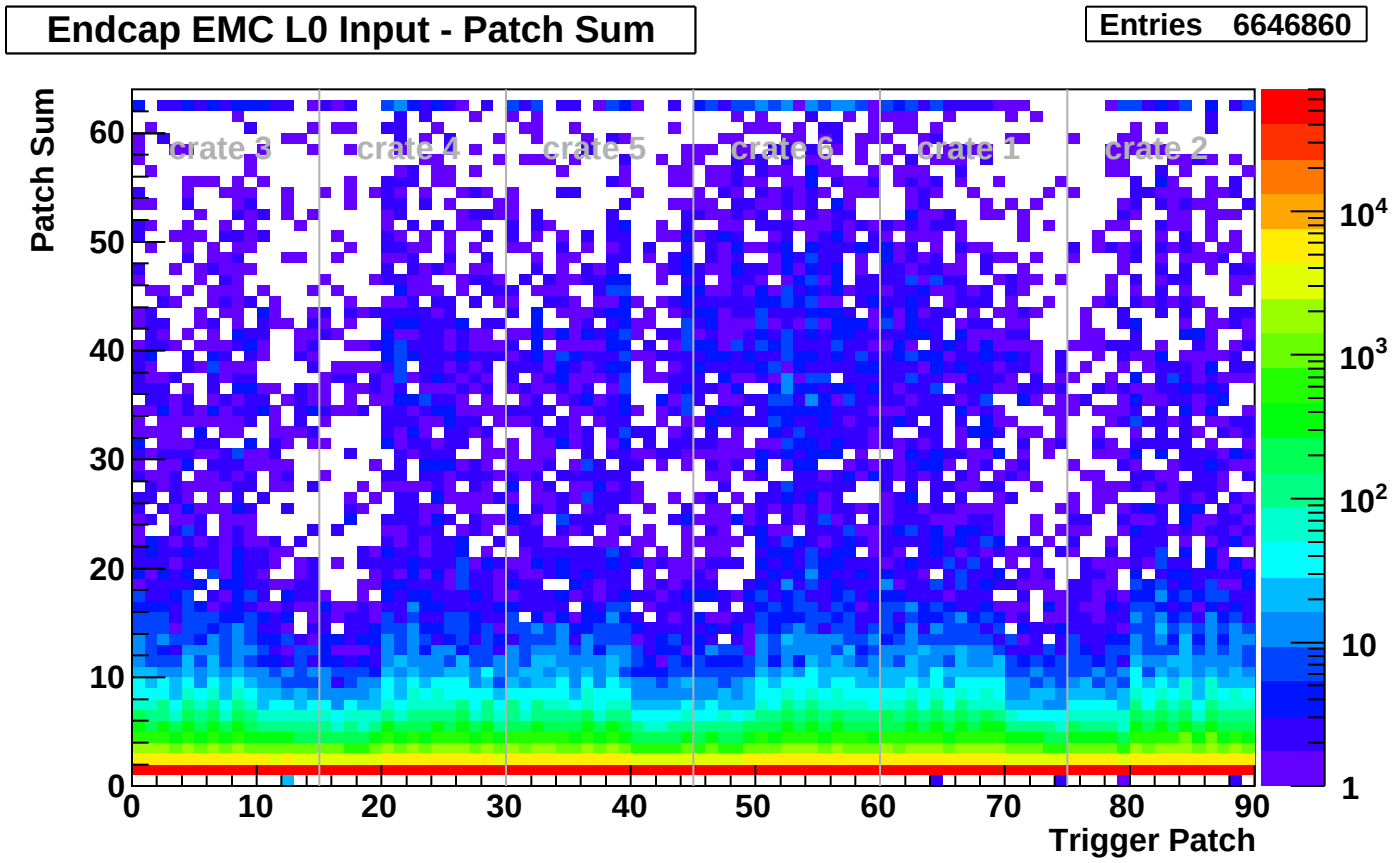

BunchId7Bit (BHT3)
Entries 3022

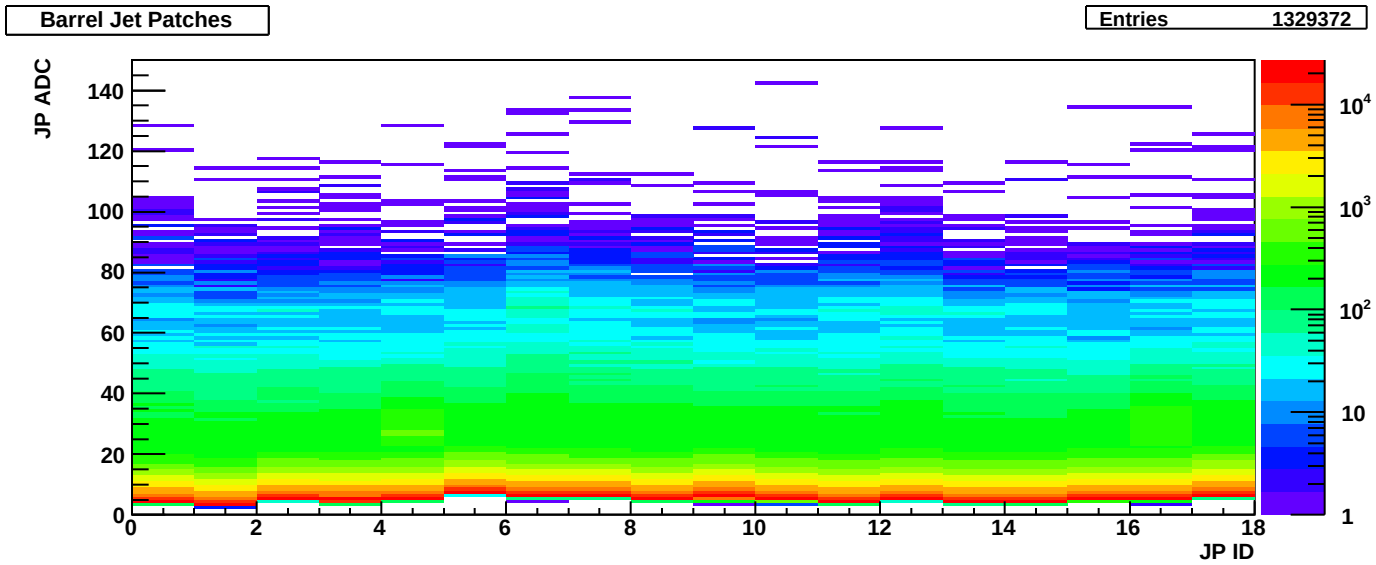

BunchId7Bit (all events)
Entries 73854

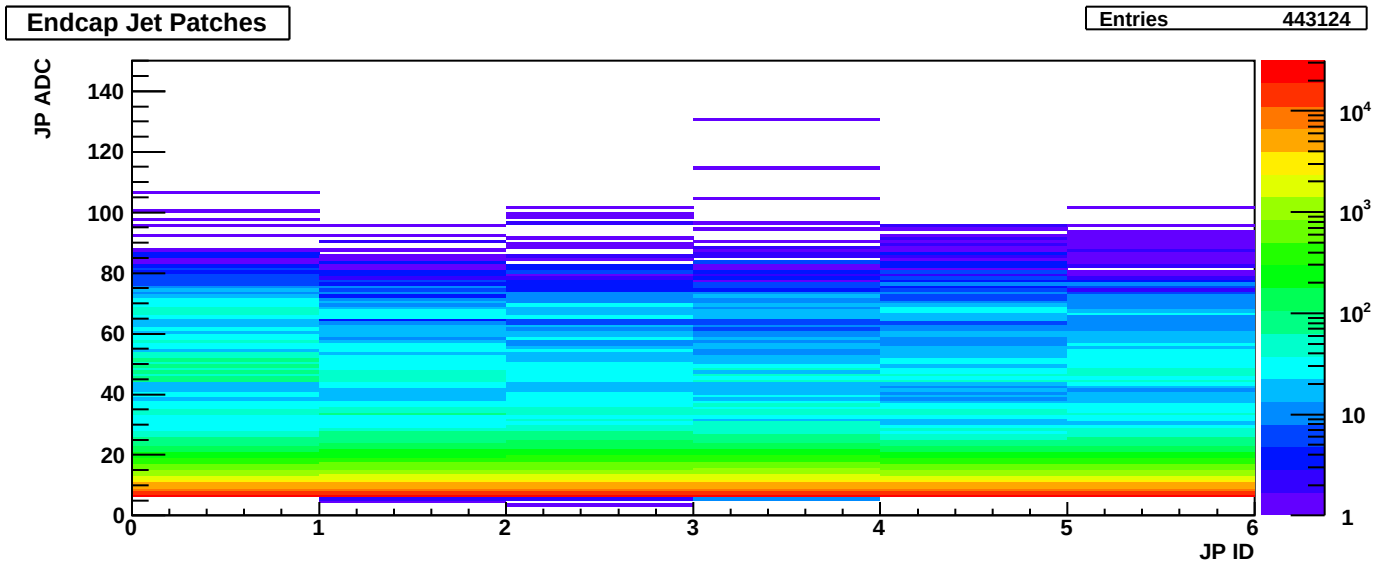

Barrel EMC L0 Input - High Tower
Entries 2.21562e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

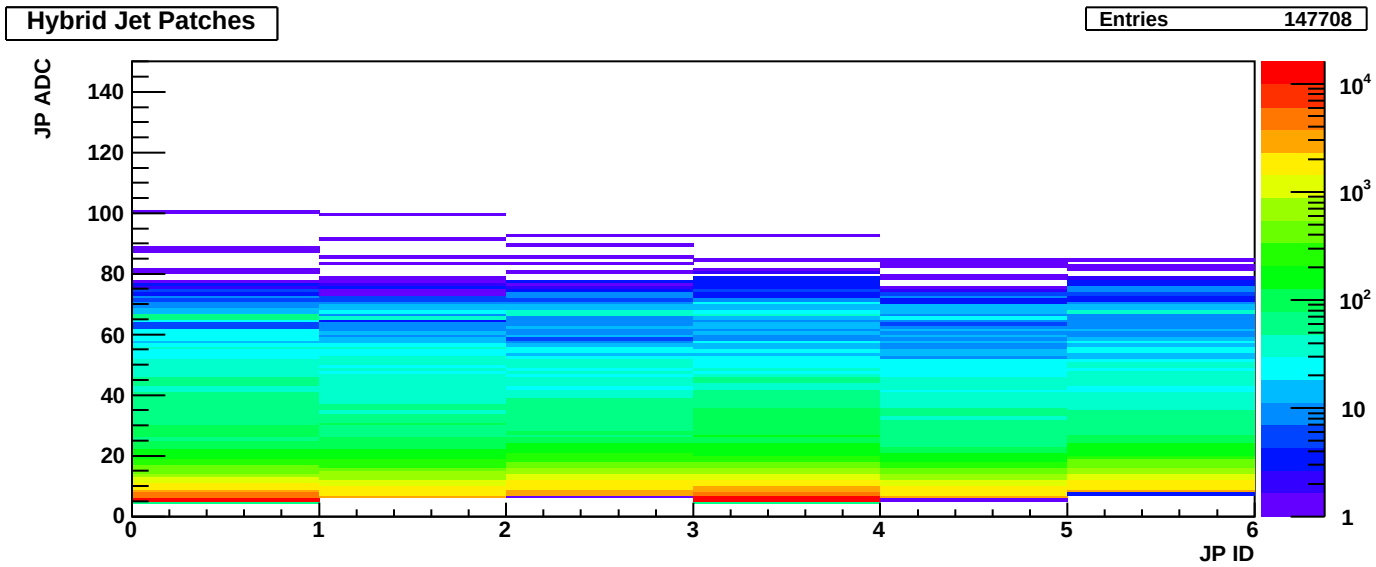

Barrel EMC L0 Input - Patch Sum
Entries 2.21562e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

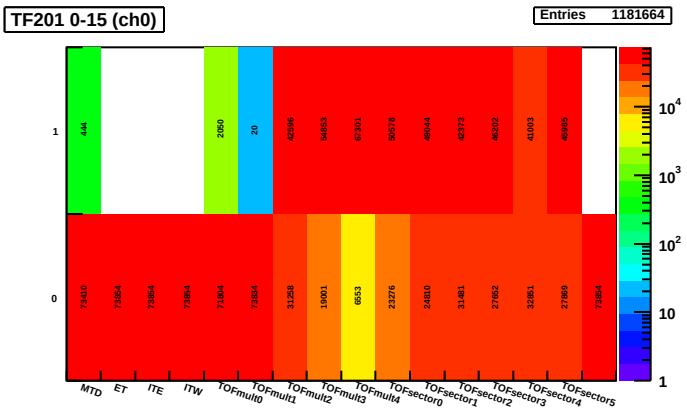

Endcap EMC L0 Input - High Tower
Entries 6646860
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

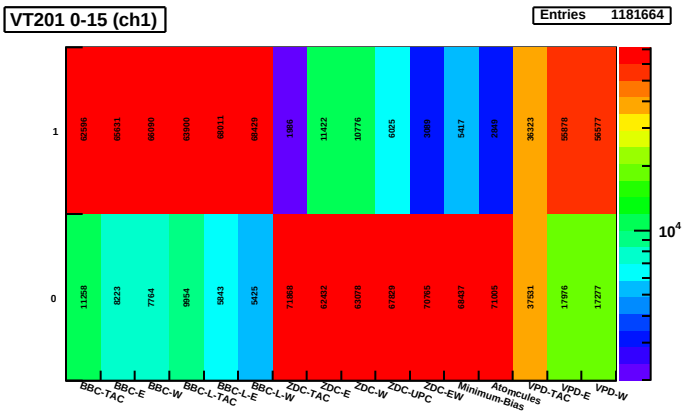

Endcap EMC L0 Input - Patch Sum
Entries 6646860
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

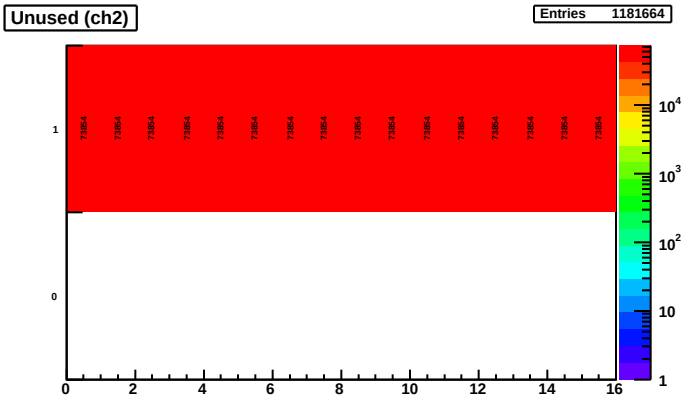

Barrel Jet Patches
Entries 1329372
JP ADC
JP ID

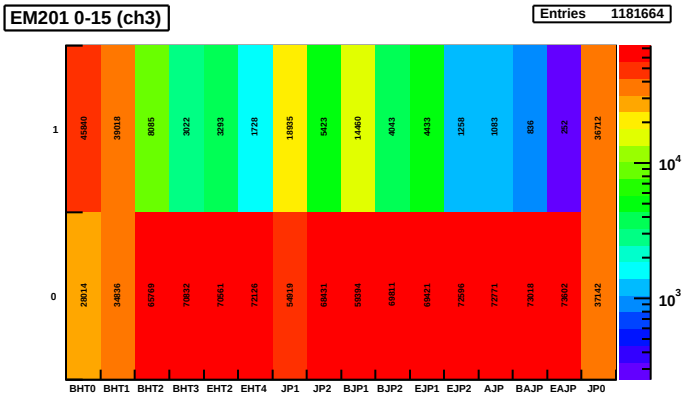

Endcap Jet Patches
Entries 443124
JP ADC
JP ID

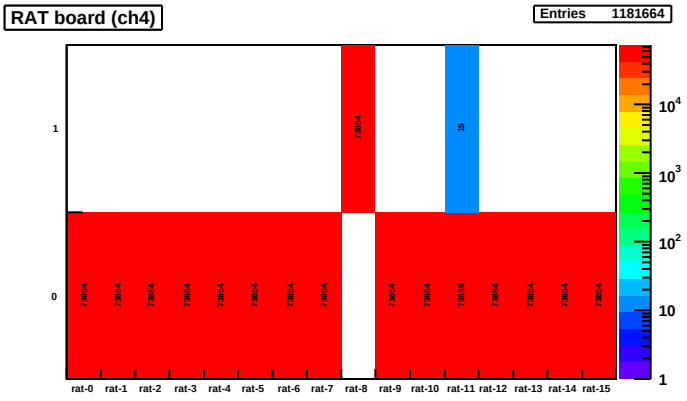

Hybrid Jet Patches
Entries 147708
JP ADC
JP ID

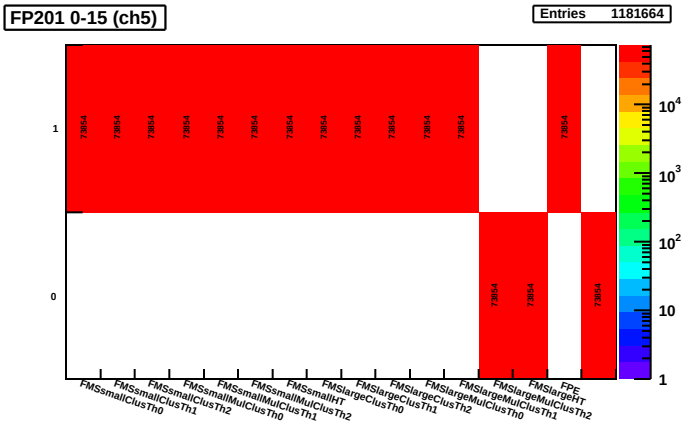

TF201 0-15 (ch0)
Entries 1181664
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

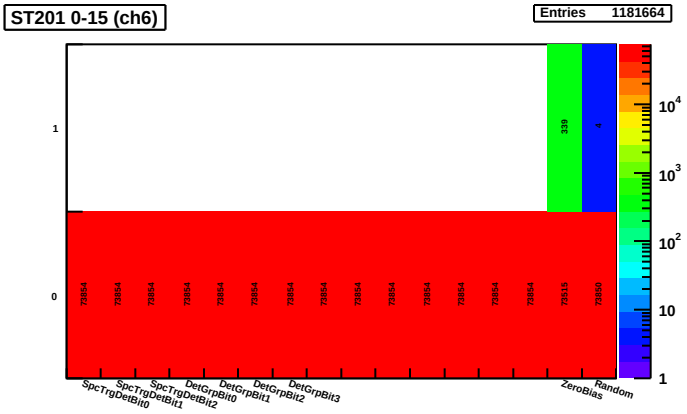

VT201 0-15 (ch1)
Entries 1181664
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

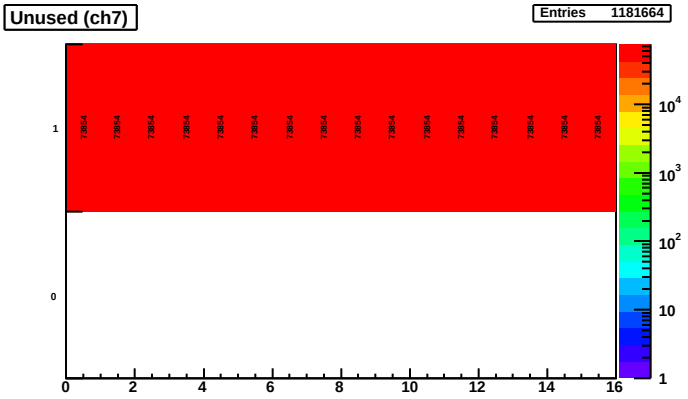

Unused (ch2)
Entries 1181664

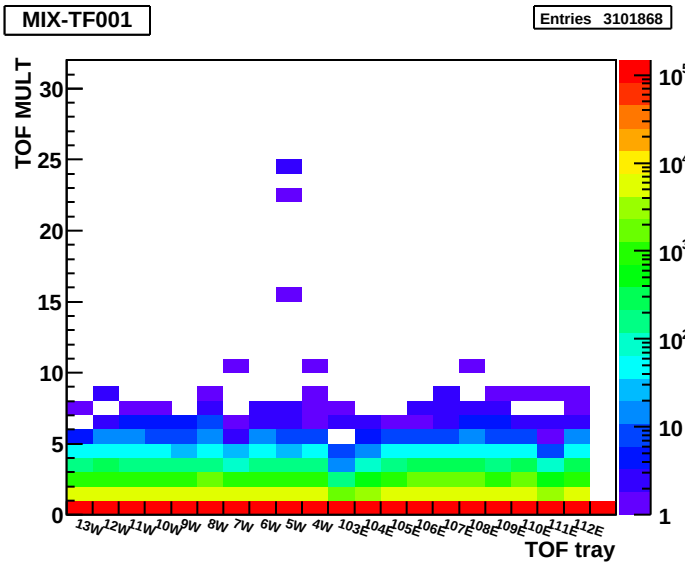

EM201 0-15 (ch3)
Entries 1181664
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

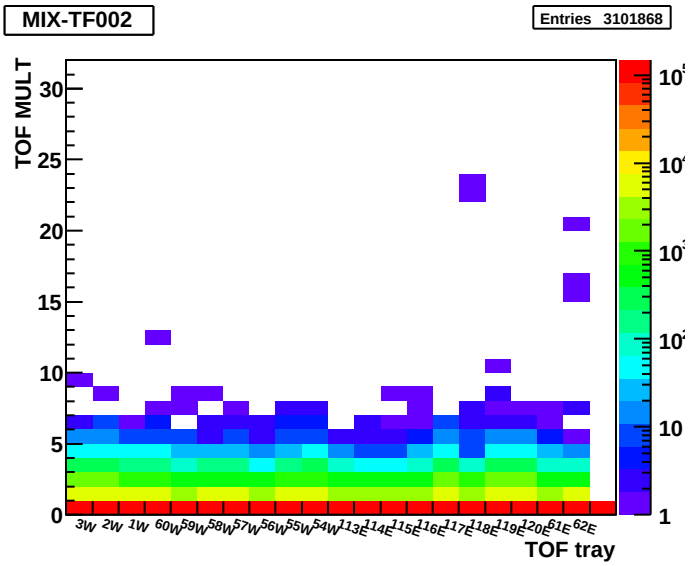

RAT board (ch4)
Entries 1181664
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

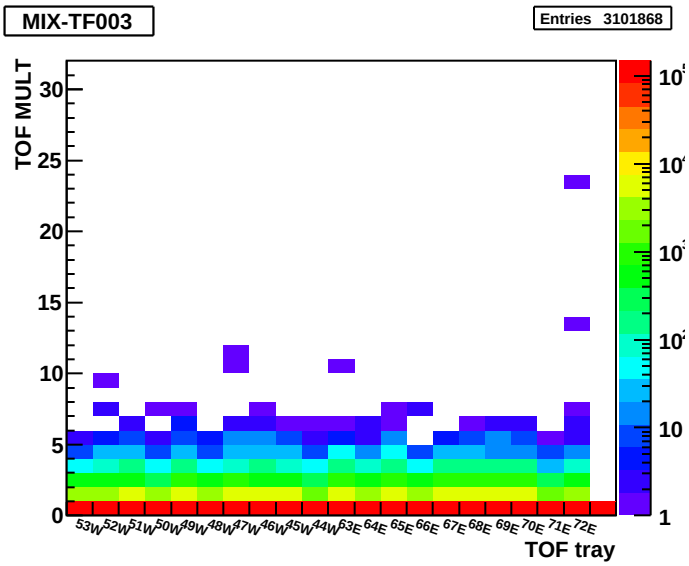

FP201 0-15 (ch5)
Entries 1181664
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

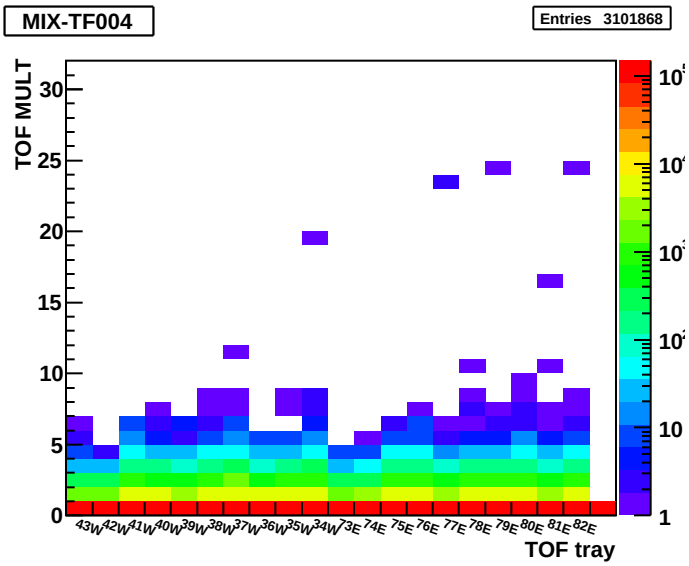

ST201 0-15 (ch6)
Entries 1181664
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

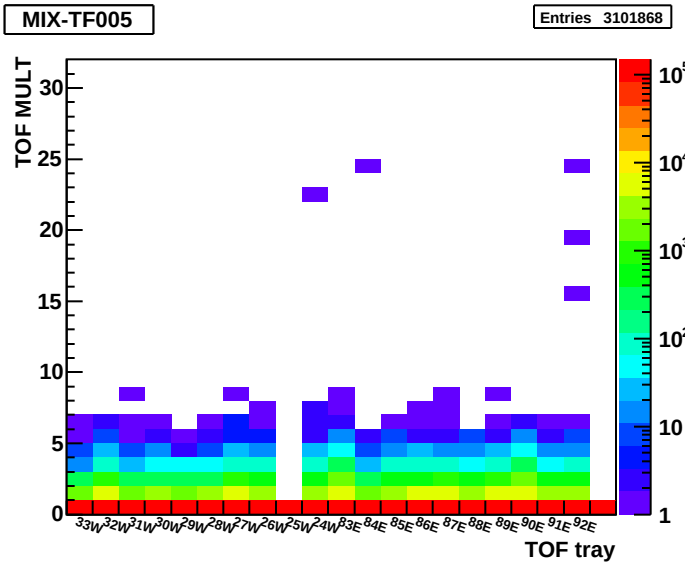

Unused (ch7)
Entries 1181664

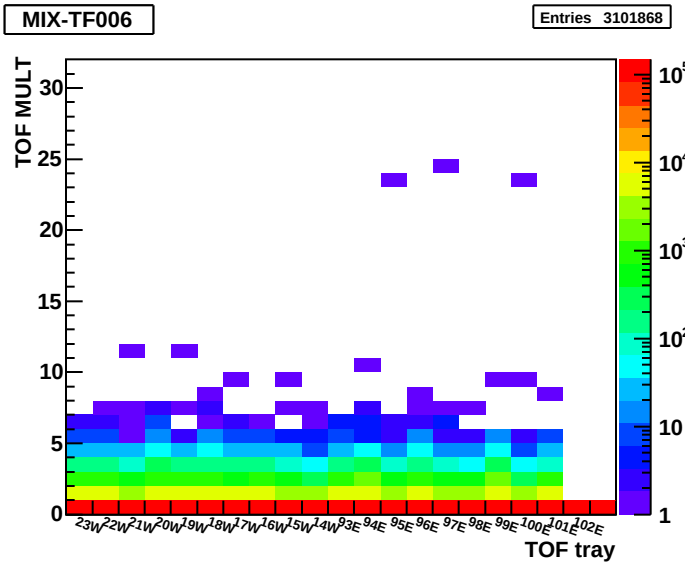

MIX-TF001
Entries 3101868
TOF MULT
TOF tray

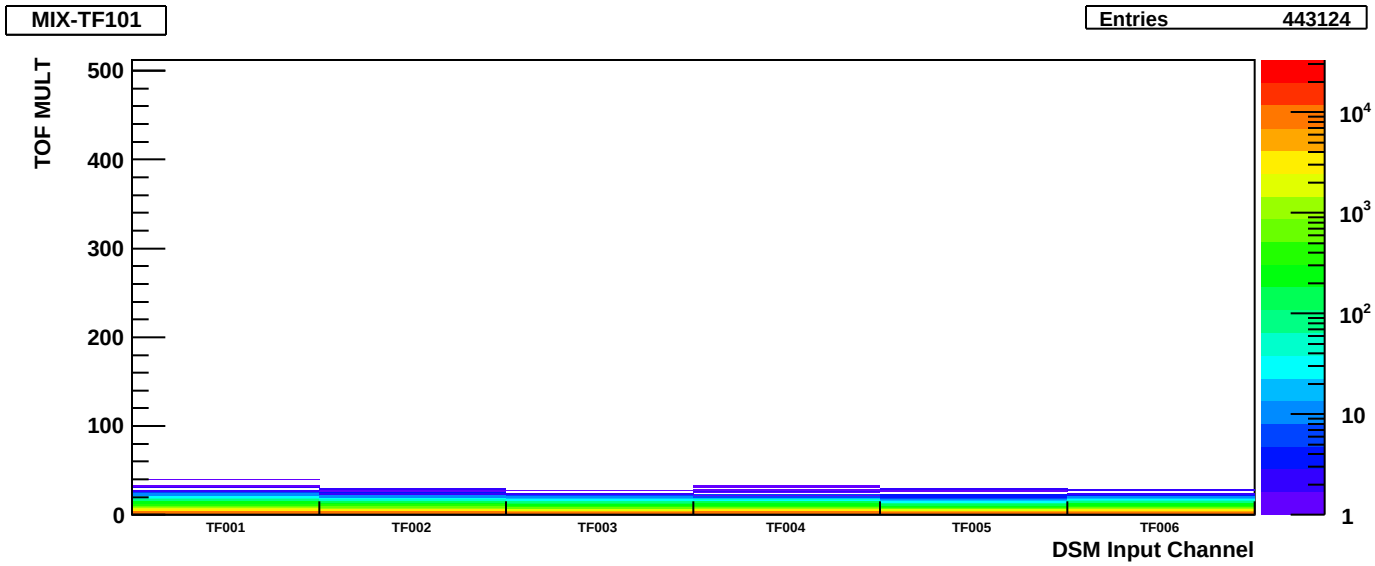

MIX-TF002
Entries 3101868
TOF MULT
TOF tray

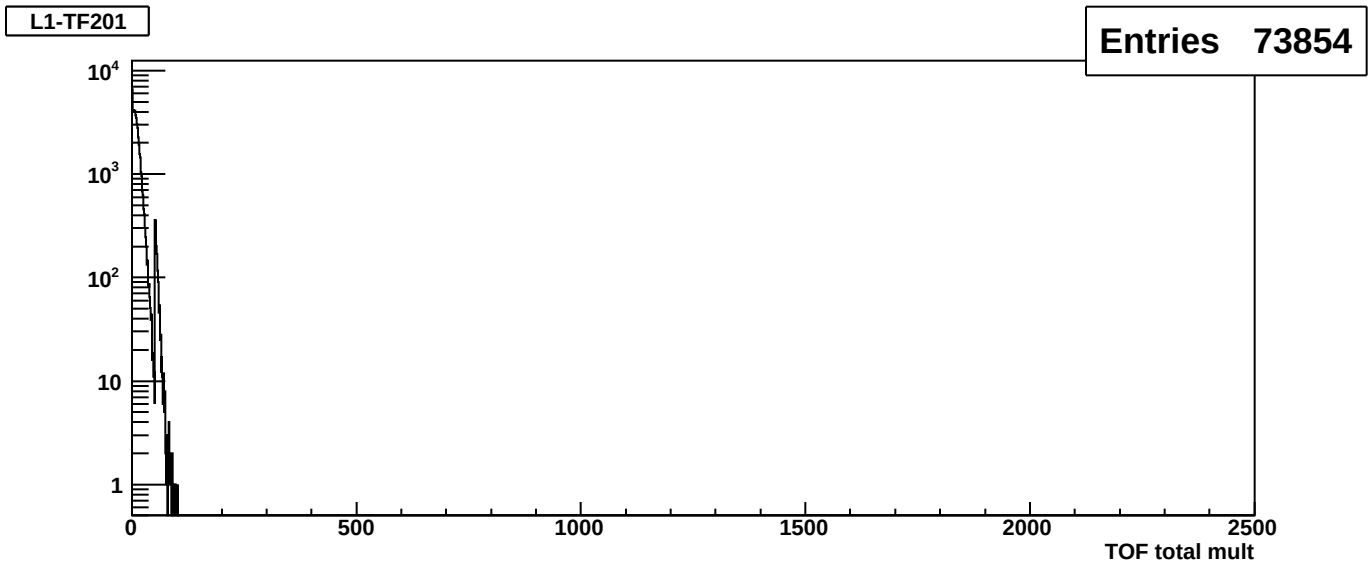

MIX-TF003
Entries 3101868
TOF MULT
TOF tray

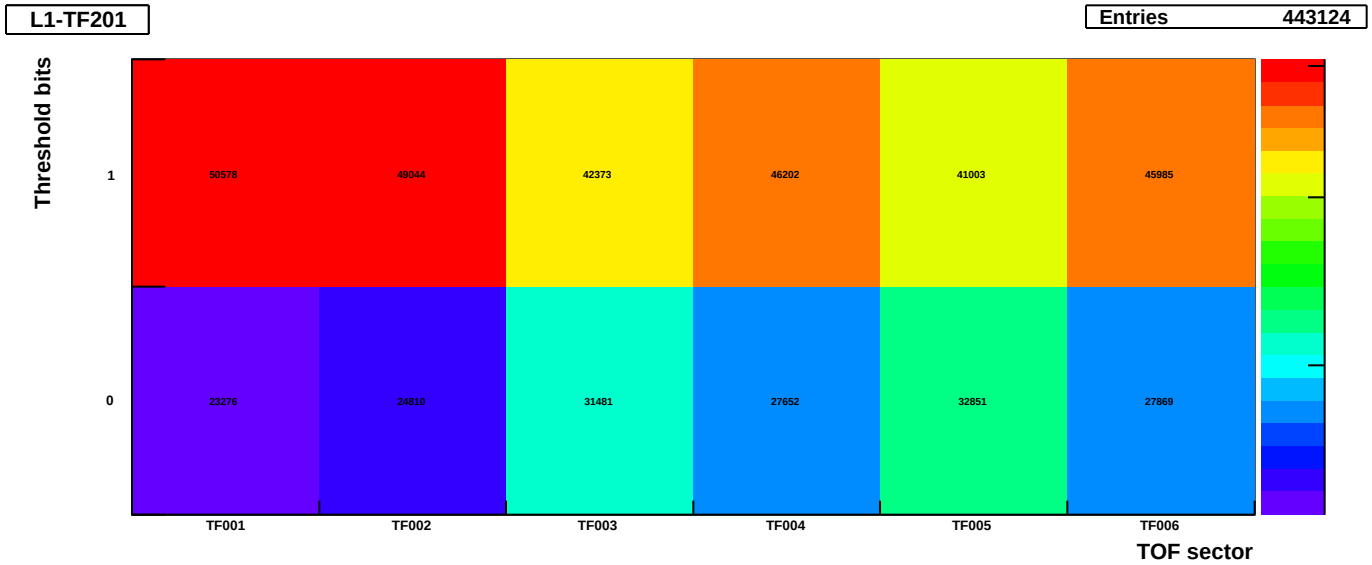

MIX-TF004
Entries 3101868
TOF MULT
TOF tray

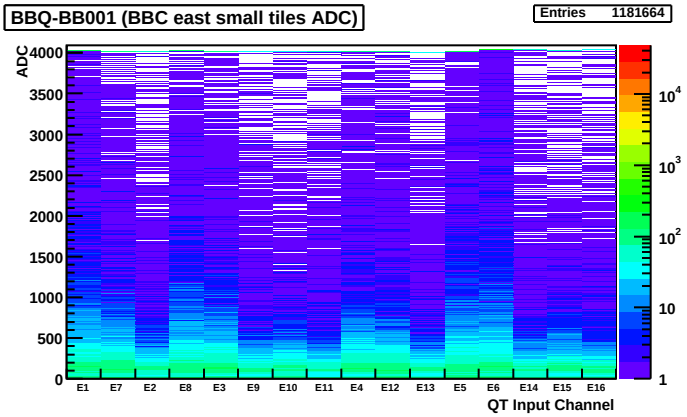

MIX-TF005
Entries 3101868
TOF MULT
TOF tray

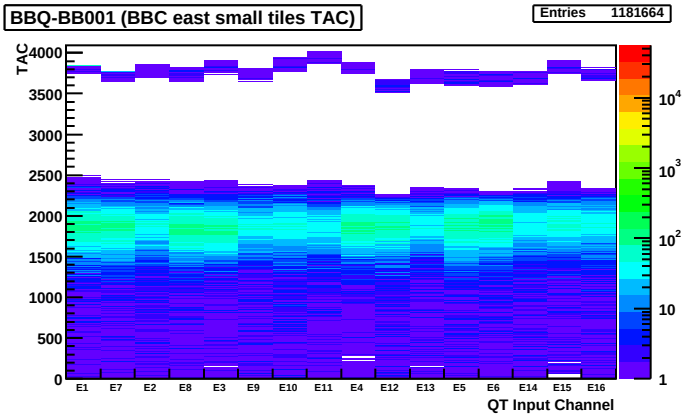

MIX-TF006
Entries 3101868
TOF MULT
TOF tray

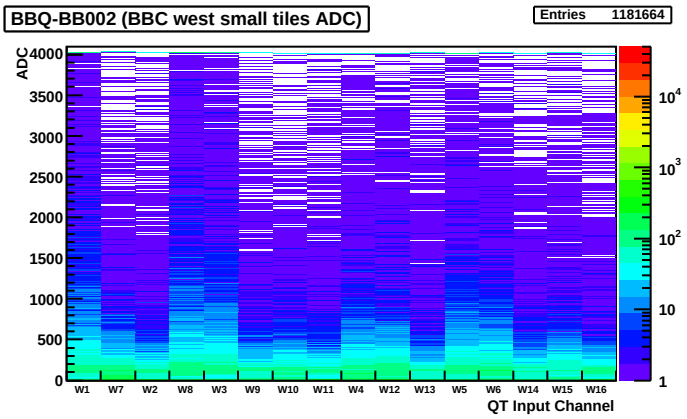

MIX-TF101
Entries 443124
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

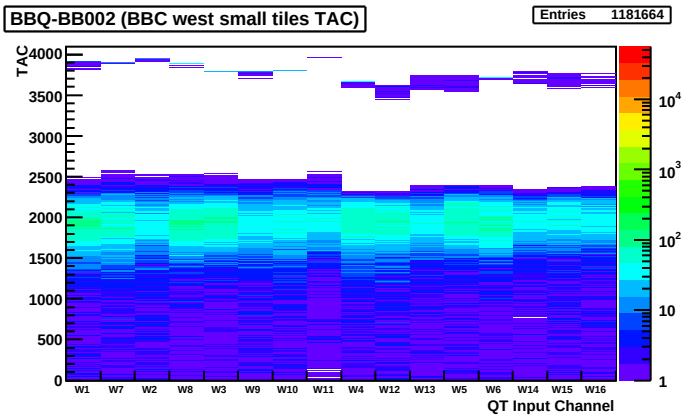

L1-TF201
Entries 73854
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

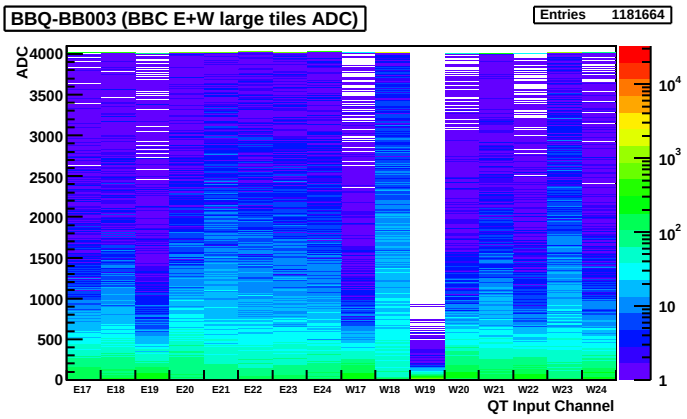

L1-TF201
Entries 443124
Threshold bits
1
0
50578
49044
42373
46202
41003
45985
23276
24810
31481
27652
32851
27869
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

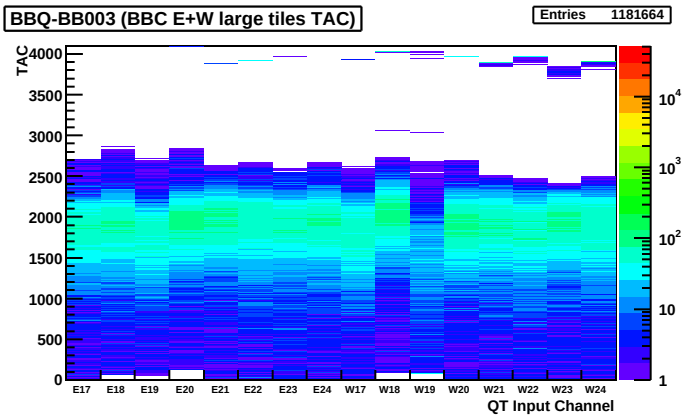

BBQ-BB001 (BBC east small tiles ADC)
Entries 1181664
ADC
QT Input Channel

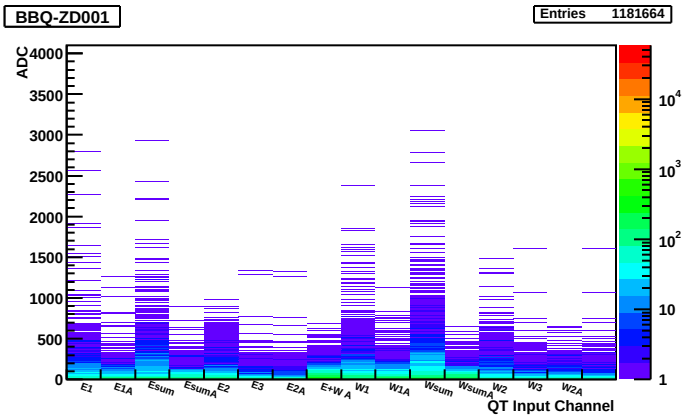

BBQ-BB001 (BBC east small tiles TAC)
Entries 1181664
TAC
QT Input Channel

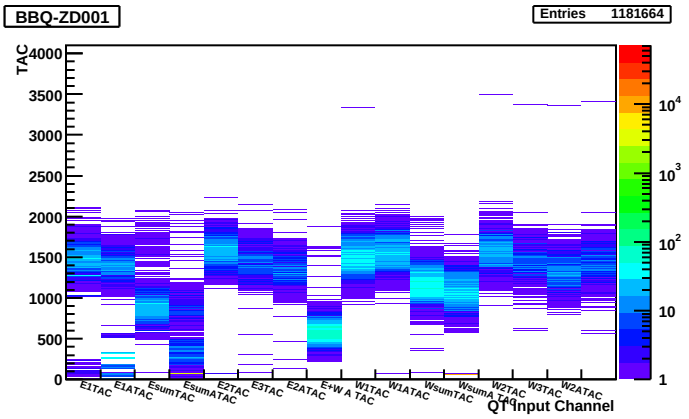

BBQ-BB002 (BBC west small tiles ADC)
Entries 1181664
ADC
QT Input Channel

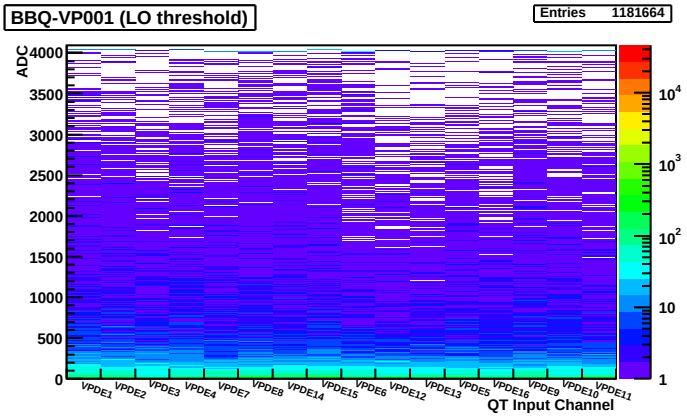

BBQ-BB002 (BBC west small tiles TAC)
Entries 1181664
TAC
QT Input Channel

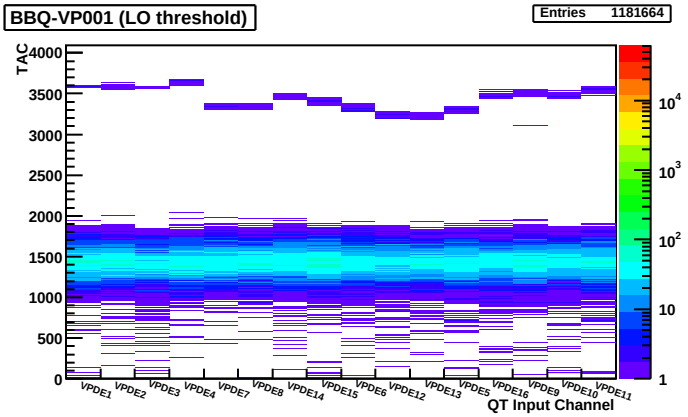

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1181664
ADC
QT Input Channel

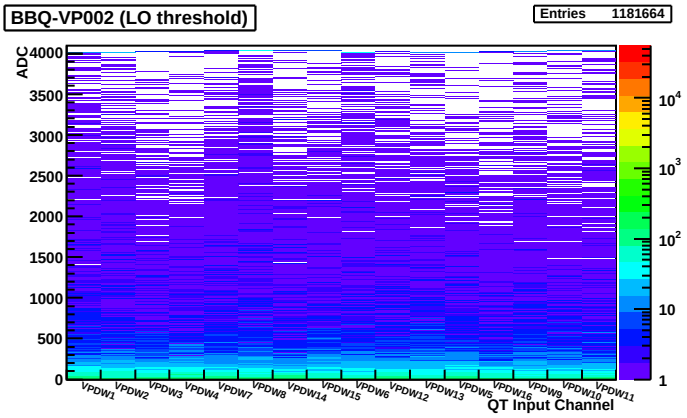

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1181664
TAC
QT Input Channel

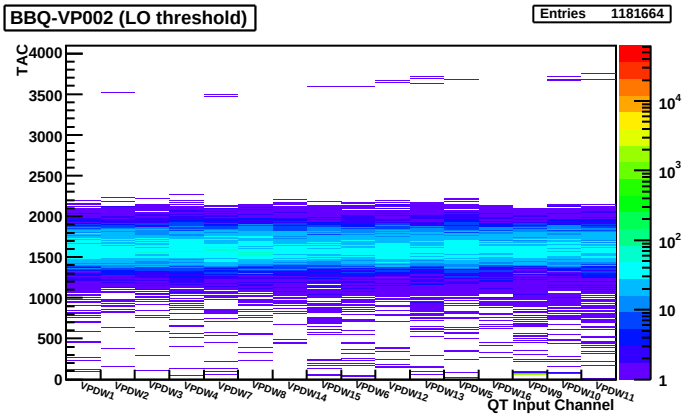

BBQ-ZD001
Entries 1181664
ADC
QT Input Channel

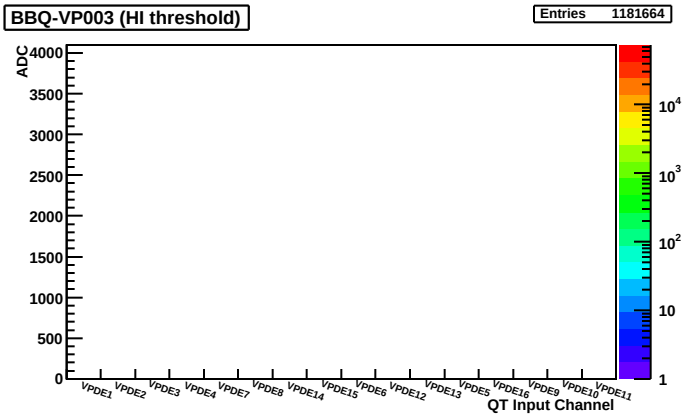

BBQ-ZD001
Entries 1181664
TAC
QT Input Channel

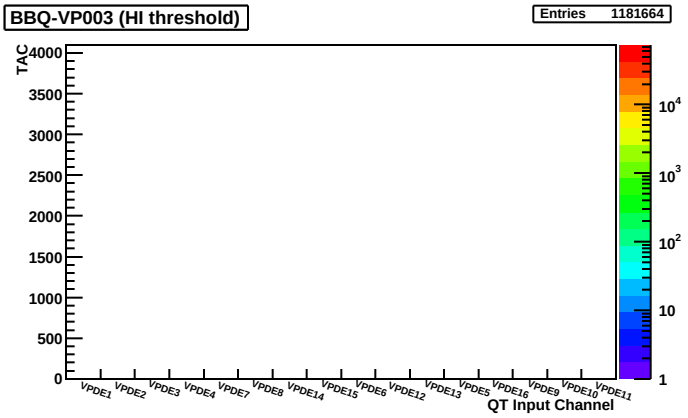

BBQ-VP001 (LO threshold)
Entries 1181664
ADC
QT Input Channel

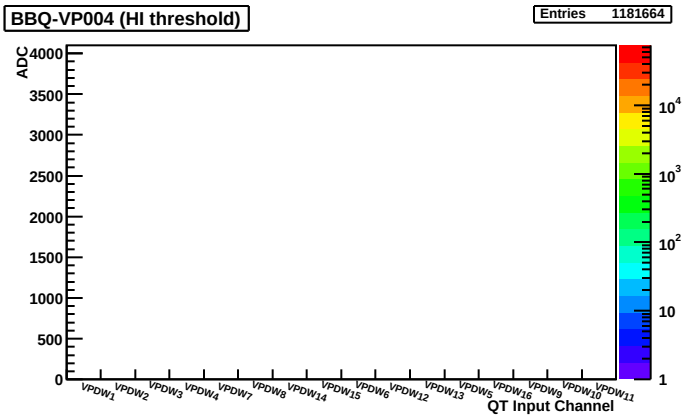

BBQ-VP001 (LO threshold)
Entries 1181664
TAC
QT Input Channel

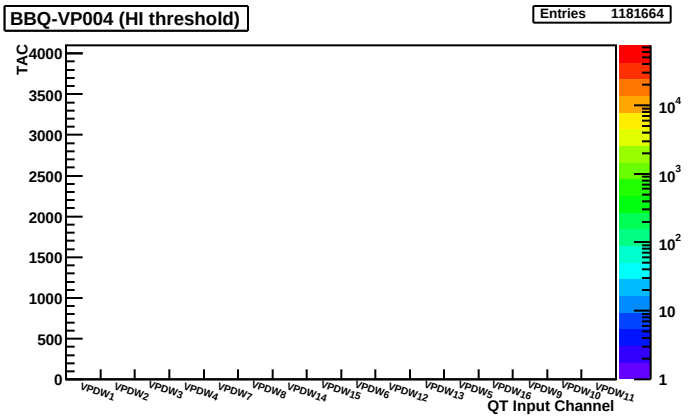

BBQ-VP002 (LO threshold)
Entries 1181664
ADC
QT Input Channel

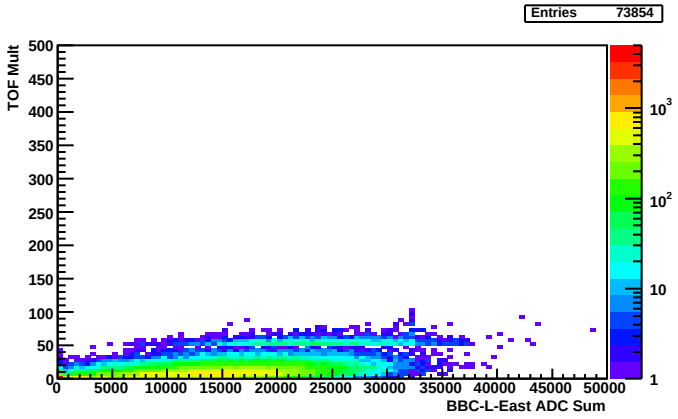

BBQ-VP002 (LO threshold)
Entries 1181664
TAC
QT Input Channel

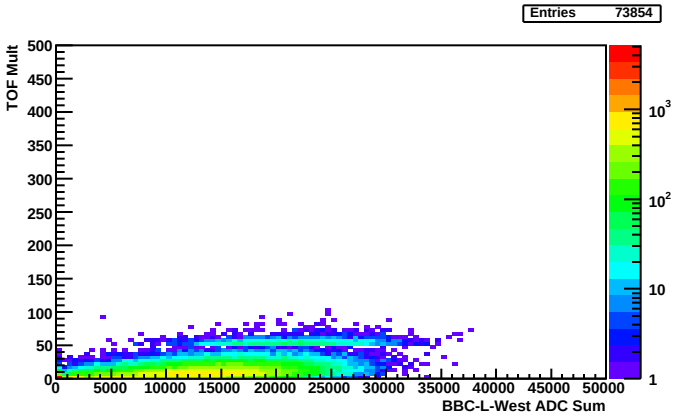

BBQ-VP003 (HI threshold)
Entries 1181664
ADC
QT Input Channel

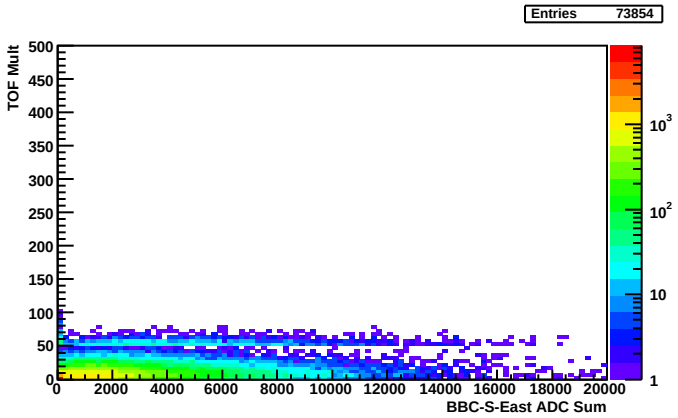

BBQ-VP003 (HI threshold)
Entries 1181664
TAC
QT Input Channel

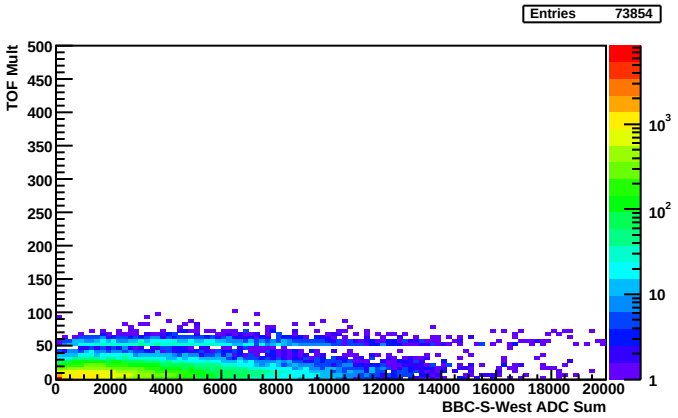

BBQ-VP004 (HI threshold)
Entries 1181664
ADC
QT Input Channel

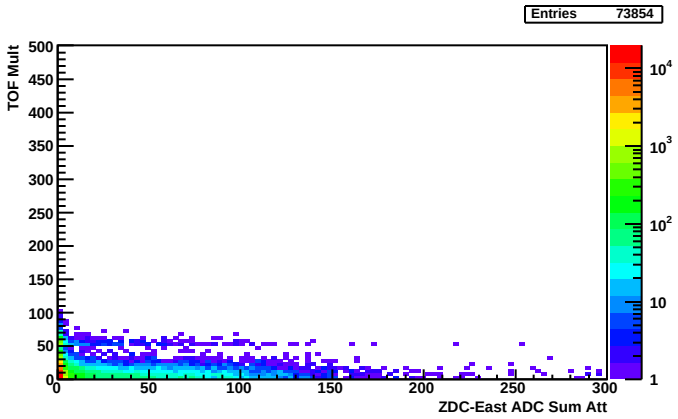

BBQ-VP004 (HI threshold)
Entries 1181664
TAC
QT Input Channel

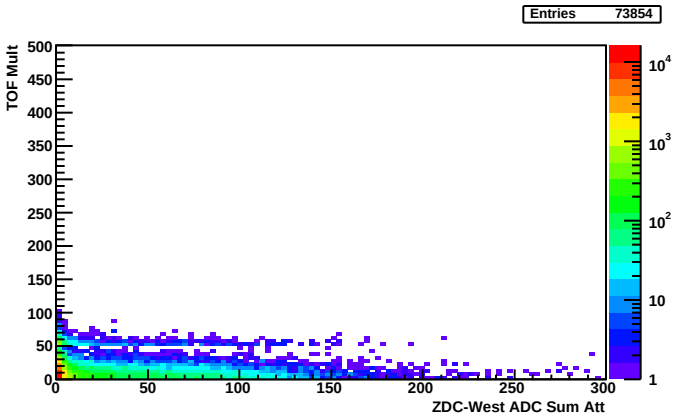

Entries 73854
TOF Mult
BBC-L-East ADC Sum

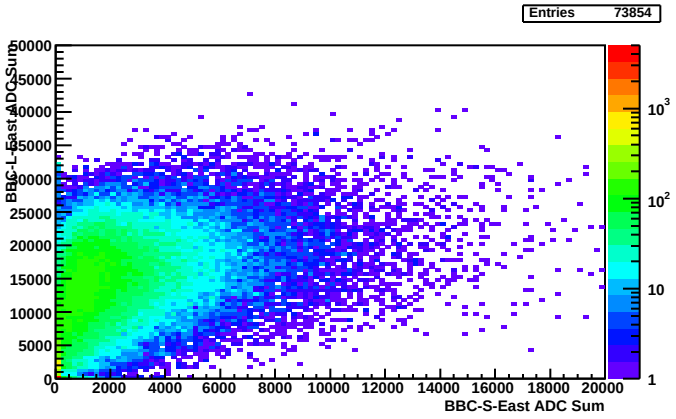

Entries 73854
TOF Mult
BBC-L-West ADC Sum

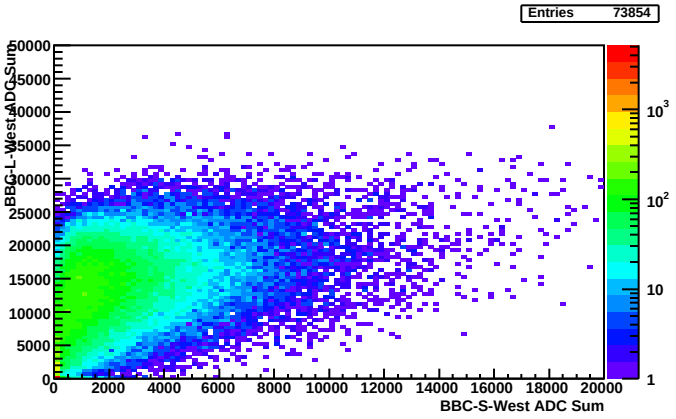

Entries 73854
TOF Mult
BBC-S-East ADC Sum

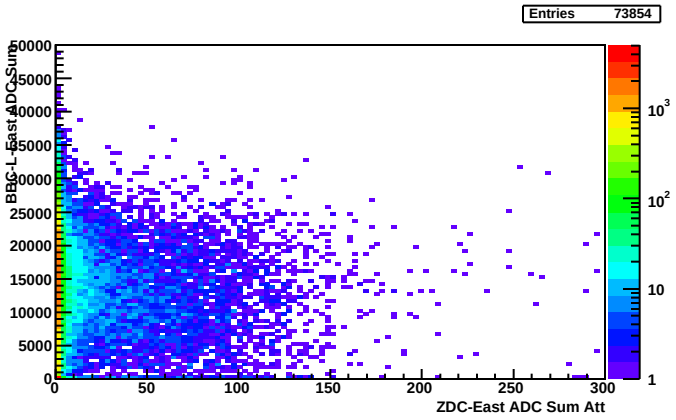

Entries 73854
TOF Mult
BBC-S-West ADC Sum

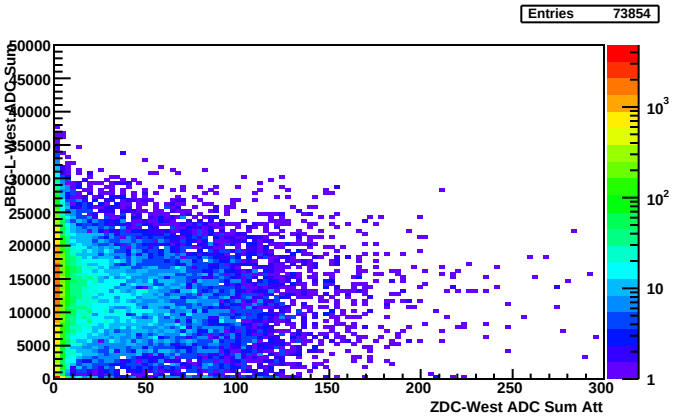

Entries 73854
TOF Mult
ZDC-East ADC Sum Att

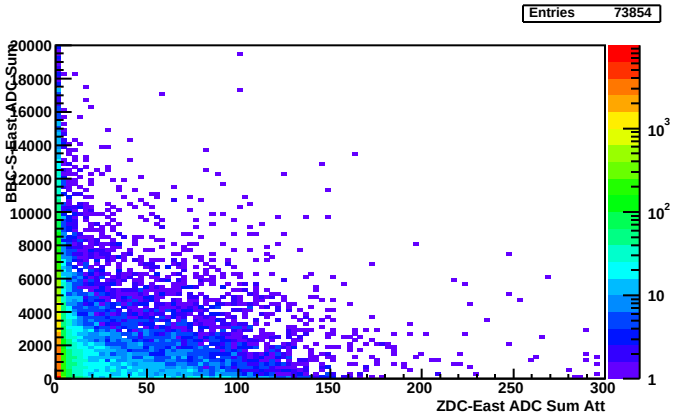

Entries 73854
TOF Mult
ZDC-West ADC Sum Att

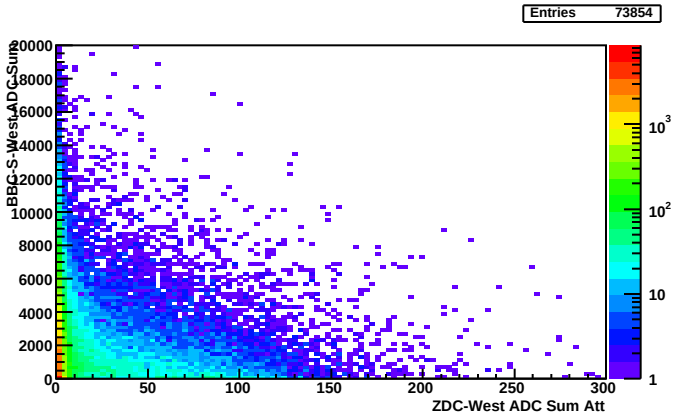

Entries 73854
BBC-L-East ADC Sum
BBC-S-East ADC Sum

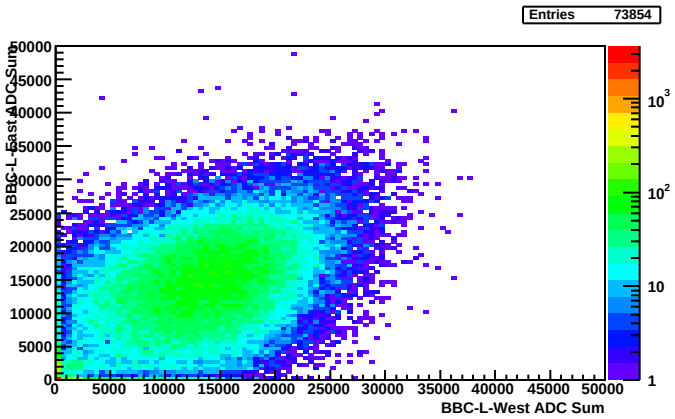

Entries 73854
BBC-L-West ADC Sum
BBC-S-West ADC Sum

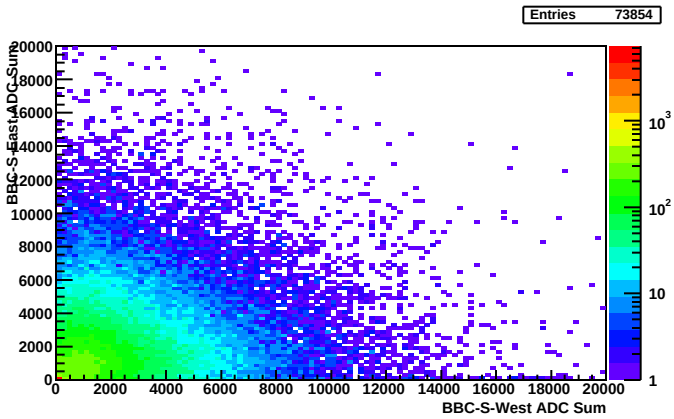

Entries 73854
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

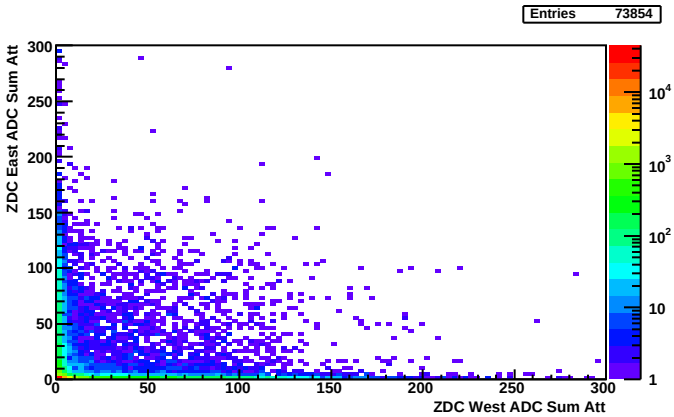

Entries 73854
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

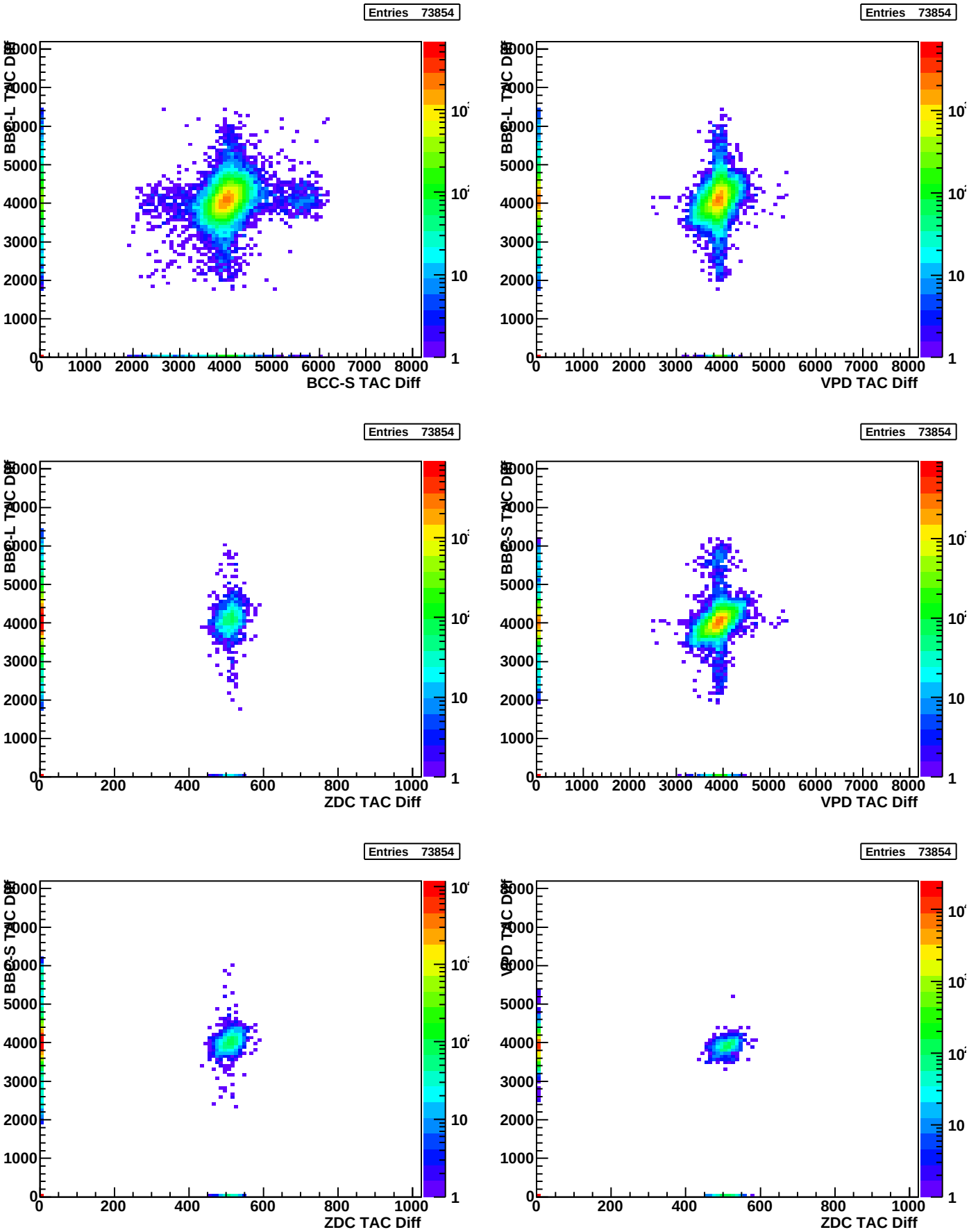

Entries 73854
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

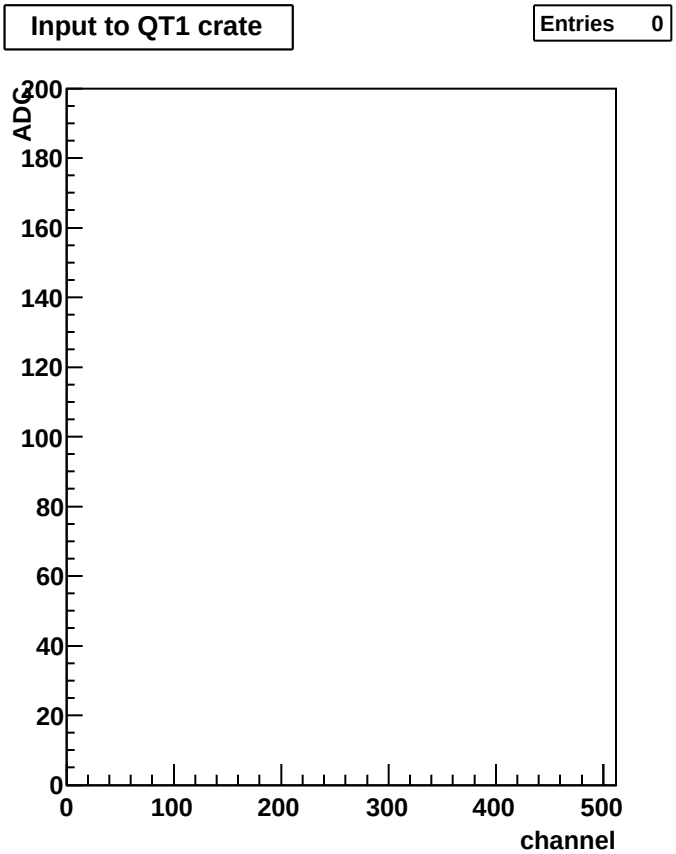

Entries 73854
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

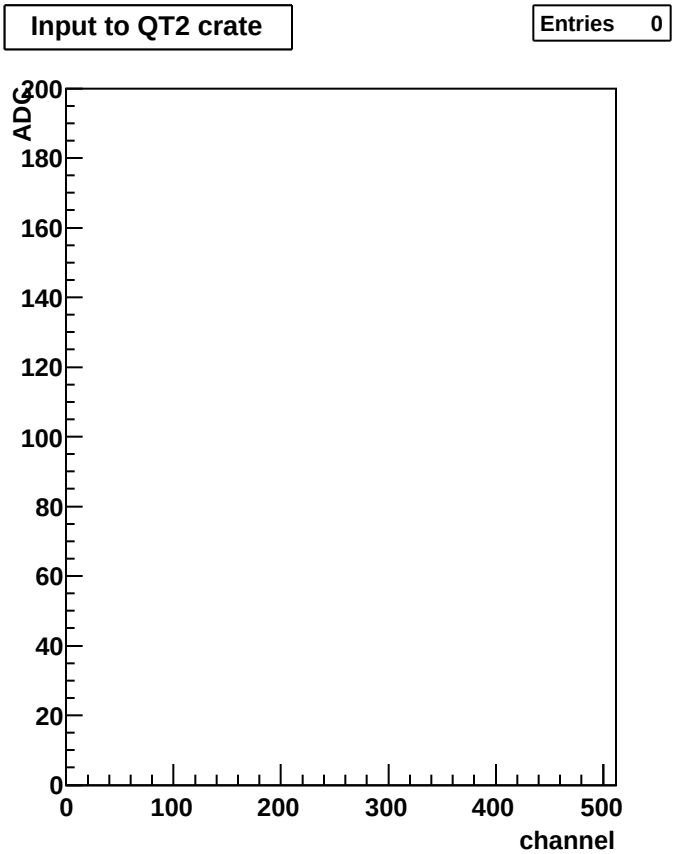

Entries 73854
BBC-L-East ADC Sum
BBC-L-West ADC Sum

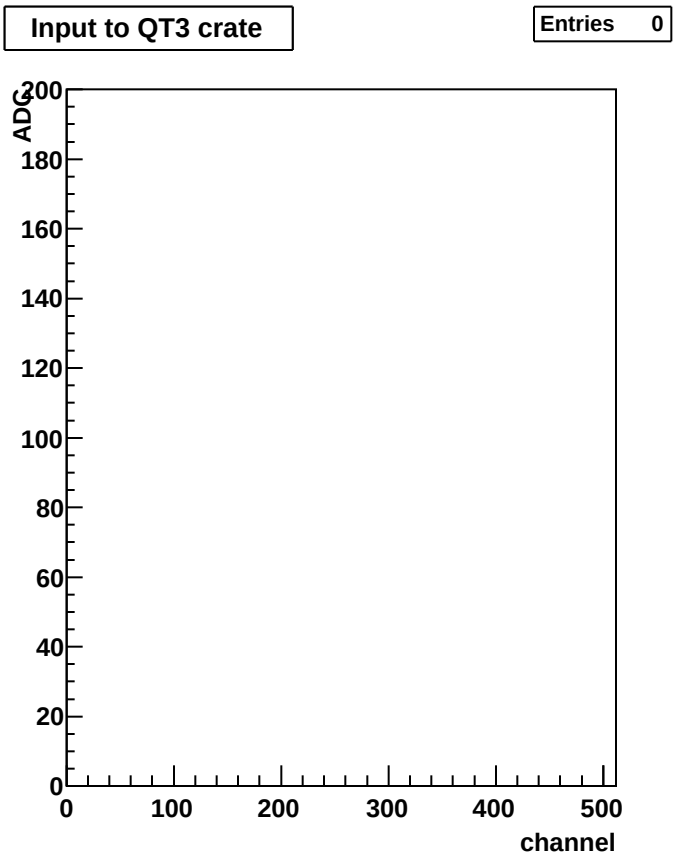

Entries 73854
BBC-S-East ADC Sum
BBC-S-West ADC Sum

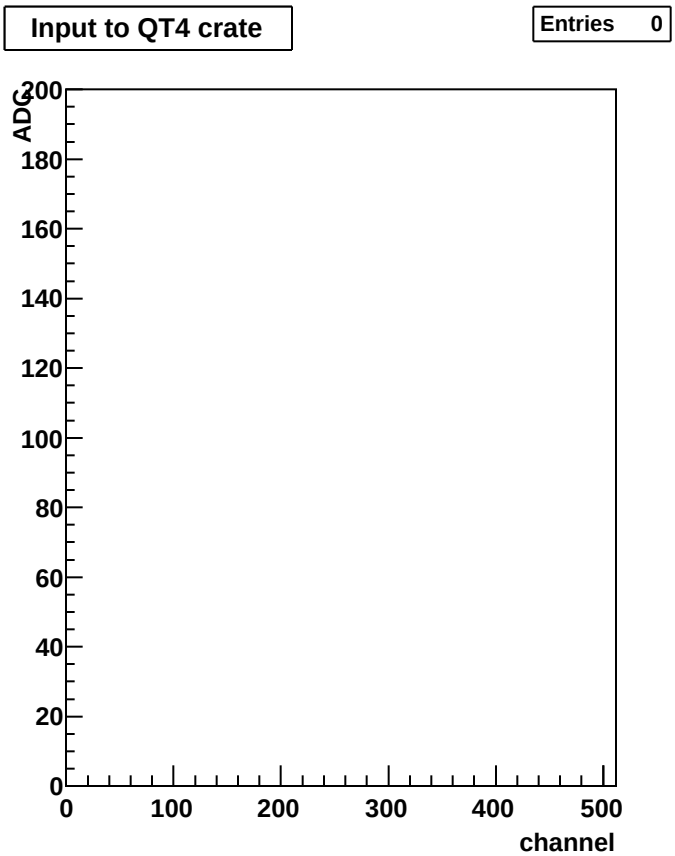

Entries 73854
ZDC East ADC Sum Att
ZDC West ADC Sum Att

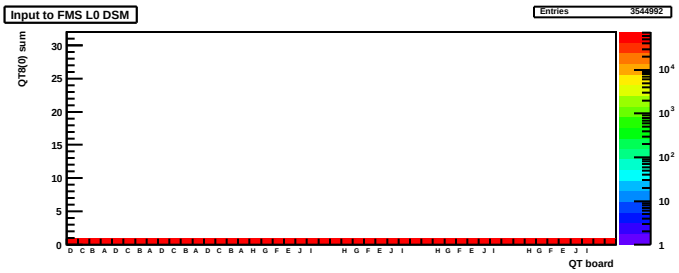

Entries 73854
BBC-L TAC Diff
BCC-S TAC Diff
Entries 73854
BBC-L TAC Diff
VPD TAC Diff
Entries 73854
BBC-L TAC Diff
ZDC TAC Diff
Entries 73854
BBC-S TAC Diff
VPD TAC Diff
Entries 73854
BBC-S TAC Diff
ZDC TAC Diff
Entries 73854
VPD TAC Diff
ZDC TAC Diff

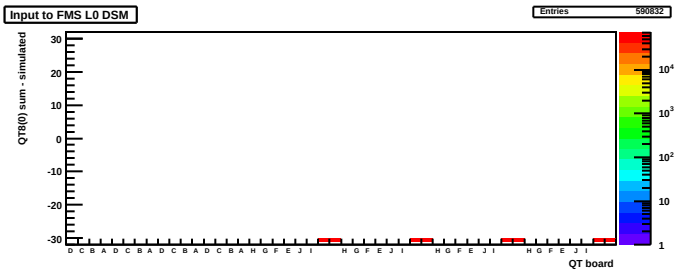

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

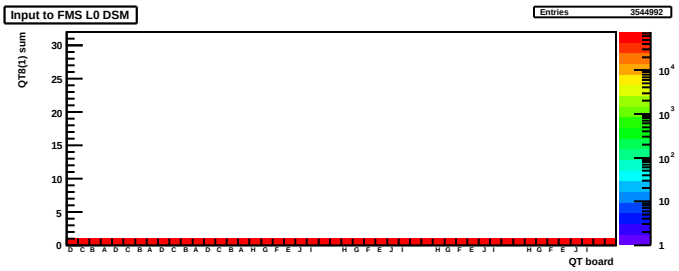

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

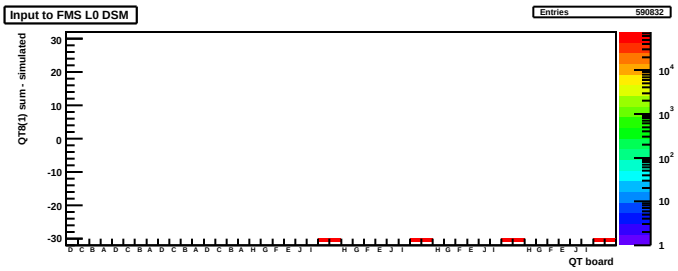

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

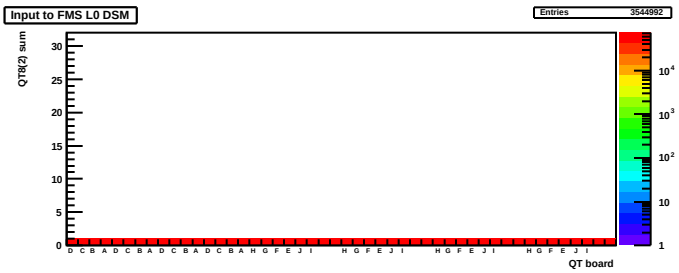

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

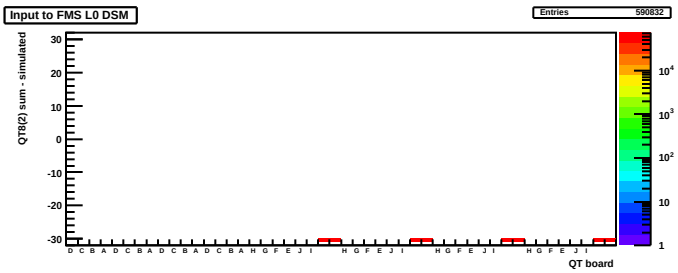

Input to FMS L0 DSM
Entries 3544992
QT8(0) sum
QT board

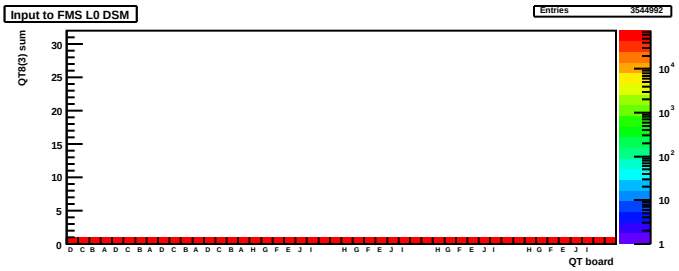

Input to FMS L0 DSM
Entries 590832
QT8(0) sum - simulated
QT board

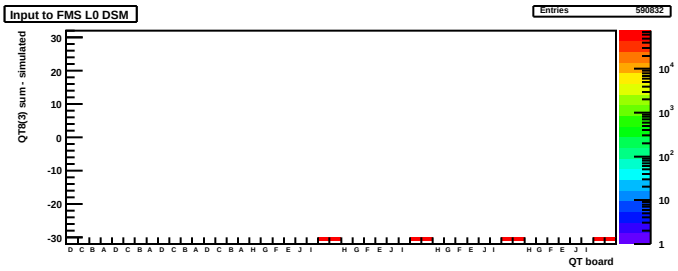

Input to FMS L0 DSM
Entries 3544992
QT8(1) sum
QT board

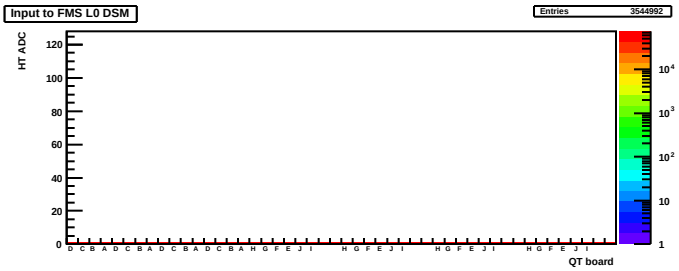

Input to FMS L0 DSM
Entries 590832
QT8(1) sum - simulated
QT board

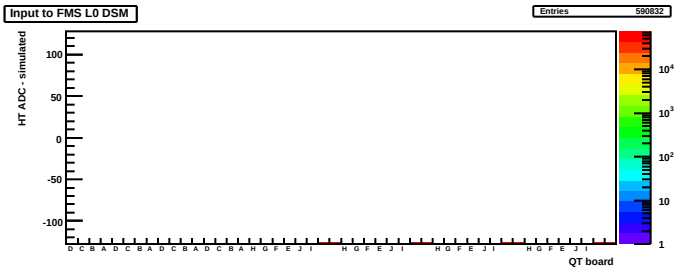

Input to FMS L0 DSM
Entries 3544992
QT8(2) sum
QT board

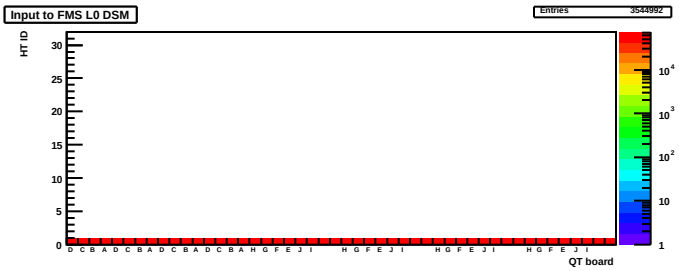

Input to FMS L0 DSM
Entries 590832
QT8(2) sum - simulated
QT board

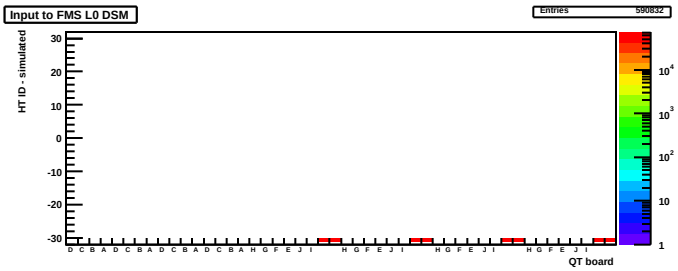

Input to FMS L0 DSM
Entries 3544992
QT8(3) sum
QT board

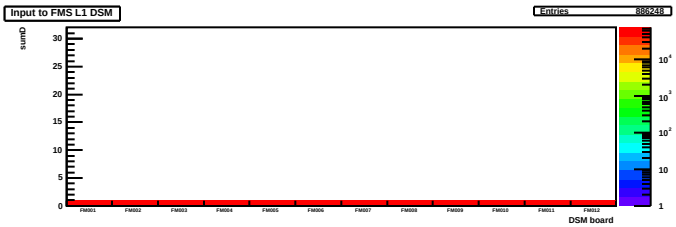

Input to FMS L0 DSM
Entries 590832
QT8(3) sum - simulated
QT board

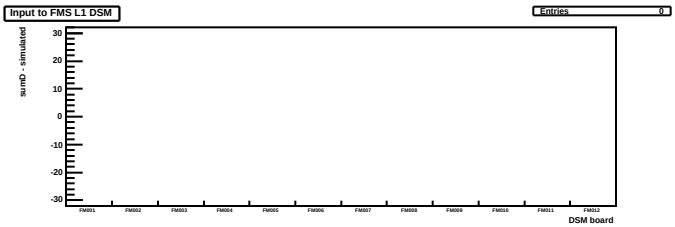

Input to FMS L0 DSM
Entries 3544992
HT ADC
QT board

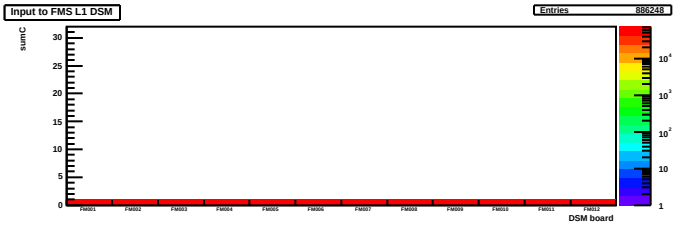

Input to FMS L0 DSM
Entries 590832
HT ADC - simulated
QT board

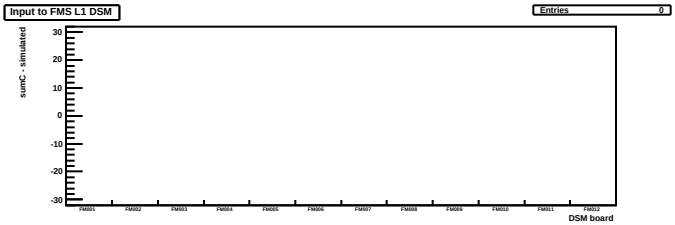

Input to FMS L0 DSM
Entries 3544992
HT ID
QT board

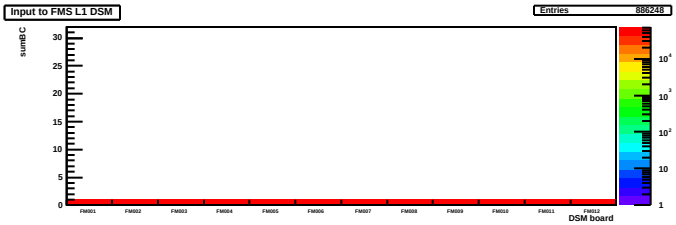

Input to FMS L0 DSM
Entries 590832
HT ID - simulated
QT board

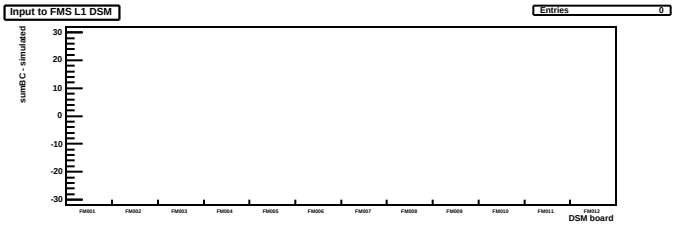

Input to FMS L1 DSM
Entries 886248
sumD
DSM board

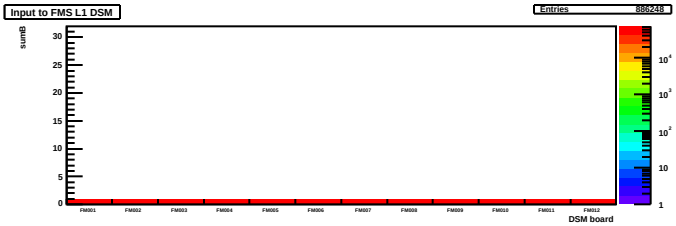

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

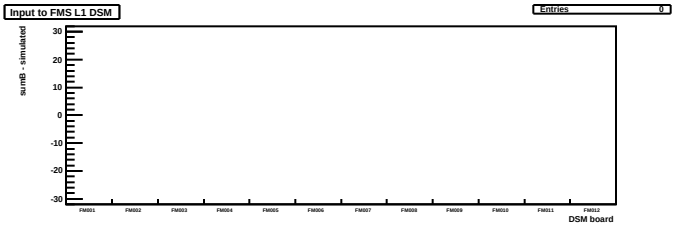

Input to FMS L1 DSM
Entries 886248
sumC
DSM board

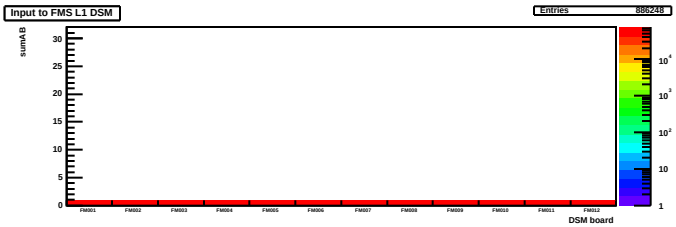

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

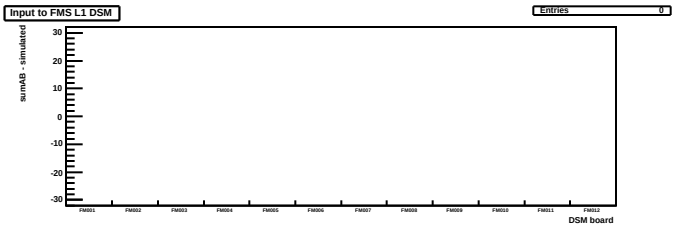

Input to FMS L1 DSM
Entries 886248
sumBC
DSM board

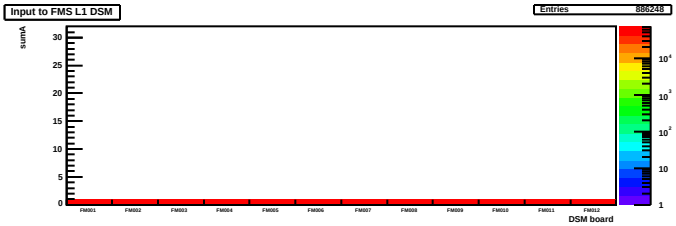

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

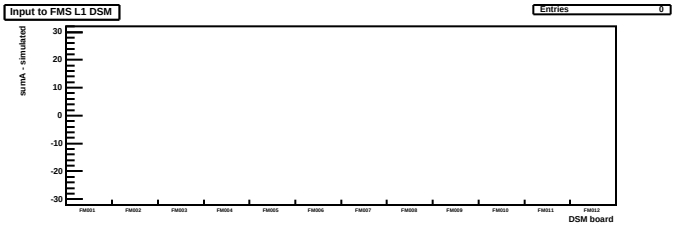

Input to FMS L1 DSM
Entries 886248
sumB
DSM board

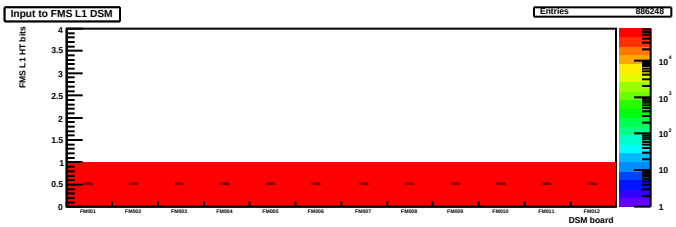

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

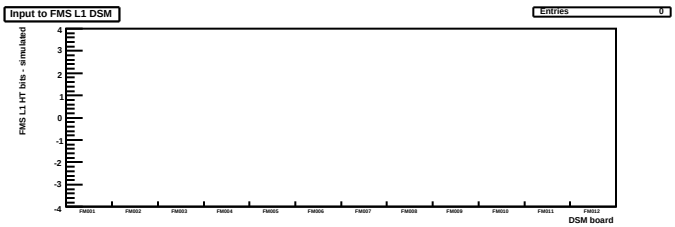

Input to FMS L1 DSM
Entries 886248
sumAB
DSM board

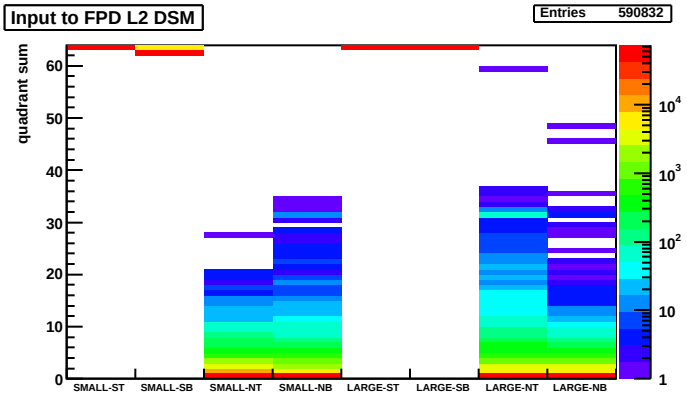

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

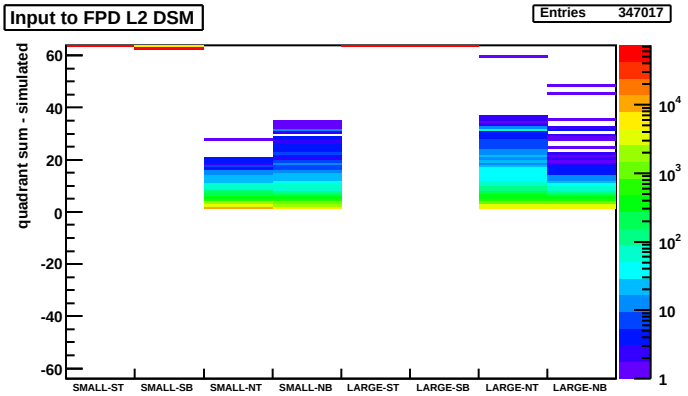

Input to FMS L1 DSM
Entries 886248
sumA
DSM board

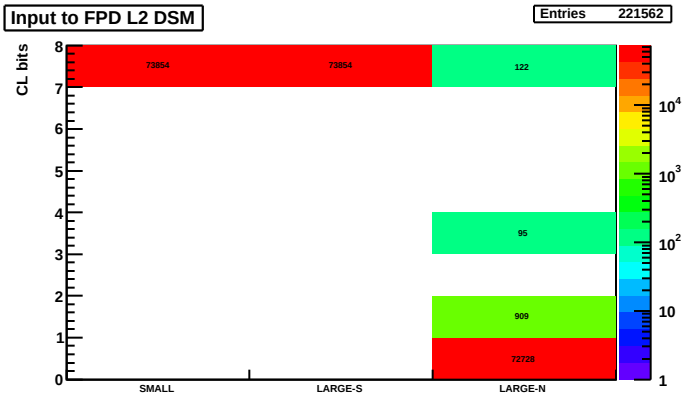

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

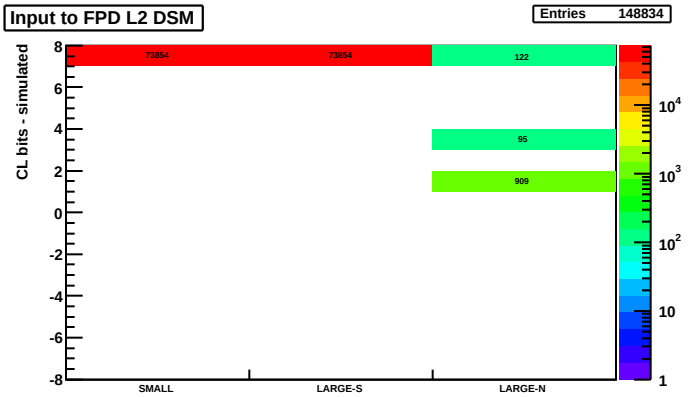

Input to FMS L1 DSM
Entries 886248
FMS L1 HT bits
DSM board

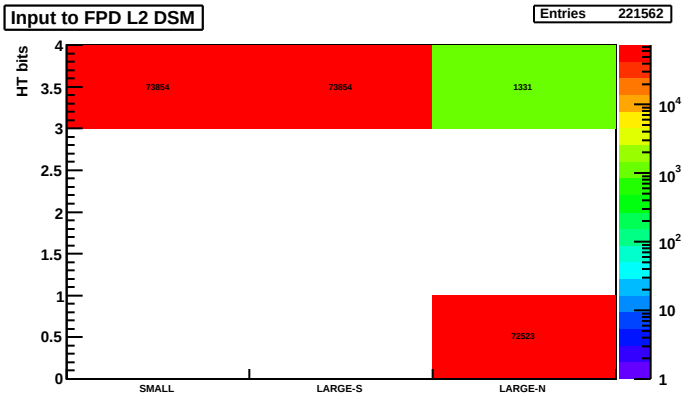

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

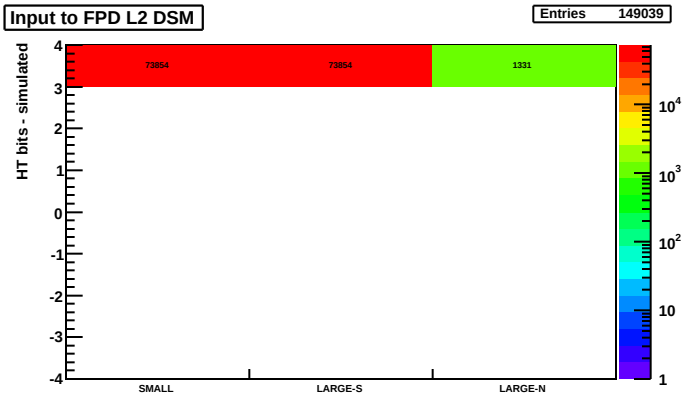

Input to FPD L2 DSM
Entries 590832
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

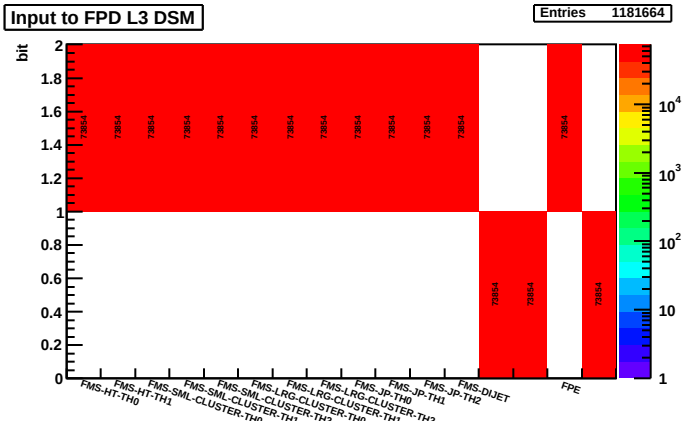

Input to FPD L2 DSM
Entries 347017
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

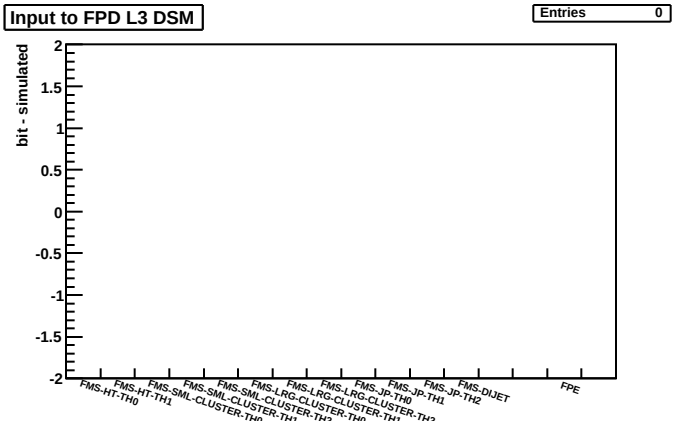

Input to FPD L2 DSM
Entries 221562
CL bits
73854
73854
122
95
909
72728
SMALL
LARGE-S
LARGE-N

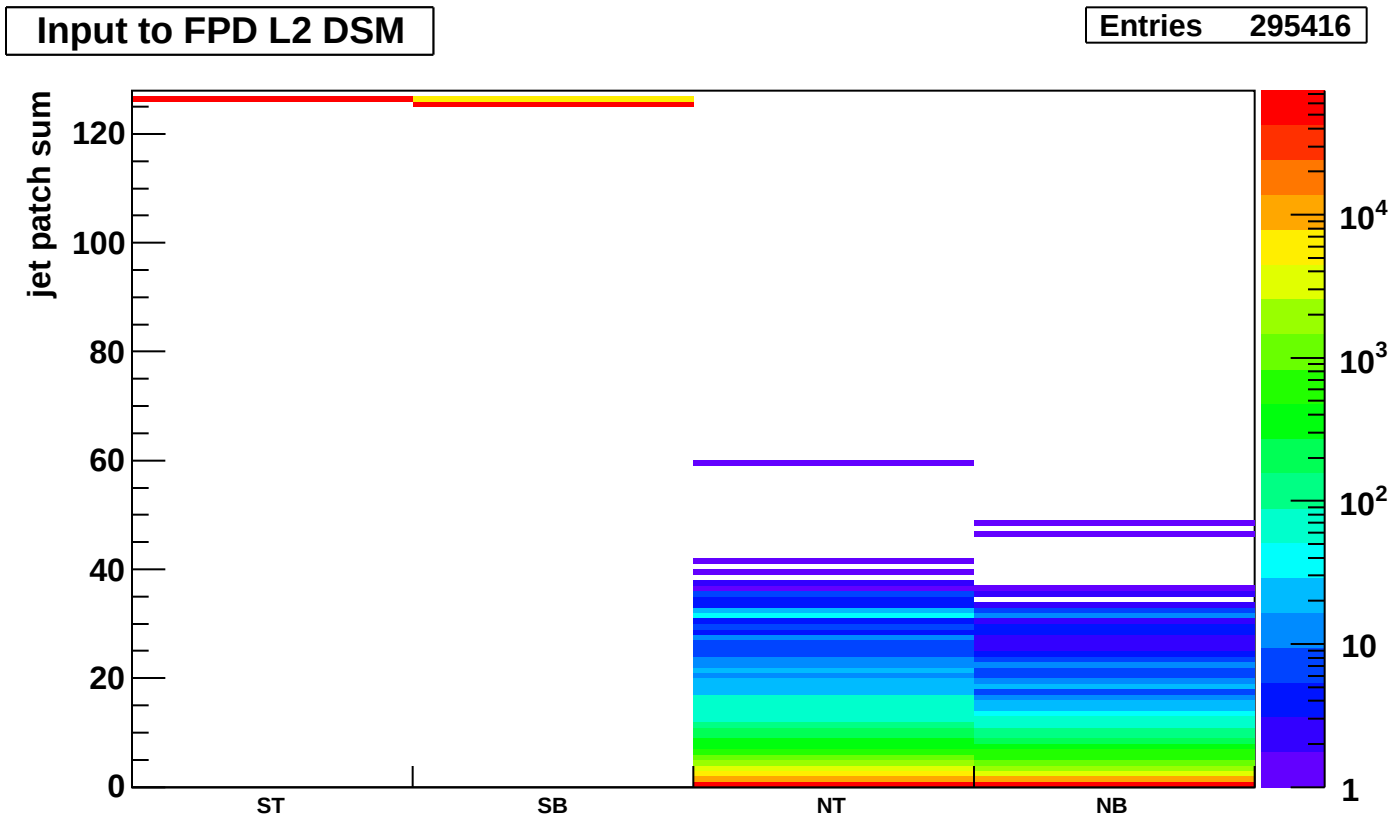

Input to FPD L2 DSM
Entries 148834
CL bits - simulated
73854
73854
122
95
909
SMALL
LARGE-S
LARGE-N

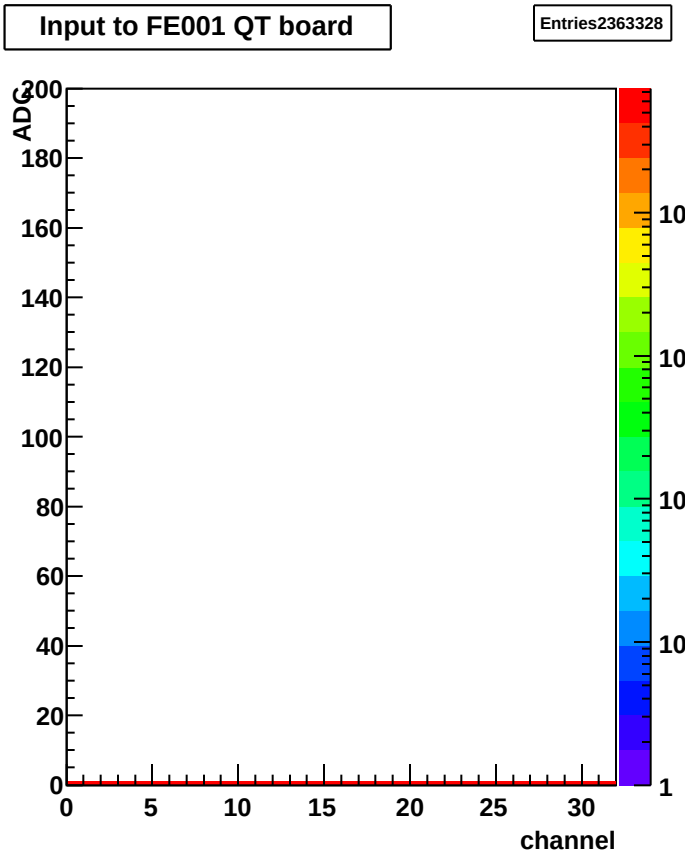

Input to FPD L2 DSM
Entries 221562
HT bits
73854
73854
1331
72523
SMALL
LARGE-S
LARGE-N

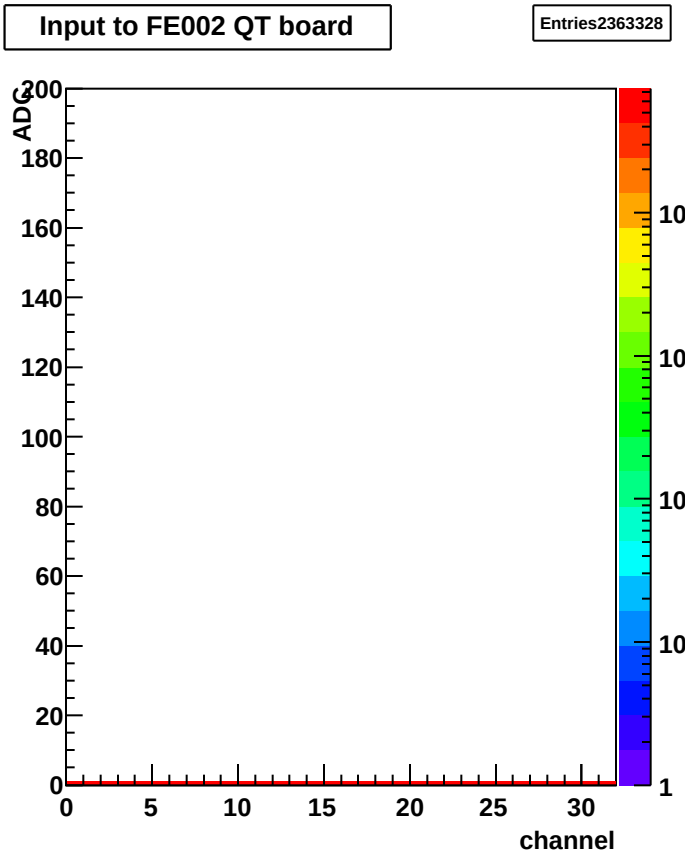

Input to FPD L2 DSM
Entries 149039
HT bits - simulated
73854
73854
1331
SMALL
LARGE-S
LARGE-N

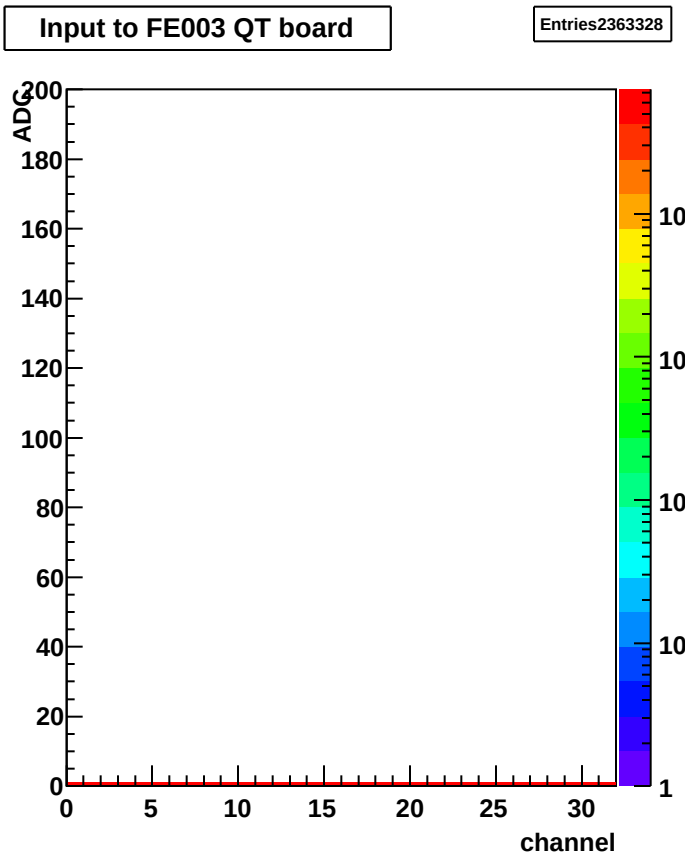

Input to FPD L3 DSM
Entries 1181664
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

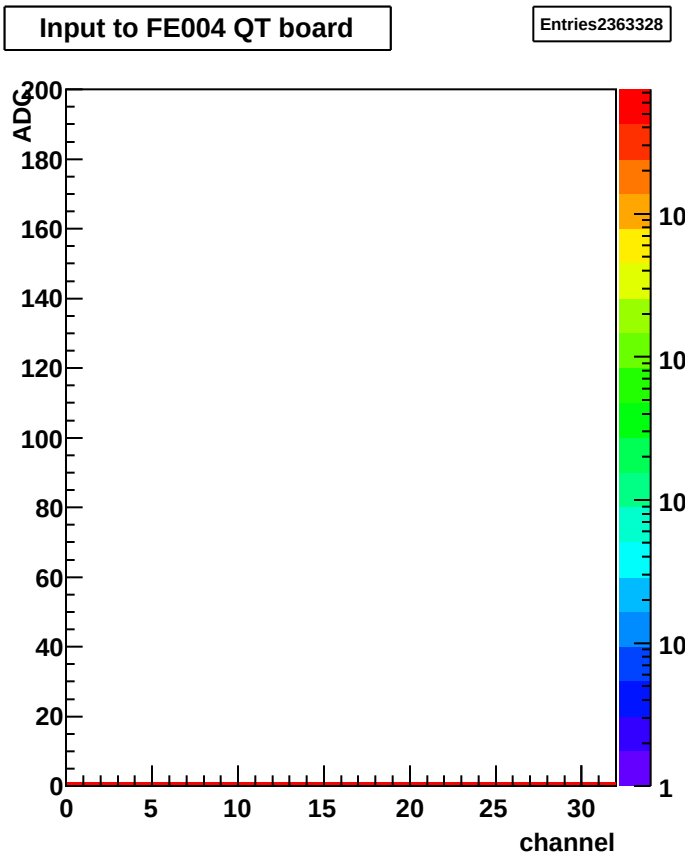

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

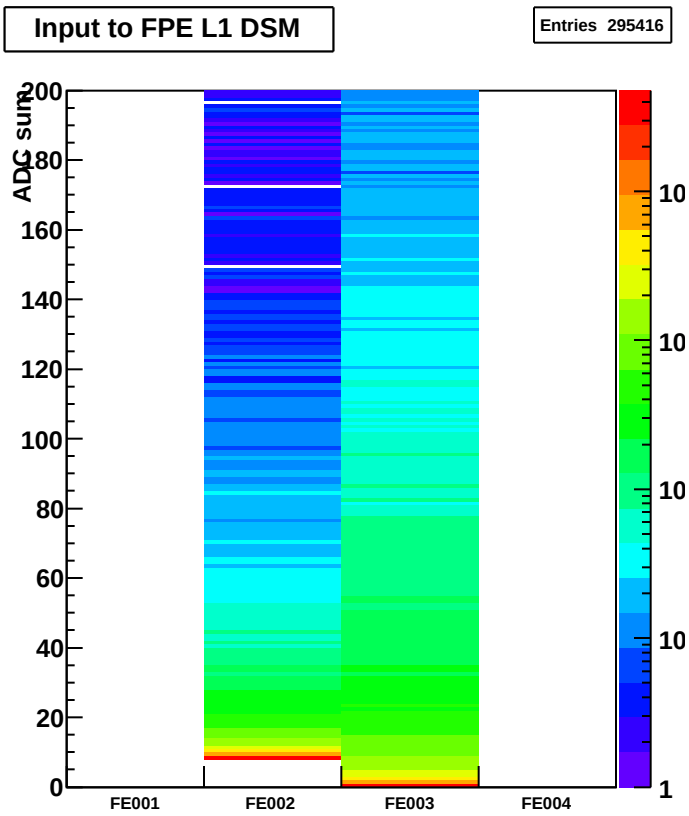

Input to FPD L2 DSM
Entries 295416
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

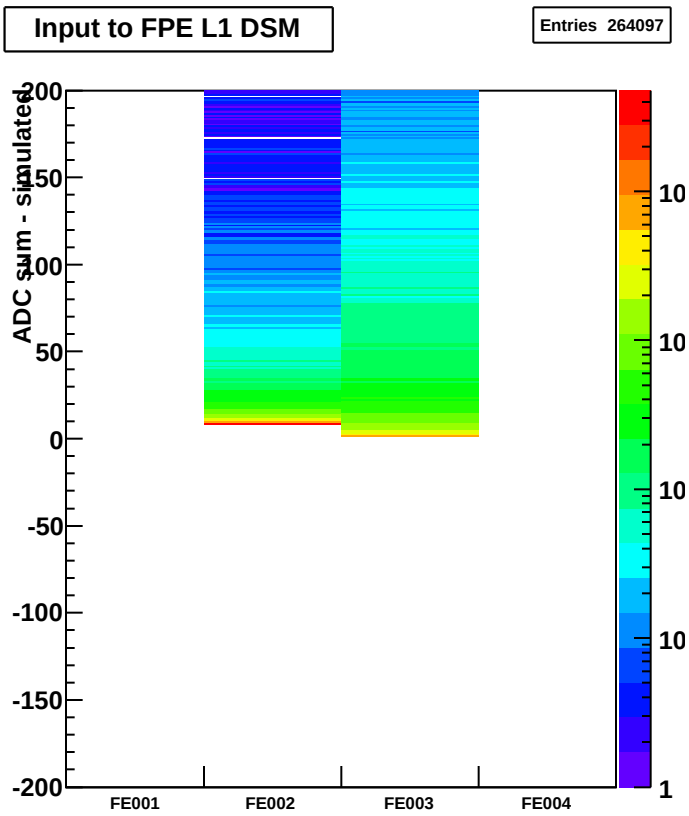

Input to FE001 QT board
Entries2363328
ADC
channel

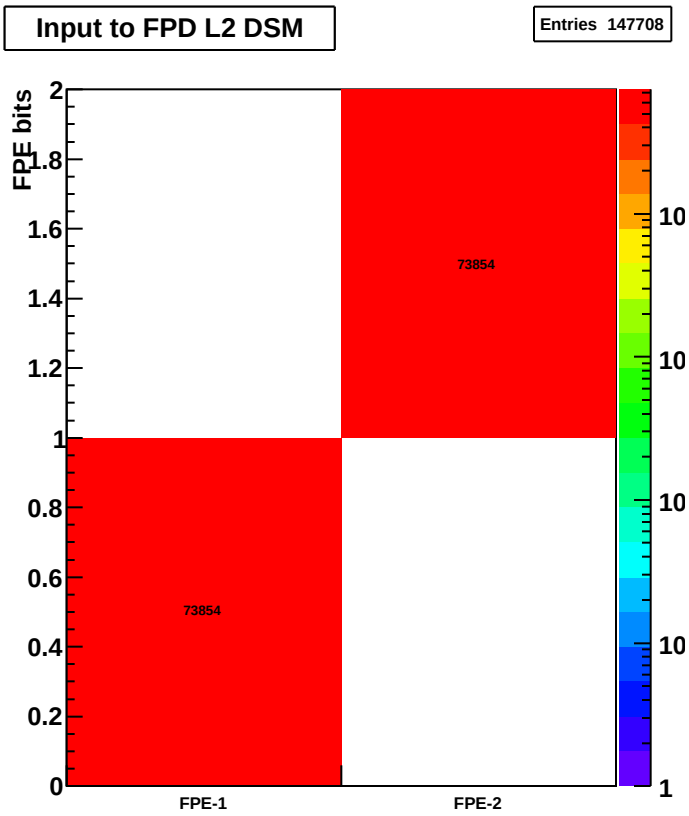

Input to FE002 QT board
Entries2363328
ADC
channel

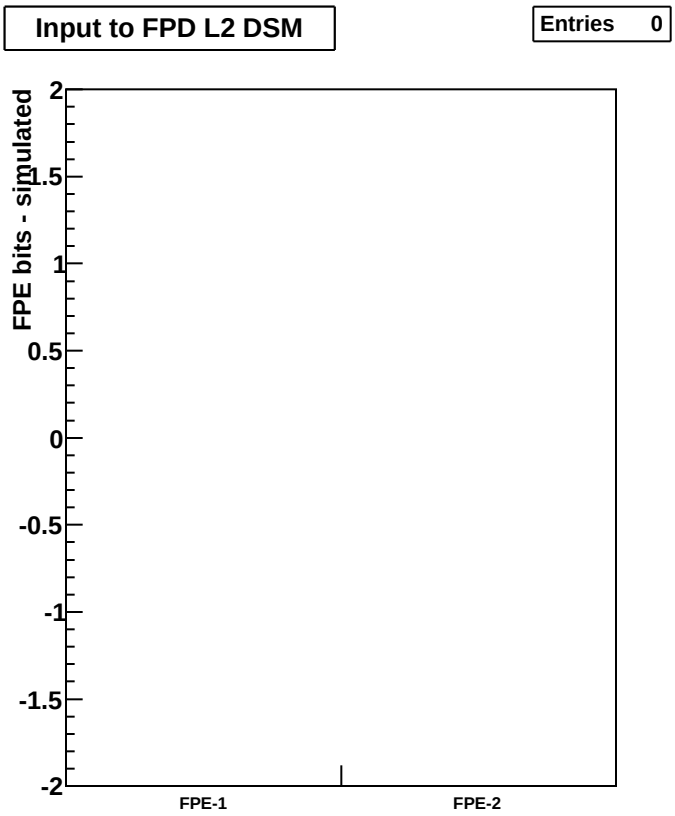

Input to FE003 QT board
Entries2363328
ADC
channel


Input to FE004 QT board
Entries2363328
ADC
channel


Input to FPE L1 DSM
Entries 295416
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 264097
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 147708
FPE bits
73854
73854
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2