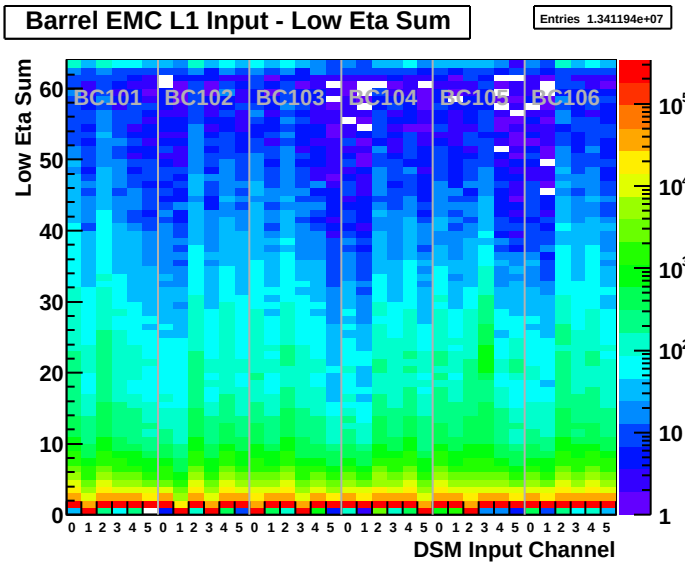
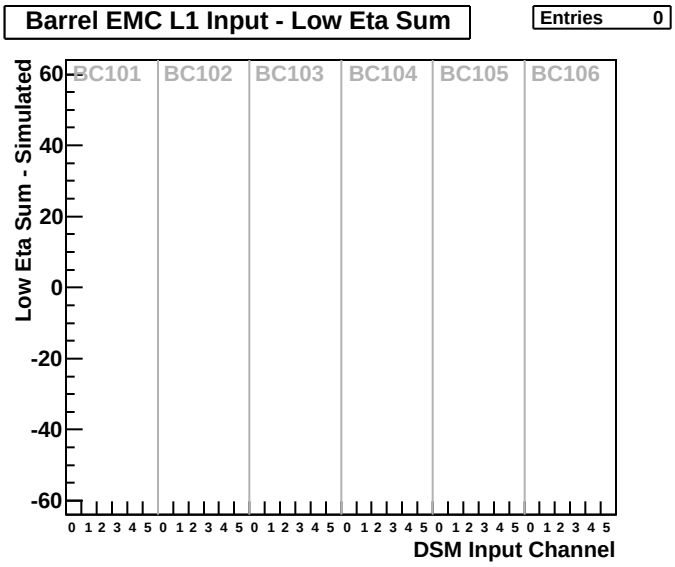
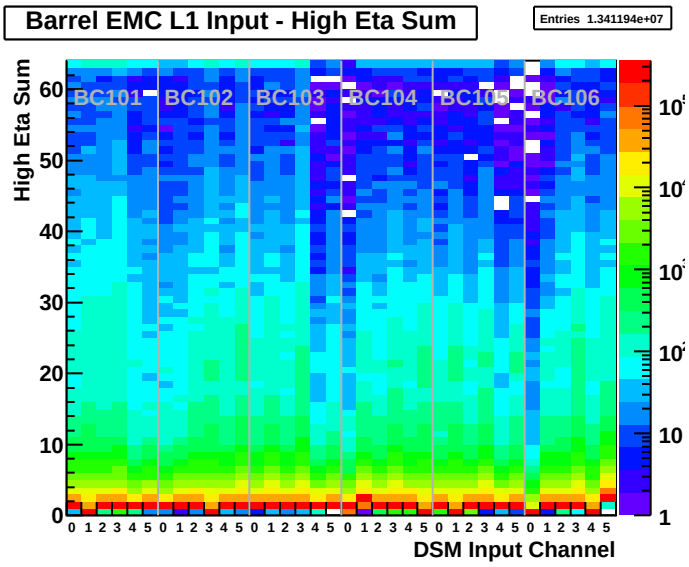
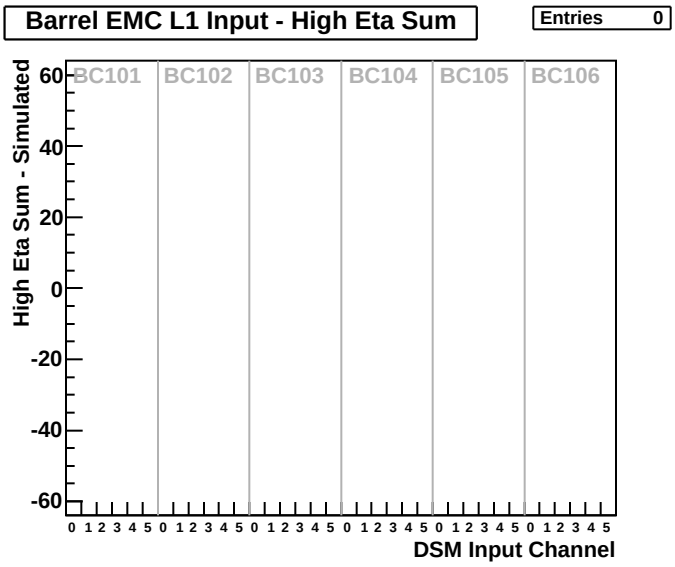
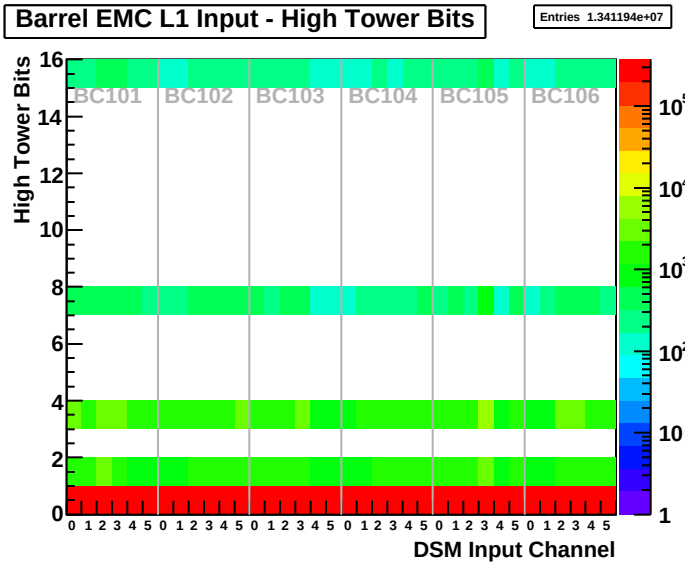
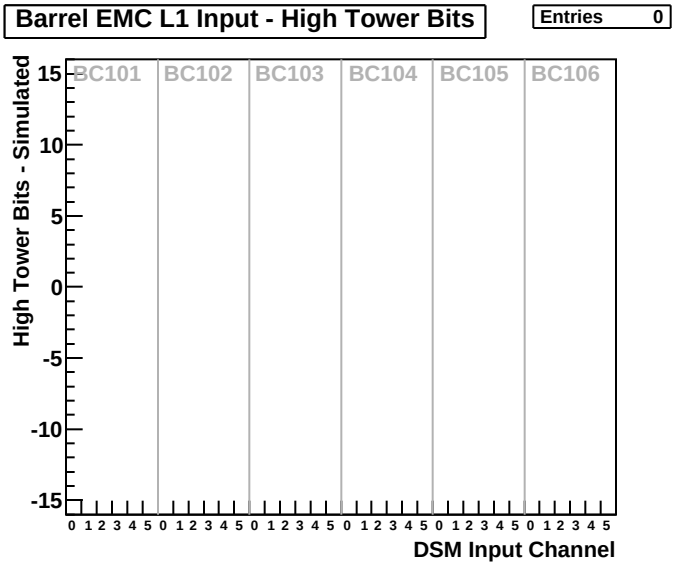

Barrel EMC L1 Input - Low Eta Sum
Entries 1.341194e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

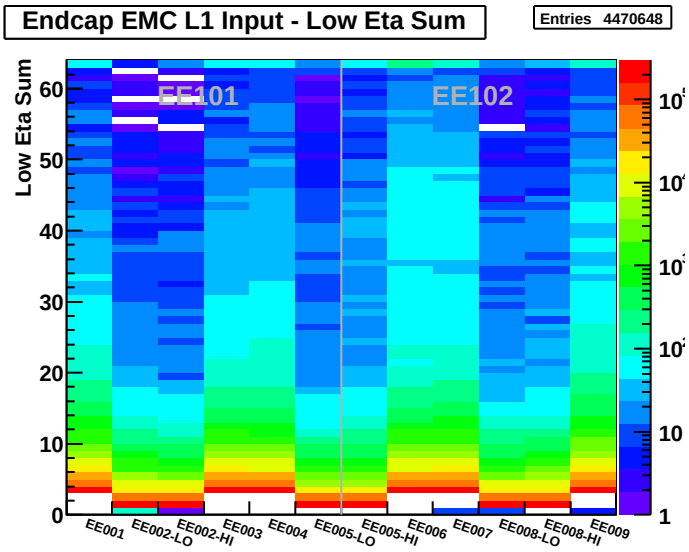

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

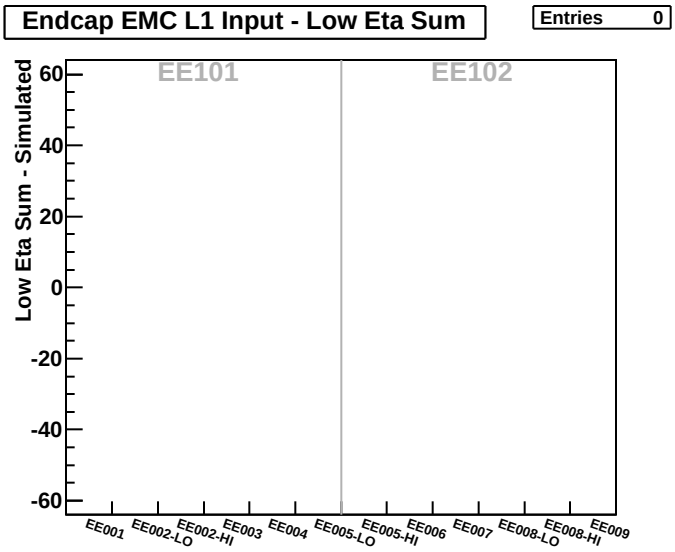

Barrel EMC L1 Input - High Eta Sum
Entries 1.341194e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

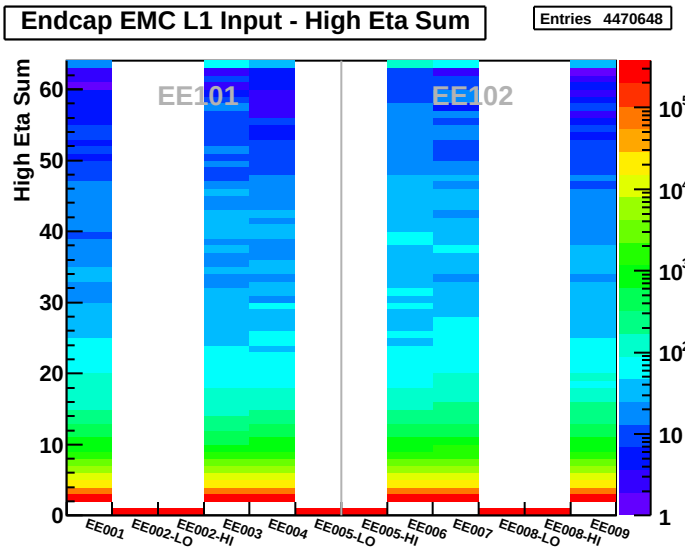

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

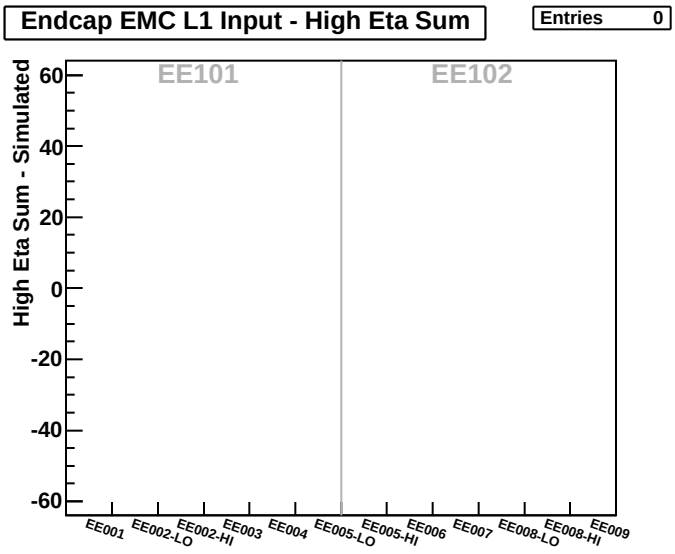

Barrel EMC L1 Input - High Tower Bits
Entries 1.341194e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

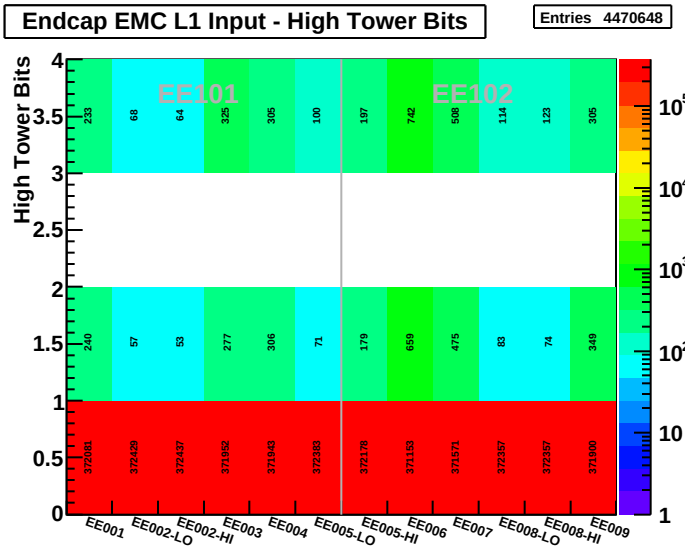

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

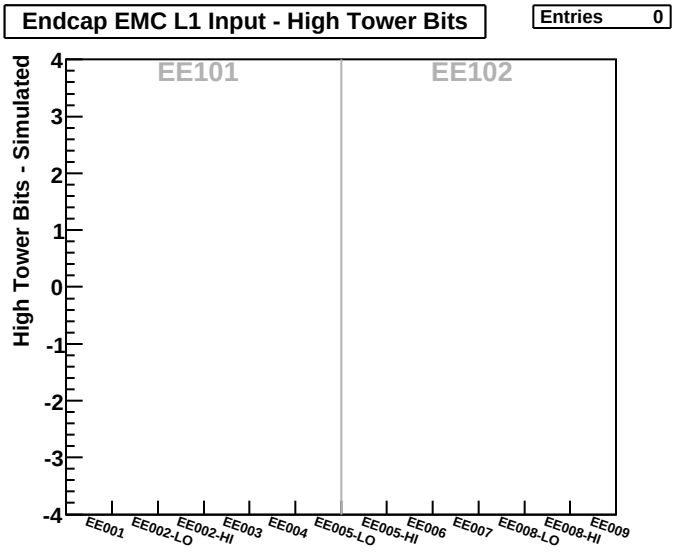

Endcap EMC L1 Input - Low Eta Sum
Entries 4470648
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

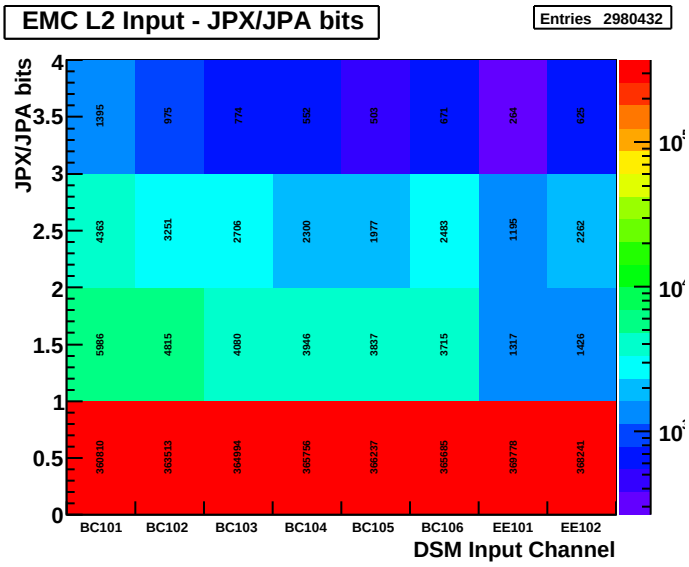

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

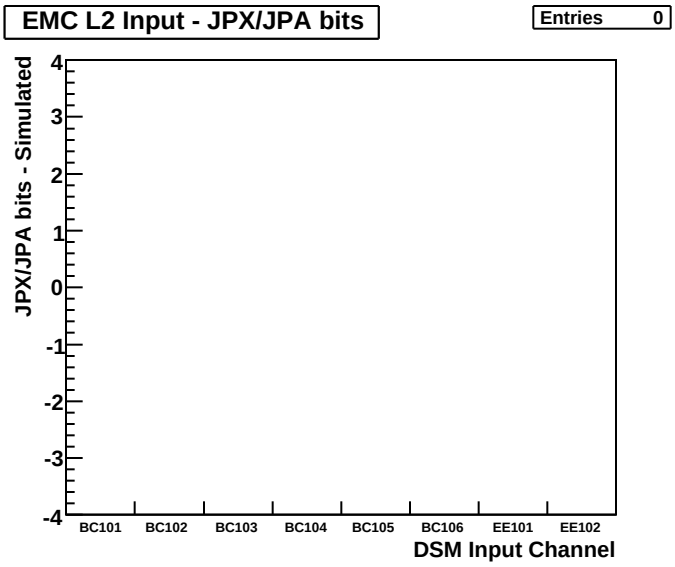

Endcap EMC L1 Input - High Eta Sum
Entries 4470648
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

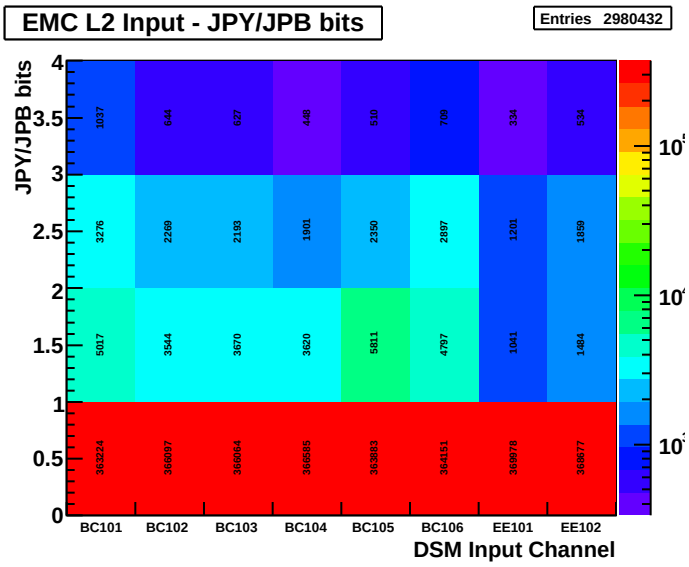

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

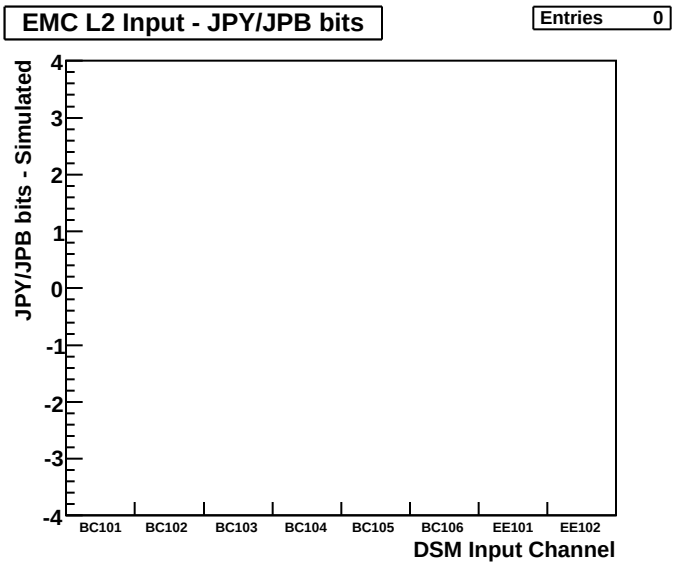

Endcap EMC L1 Input - High Tower Bits
Entries 4470648
High Tower Bits
EE101
EE102
233 68 64 325 305 100 197 742 508 114 123 305
240 57 53 277 306 71 179 659 475 83 74 349
372081 372429 372437 371952 371943 372383 372178 371153 371571 372357 372357 371900
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

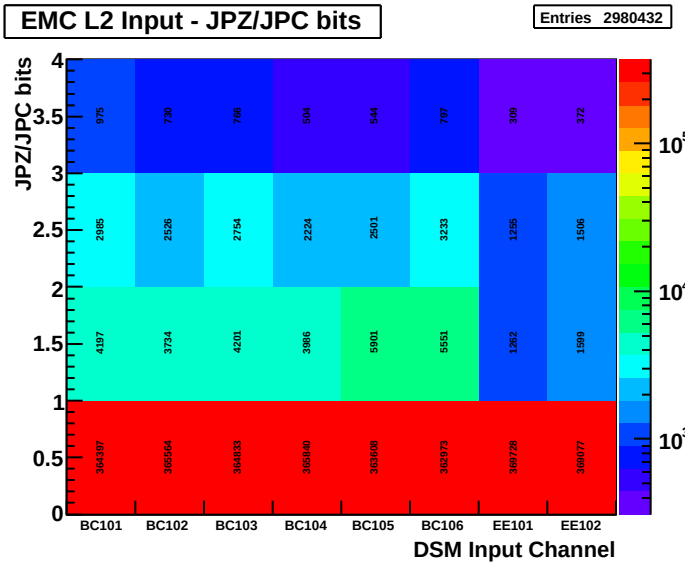

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

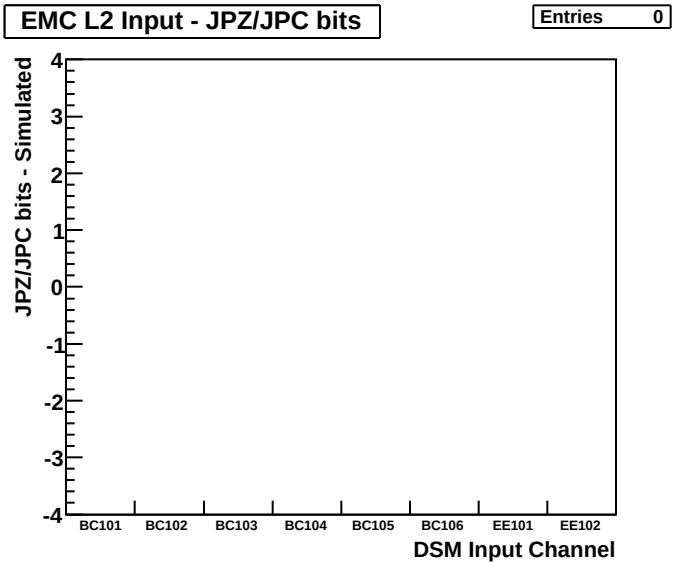

EMC L2 Input - JPX/JPA bits
Entries 2980432
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1395 975 774 552 503 671 264 625
4363 3251 2706 2300 1977 2483 1195 2262
5986 4815 4080 3946 3837 3715 1317 1426
360610 363513 364934 365756 366237 365685 369778 368241

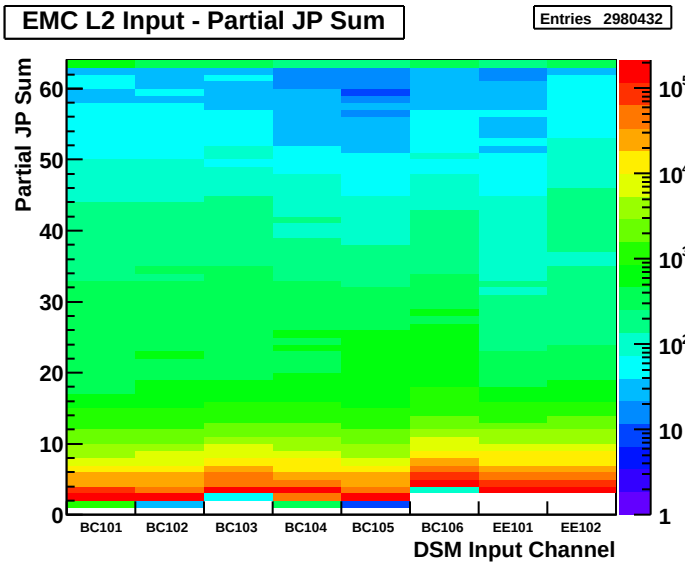

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

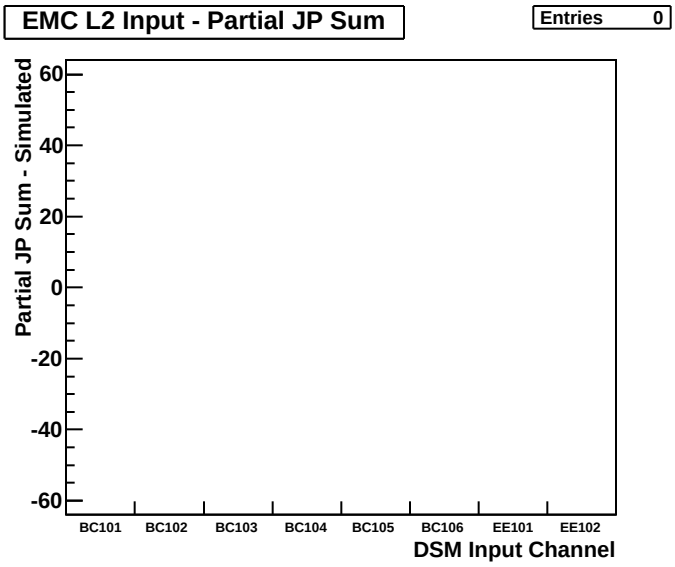

EMC L2 Input - JPY/JPB bits
Entries 2980432
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1037 644 627 448 510 709 334 534
3276 2269 2193 1901 2350 2897 1201 1859
5017 3544 3670 3620 5811 4797 1041 1484
363224 366097 366064 366585 363983 364151 368978 368677

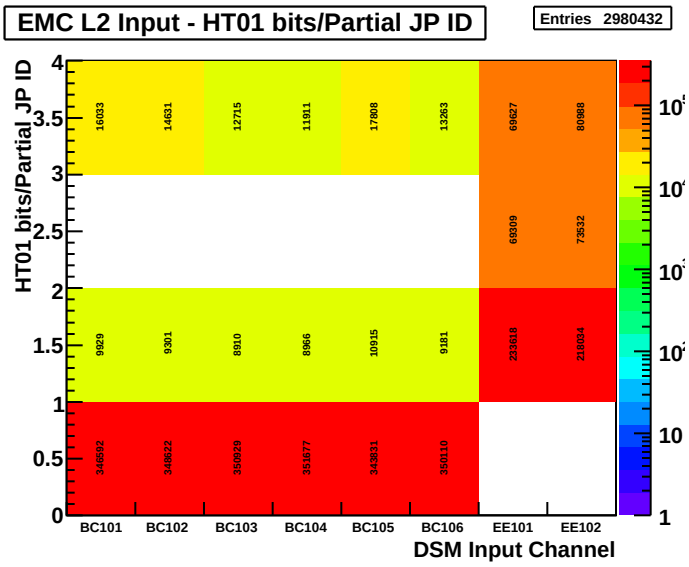

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

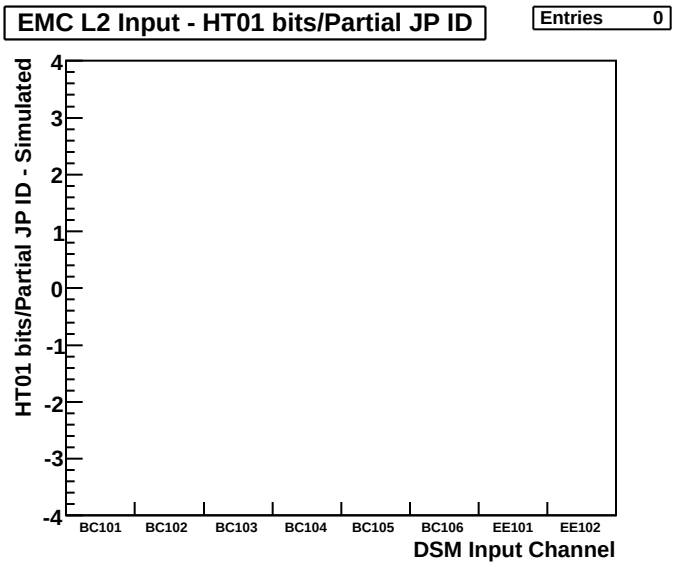

EMC L2 Input - JPZ/JPC bits
Entries 2980432
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
975 730 766 504 544 797 309 372
2985 2526 2754 2224 2501 3233 1255 1506
4197 3734 4201 3986 5901 5551 1262 1599
364397 365564 364833 365340 363508 362973 369728 369077

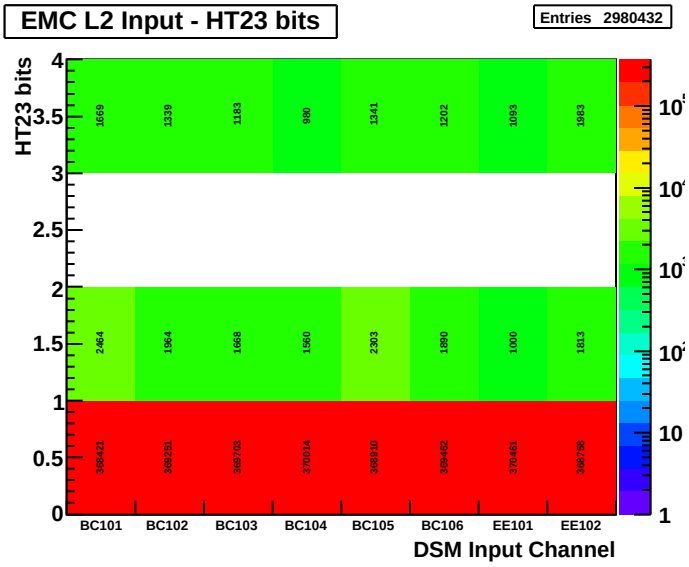

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

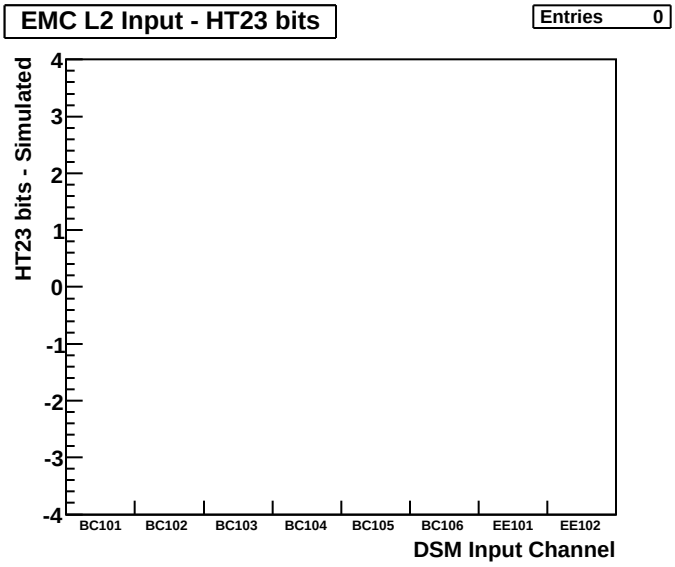

EMC L2 Input - Partial JP Sum
Entries 2980432
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

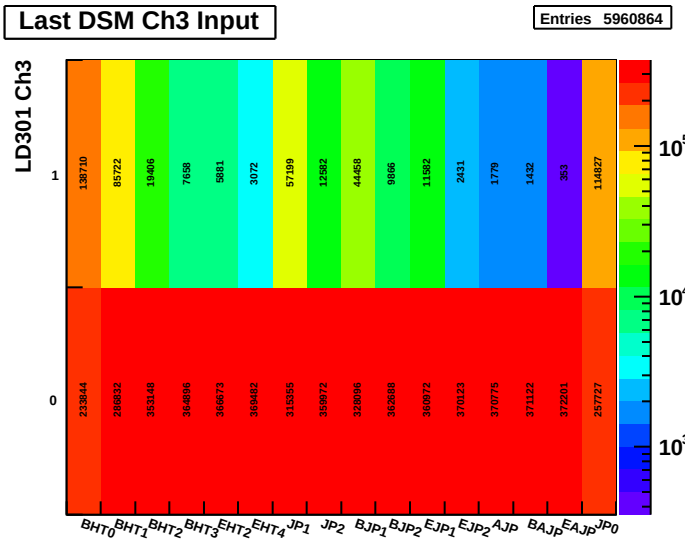

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

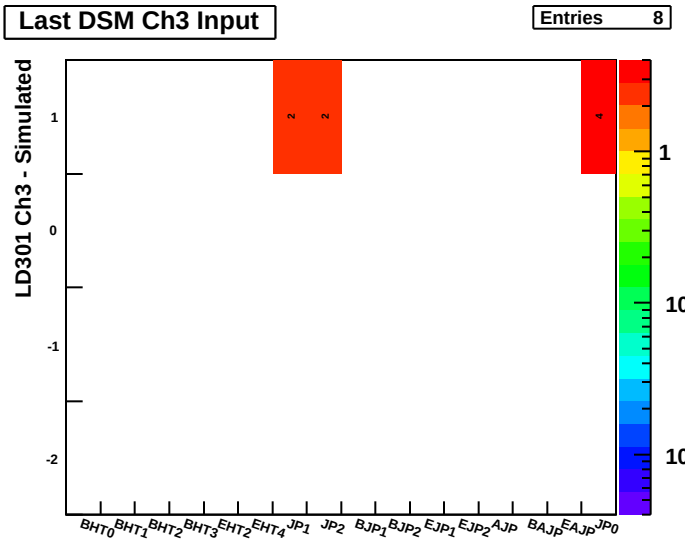

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2980432
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

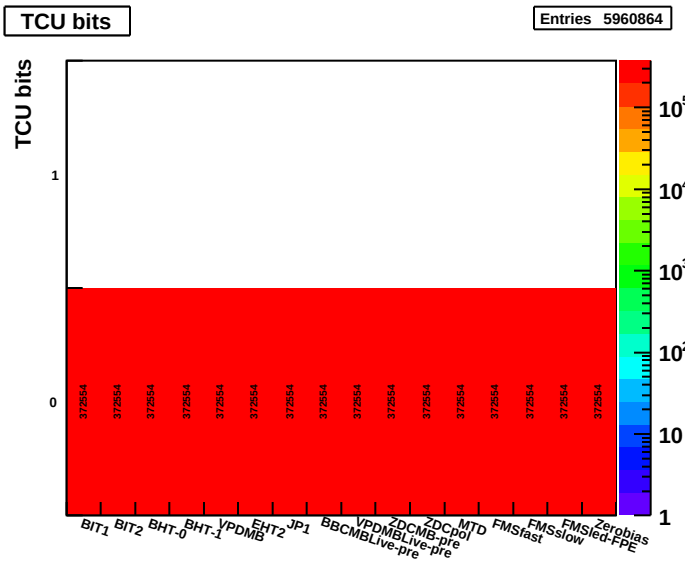

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

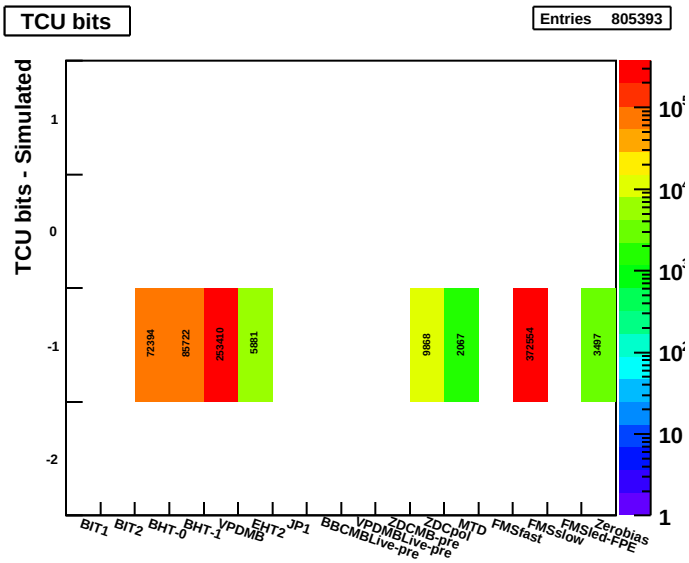

EMC L2 Input - HT23 bits
Entries 2980432
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

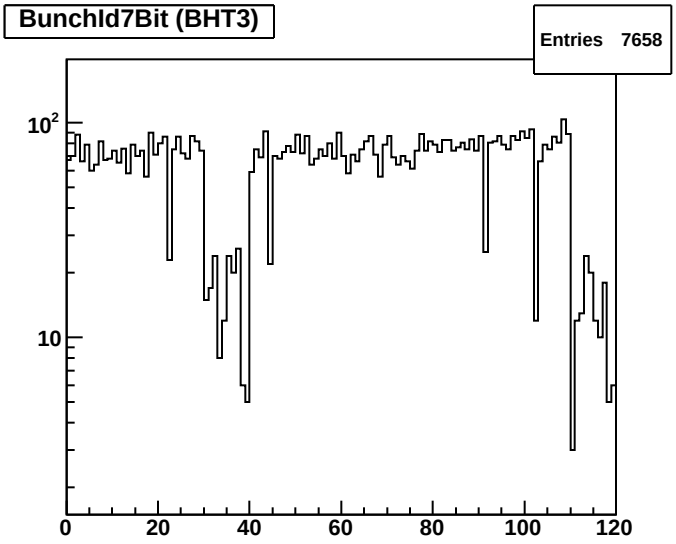

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

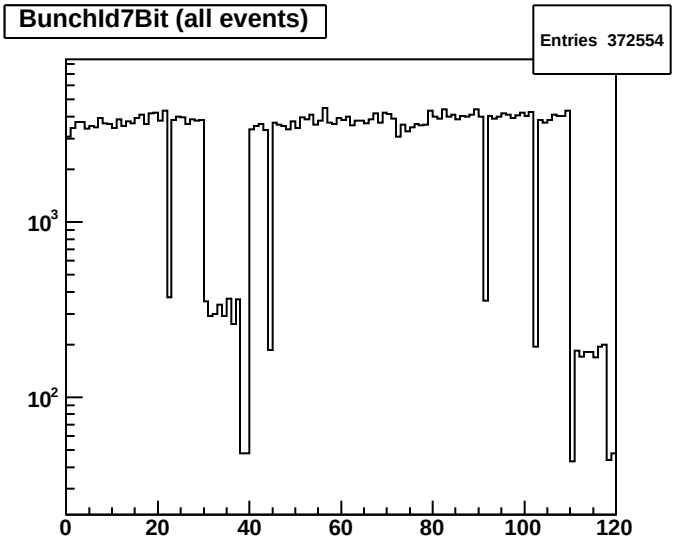

Last DSM Ch3 Input
Entries 5960864
LD301 Ch3
138710 85722 19406 7658 5881 3072 57199 12582 44458 9866 11582 2431 1779 1432 353 114827
233044 286832 353148 364896 366673 369462 315355 359972 328096 362688 360972 370123 370775 371122 372201 257727
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

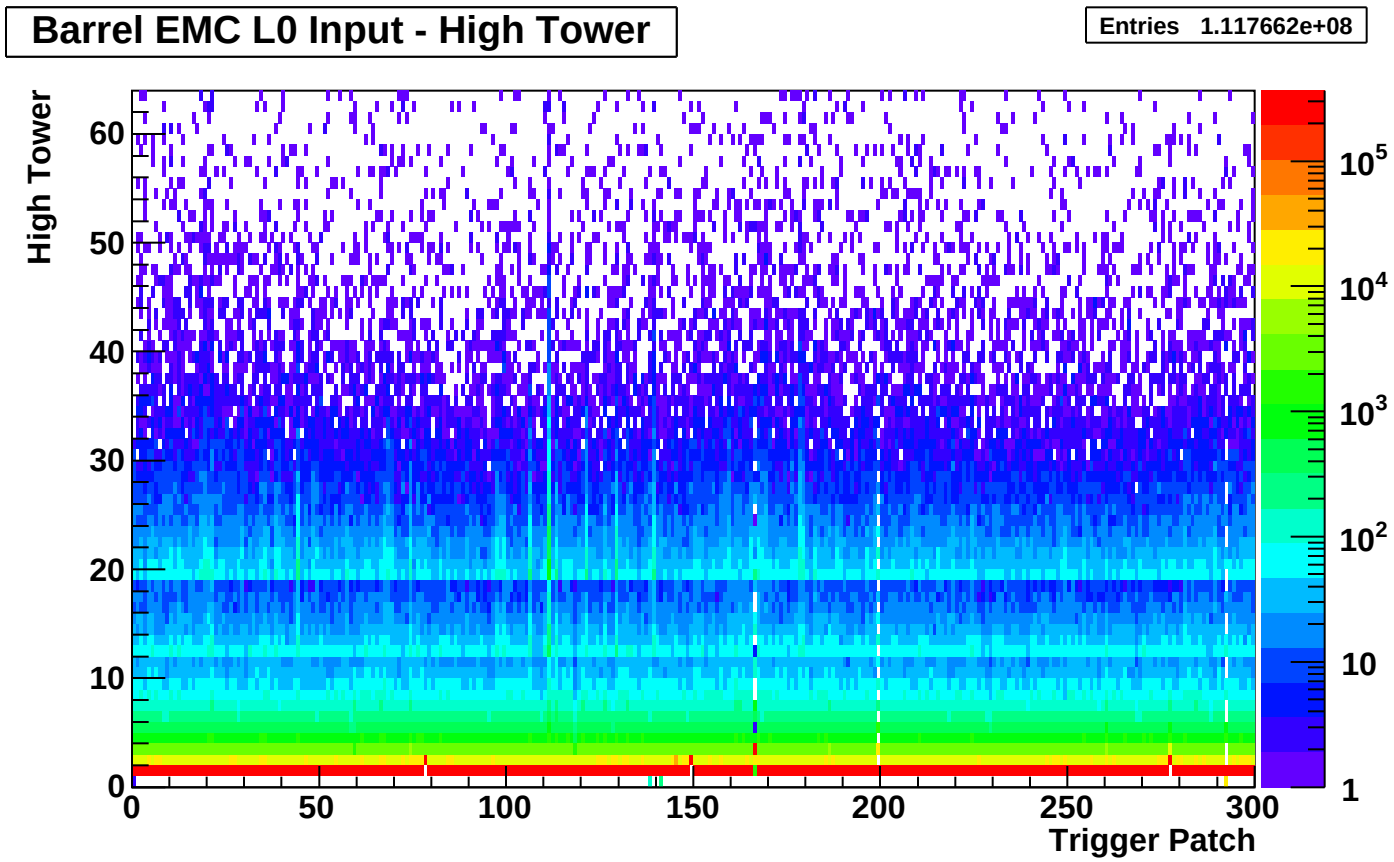

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
2 2 4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

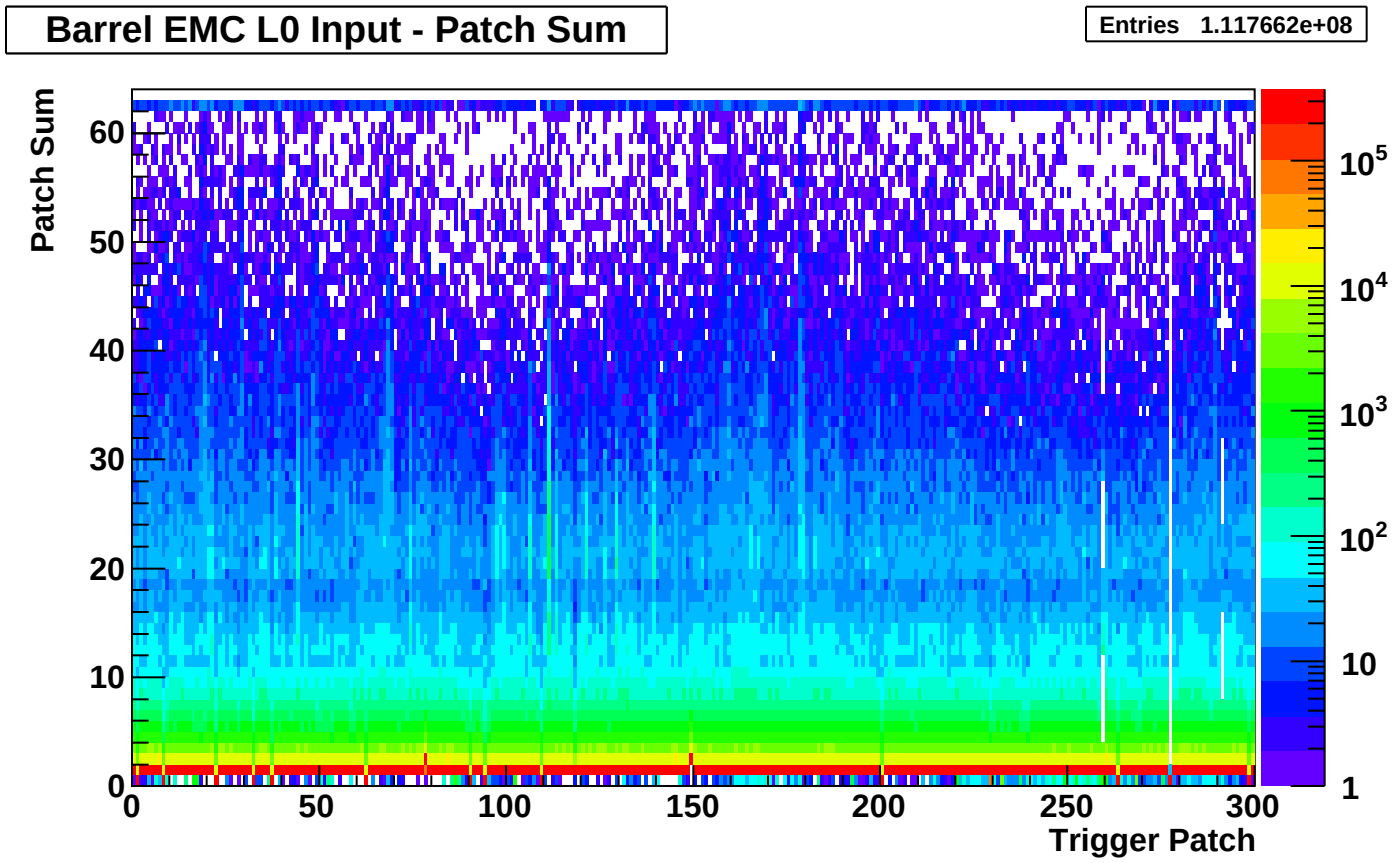

TCU bits
Entries 5960864
TCU bits
372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554 372554
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

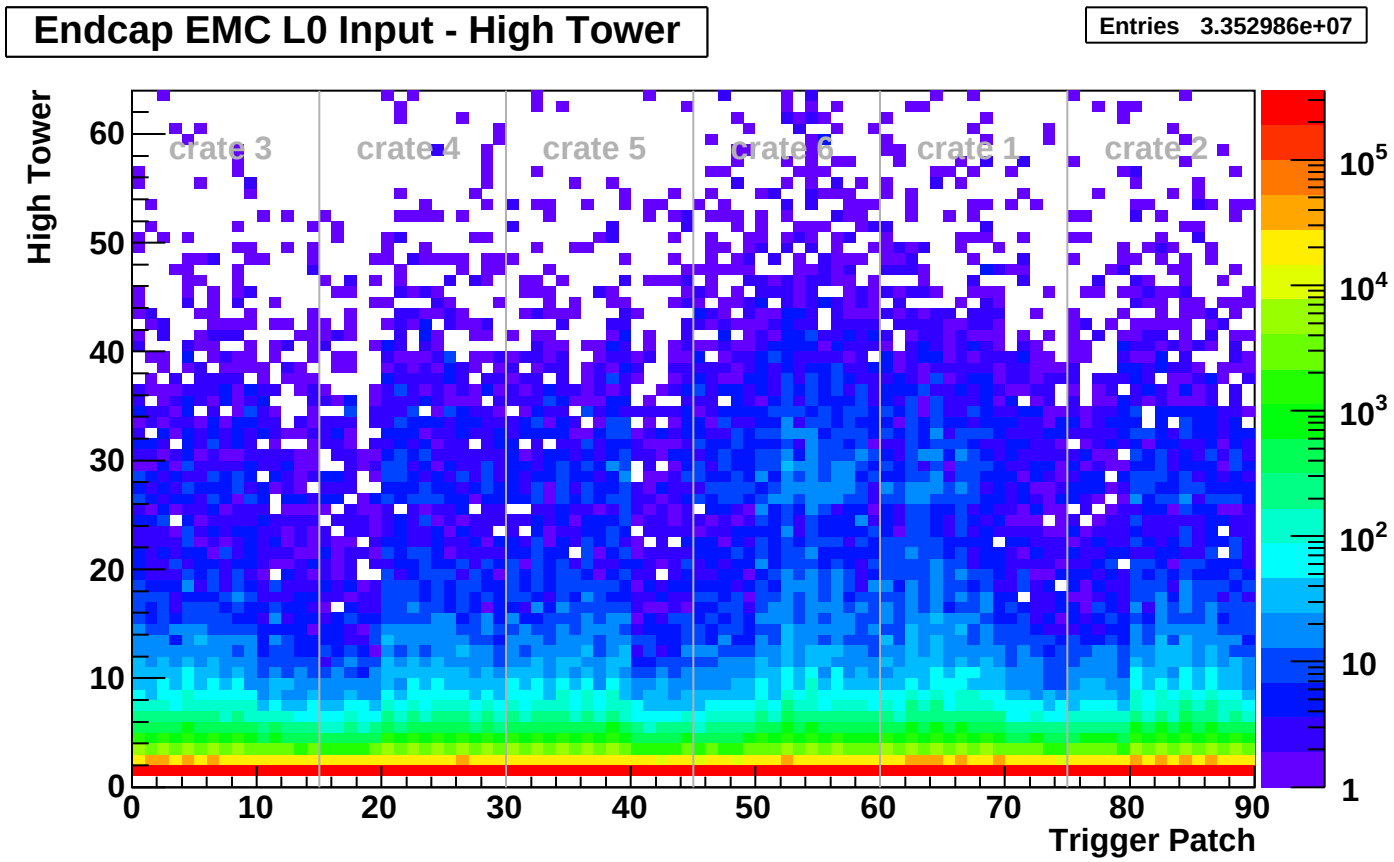

TCU bits
Entries 805393
TCU bits - Simulated
72394 85722 263410 5881 9868 2067 372554 3497
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

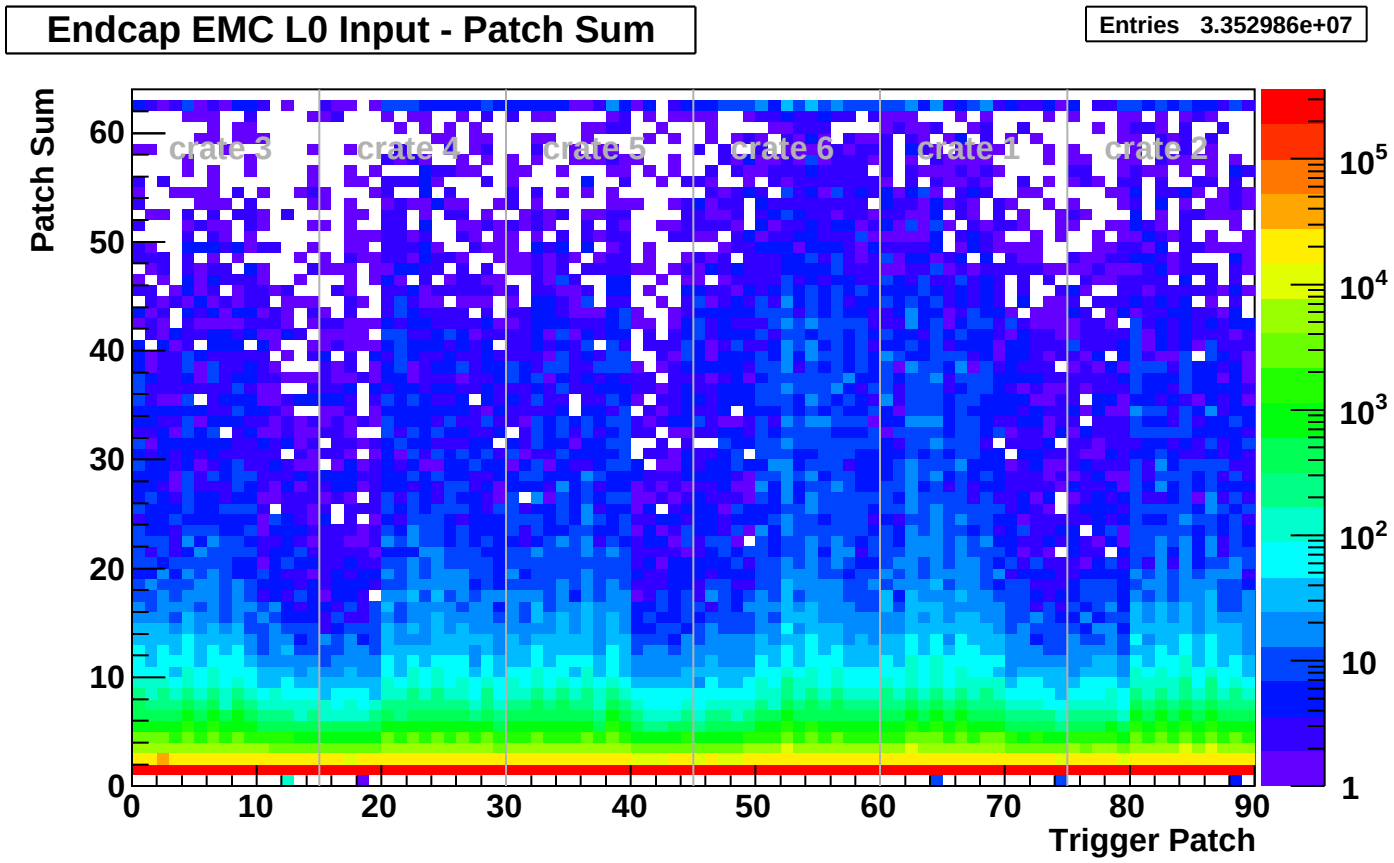

BunchId7Bit (BHT3)
Entries 7658

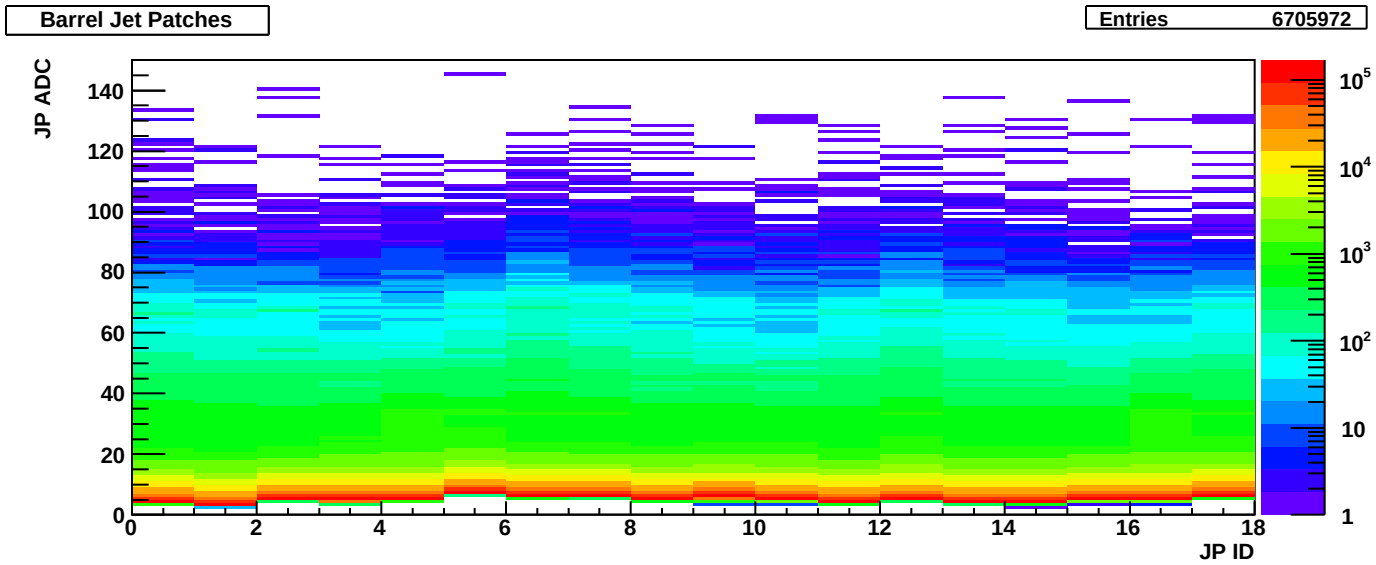

BunchId7Bit (all events)
Entries 372554

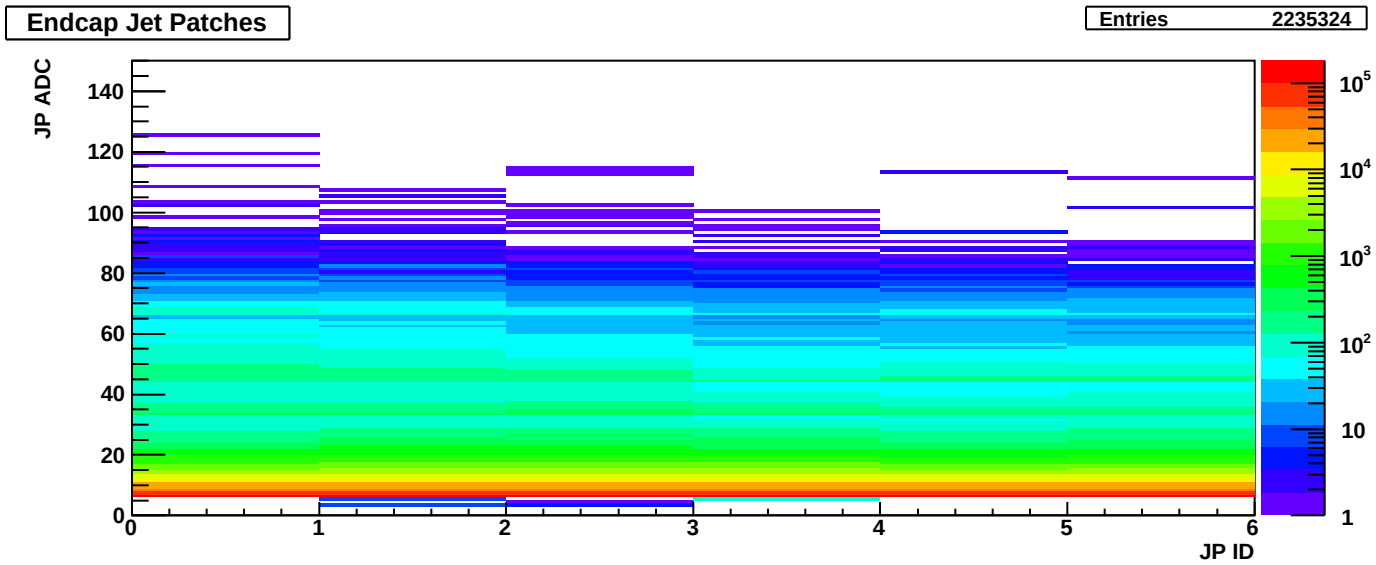

Barrel EMC L0 Input - High Tower
Entries 1.117662e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

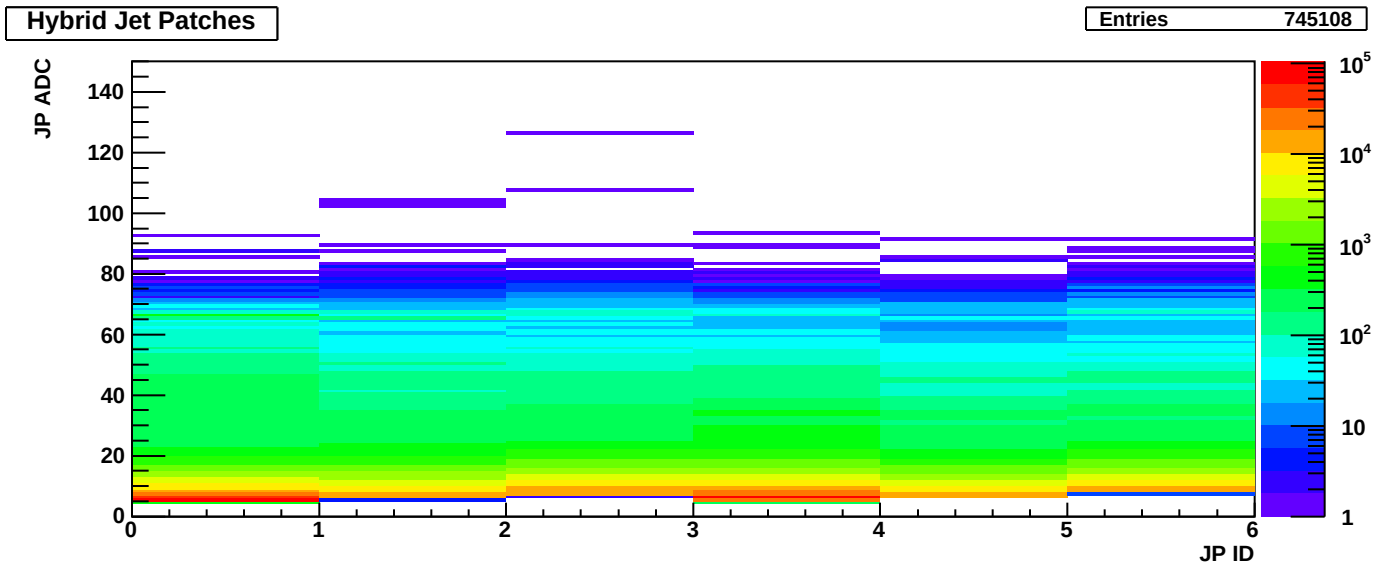

Barrel EMC L0 Input - Patch Sum
Entries 1.117662e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

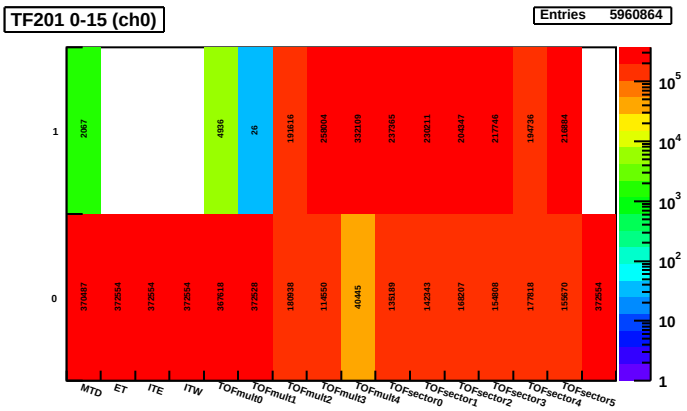

Endcap EMC L0 Input - High Tower
Entries 3.352986e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

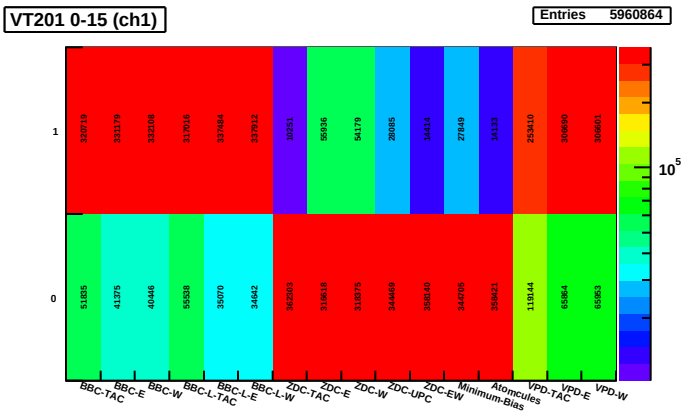

Endcap EMC L0 Input - Patch Sum
Entries 3.352986e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

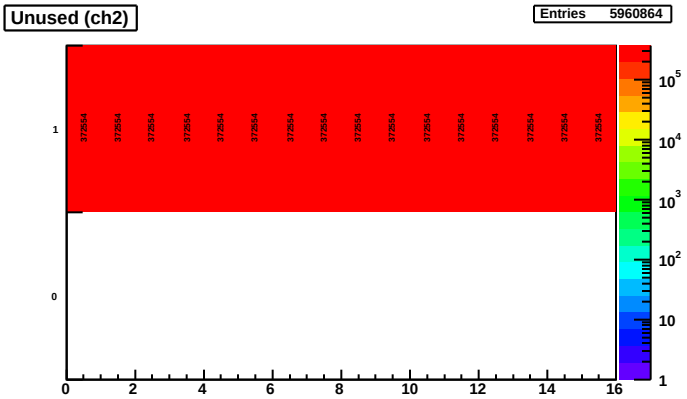

Barrel Jet Patches
Entries 6705972
JP ADC
JP ID

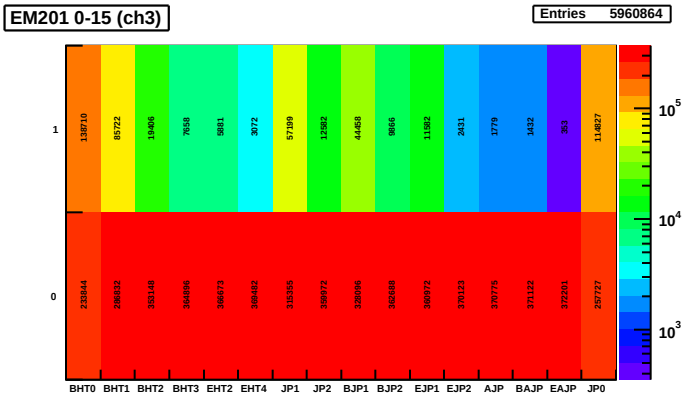

Endcap Jet Patches
Entries 2235324
JP ADC
JP ID

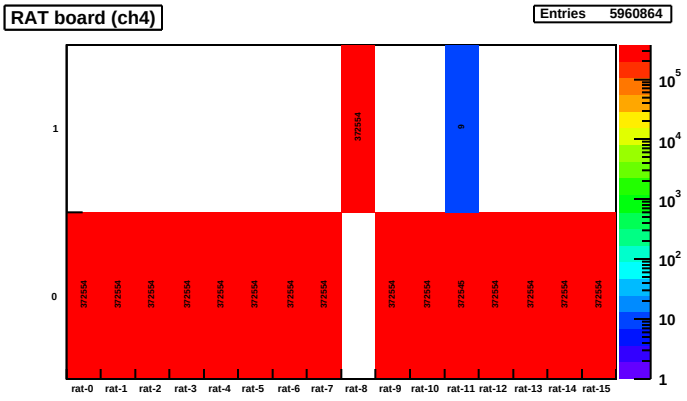

Hybrid Jet Patches
Entries 745108
JP ADC
JP ID

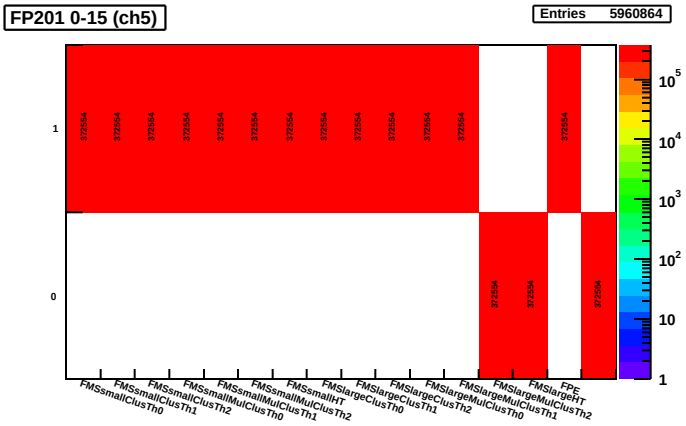

TF201 0-15 (ch0)
Entries 5960864
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

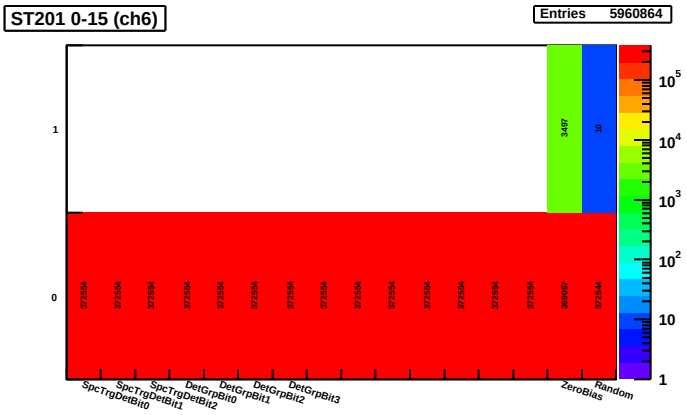

VT201 0-15 (ch1)
Entries 5960864
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

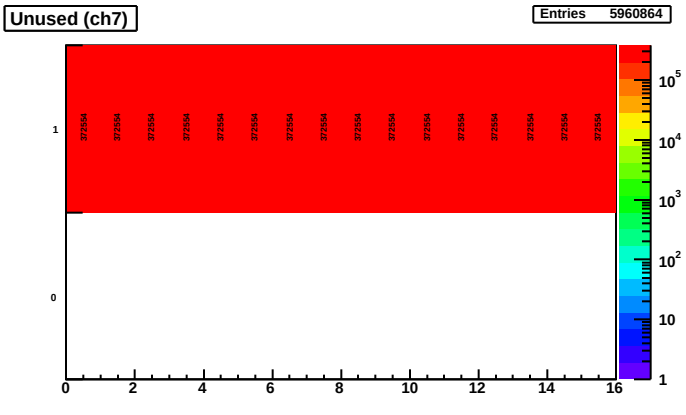

Unused (ch2)
Entries 5960864

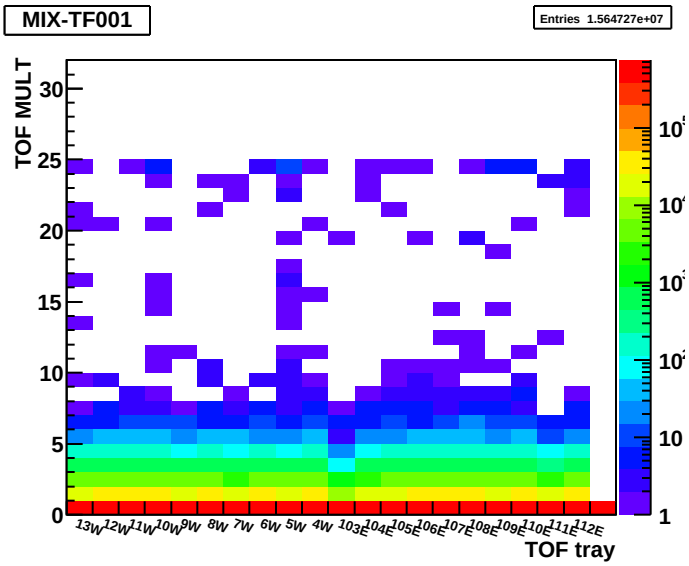

EM201 0-15 (ch3)
Entries 5960864
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

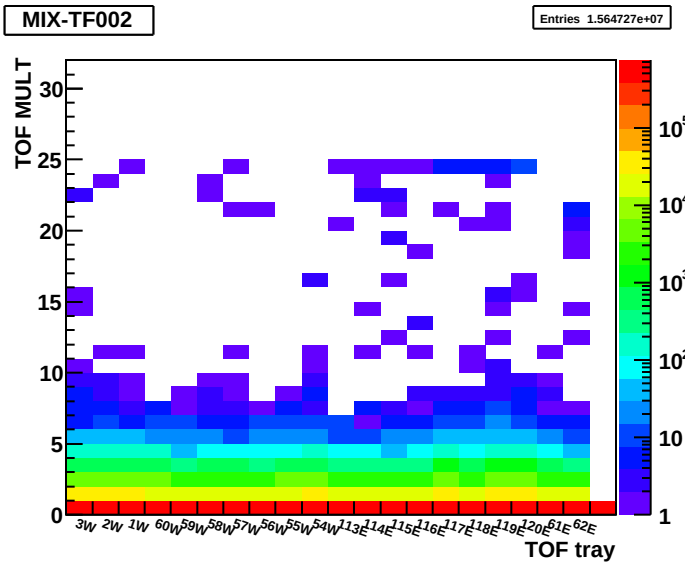

RAT board (ch4)
Entries 5960864
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

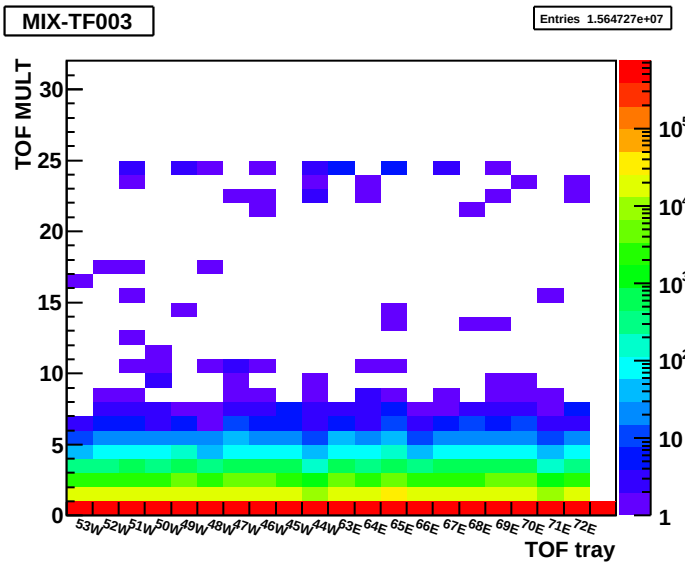

FP201 0-15 (ch5)
Entries 5960864
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

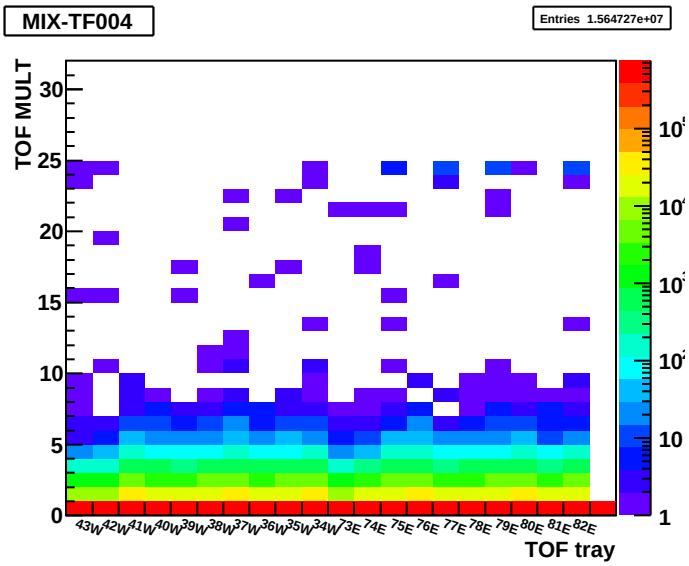

ST201 0-15 (ch6)
Entries 5960864
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

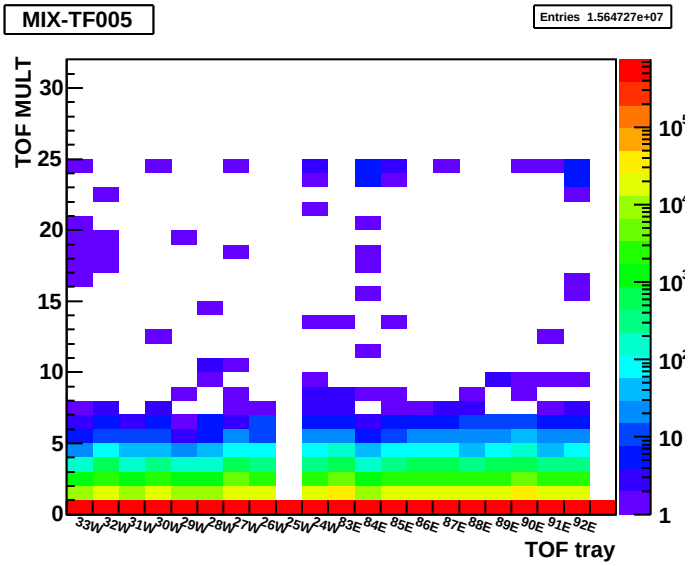

Unused (ch7)
Entries 5960864

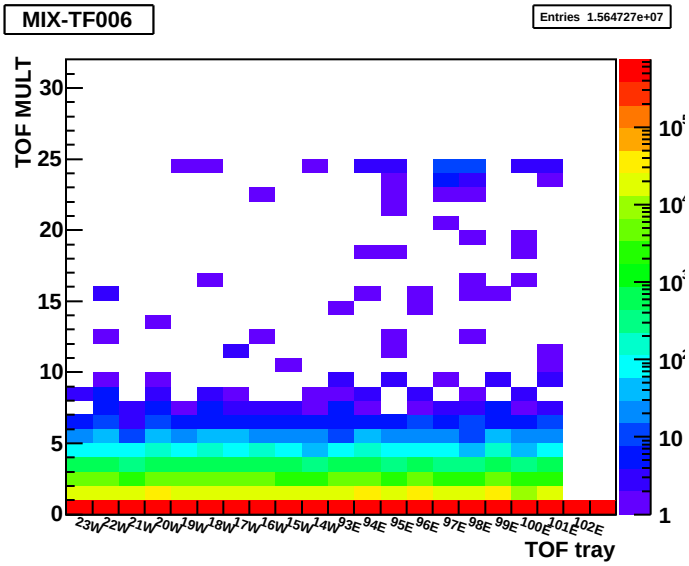

MIX-TF001
Entries 1.564727e+07
TOF MULT
TOF tray

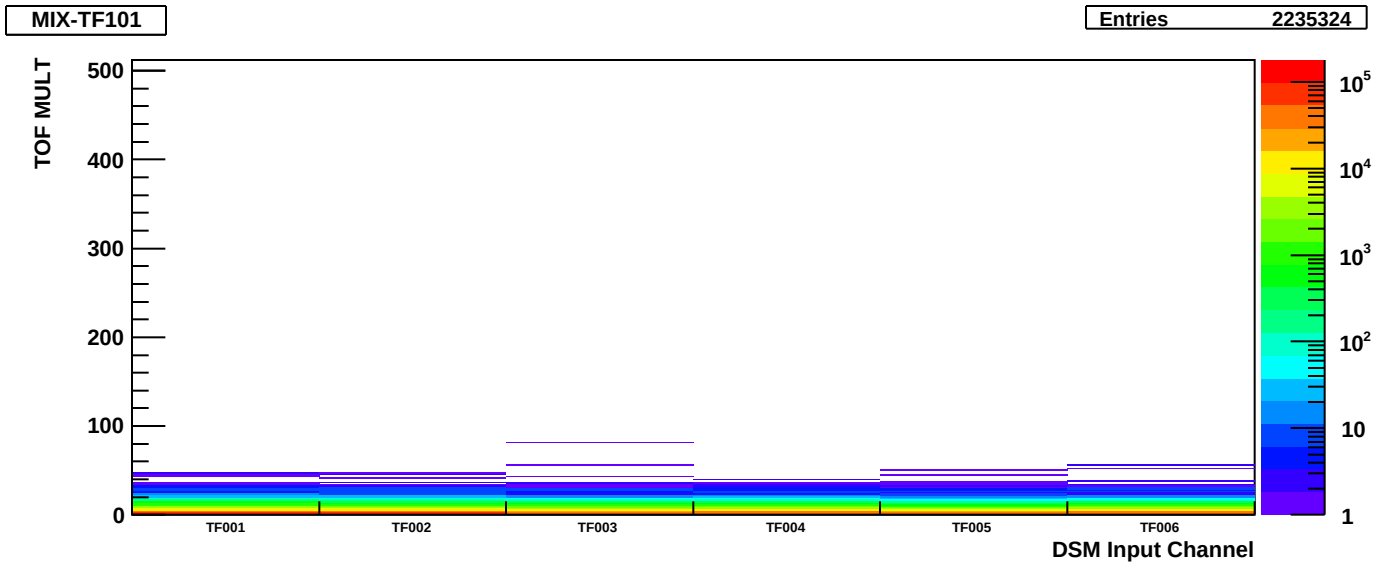

MIX-TF002
Entries 1.564727e+07
TOF MULT
TOF tray

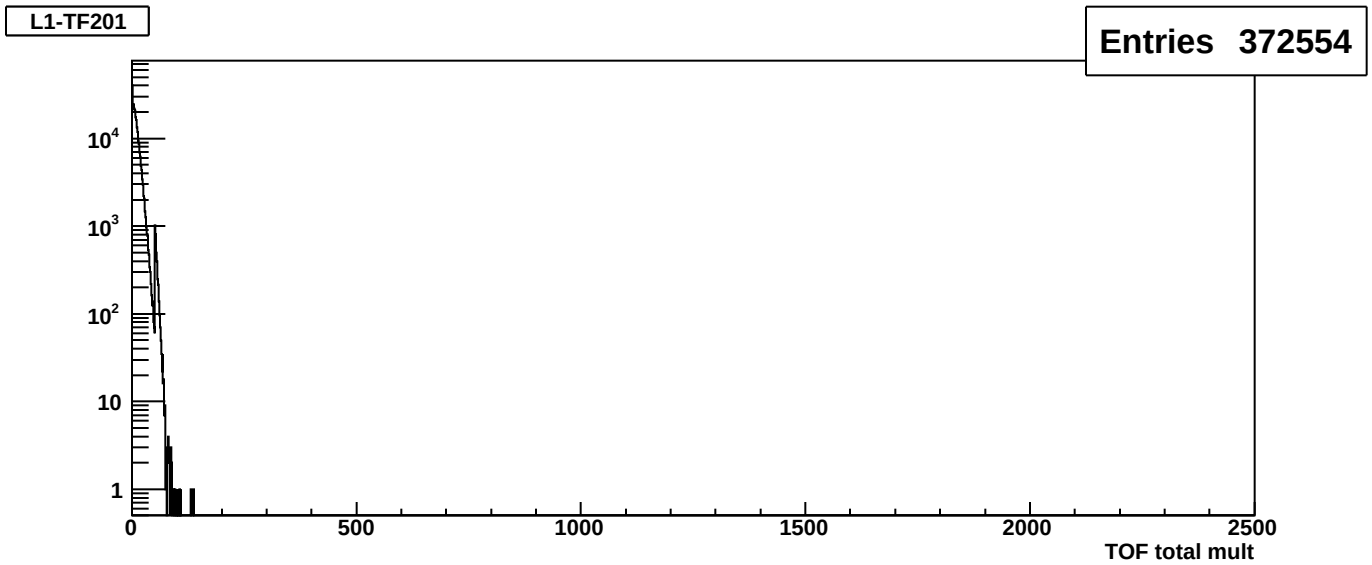

MIX-TF003
Entries 1.564727e+07
TOF MULT
TOF tray

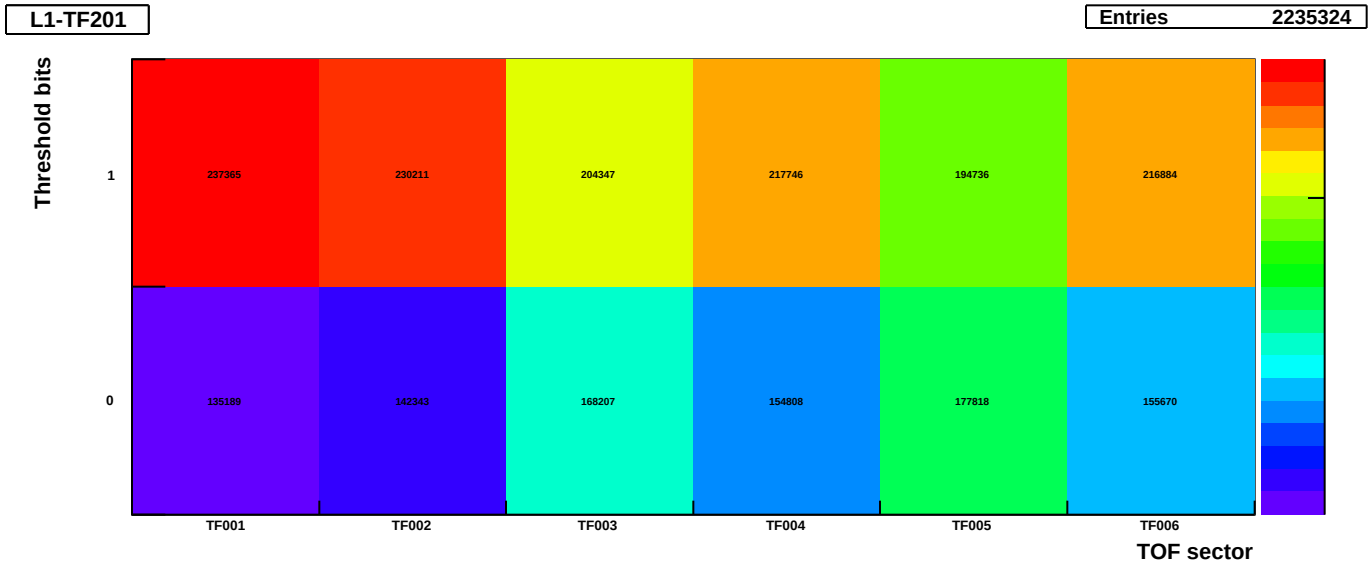

MIX-TF004
Entries 1.564727e+07
TOF MULT
TOF tray

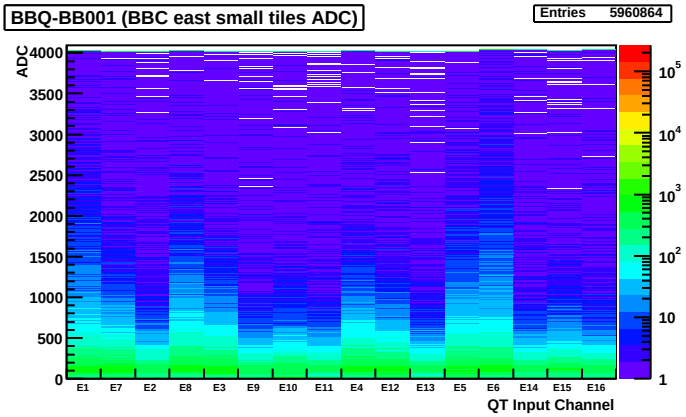

MIX-TF005
Entries 1.564727e+07
TOF MULT
TOF tray

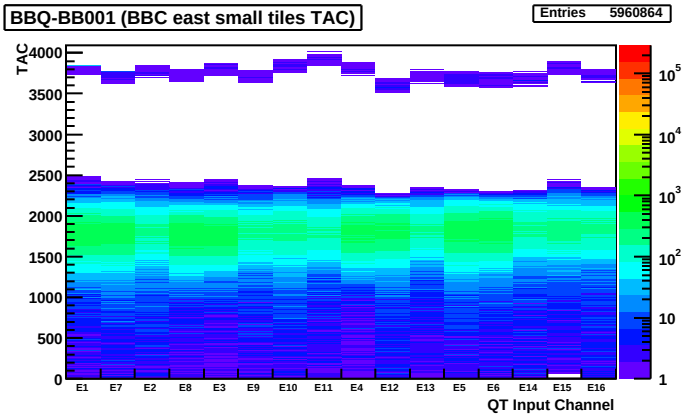

MIX-TF006
Entries 1.564727e+07
TOF MULT
TOF tray

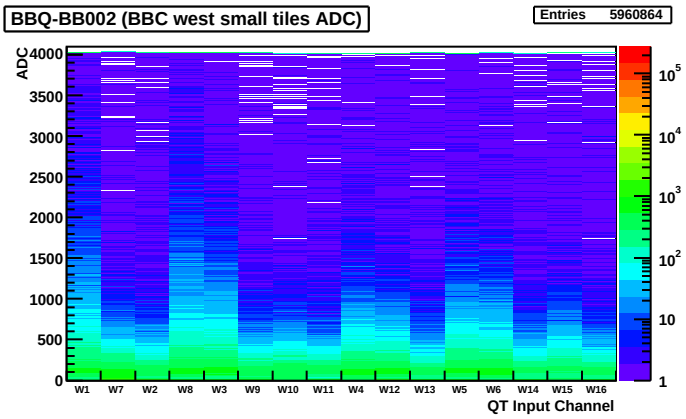

MIX-TF101
Entries 2235324
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

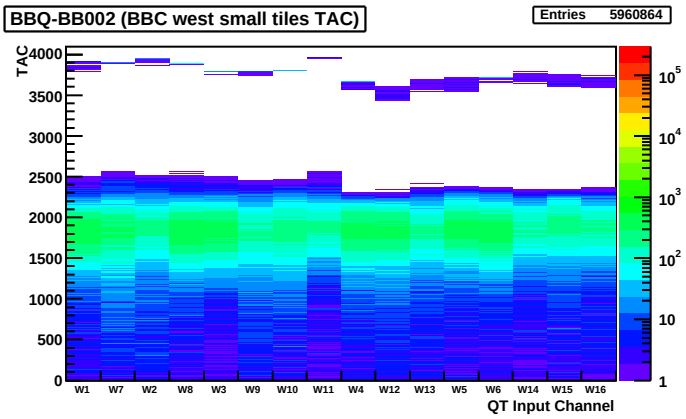

L1-TF201
Entries 372554
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

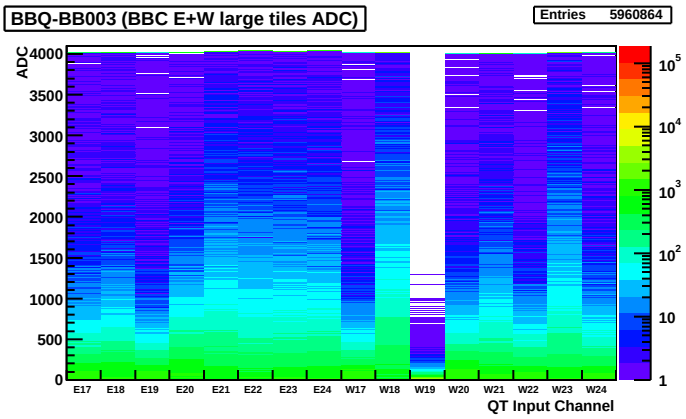

L1-TF201
Entries 2235324
Threshold bits
1
0
237365
230211
204347
217746
194736
216884
135189
142343
168207
154808
177818
155670
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

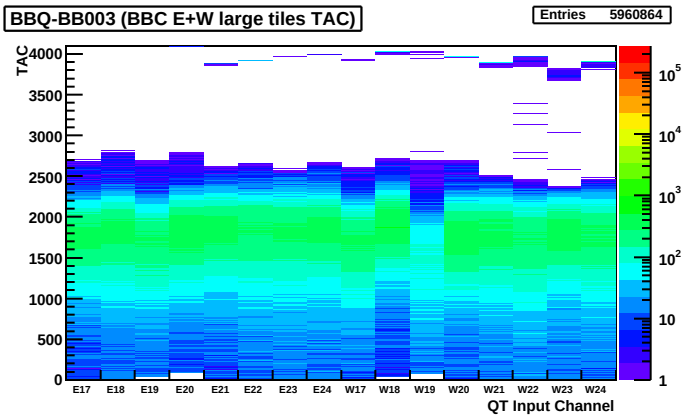

BBQ-BB001 (BBC east small tiles ADC)
Entries 5960864
ADC
QT Input Channel

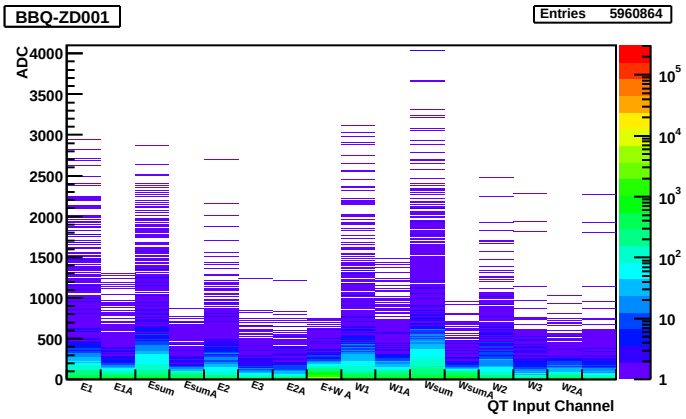

BBQ-BB001 (BBC east small tiles TAC)
Entries 5960864
TAC
QT Input Channel

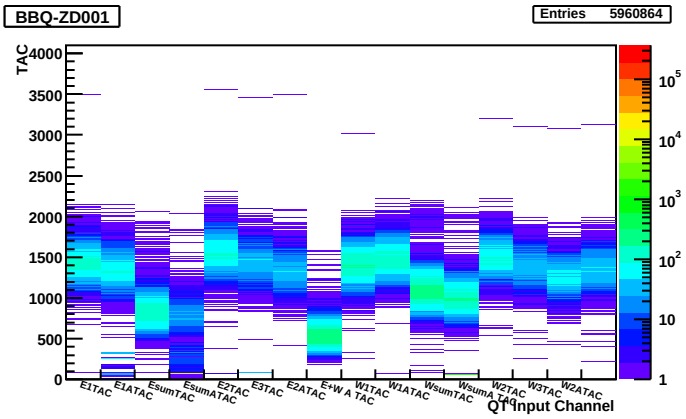

BBQ-BB002 (BBC west small tiles ADC)
Entries 5960864
ADC
QT Input Channel

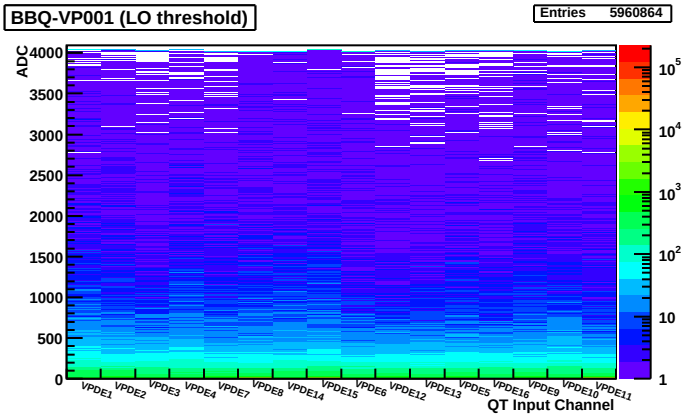

BBQ-BB002 (BBC west small tiles TAC)
Entries 5960864
TAC
QT Input Channel

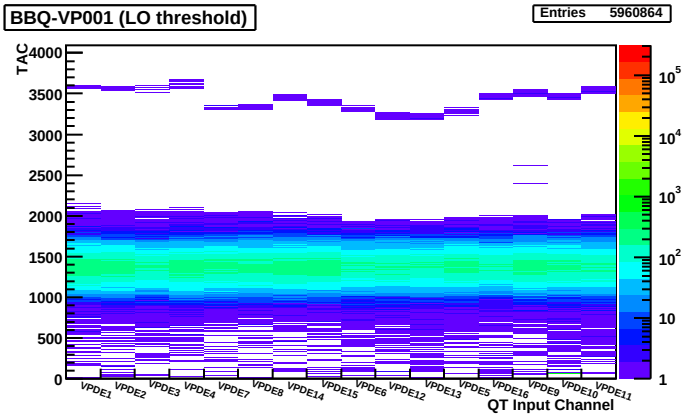

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5960864
ADC
QT Input Channel

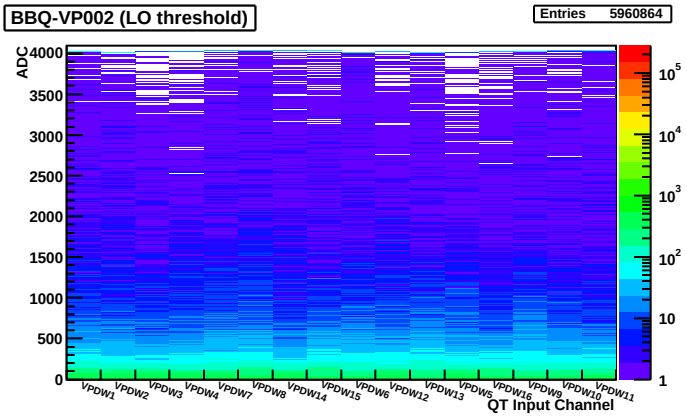

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5960864
TAC
QT Input Channel

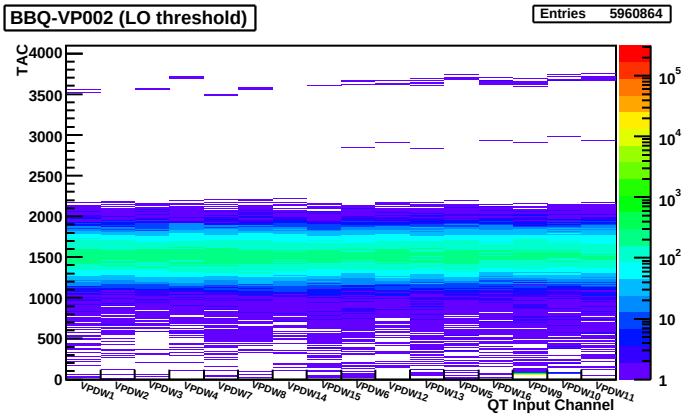

BBQ-ZD001
Entries 5960864
ADC
QT Input Channel

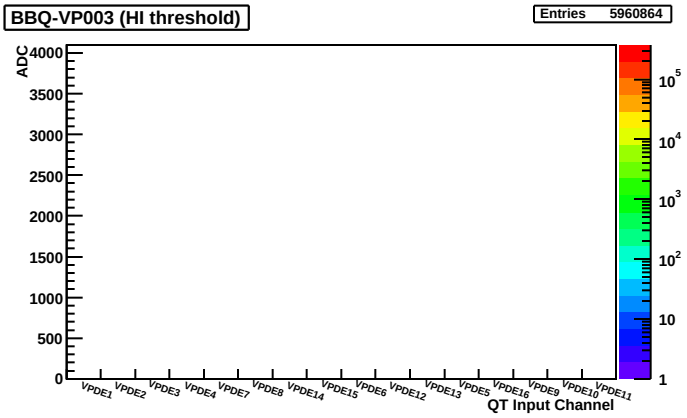

BBQ-ZD001
Entries 5960864
TAC
QT Input Channel

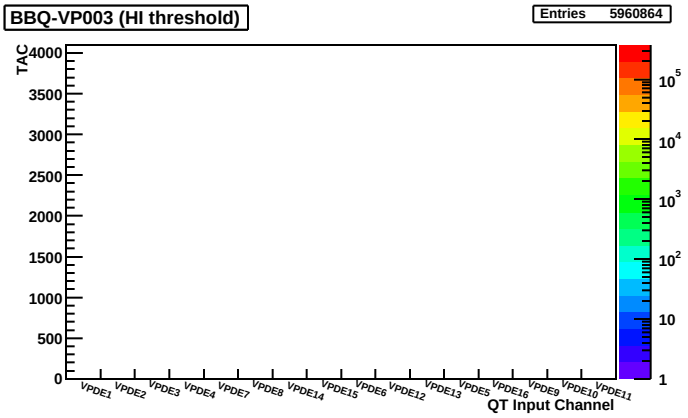

BBQ-VP001 (LO threshold)
Entries 5960864
ADC
QT Input Channel

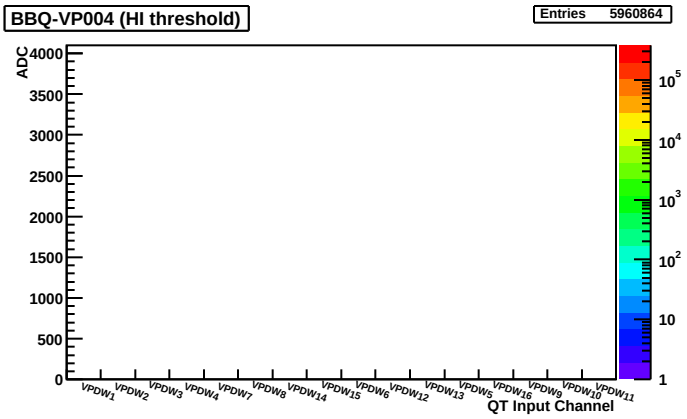

BBQ-VP001 (LO threshold)
Entries 5960864
TAC
QT Input Channel

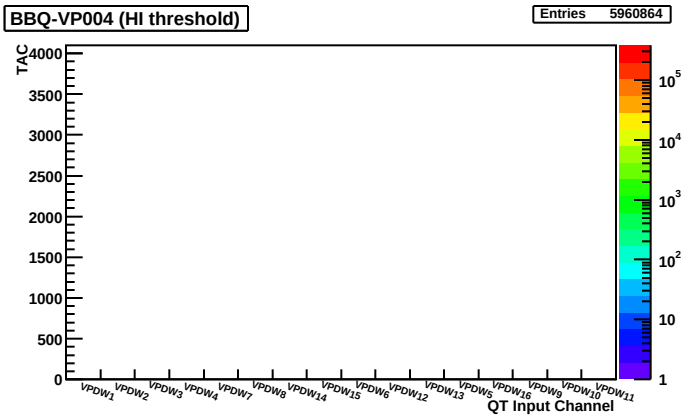

BBQ-VP002 (LO threshold)
Entries 5960864
ADC
QT Input Channel

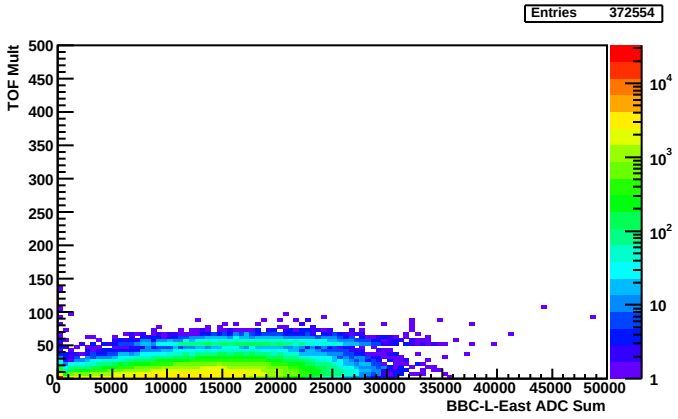

BBQ-VP002 (LO threshold)
Entries 5960864
TAC
QT Input Channel

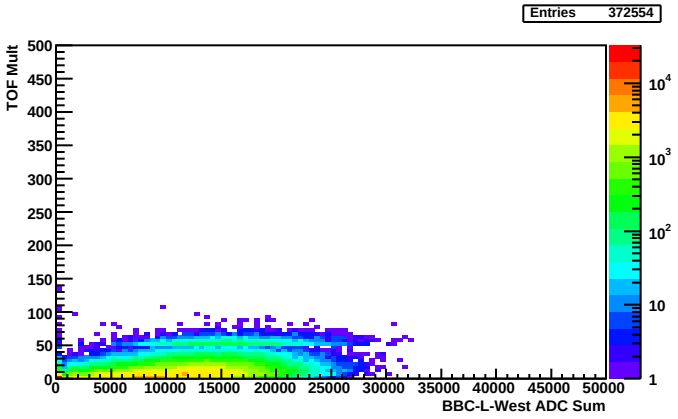

BBQ-VP003 (HI threshold)
Entries 5960864
ADC
QT Input Channel

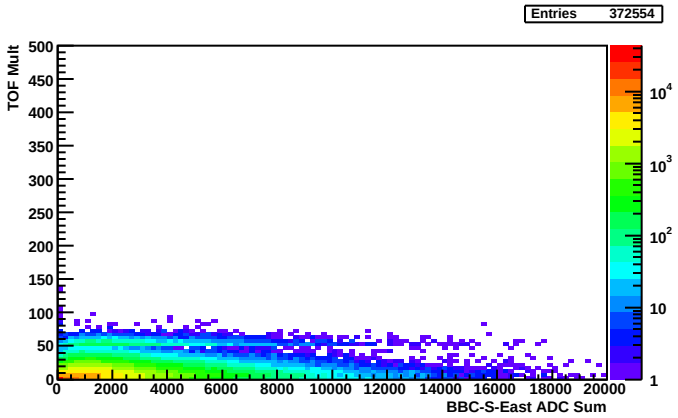

BBQ-VP003 (HI threshold)
Entries 5960864
TAC
QT Input Channel

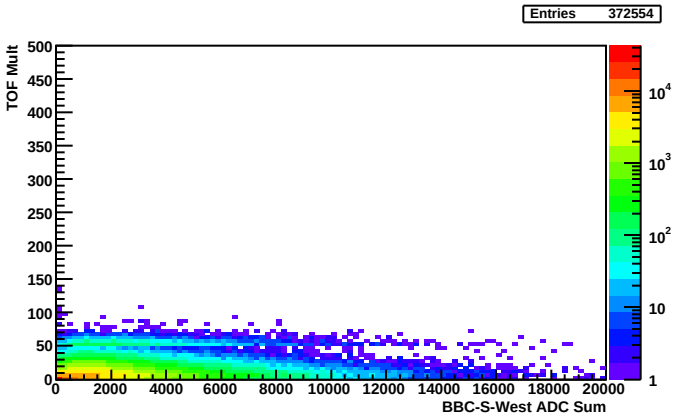

BBQ-VP004 (HI threshold)
Entries 5960864
ADC
QT Input Channel

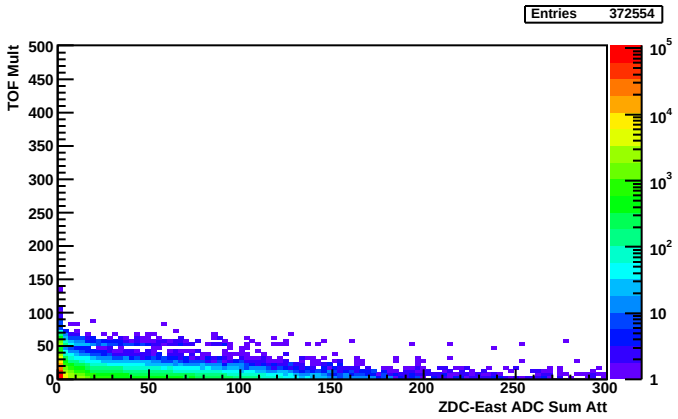

BBQ-VP004 (HI threshold)
Entries 5960864
TAC
QT Input Channel

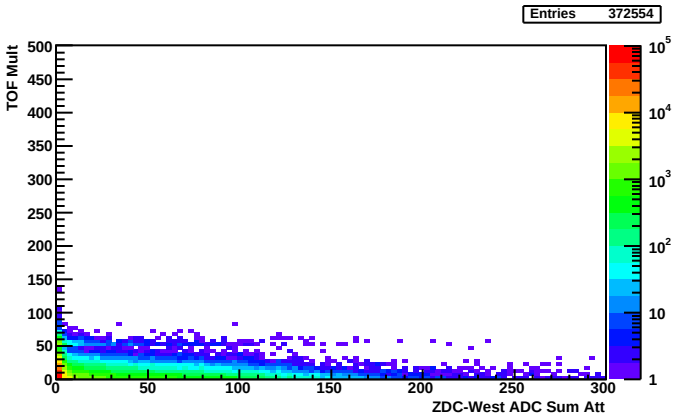

Entries 372554
TOF Mult
BBC-L-East ADC Sum

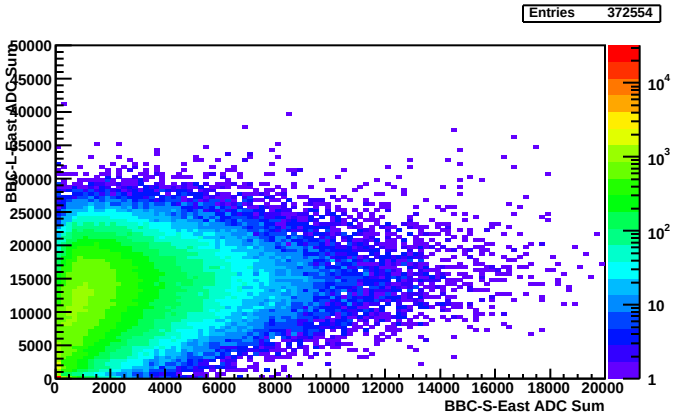

Entries 372554
TOF Mult
BBC-L-West ADC Sum

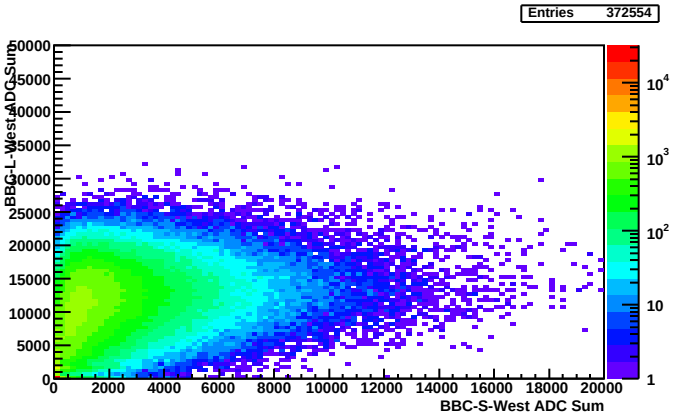

Entries 372554
TOF Mult
BBC-S-East ADC Sum

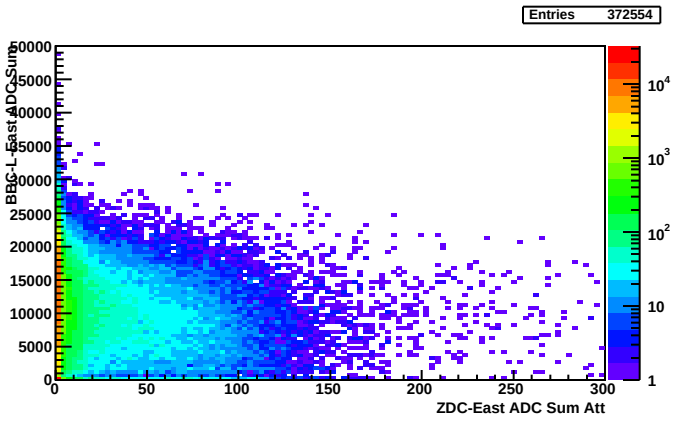

Entries 372554
TOF Mult
BBC-S-West ADC Sum

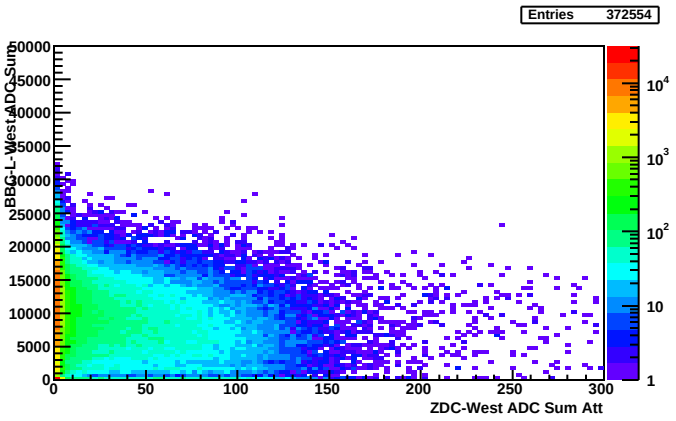

Entries 372554
TOF Mult
ZDC-East ADC Sum Att

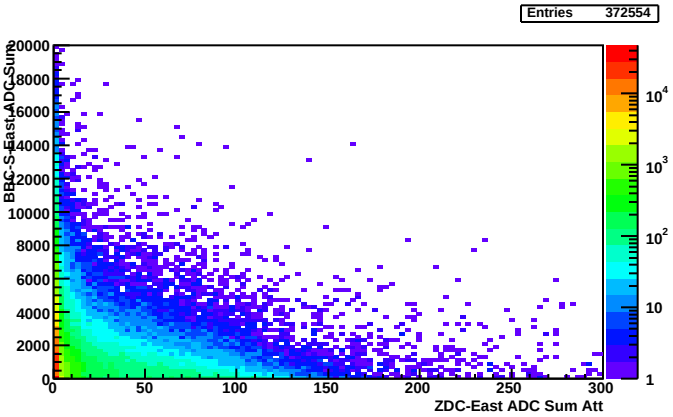

Entries 372554
TOF Mult
ZDC-West ADC Sum Att

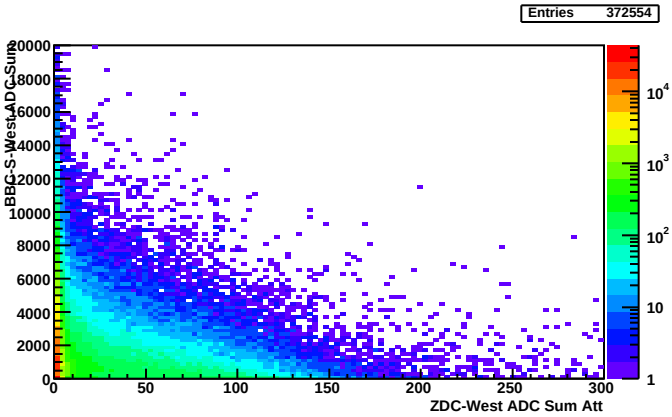

Entries 372554
BBC-L-East ADC Sum
BBC-S-East ADC Sum

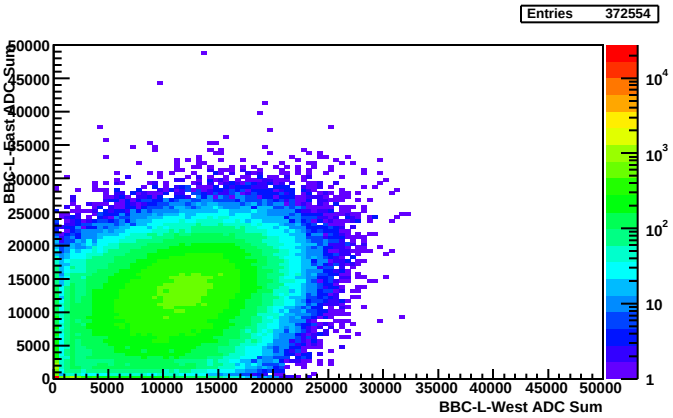

Entries 372554
BBC-L-West ADC Sum
BBC-S-West ADC Sum

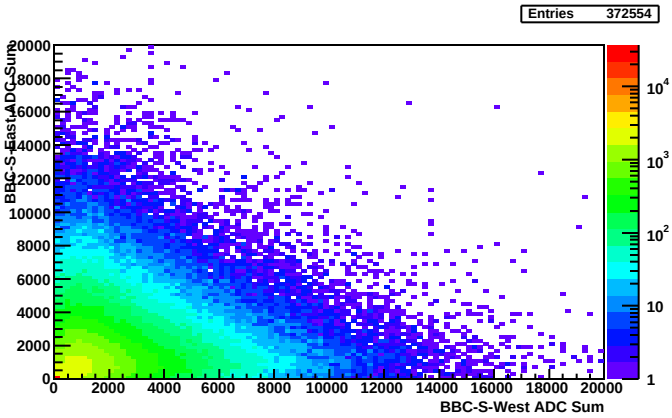

Entries 372554
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

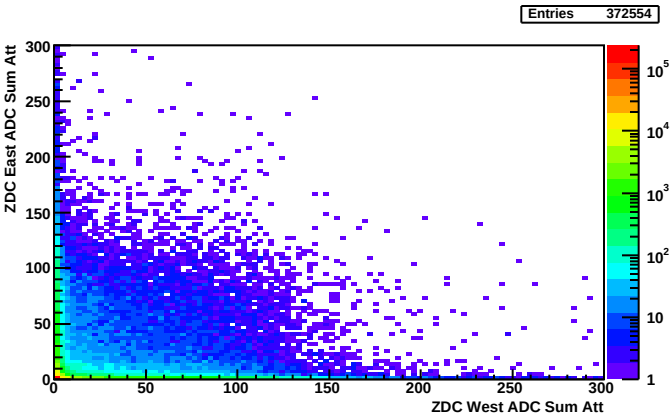

Entries 372554
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

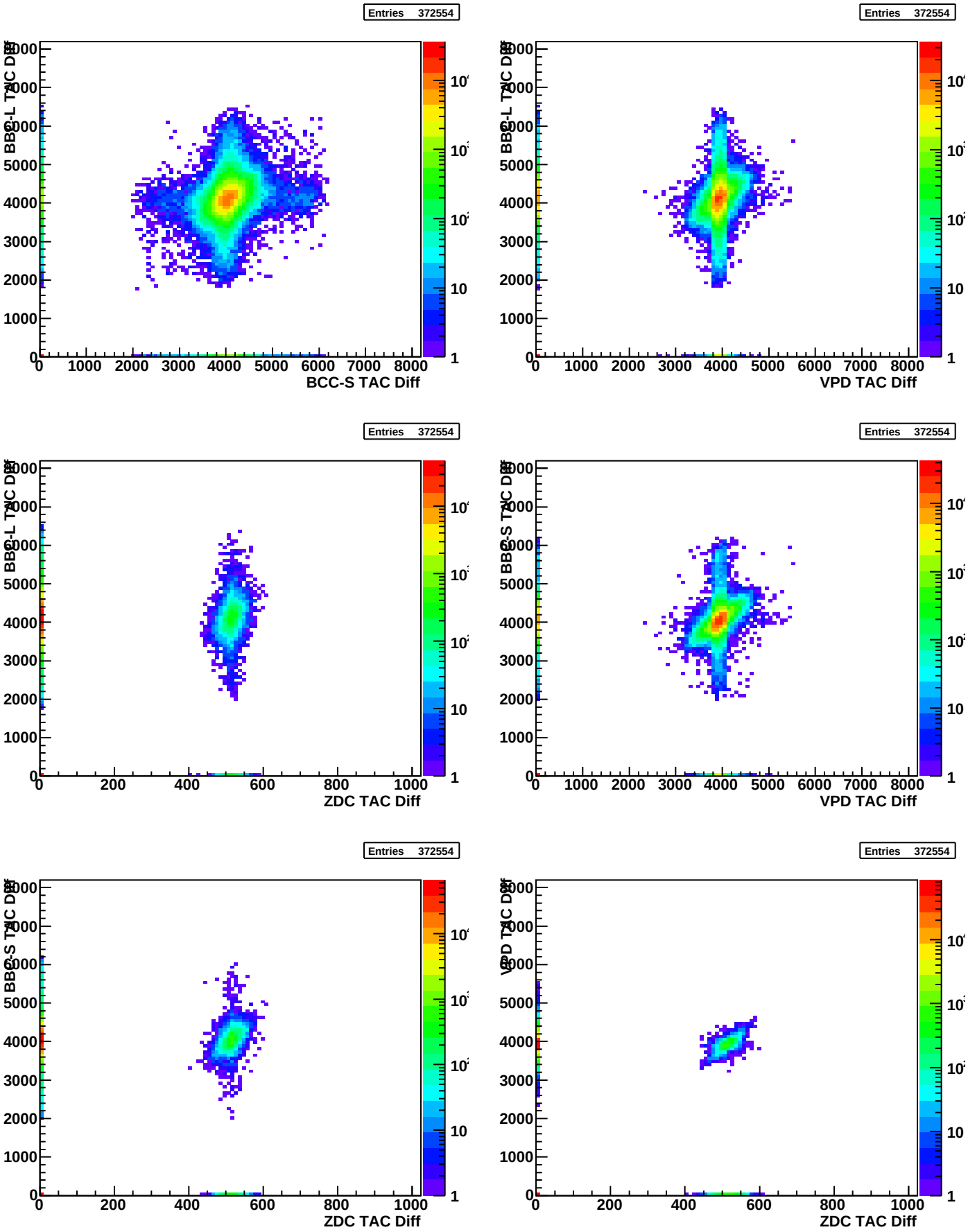

Entries 372554
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

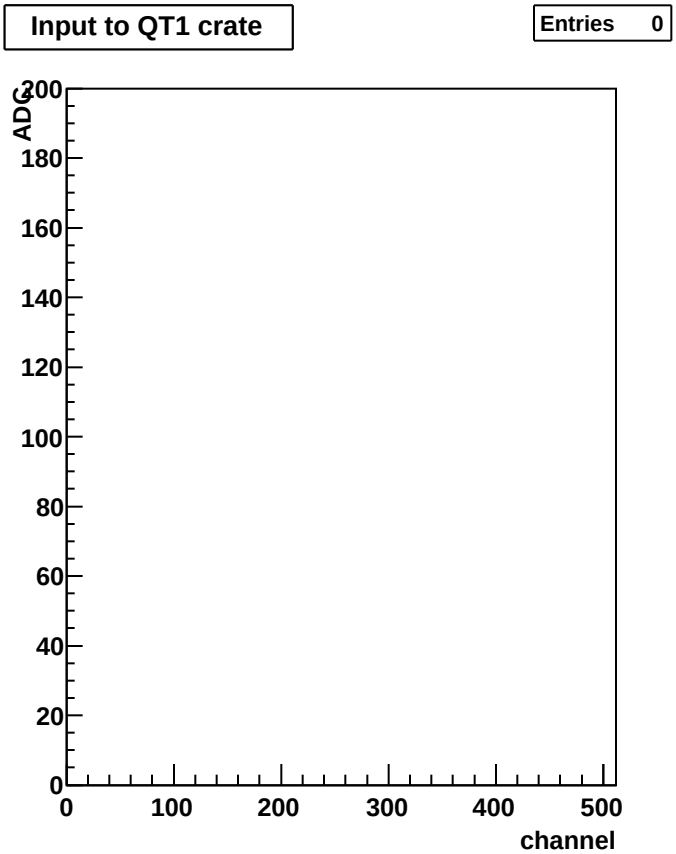

Entries 372554
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

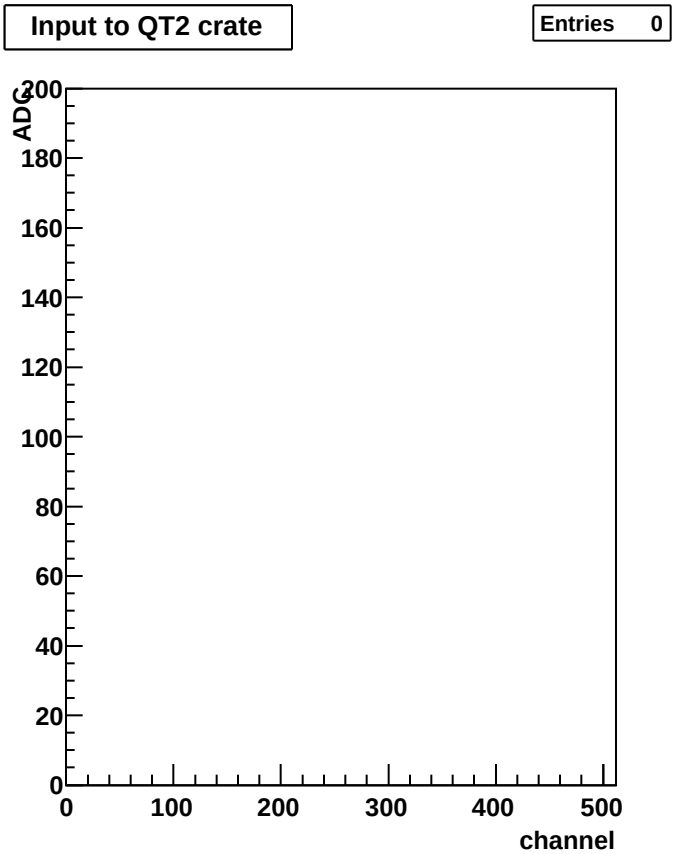

Entries 372554
BBC-L-East ADC Sum
BBC-L-West ADC Sum

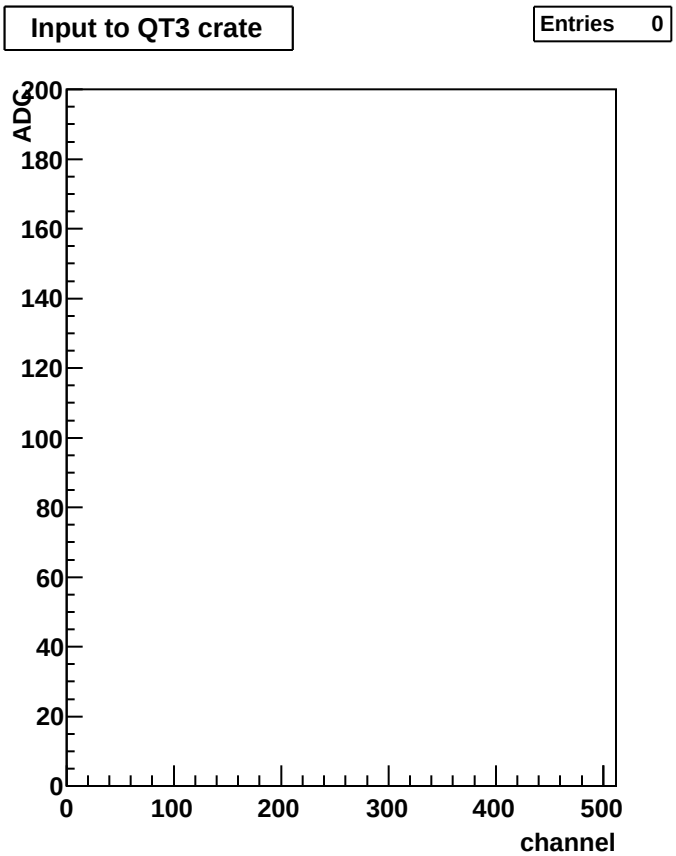

Entries 372554
BBC-S-East ADC Sum
BBC-S-West ADC Sum

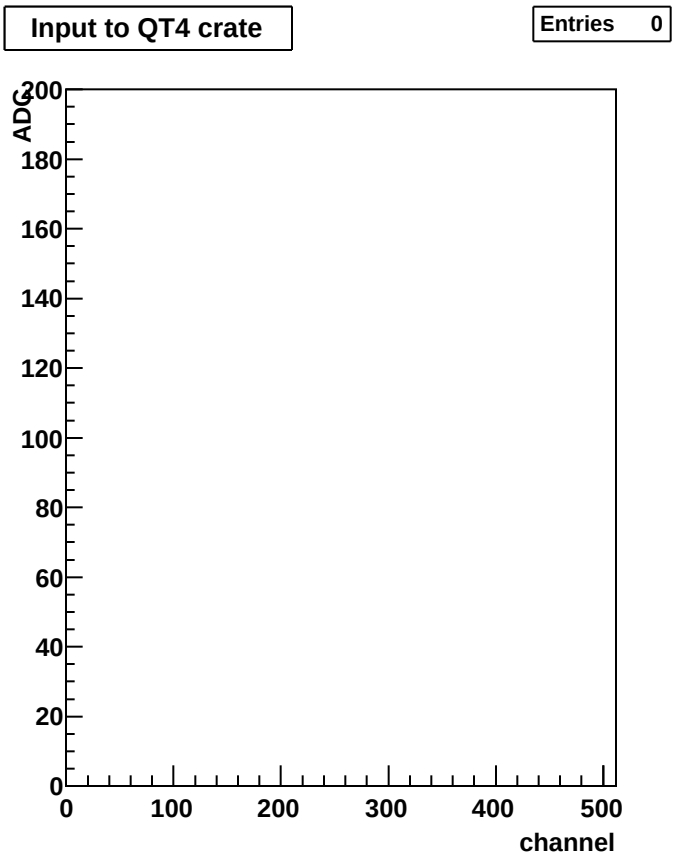

Entries 372554
ZDC East ADC Sum Att
ZDC West ADC Sum Att

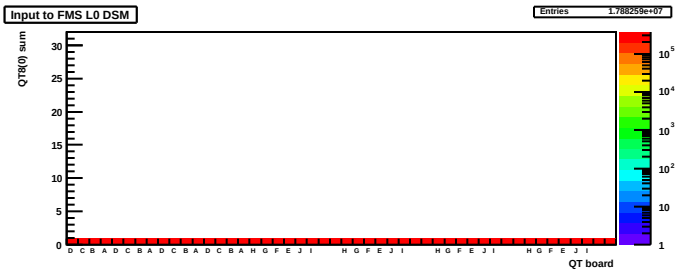

Entries 372554
BBC-L TAC Diff
BCC-S TAC Diff
Entries 372554
BBC-L TAC Diff
VPD TAC Diff
Entries 372554
BBC-L TAC Diff
ZDC TAC Diff
Entries 372554
BBC-S TAC Diff
VPD TAC Diff
Entries 372554
BBC-S TAC Diff
ZDC TAC Diff
Entries 372554
VPD TAC Diff
ZDC TAC Diff

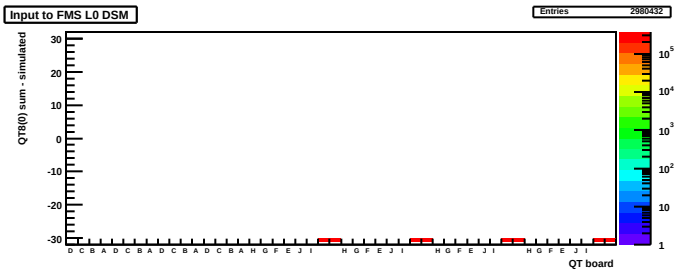

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

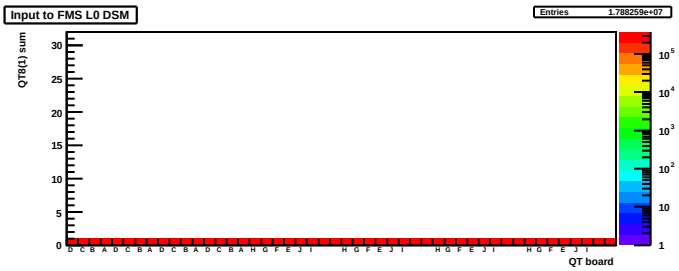

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

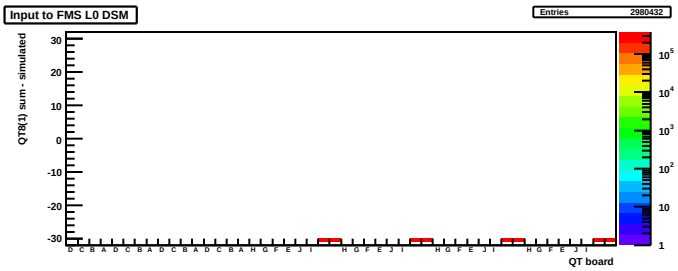

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

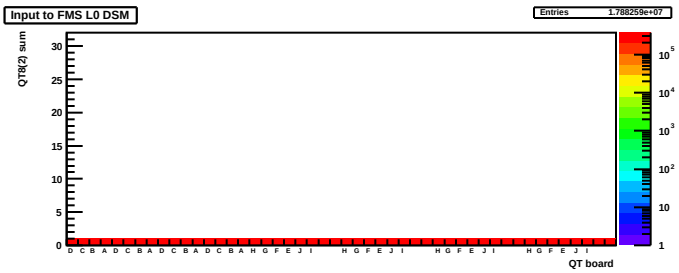

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

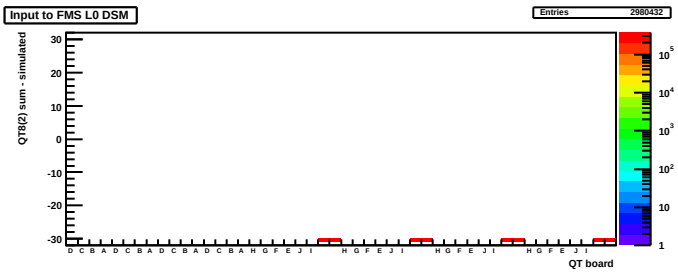

Input to FMS L0 DSM
Entries 1.788259e+07
QT8(0) sum
QT board

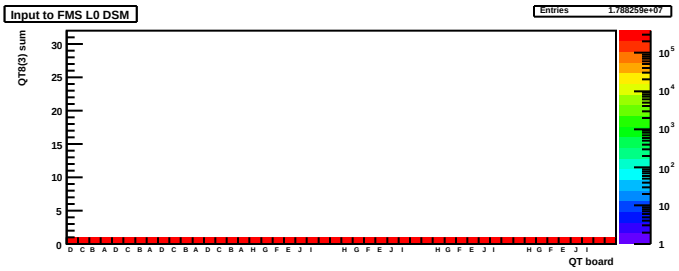

Input to FMS L0 DSM
Entries 2980432
QT8(0) sum - simulated
QT board

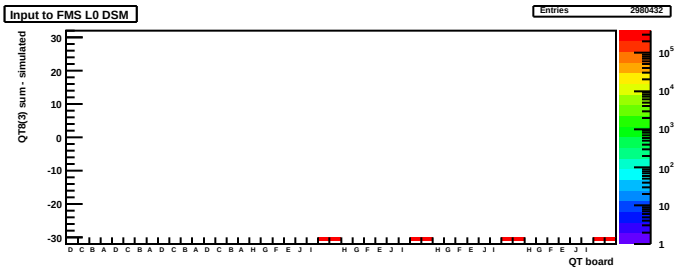

Input to FMS L0 DSM
Entries 1.788259e+07
QT8(1) sum
QT board

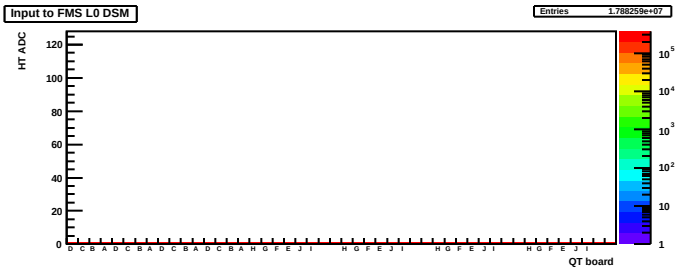

Input to FMS L0 DSM
Entries 2980432
QT8(1) sum - simulated
QT board

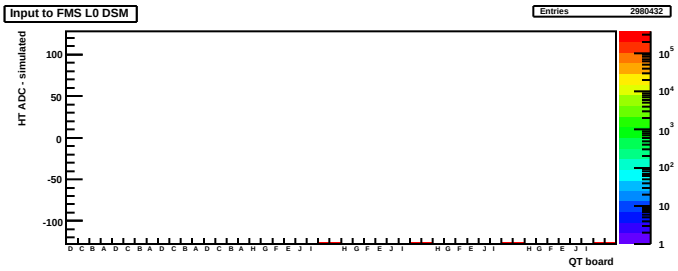

Input to FMS L0 DSM
Entries 1.788259e+07
QT8(2) sum
QT board

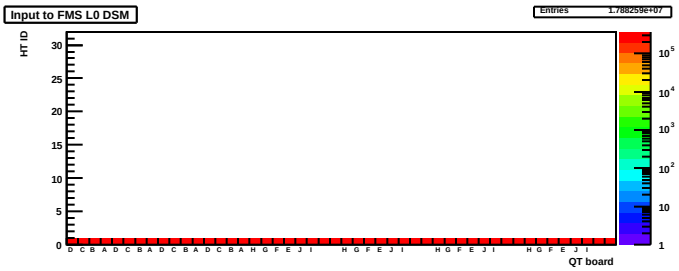

Input to FMS L0 DSM
Entries 2980432
QT8(2) sum - simulated
QT board

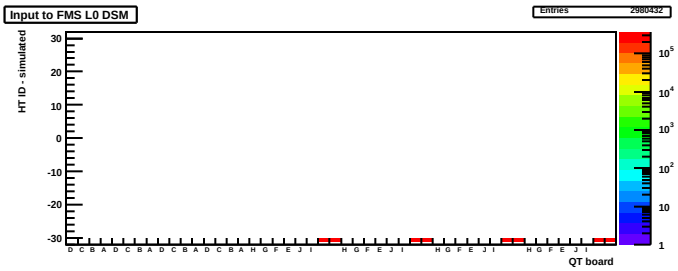

Input to FMS L0 DSM
Entries 1.788259e+07
QT8(3) sum
QT board

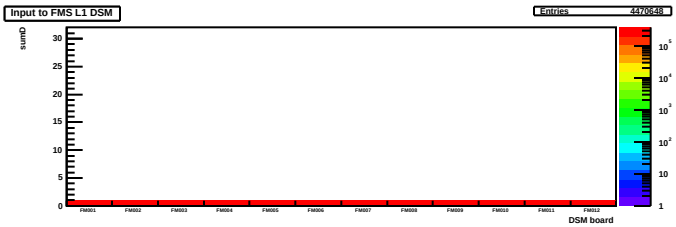

Input to FMS L0 DSM
Entries 2980432
QT8(3) sum - simulated
QT board

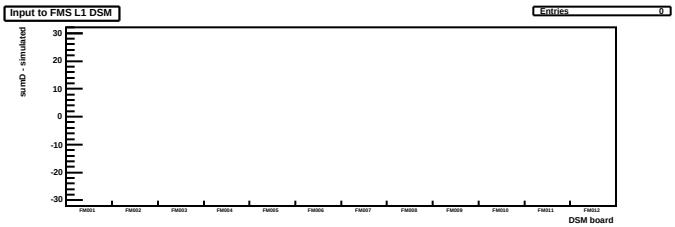

Input to FMS L0 DSM
Entries 1.788259e+07
HT ADC
QT board

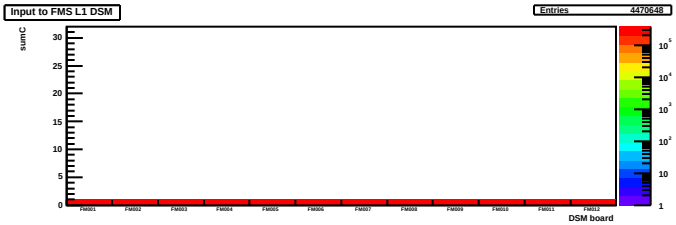

Input to FMS L0 DSM
Entries 2980432
HT ADC - simulated
QT board

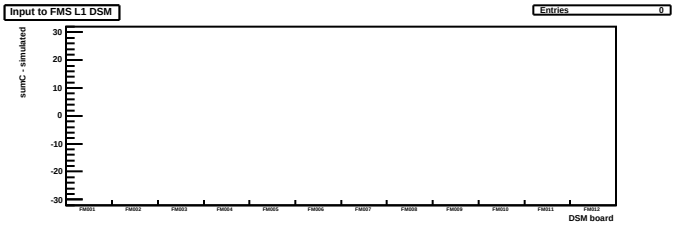

Input to FMS L0 DSM
Entries 1.788259e+07
HT ID
QT board

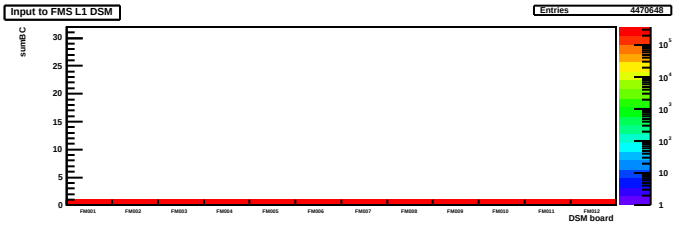

Input to FMS L0 DSM
Entries 2980432
HT ID - simulated
QT board

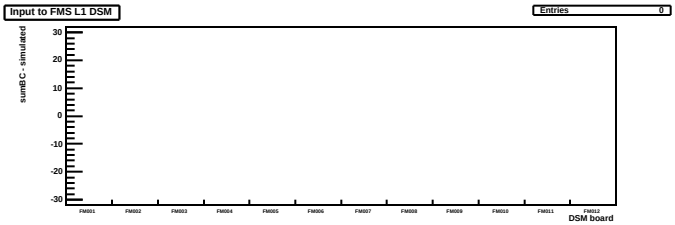

Input to FMS L1 DSM
Entries 4470648
sumD
DSM board

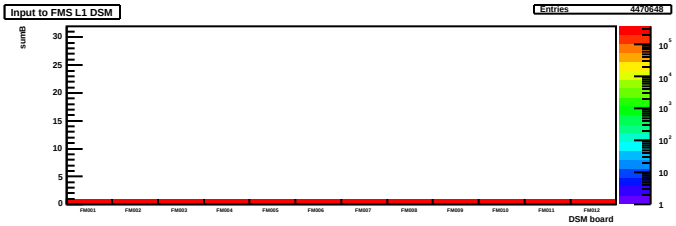

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

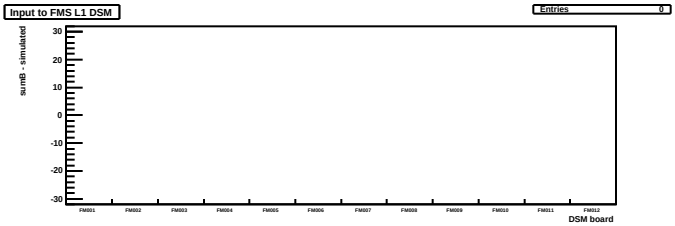

Input to FMS L1 DSM
Entries 4470648
sumC
DSM board

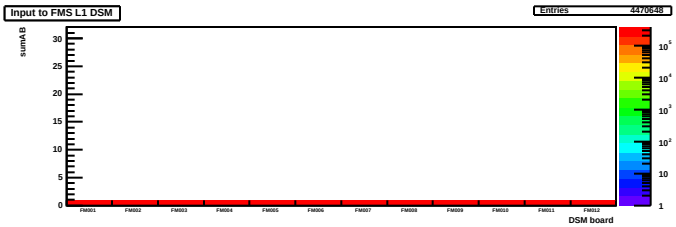

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

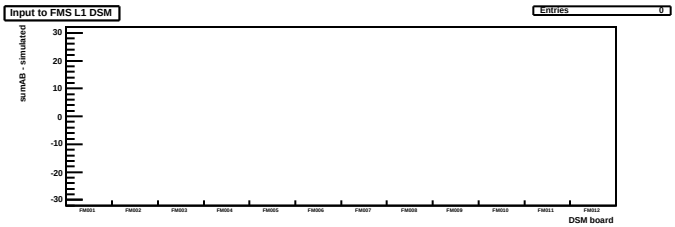

Input to FMS L1 DSM
Entries 4470648
sumBC
DSM board

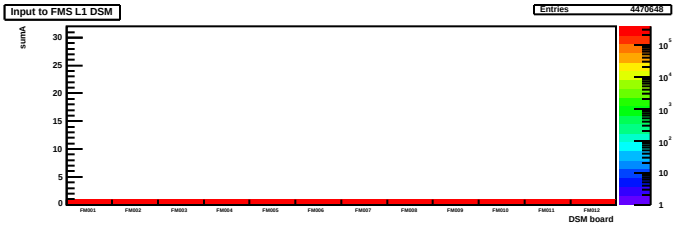

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

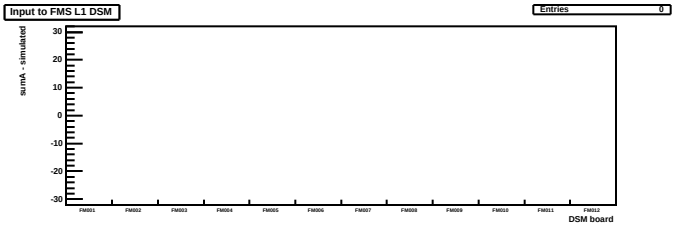

Input to FMS L1 DSM
Entries 4470648
sumB
DSM board

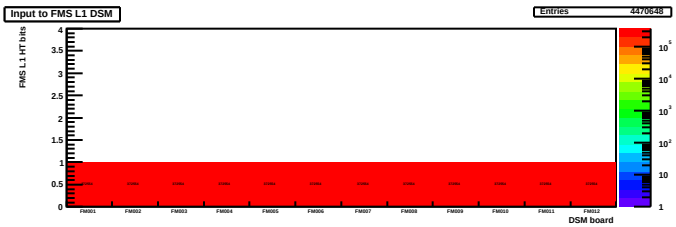

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

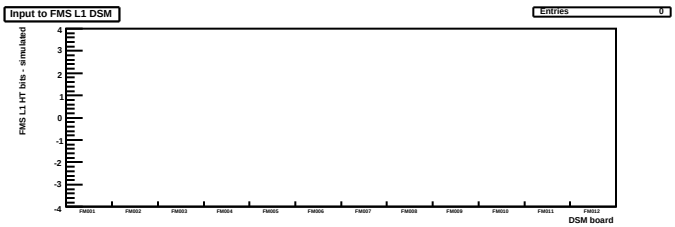

Input to FMS L1 DSM
Entries 4470648
sumAB
DSM board

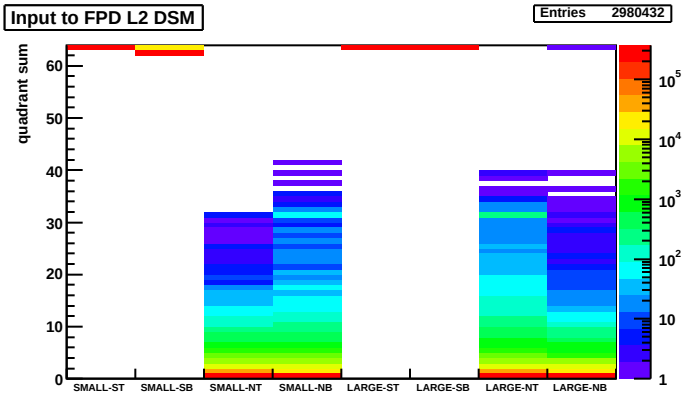

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

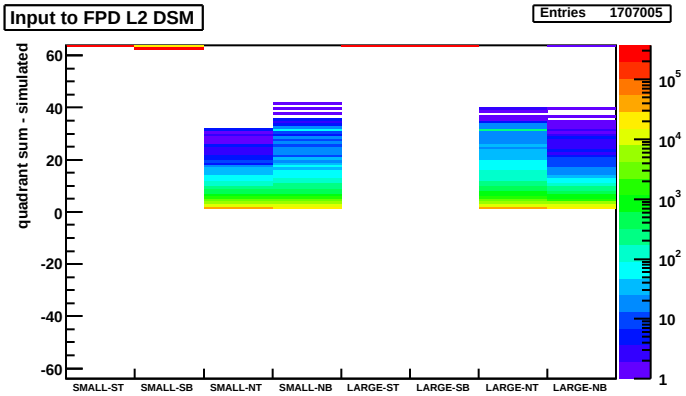

Input to FMS L1 DSM
Entries 4470648
sumA
DSM board

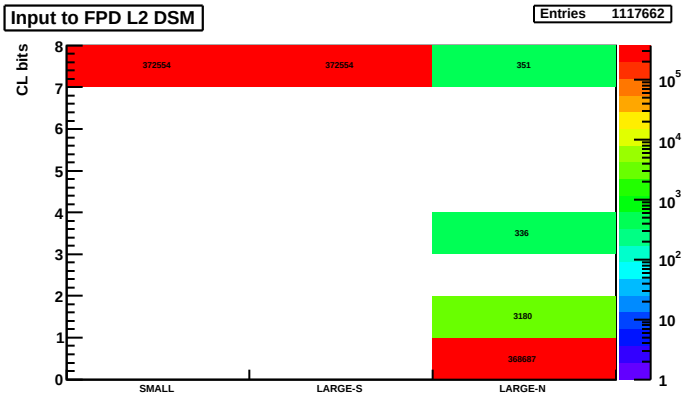

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

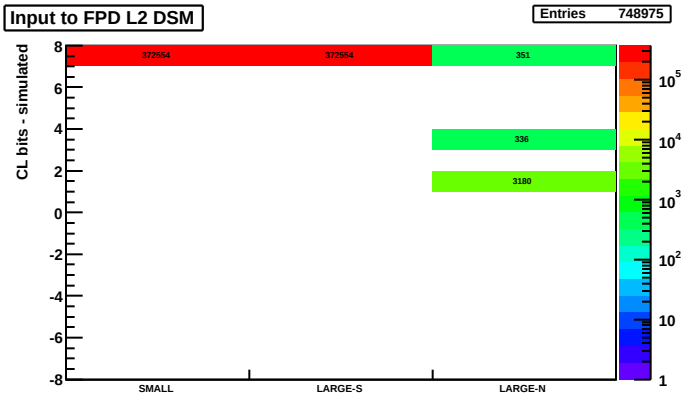

Input to FMS L1 DSM
Entries 4470648
FMS L1 HT bits
DSM board

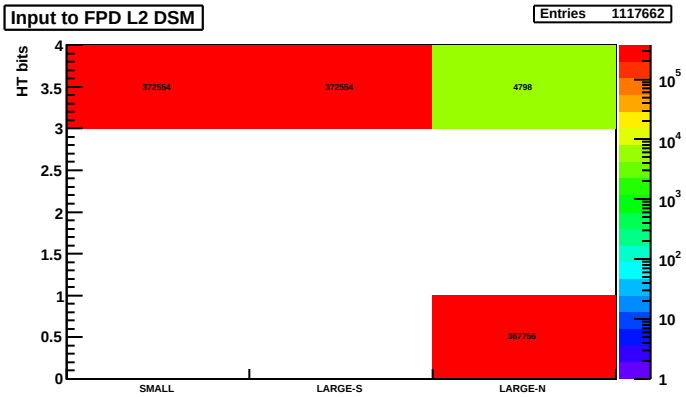

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

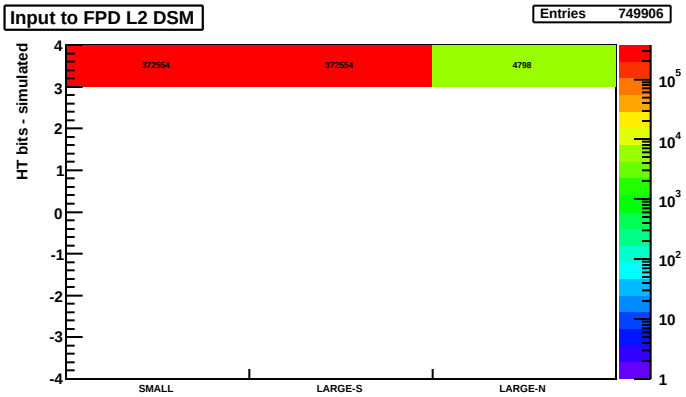

Input to FPD L2 DSM
Entries 2980432
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

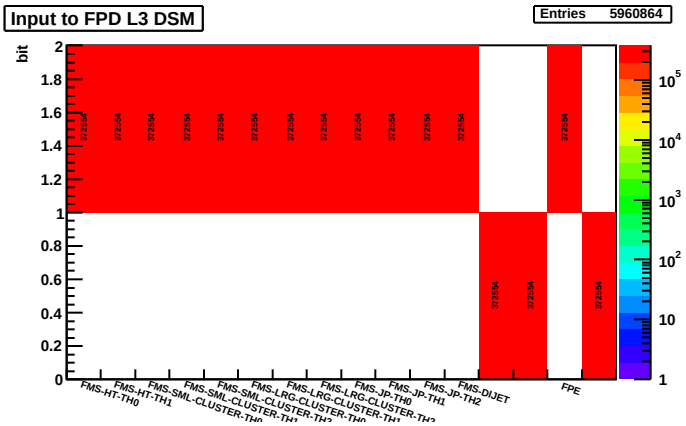

Input to FPD L2 DSM
Entries 1707005
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

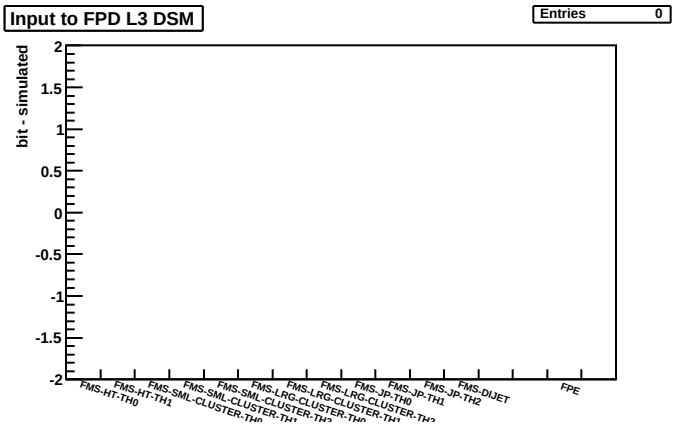

Input to FPD L2 DSM
Entries 1117662
CL bits
372554
372554
351
336
3180
368687
SMALL
LARGE-S
LARGE-N

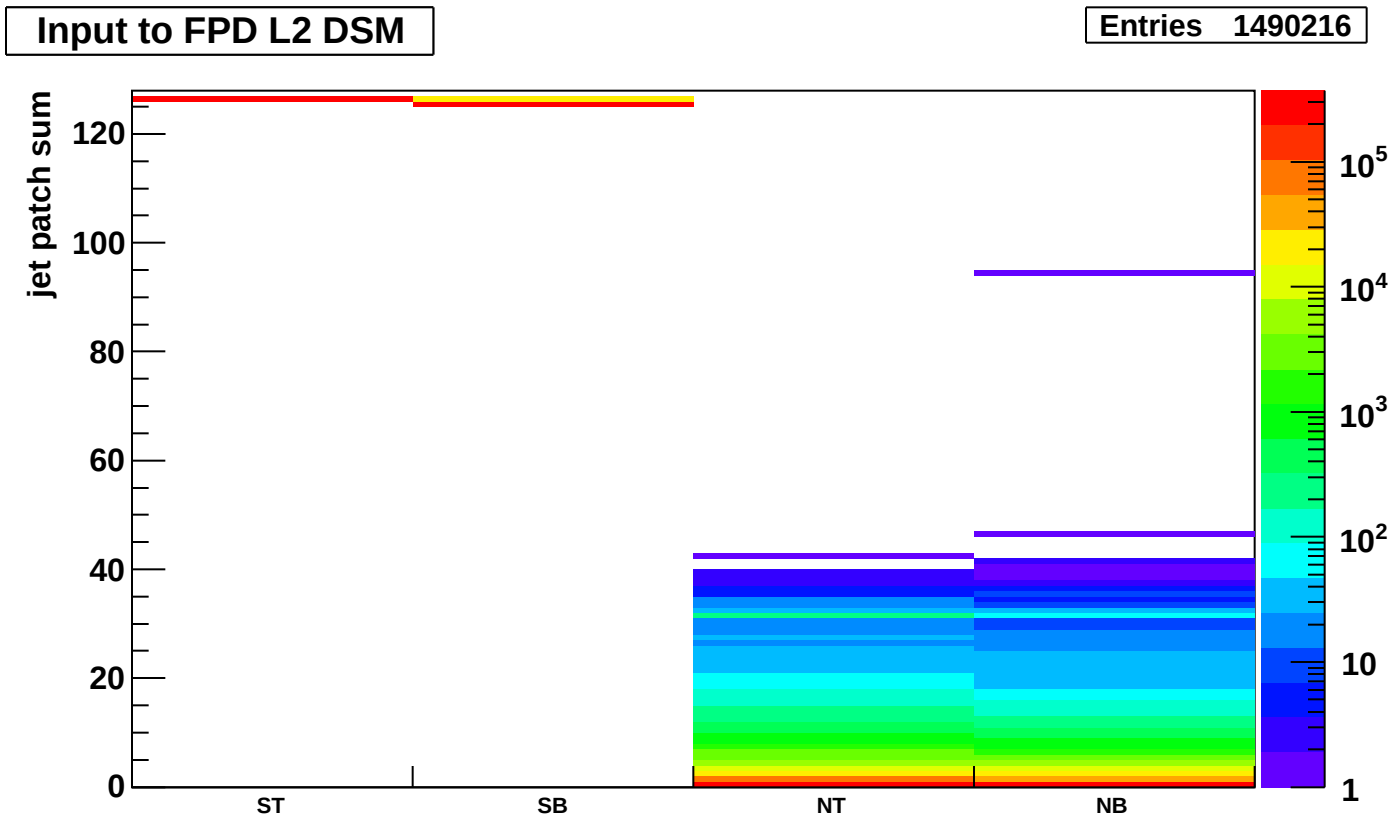

Input to FPD L2 DSM
Entries 748975
CL bits - simulated
372554
372554
351
336
3180
SMALL
LARGE-S
LARGE-N

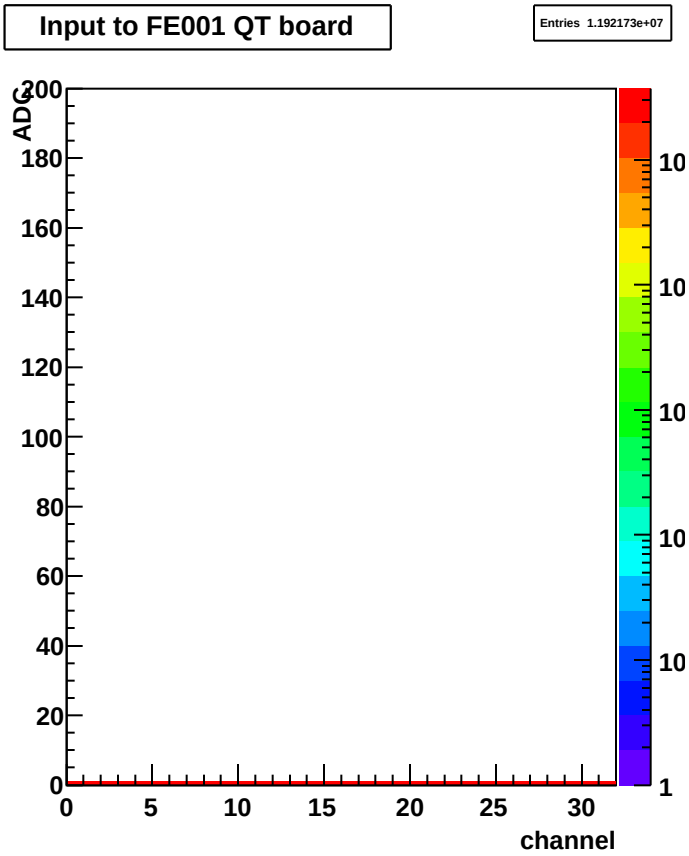

Input to FPD L2 DSM
Entries 1117662
HT bits
372554
372554
4798
367756
SMALL
LARGE-S
LARGE-N

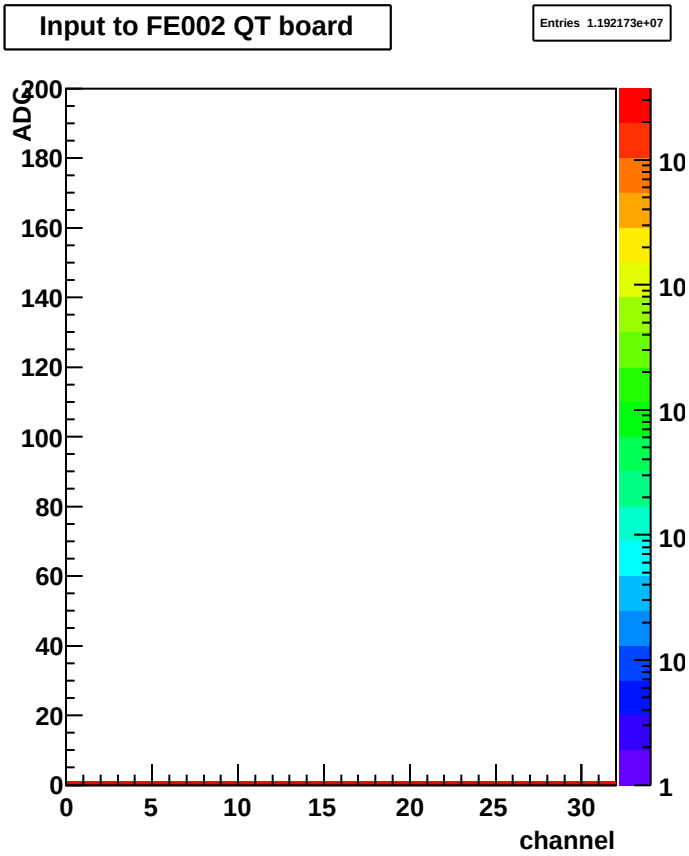

Input to FPD L2 DSM
Entries 749906
HT bits - simulated
372554
372554
4798
SMALL
LARGE-S
LARGE-N

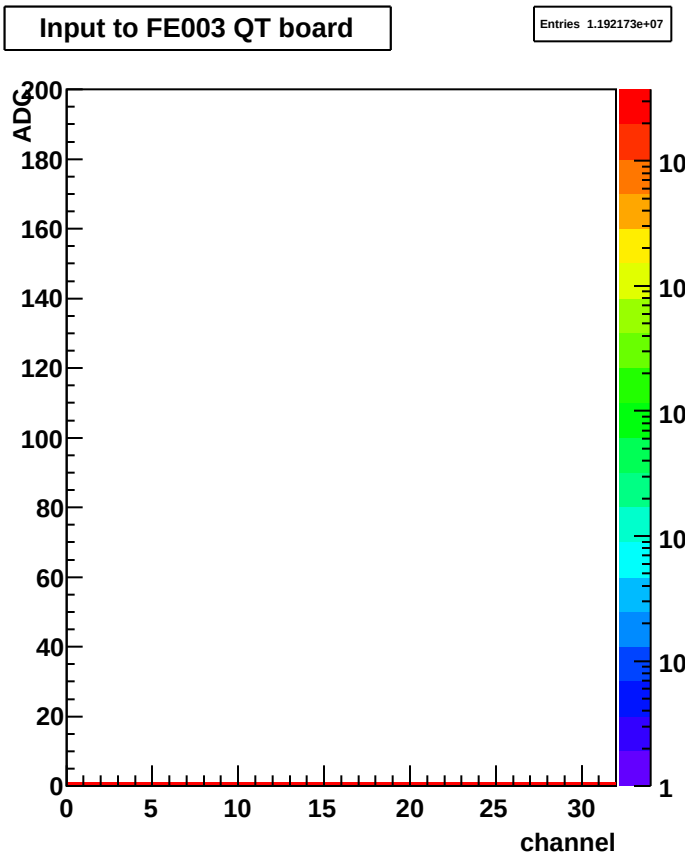

Input to FPD L3 DSM
Entries 5960864
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

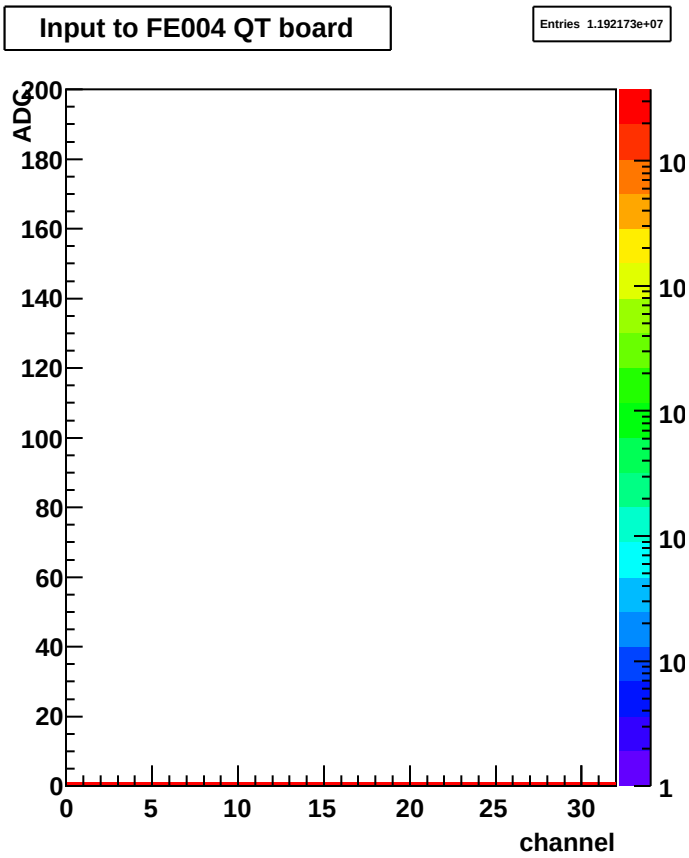

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

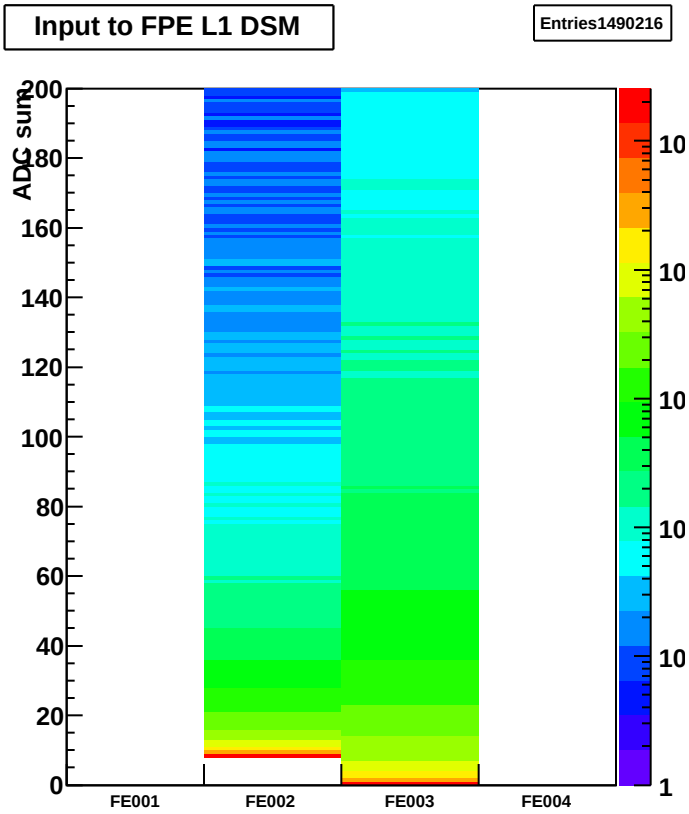

Input to FPD L2 DSM
Entries 1490216
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

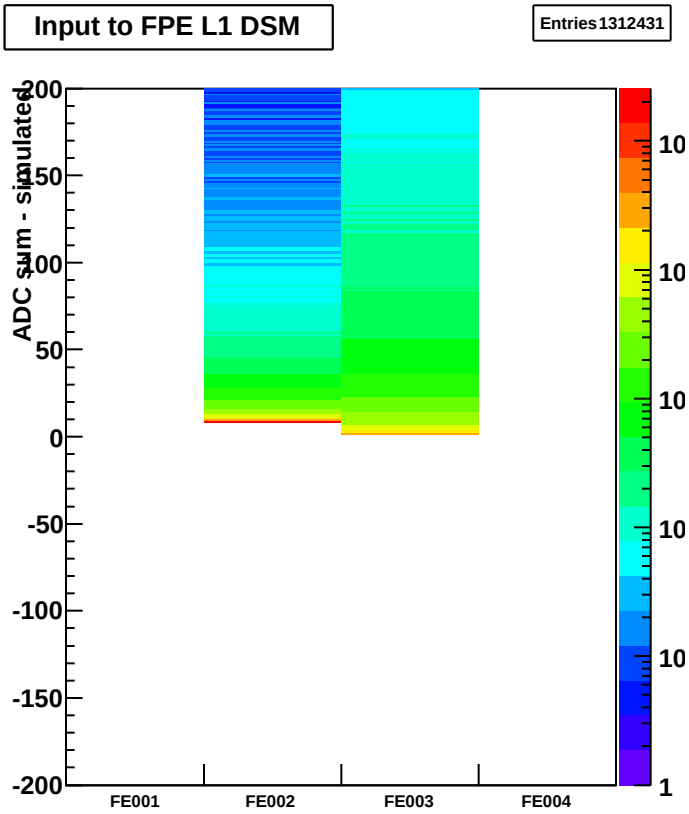

Input to FE001 QT board
Entries 1.192173e+07
ADC
channel

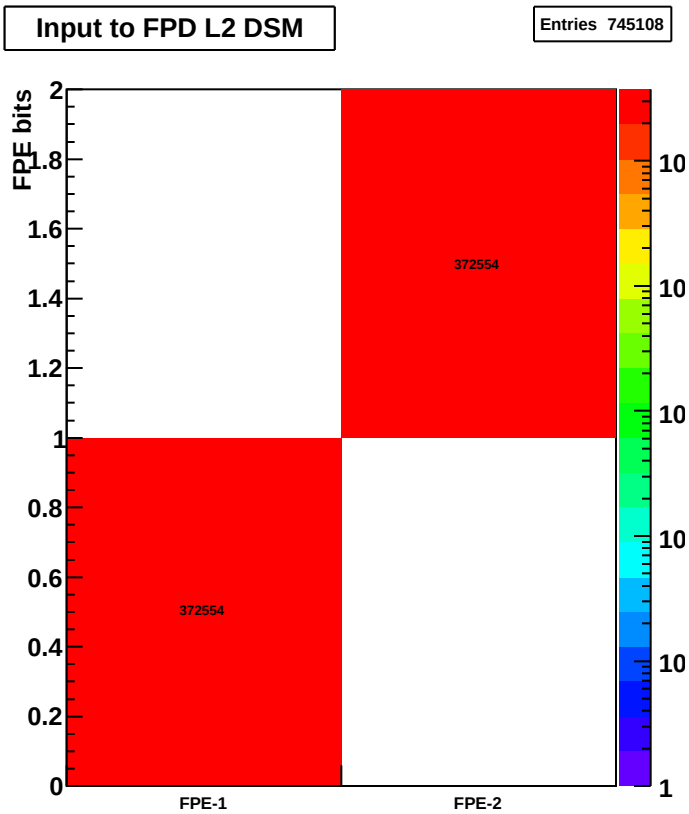

Input to FE002 QT board
Entries 1.192173e+07
ADC
channel

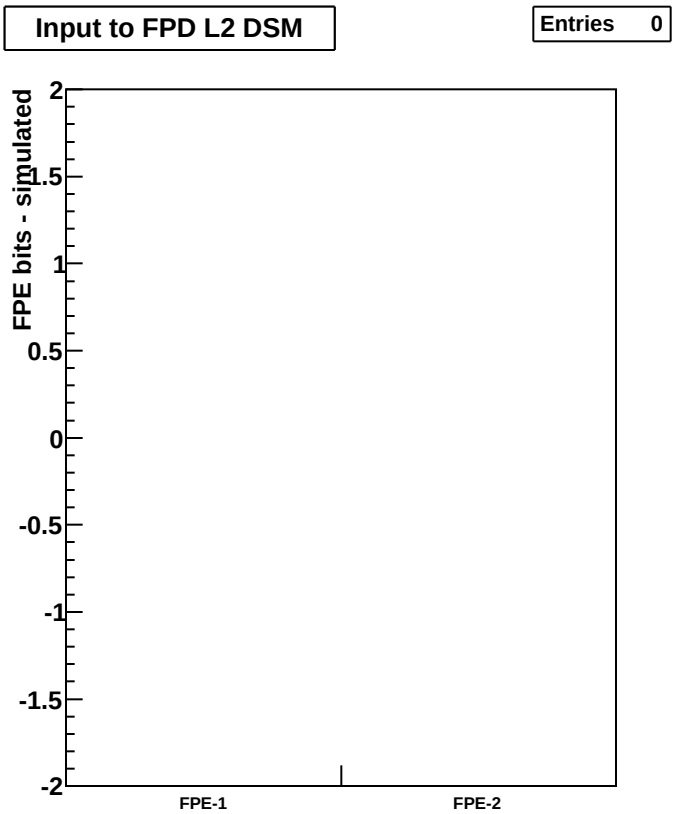

Input to FE003 QT board
Entries 1.192173e+07
ADC
channel


Input to FE004 QT board
Entries 1.192173e+07
ADC
channel


Input to FPE L1 DSM
Entries1490216
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1312431
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 745108
FPE bits
372554
372554
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2