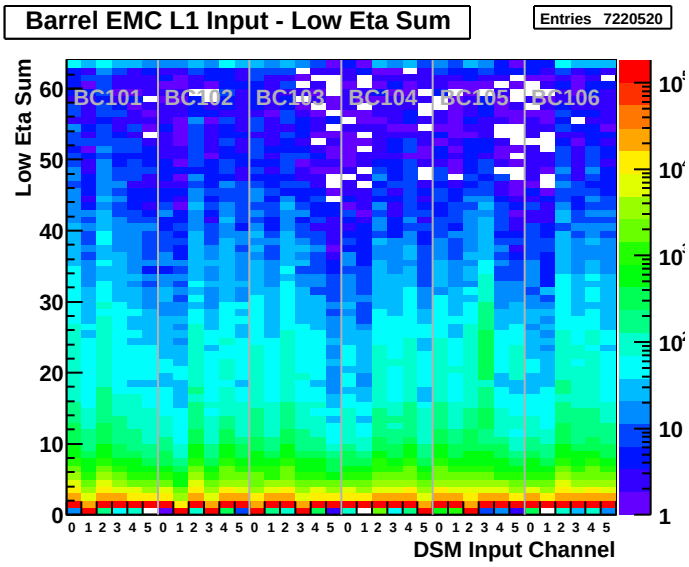
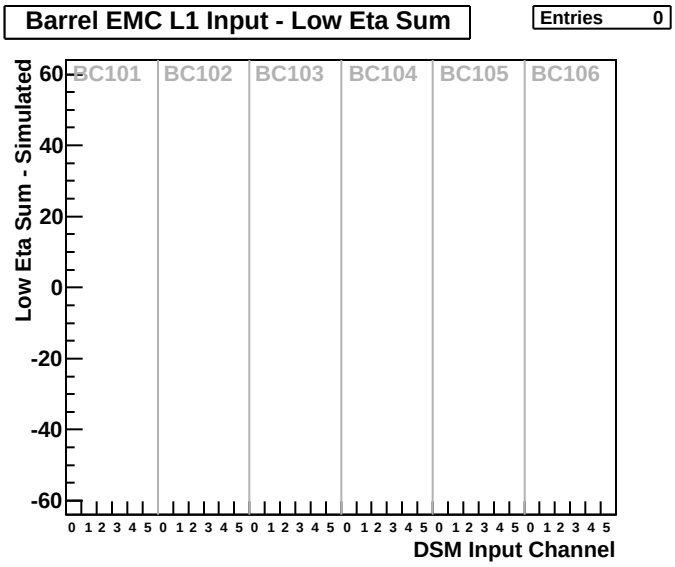
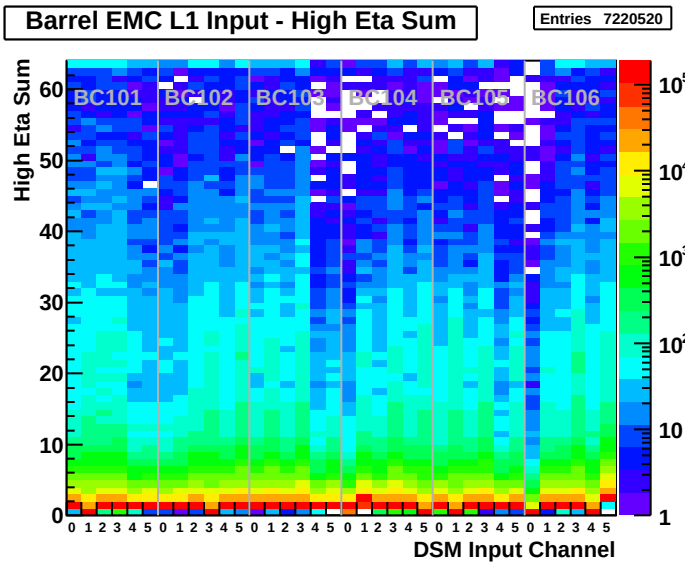
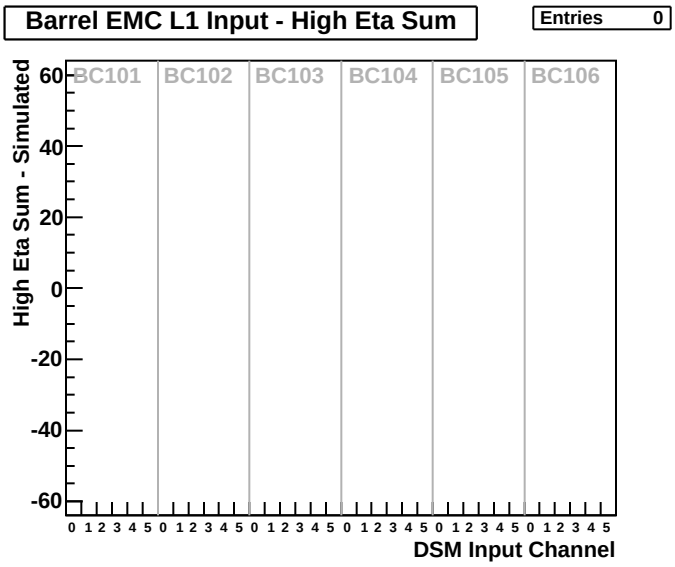
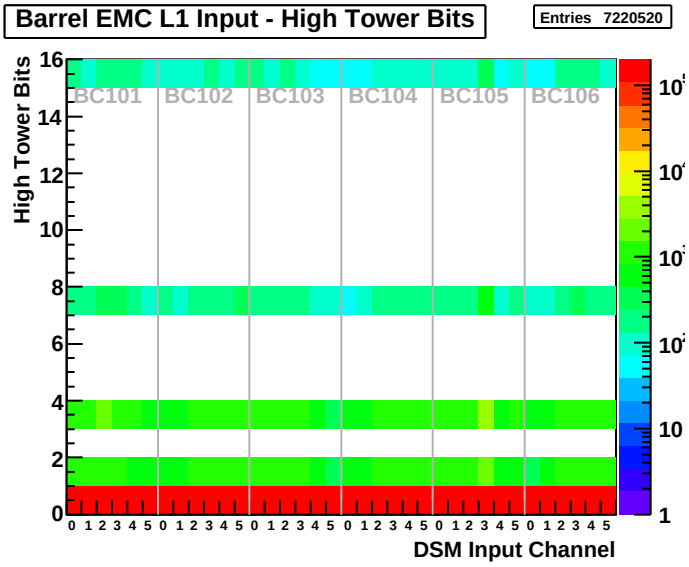
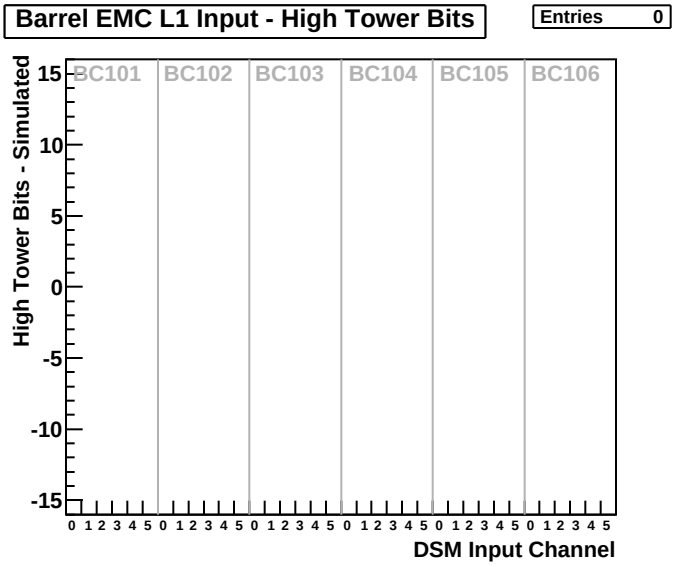

Barrel EMC L1 Input - Low Eta Sum
Entries 7220520
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

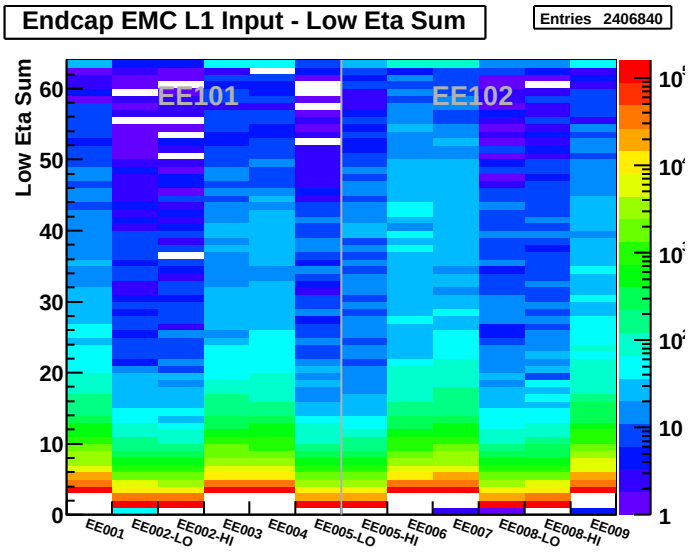

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

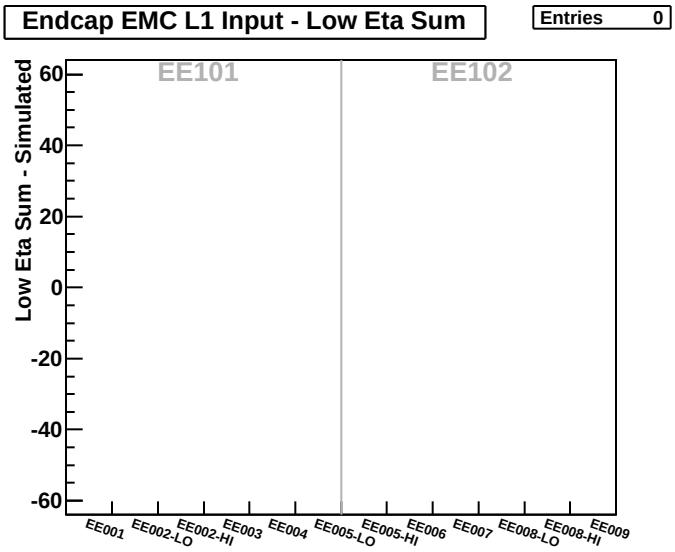

Barrel EMC L1 Input - High Eta Sum
Entries 7220520
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

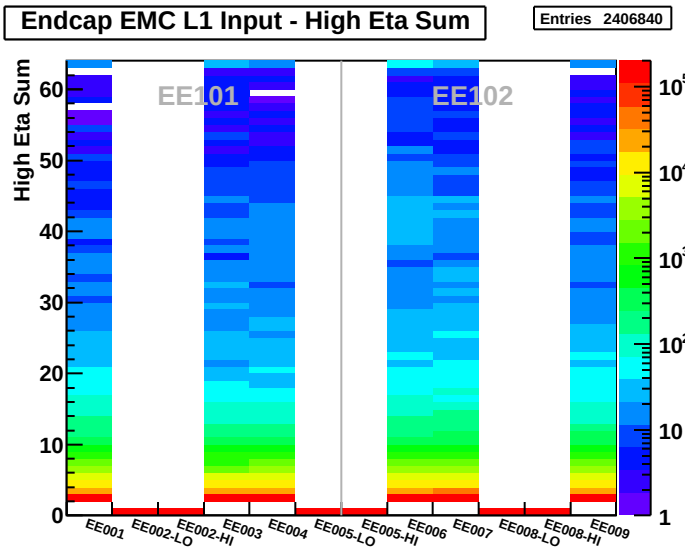

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

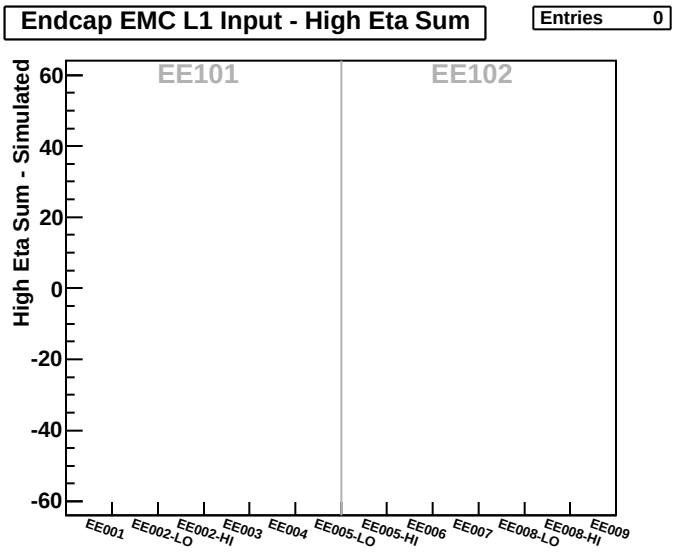

Barrel EMC L1 Input - High Tower Bits
Entries 7220520
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

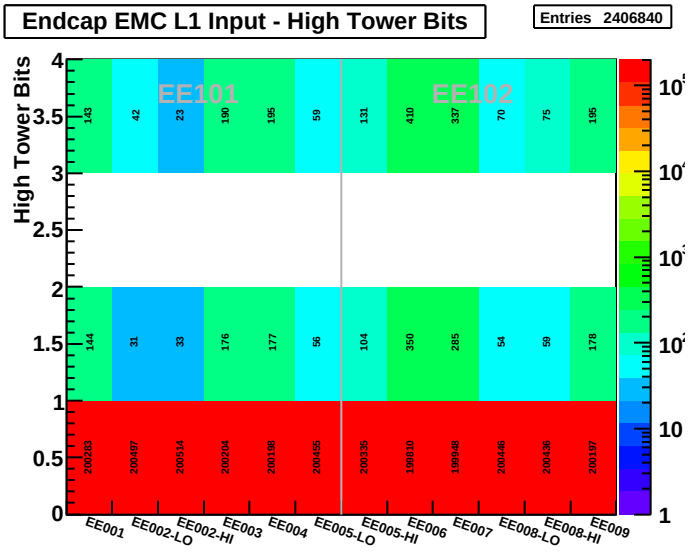

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

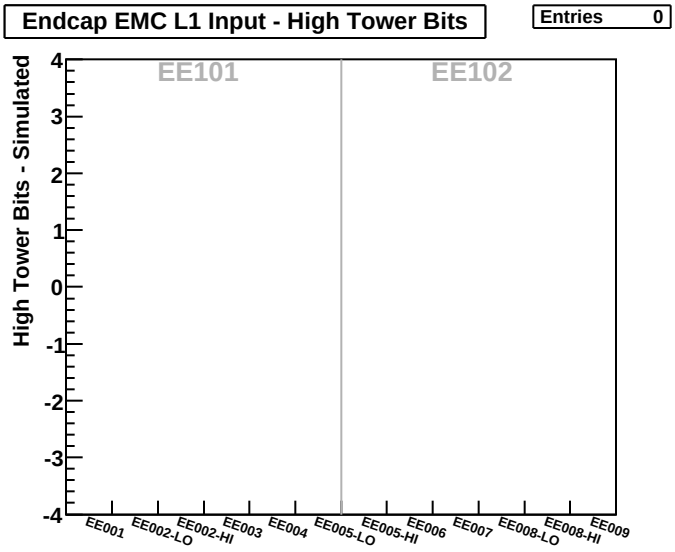

Endcap EMC L1 Input - Low Eta Sum
Entries 2406840
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

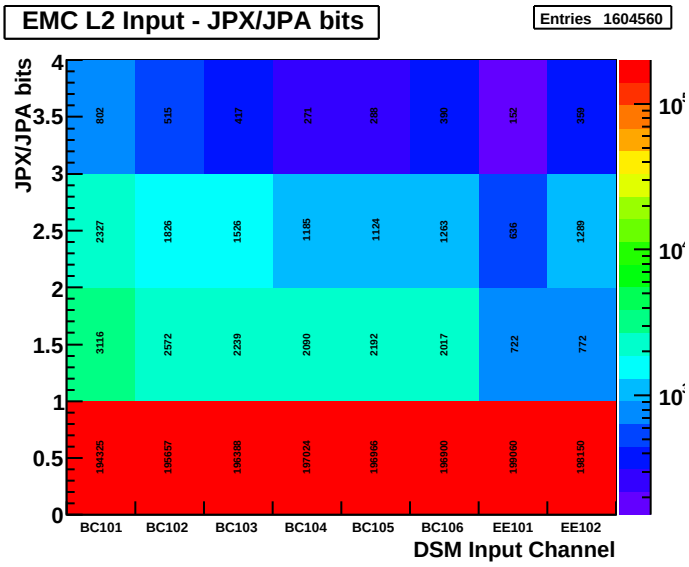

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

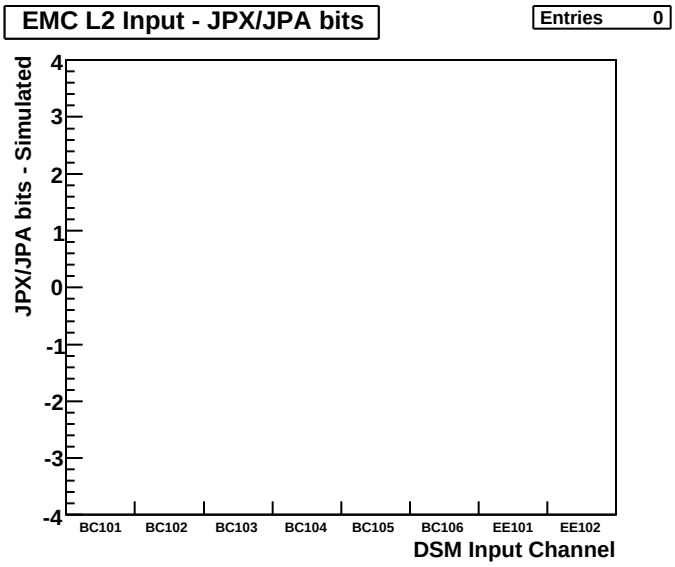

Endcap EMC L1 Input - High Eta Sum
Entries 2406840
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

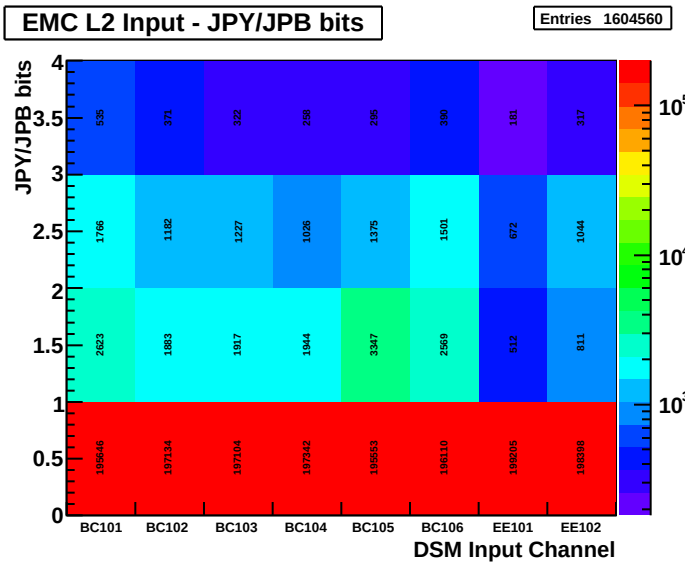

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

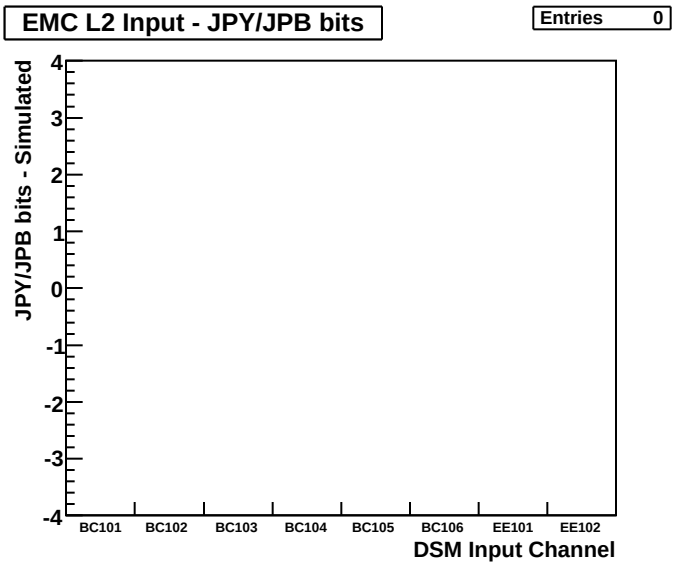

Endcap EMC L1 Input - High Tower Bits
Entries 2406840
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

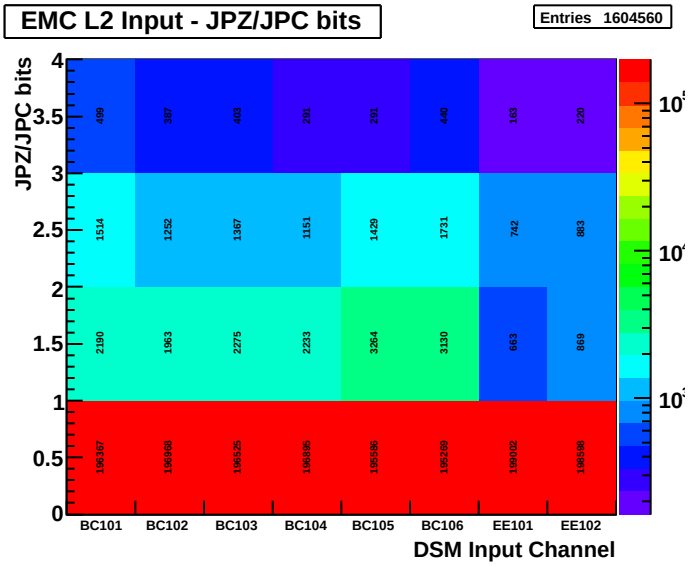

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

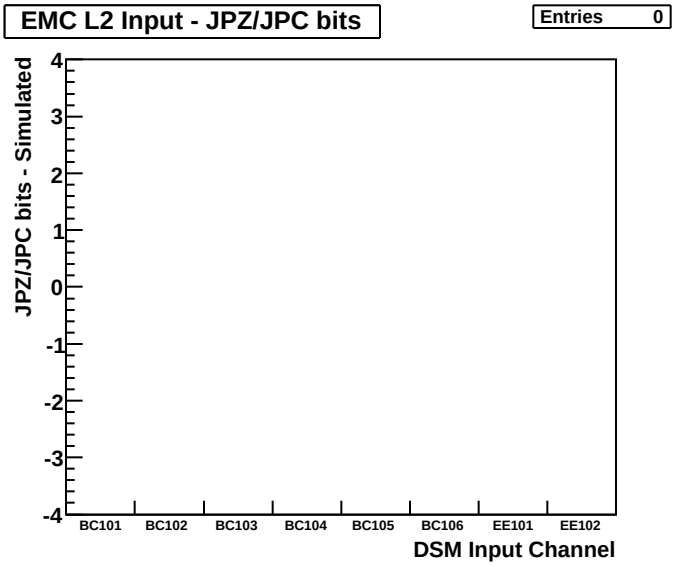

EMC L2 Input - JPX/JPA bits
Entries 1604560
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
802 515 417 271 288 390 152 359
2327 1826 1526 1185 1124 1263 636 1289
3116 2572 2239 2090 2192 2017 722 772
194325 195657 196388 197024 196966 196900 199060 198150

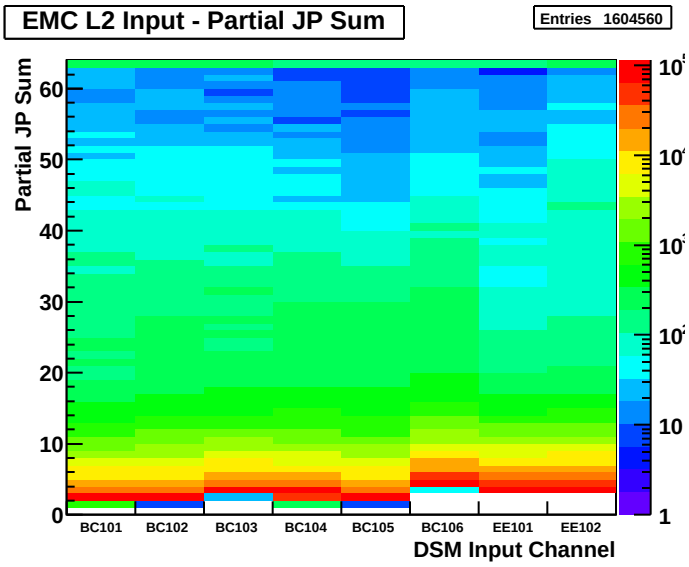

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

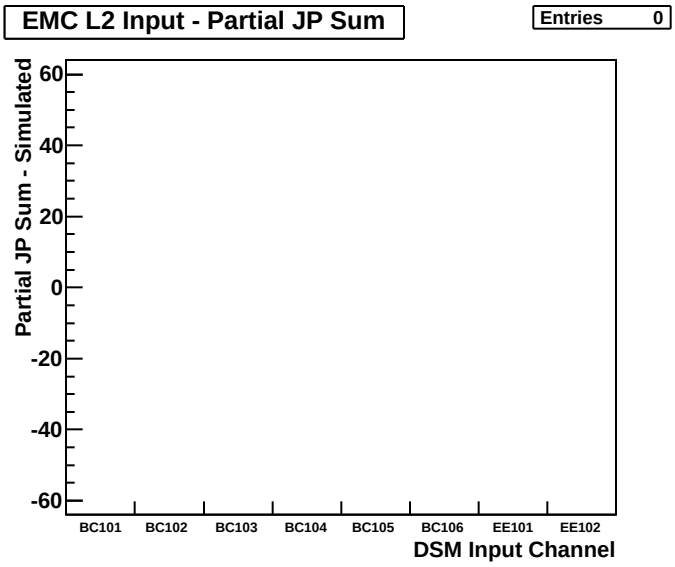

EMC L2 Input - JPY/JPB bits
Entries 1604560
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
535 371 322 258 295 390 181 317
1766 1182 1227 1026 1375 1501 672 1044
2623 1983 1917 1944 3347 2569 512 811
195646 197134 197104 197342 195553 196110 199205 198398

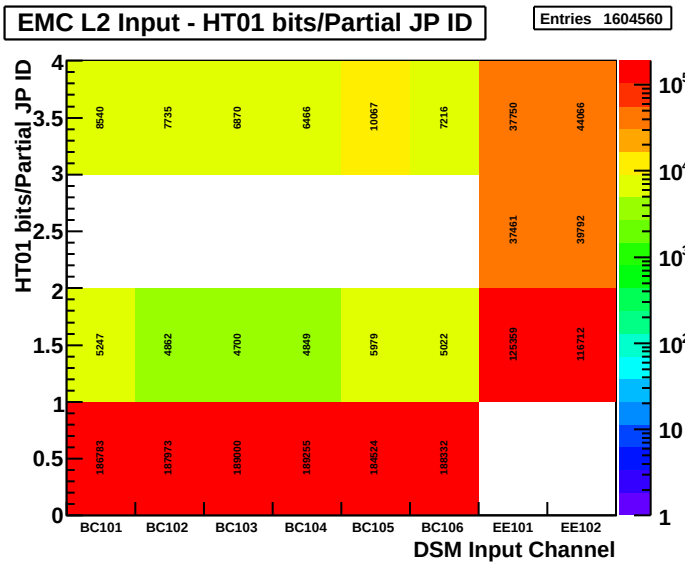

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

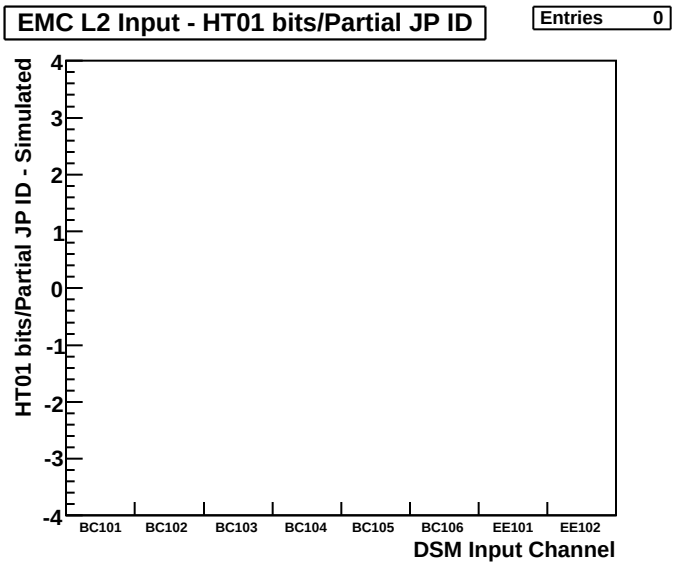

EMC L2 Input - JPZ/JPC bits
Entries 1604560
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
499 387 403 291 291 440 163 220
1514 1252 1367 1151 1429 1731 742 883
2190 1963 2275 2233 3264 3130 663 869
196367 196368 196525 196895 195588 195269 199002 198598

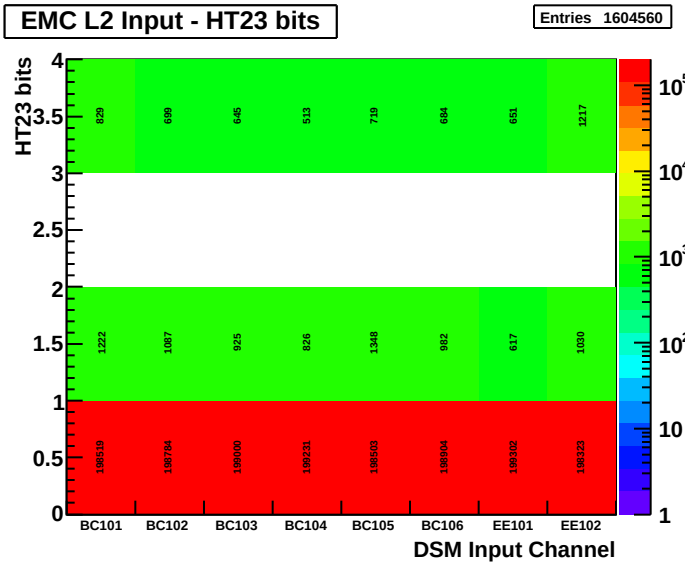

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

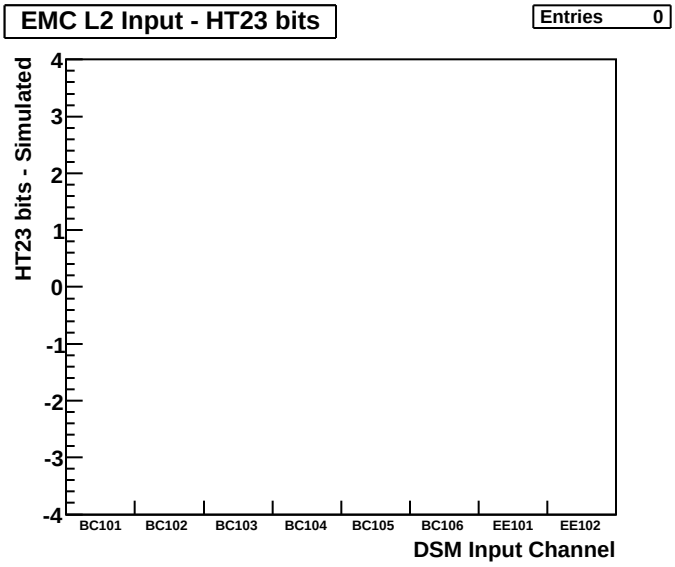

EMC L2 Input - Partial JP Sum
Entries 1604560
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

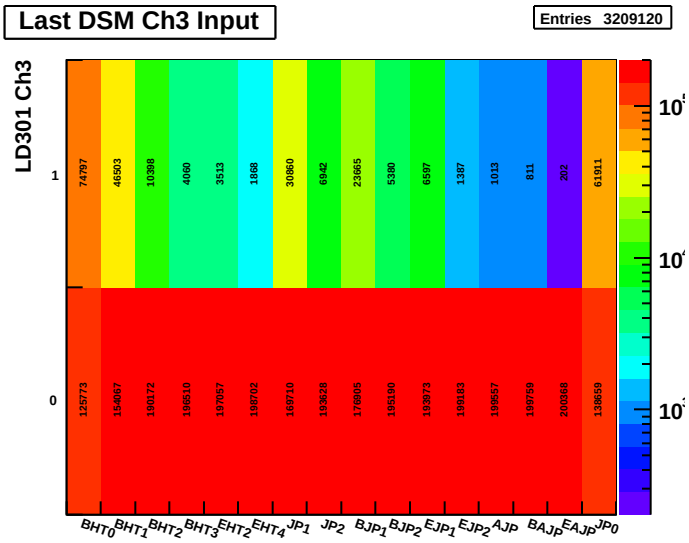

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

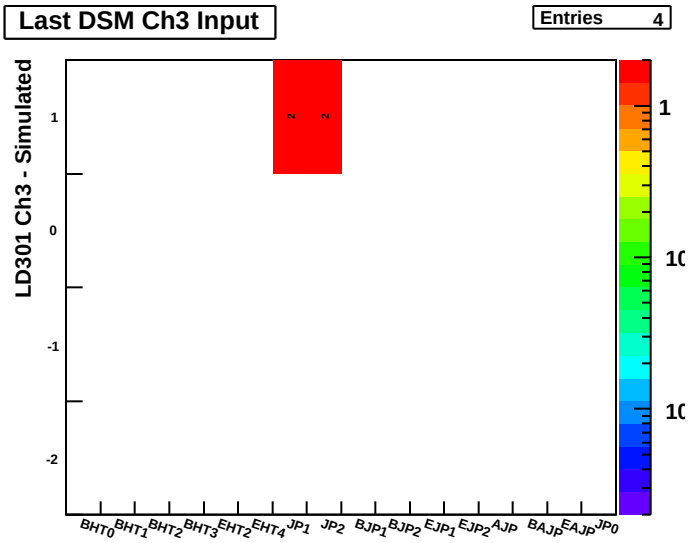

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1604560
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

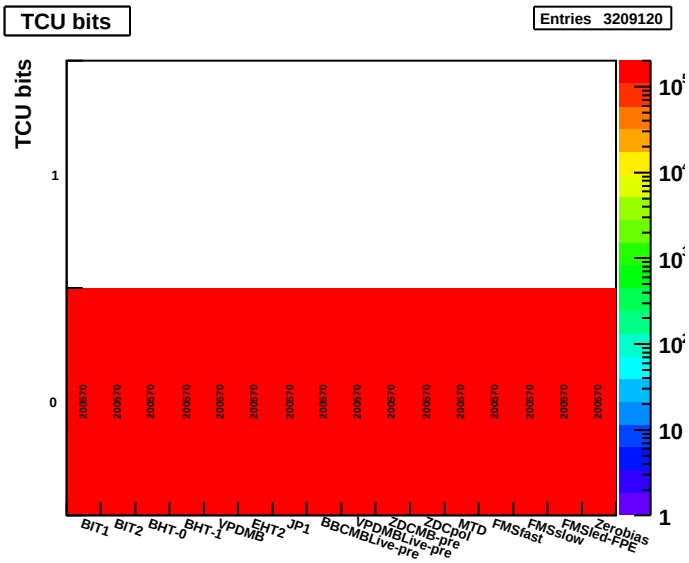

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

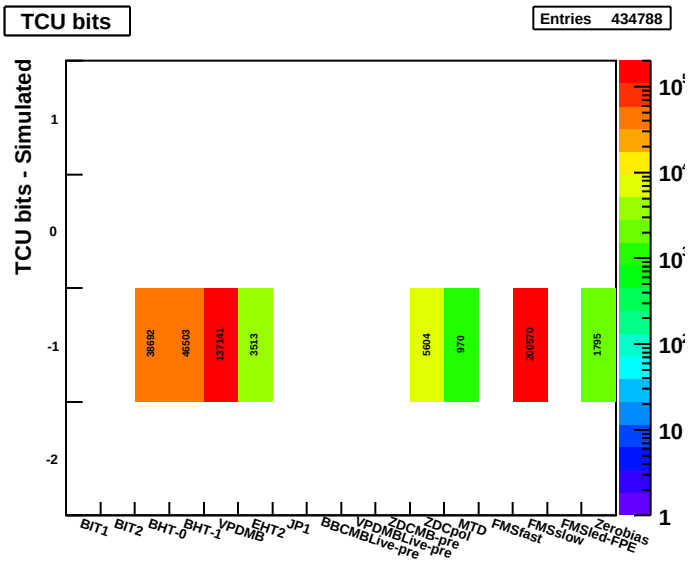

EMC L2 Input - HT23 bits
Entries 1604560
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

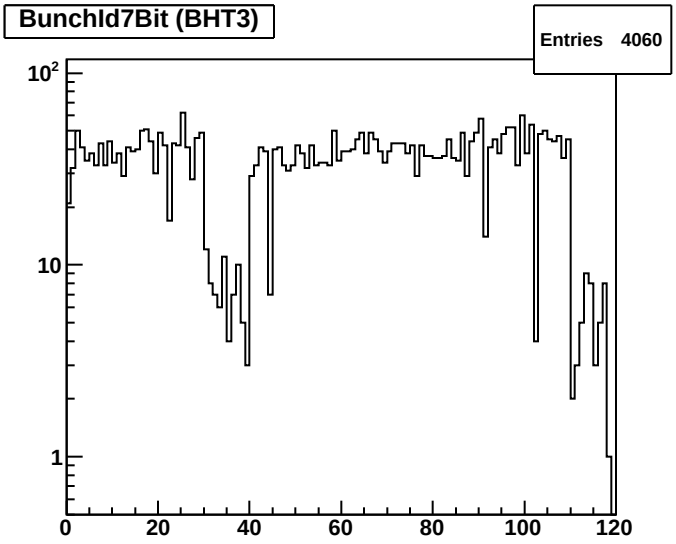

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

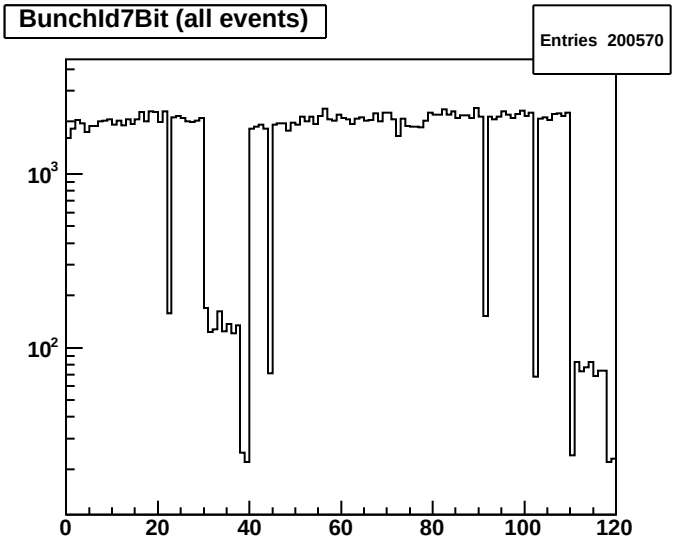

Last DSM Ch3 Input
Entries 3209120
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

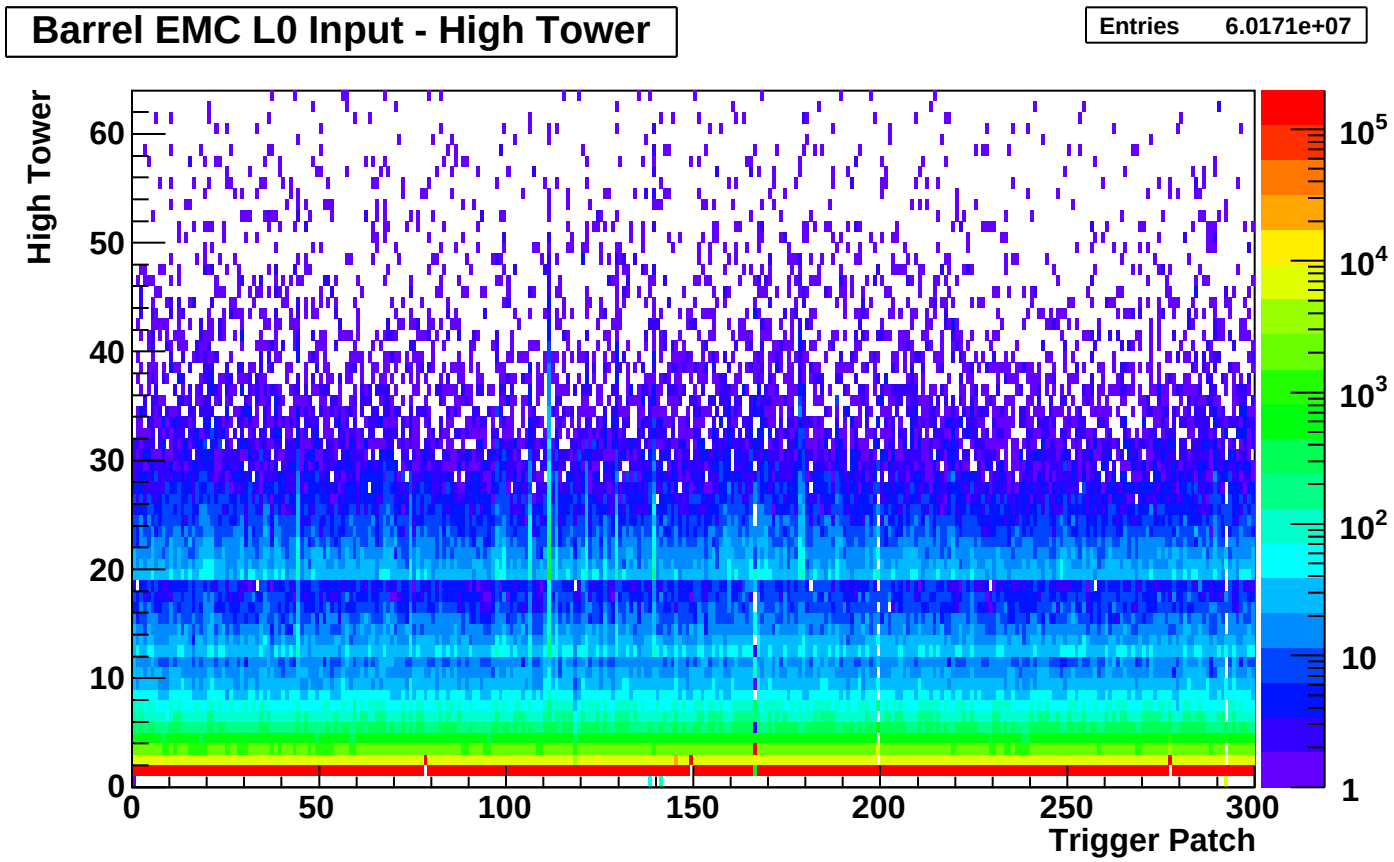

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

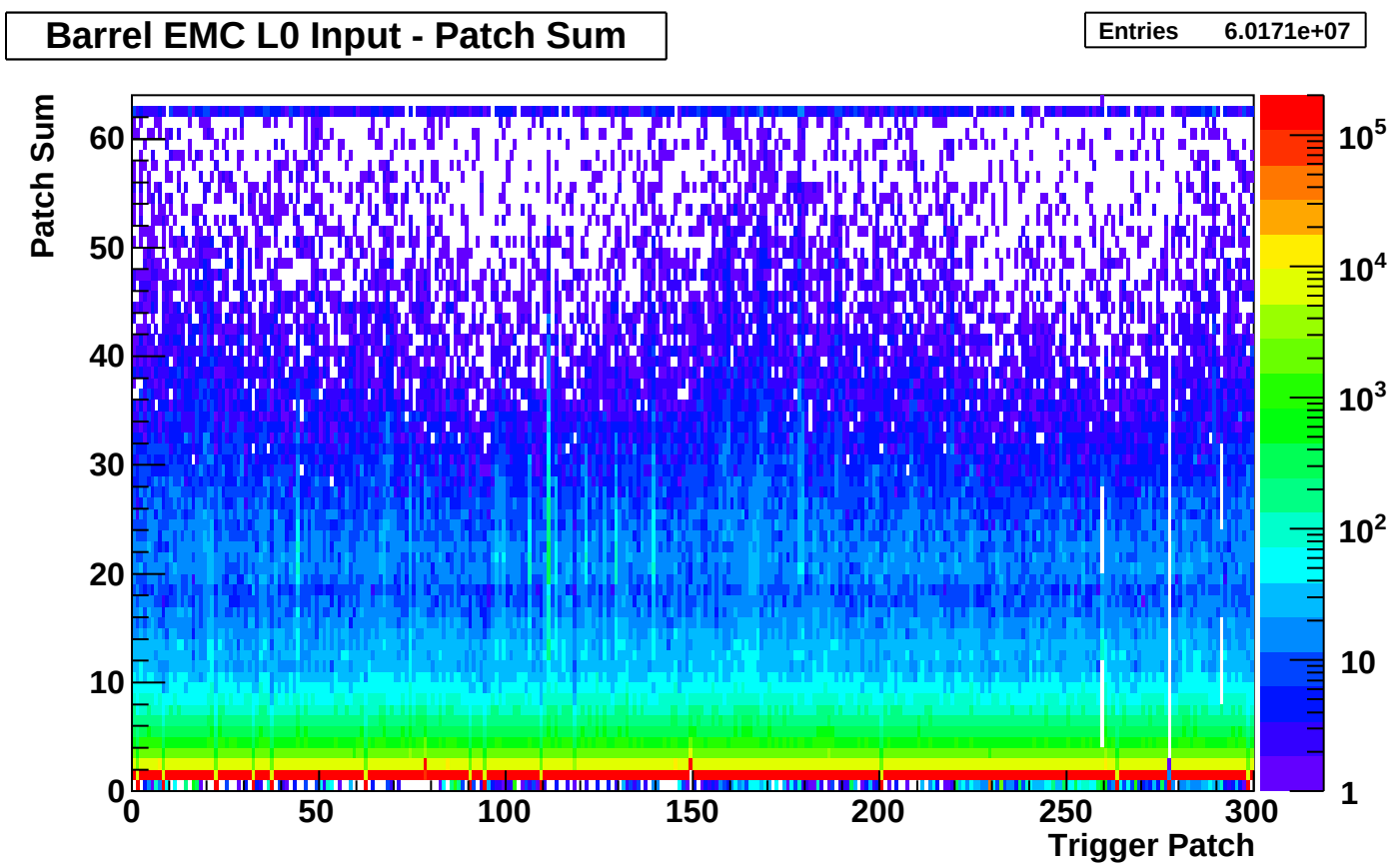

TCU bits
Entries 3209120
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

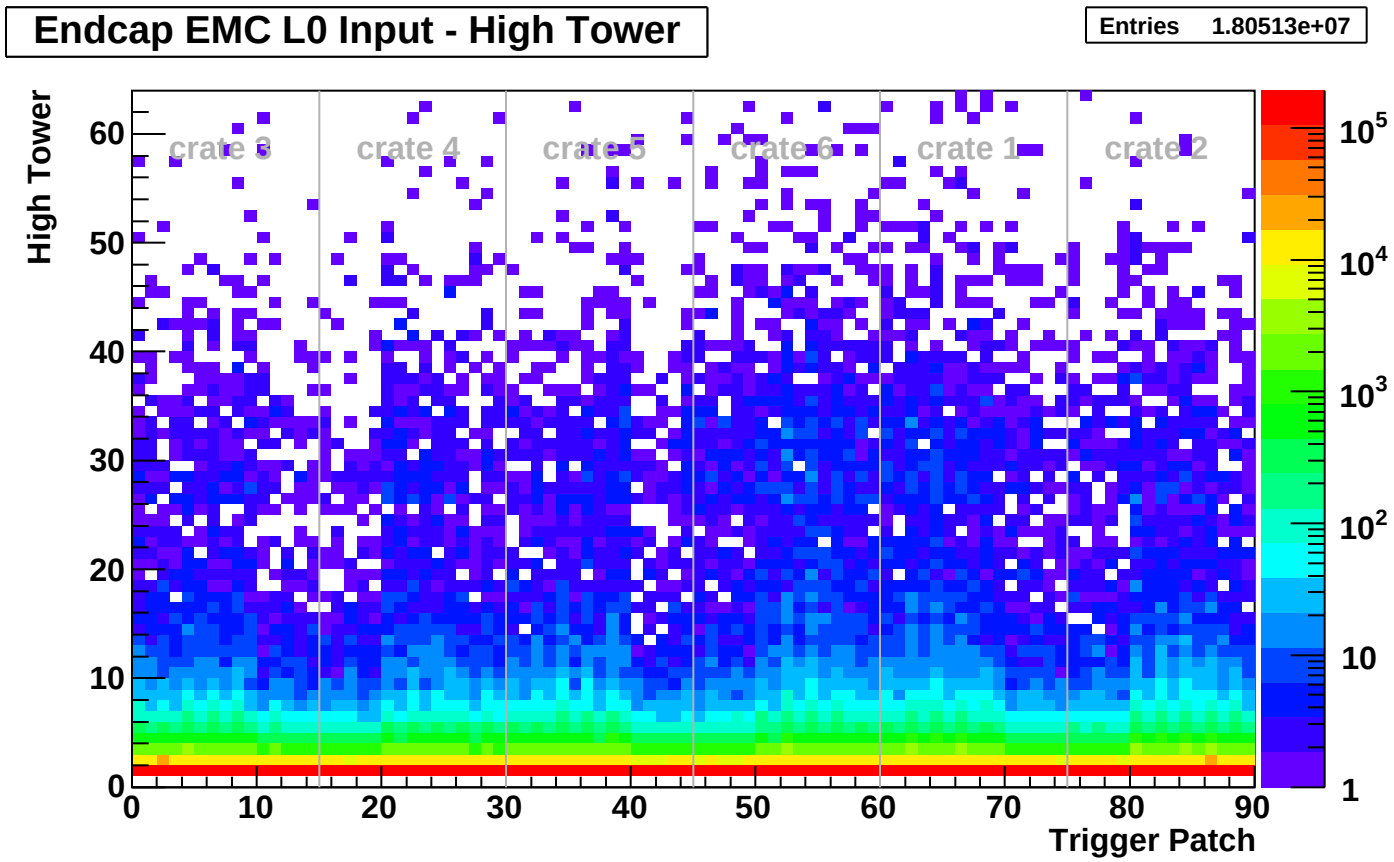

TCU bits
Entries 434788
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

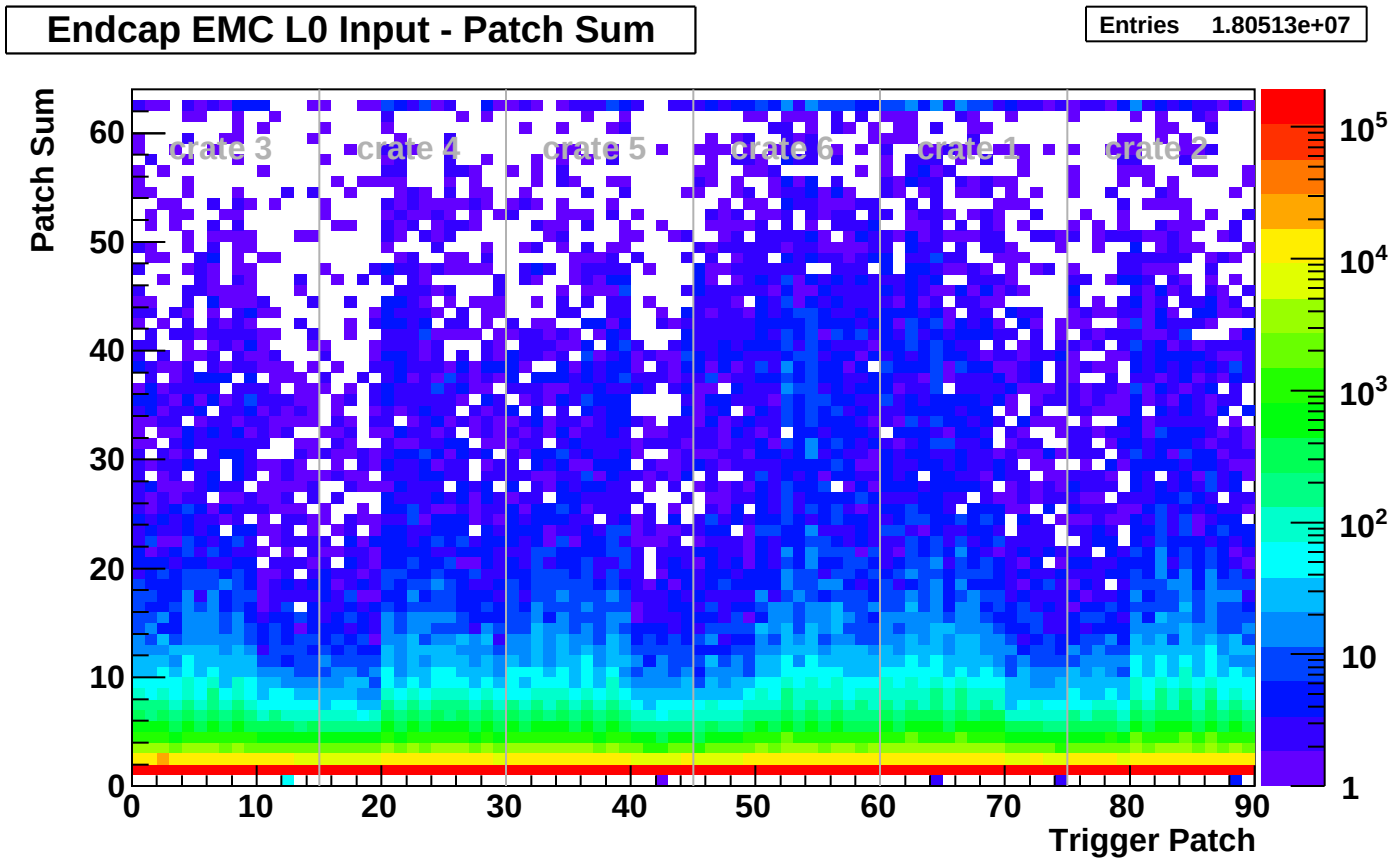

BunchId7Bit (BHT3)
Entries 4060

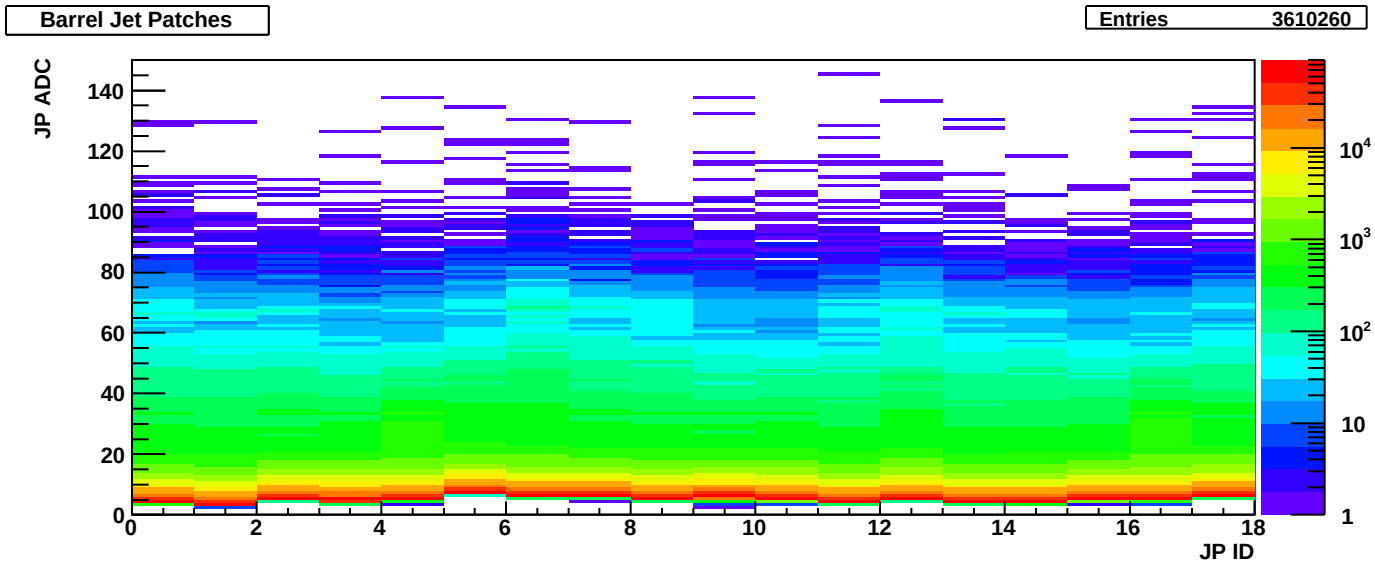

BunchId7Bit (all events)
Entries 200570

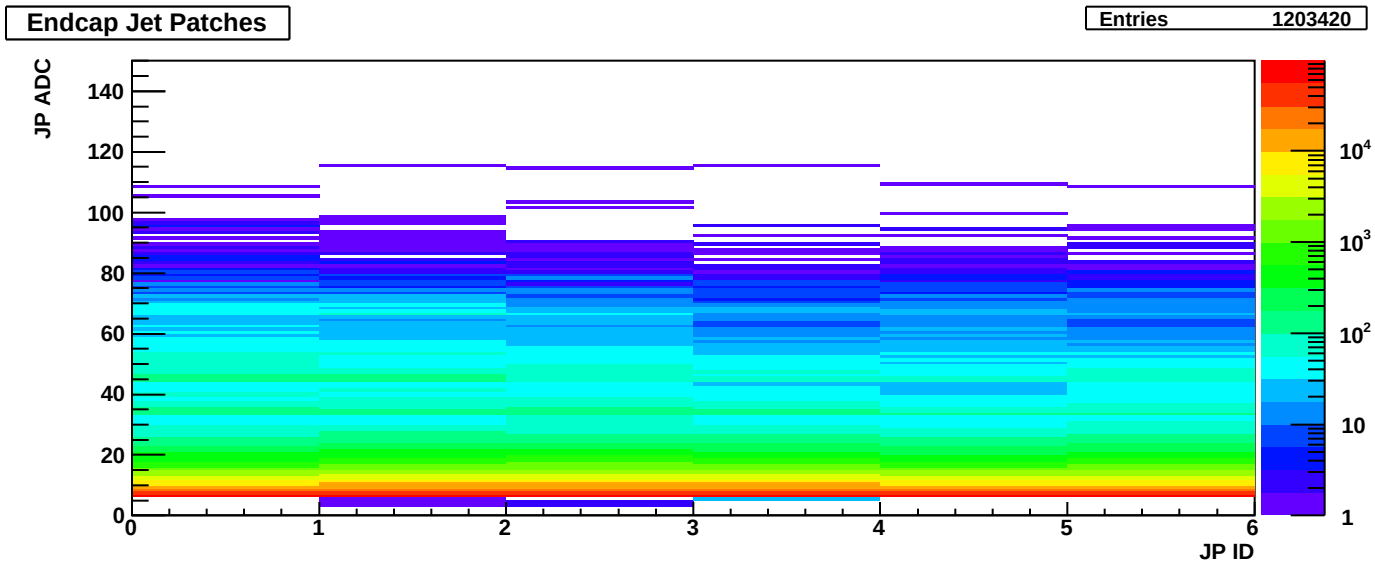

Barrel EMC L0 Input - High Tower
Entries 6.0171e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

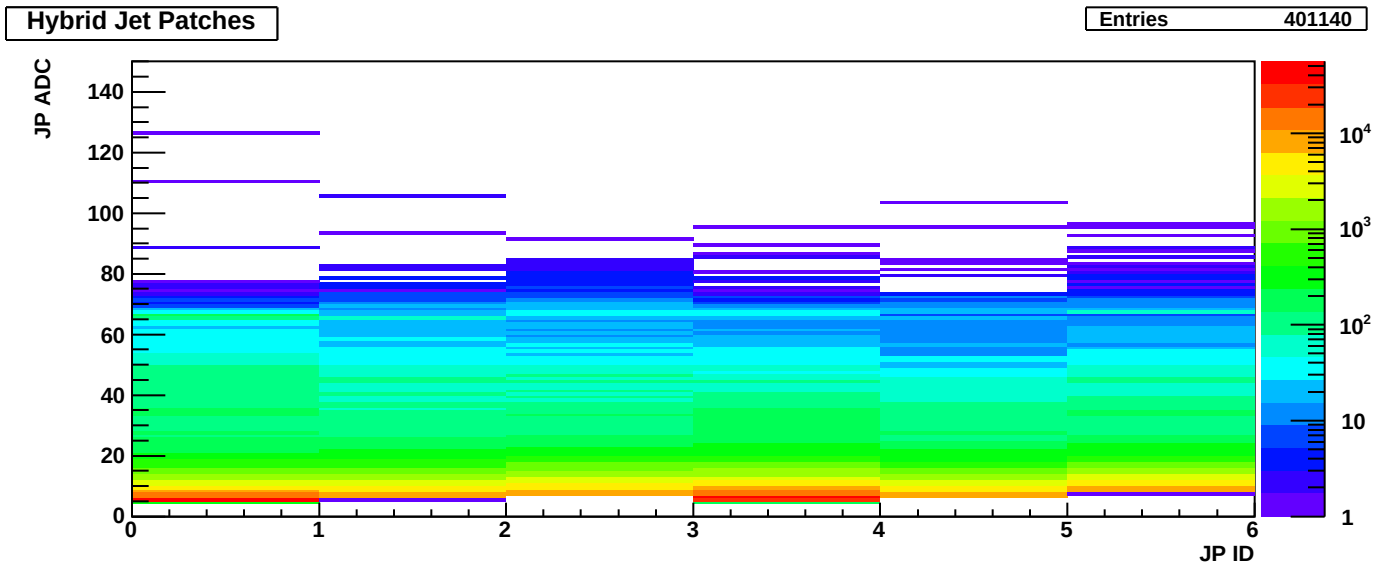

Barrel EMC L0 Input - Patch Sum
Entries 6.0171e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

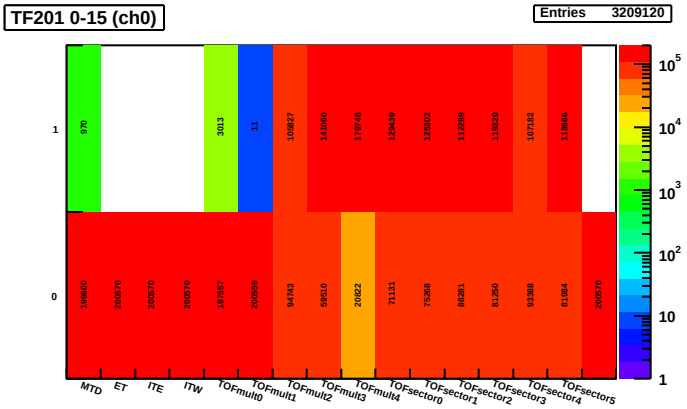

Endcap EMC L0 Input - High Tower
Entries 1.80513e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

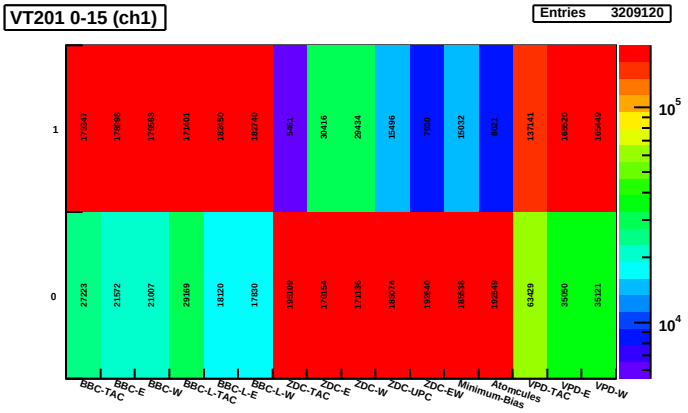

Endcap EMC L0 Input - Patch Sum
Entries 1.80513e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

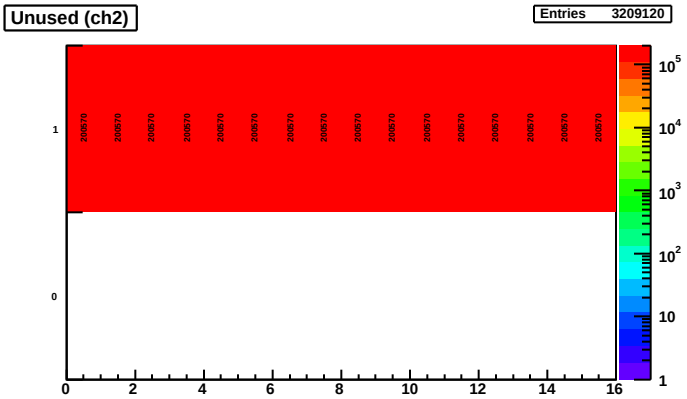

Barrel Jet Patches
Entries 3610260
JP ADC
JP ID

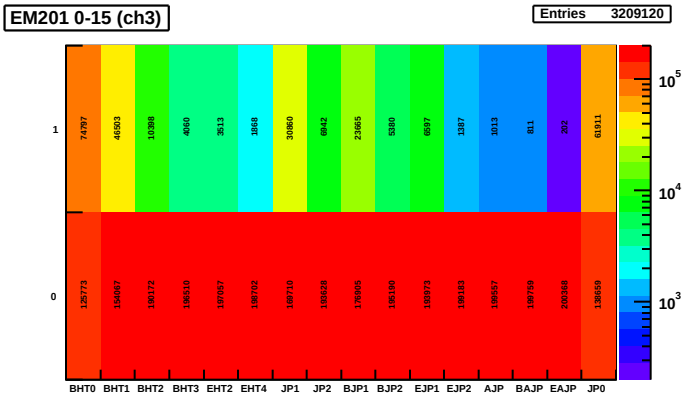

Endcap Jet Patches
Entries 1203420
JP ADC
JP ID

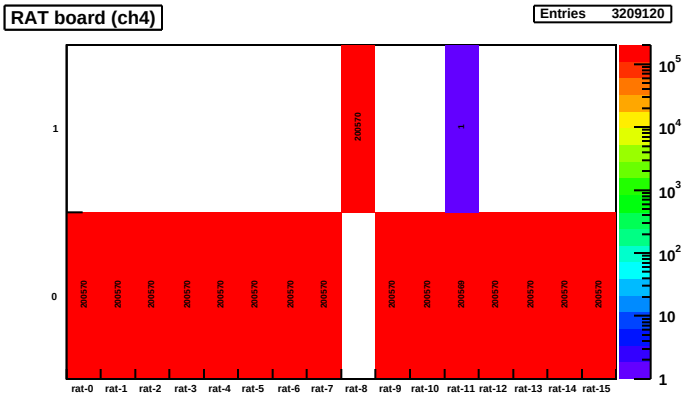

Hybrid Jet Patches
Entries 401140
JP ADC
JP ID

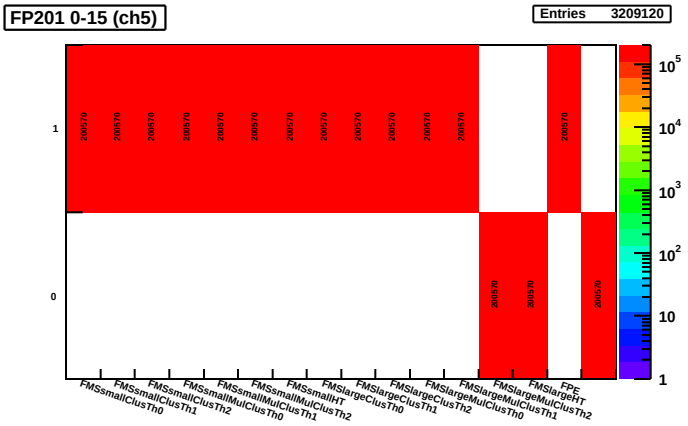

TF201 0-15 (ch0)
Entries 3209120
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

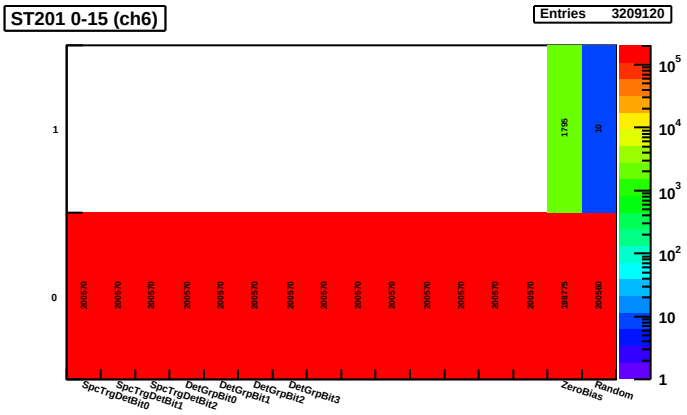

VT201 0-15 (ch1)
Entries 3209120
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

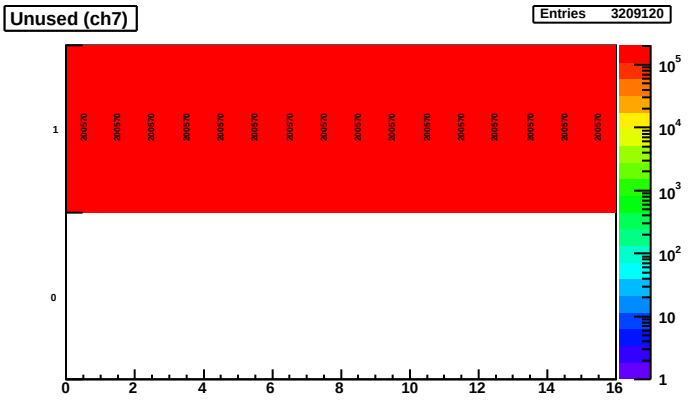

Unused (ch2)
Entries 3209120

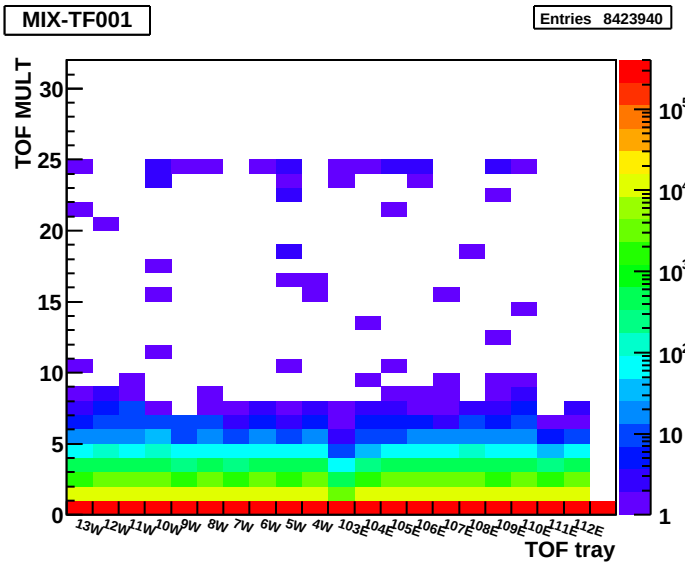

EM201 0-15 (ch3)
Entries 3209120
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

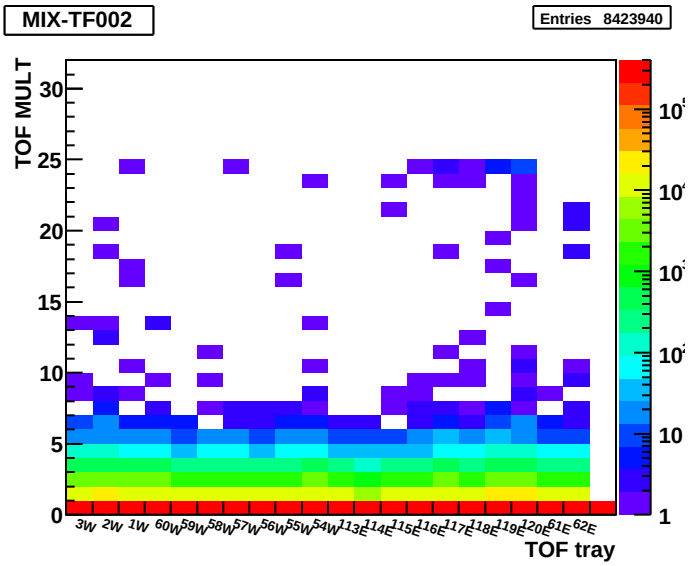

RAT board (ch4)
Entries 3209120
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

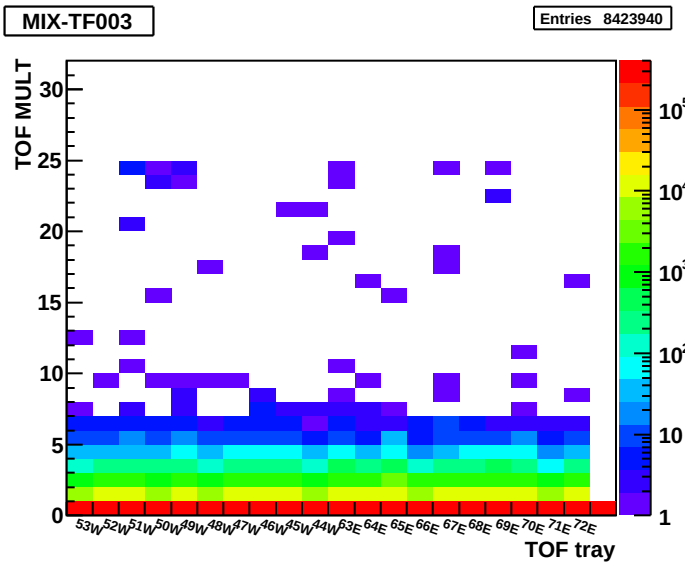

FP201 0-15 (ch5)
Entries 3209120
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

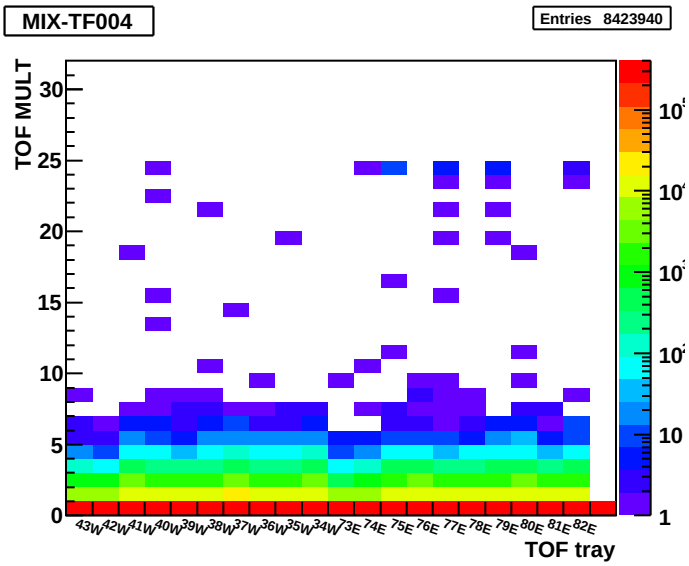

ST201 0-15 (ch6)
Entries 3209120
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

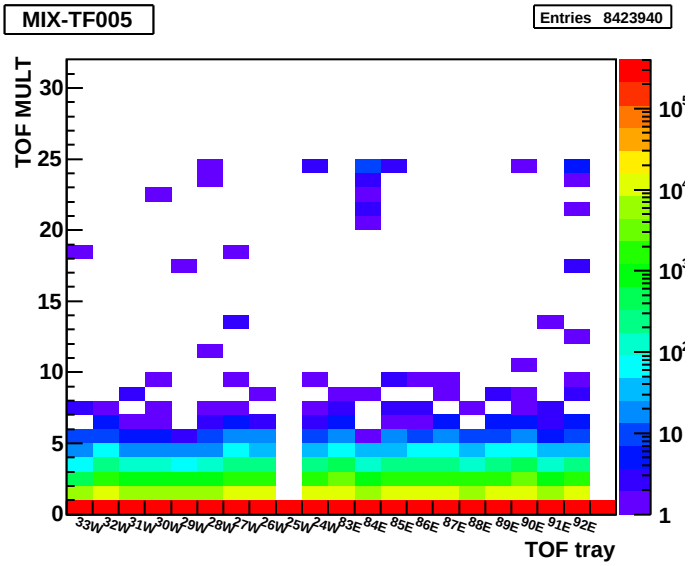

Unused (ch7)
Entries 3209120

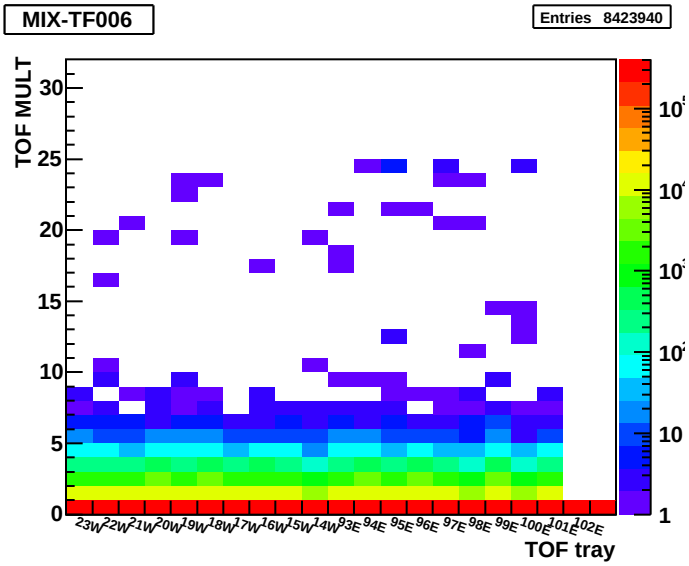

MIX-TF001
Entries 8423940
TOF MULT
TOF tray

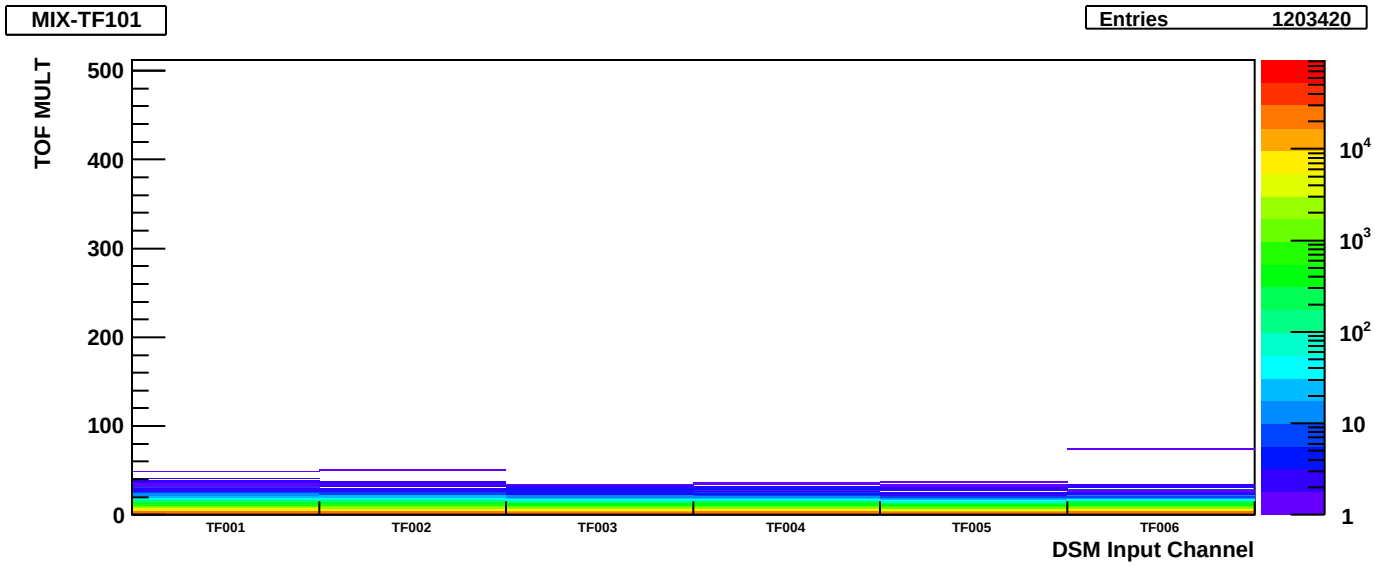

MIX-TF002
Entries 8423940
TOF MULT
TOF tray

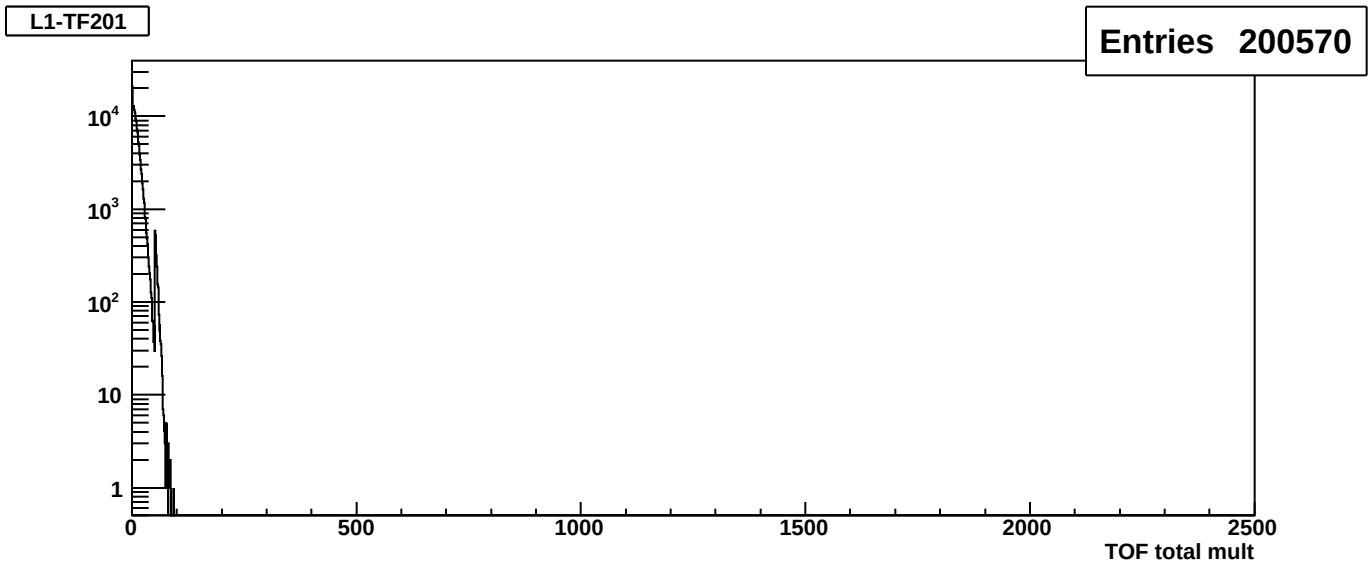

MIX-TF003
Entries 8423940
TOF MULT
TOF tray

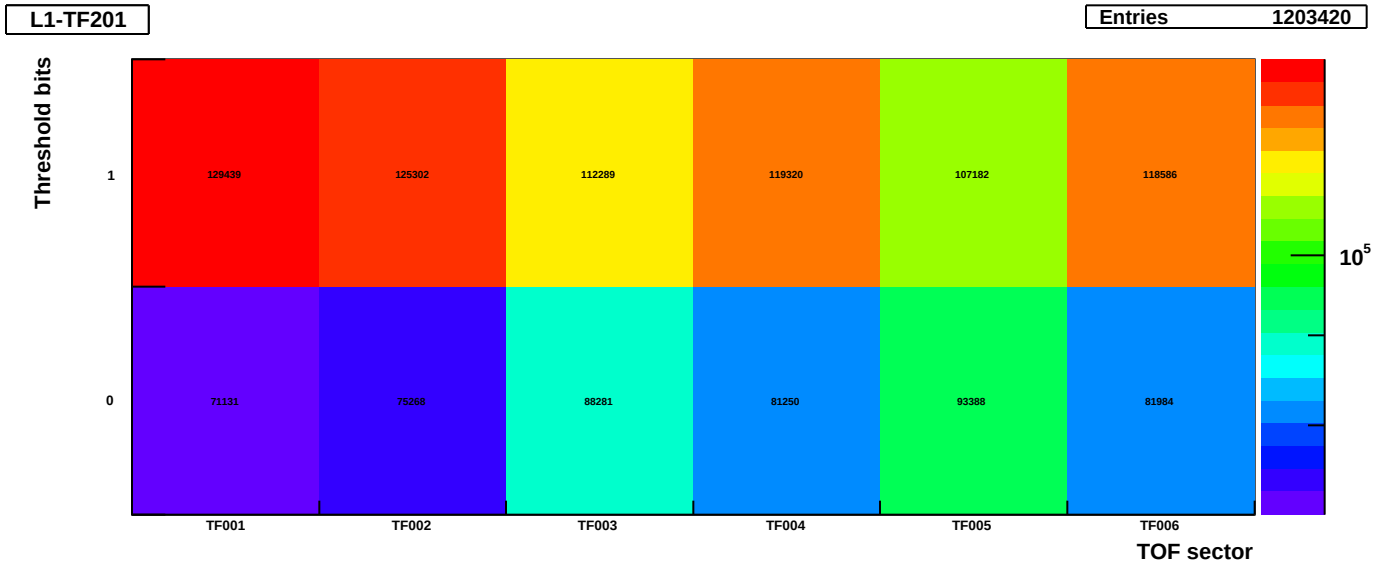

MIX-TF004
Entries 8423940
TOF MULT
TOF tray

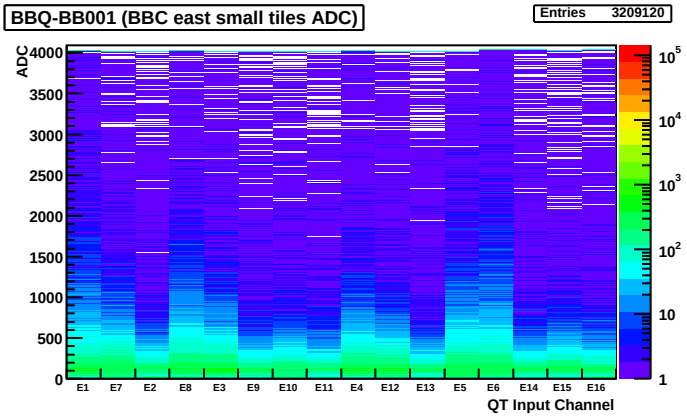

MIX-TF005
Entries 8423940
TOF MULT
TOF tray

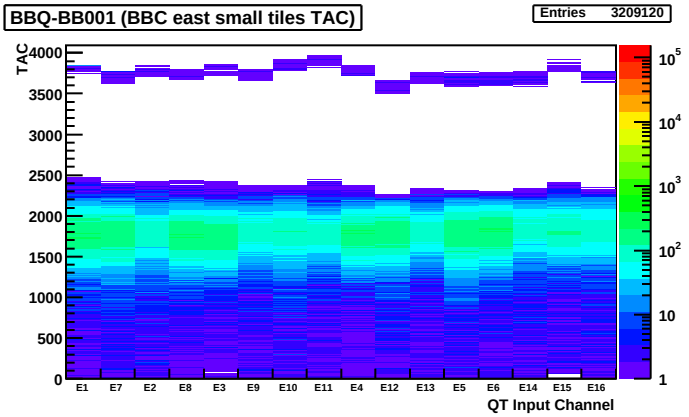

MIX-TF006
Entries 8423940
TOF MULT
TOF tray

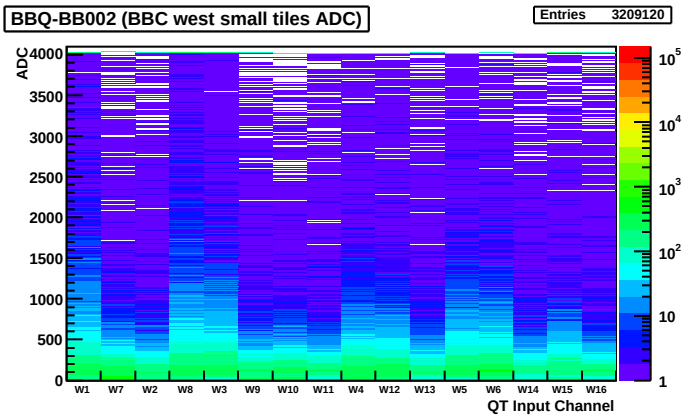

MIX-TF101
Entries 1203420
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

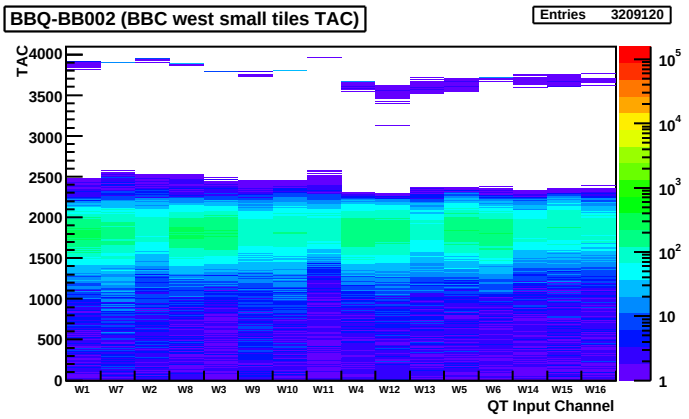

L1-TF201
Entries 200570
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

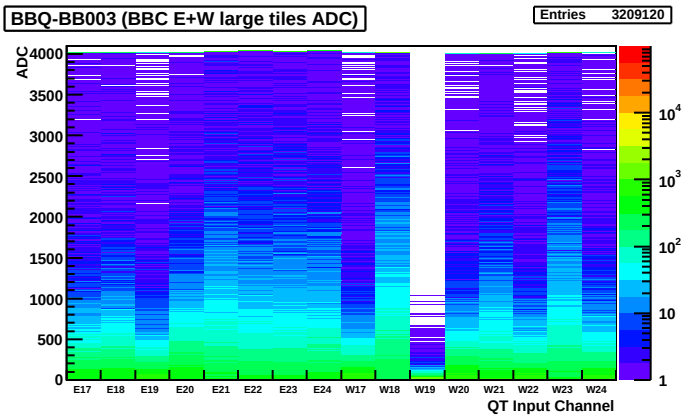

L1-TF201
Entries 1203420
Threshold bits
1
0
129439
125302
112289
119320
107182
118586
71131
75268
88281
81250
93388
81984
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

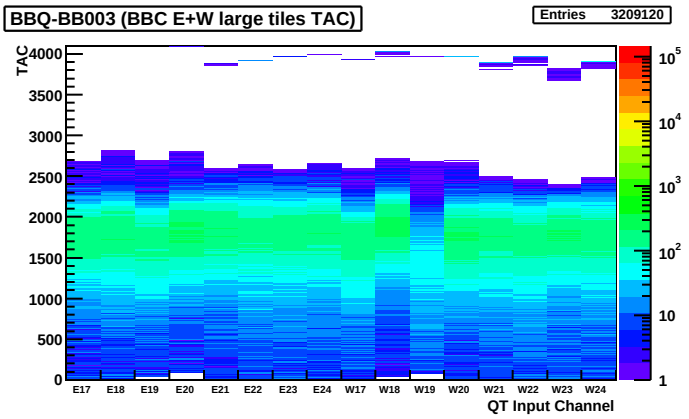

BBQ-BB001 (BBC east small tiles ADC)
Entries 3209120
ADC
QT Input Channel

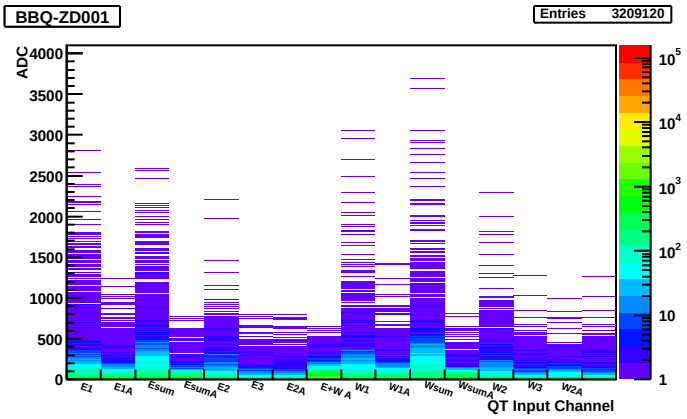

BBQ-BB001 (BBC east small tiles TAC)
Entries 3209120
TAC
QT Input Channel

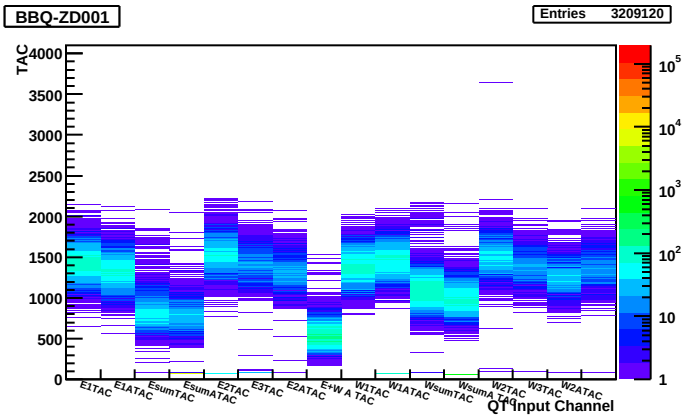

BBQ-BB002 (BBC west small tiles ADC)
Entries 3209120
ADC
QT Input Channel

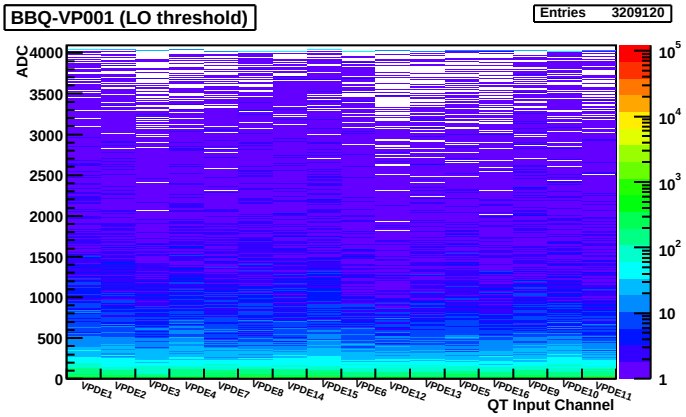

BBQ-BB002 (BBC west small tiles TAC)
Entries 3209120
TAC
QT Input Channel

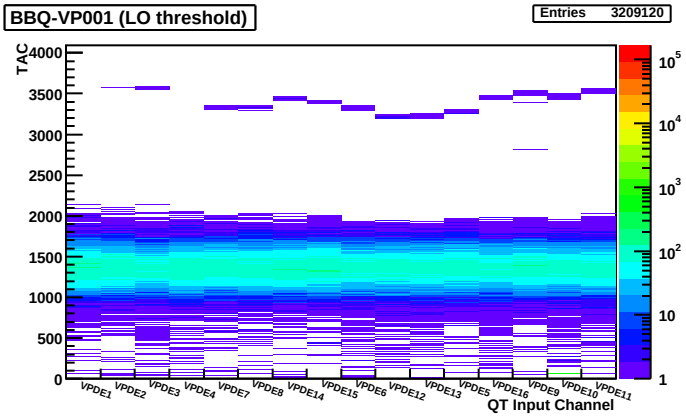

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3209120
ADC
QT Input Channel

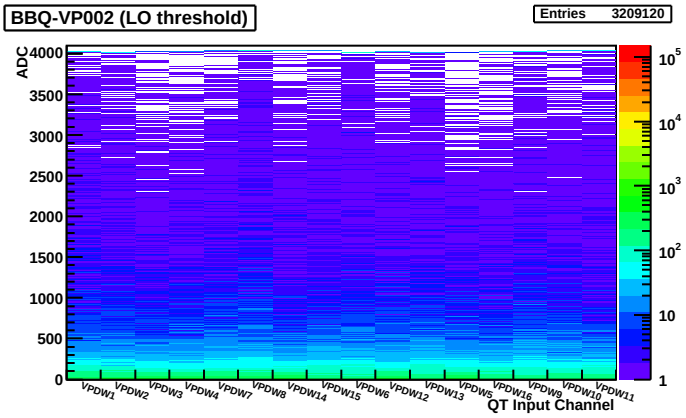

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3209120
TAC
QT Input Channel

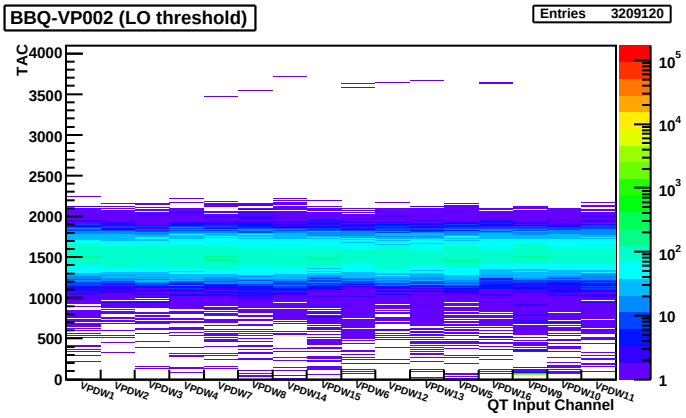

BBQ-ZD001
Entries 3209120
ADC
QT Input Channel

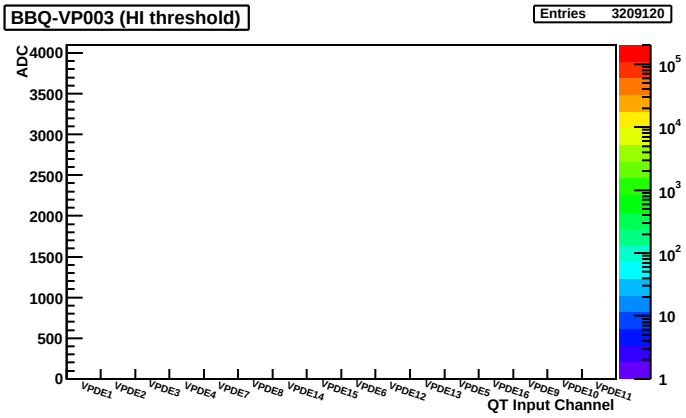

BBQ-ZD001
Entries 3209120
TAC
QT Input Channel

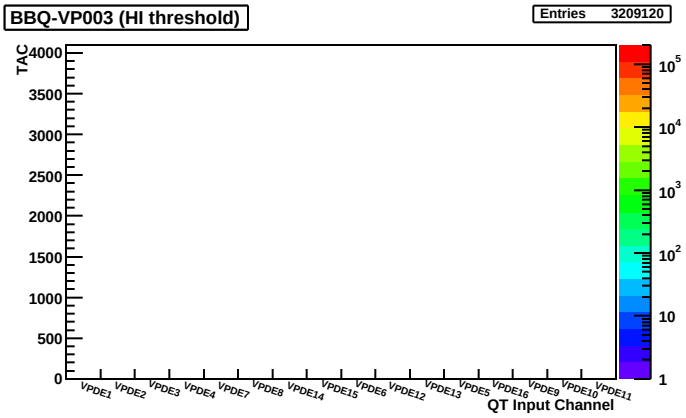

BBQ-VP001 (LO threshold)
Entries 3209120
ADC
QT Input Channel

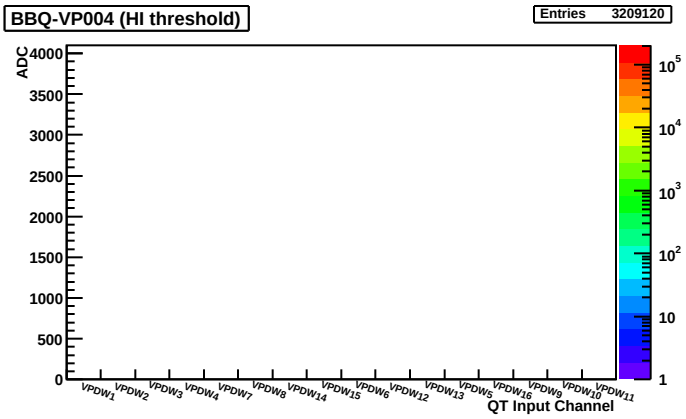

BBQ-VP001 (LO threshold)
Entries 3209120
TAC
QT Input Channel

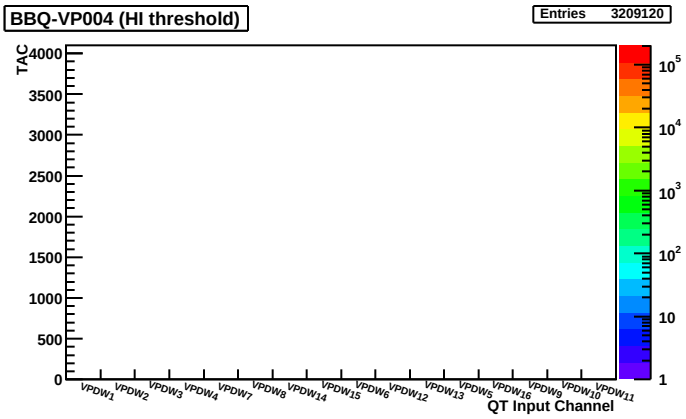

BBQ-VP002 (LO threshold)
Entries 3209120
ADC
QT Input Channel

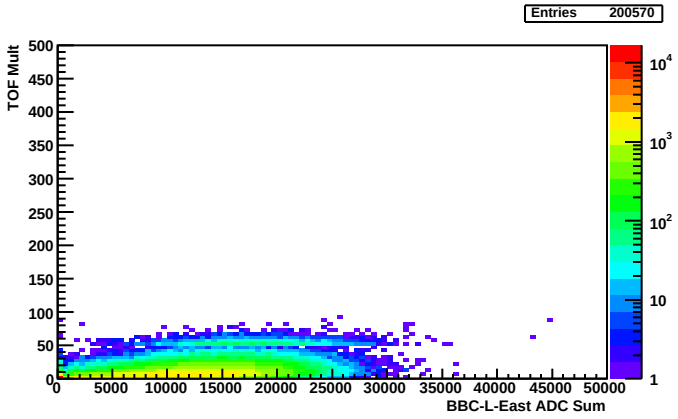

BBQ-VP002 (LO threshold)
Entries 3209120
TAC
QT Input Channel

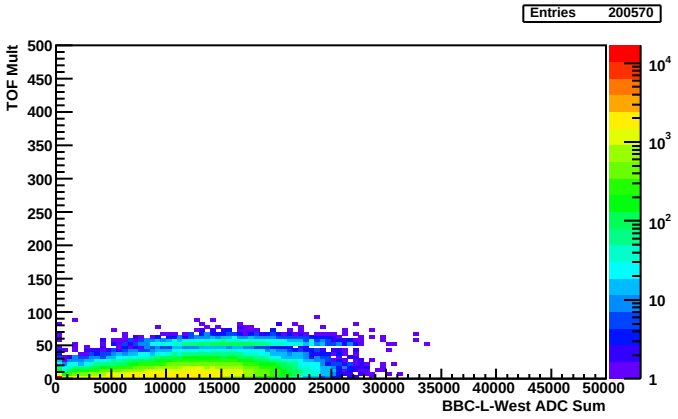

BBQ-VP003 (HI threshold)
Entries 3209120
ADC
QT Input Channel

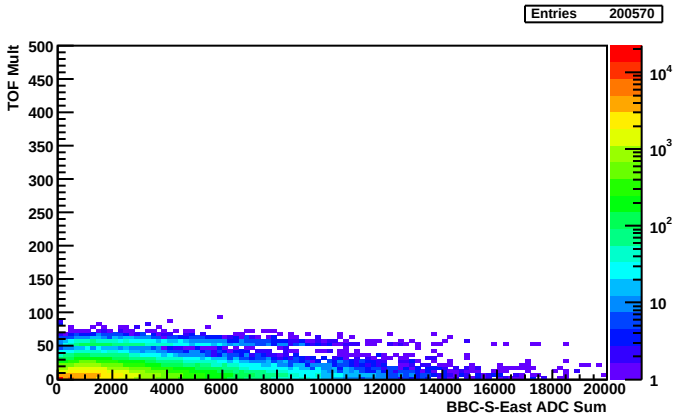

BBQ-VP003 (HI threshold)
Entries 3209120
TAC
QT Input Channel

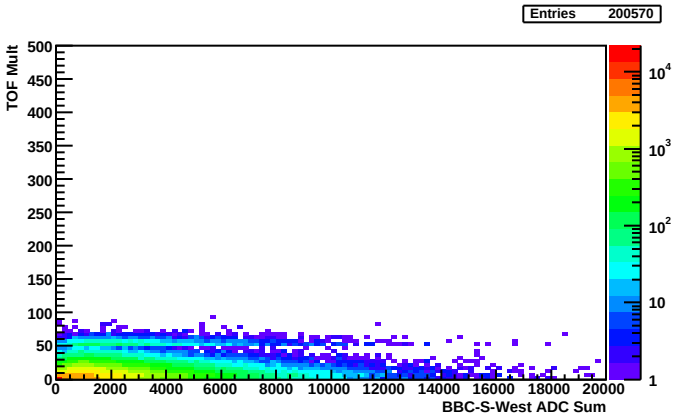

BBQ-VP004 (HI threshold)
Entries 3209120
ADC
QT Input Channel

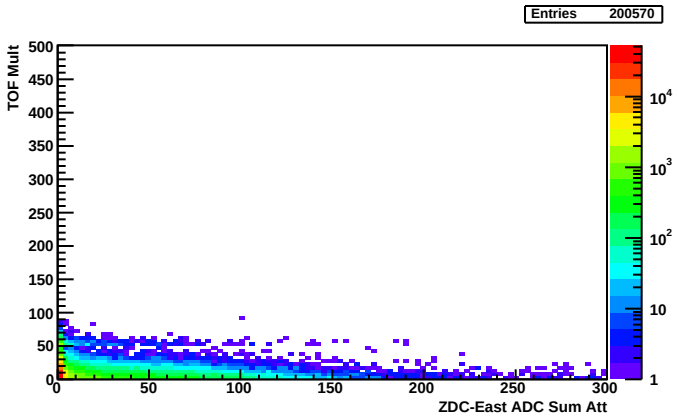

BBQ-VP004 (HI threshold)
Entries 3209120
TAC
QT Input Channel

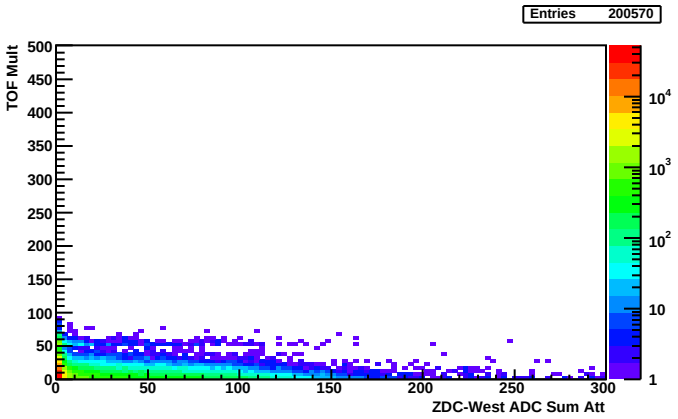

Entries 200570
TOF Mult
BBC-L-East ADC Sum

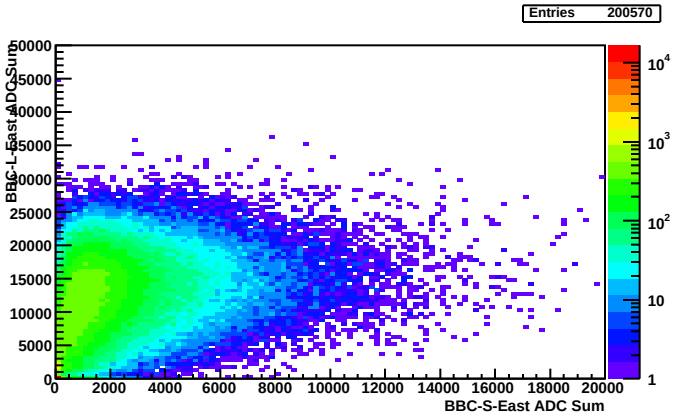

Entries 200570
TOF Mult
BBC-L-West ADC Sum

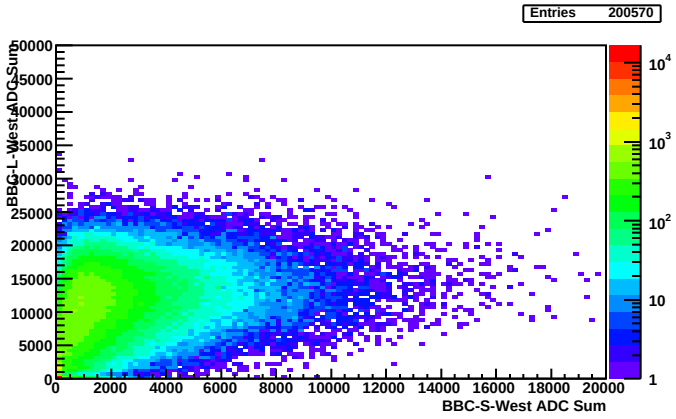

Entries 200570
TOF Mult
BBC-S-East ADC Sum

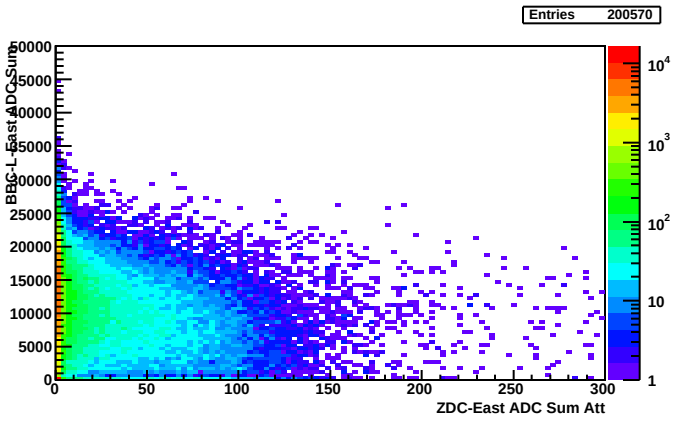

Entries 200570
TOF Mult
BBC-S-West ADC Sum

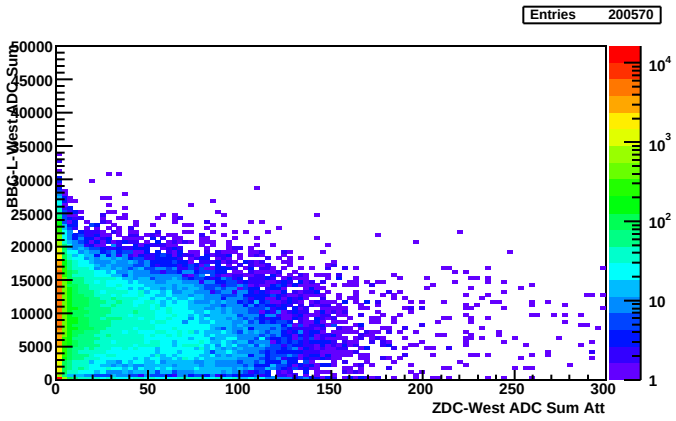

Entries 200570
TOF Mult
ZDC-East ADC Sum Att

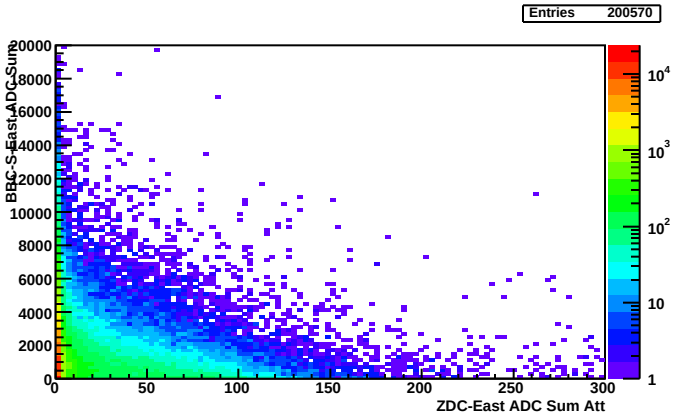

Entries 200570
TOF Mult
ZDC-West ADC Sum Att

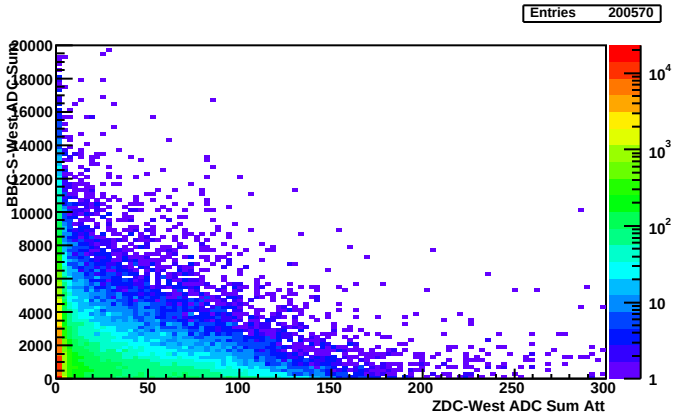

Entries 200570
BBC-L-East ADC Sum
BBC-S-East ADC Sum

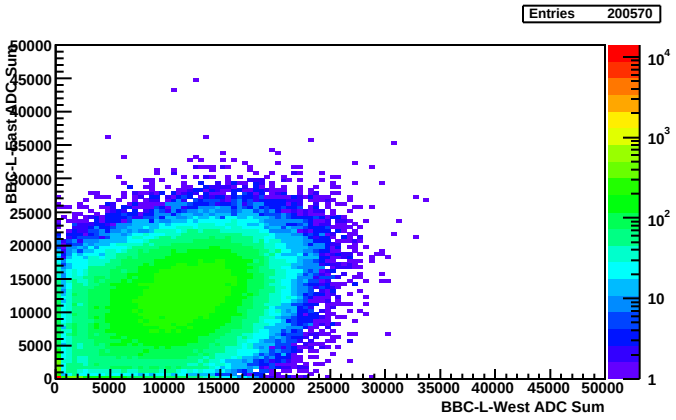

Entries 200570
BBC-L-West ADC Sum
BBC-S-West ADC Sum

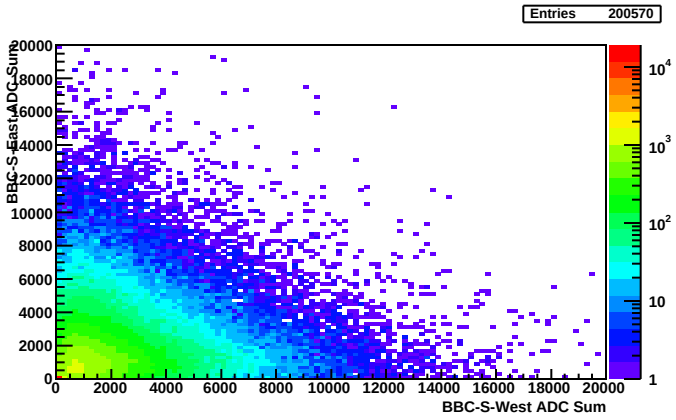

Entries 200570
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

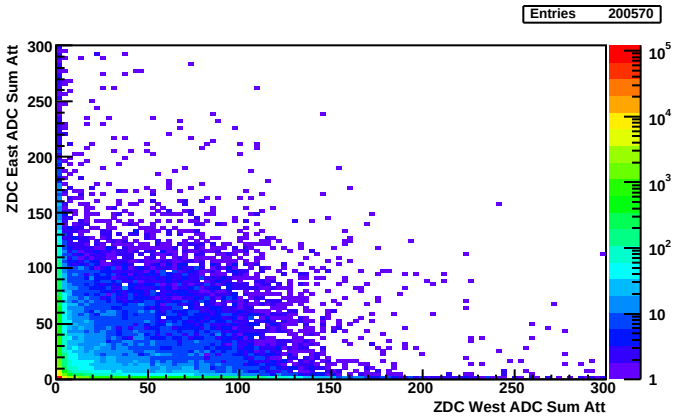

Entries 200570
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

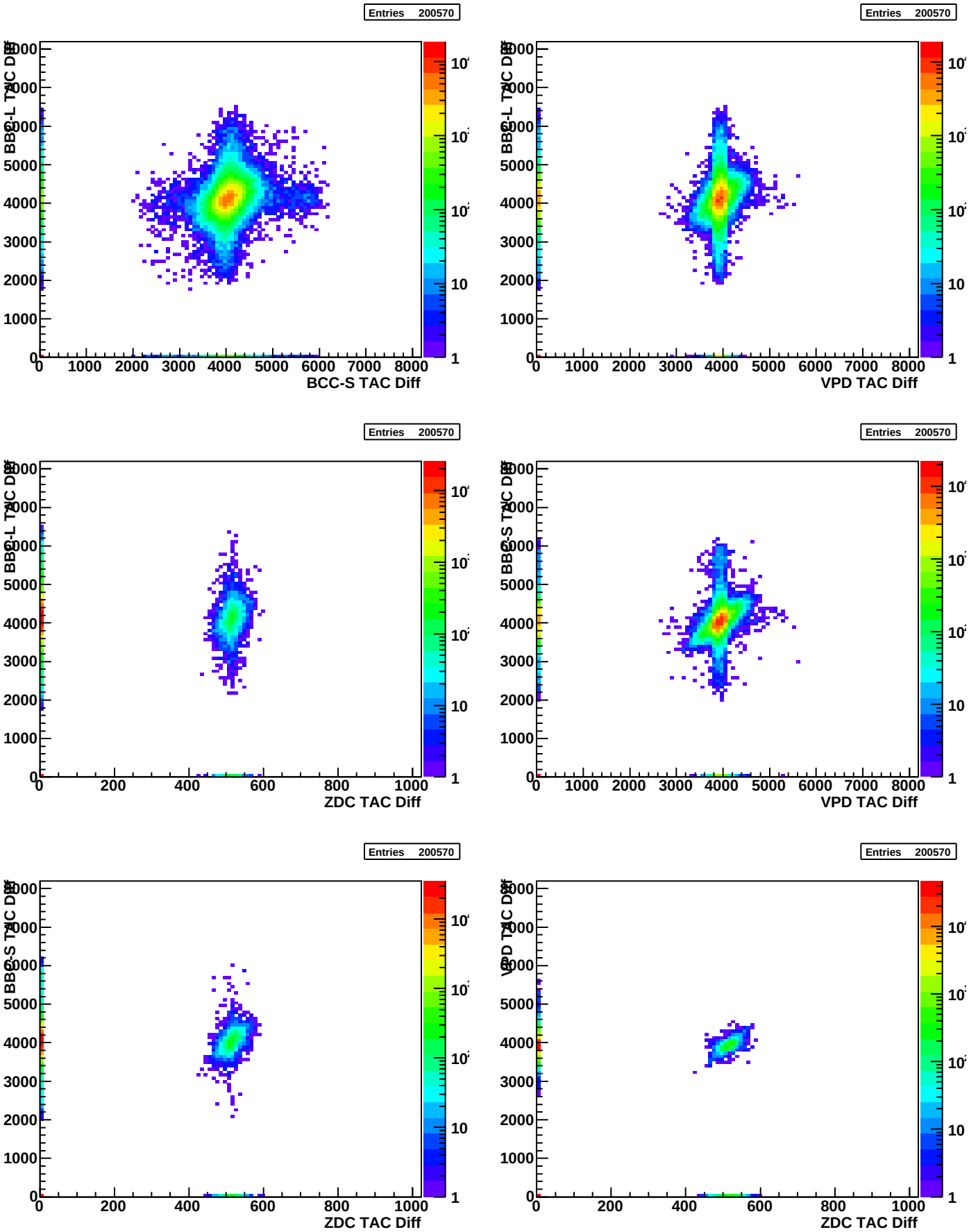

Entries 200570
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

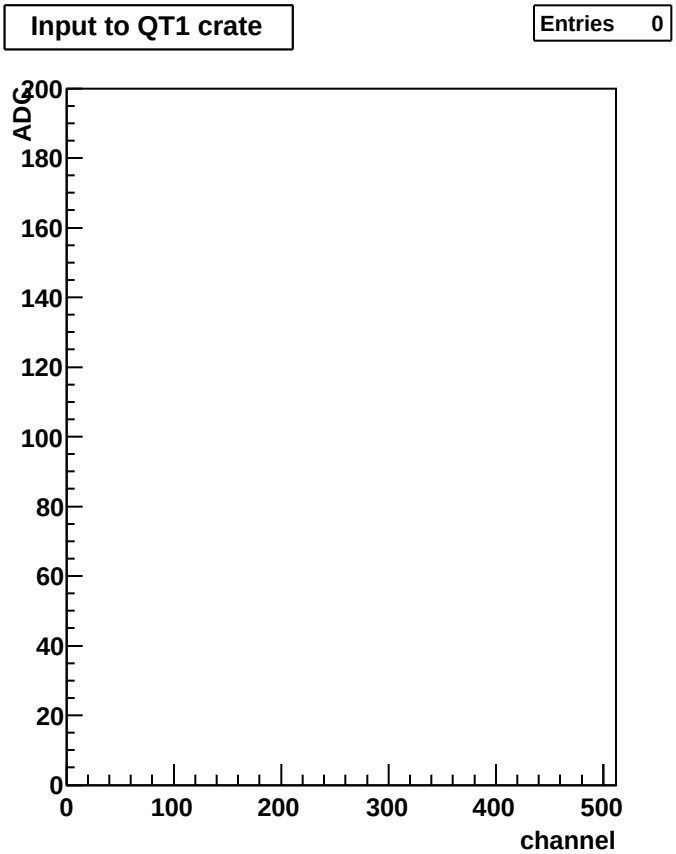

Entries 200570
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

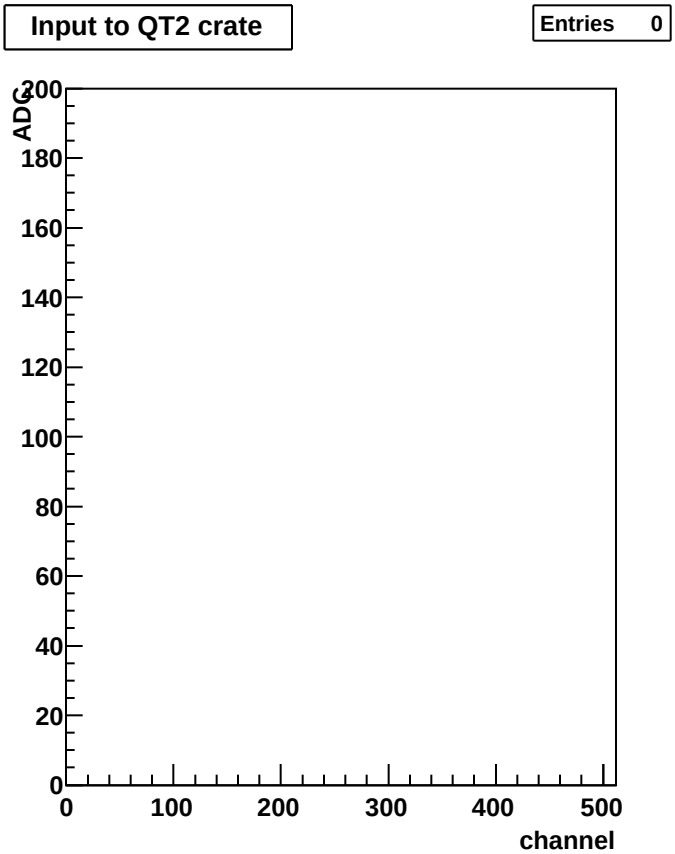

Entries 200570
BBC-L-East ADC Sum
BBC-L-West ADC Sum

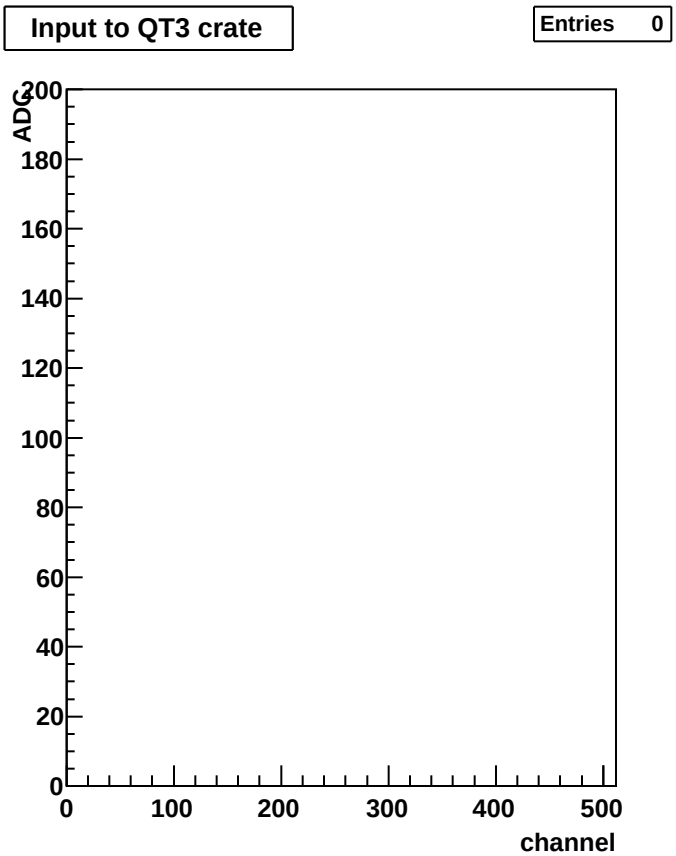

Entries 200570
BBC-S-East ADC Sum
BBC-S-West ADC Sum

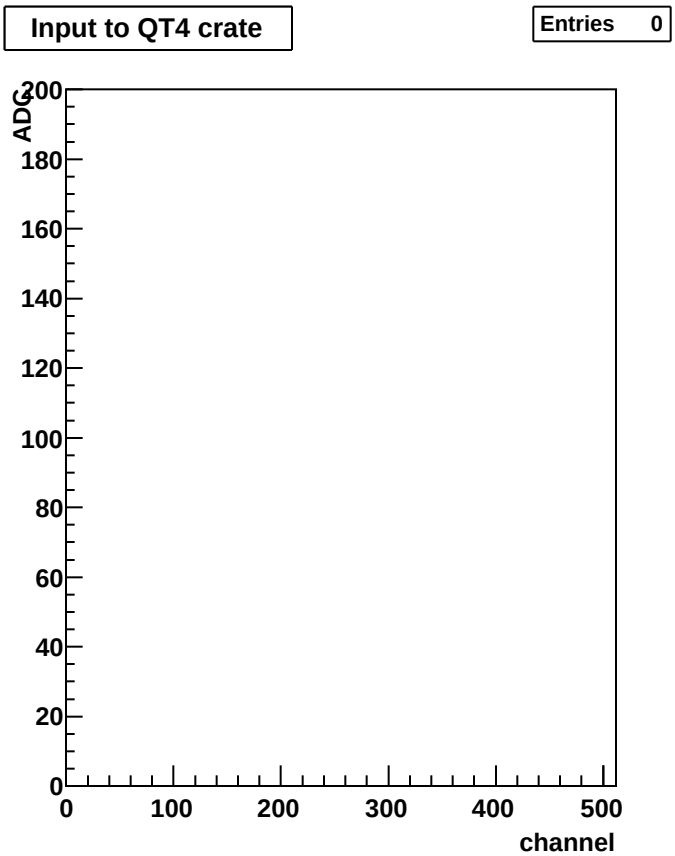

Entries 200570
ZDC East ADC Sum Att
ZDC West ADC Sum Att

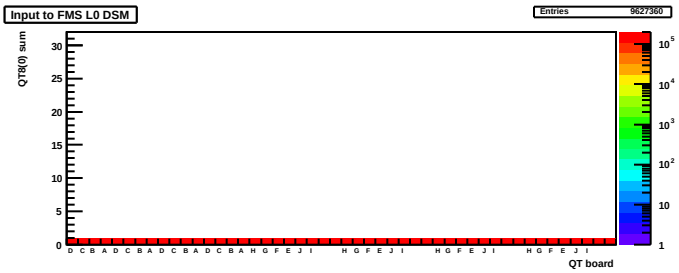

Entries 200570
BBC-L TAC Diff
BCC-S TAC Diff
Entries 200570
BBC-L TAC Diff
VPD TAC Diff
Entries 200570
BBC-L TAC Diff
ZDC TAC Diff
Entries 200570
BBC-S TAC Diff
VPD TAC Diff
Entries 200570
BBC-S TAC Diff
ZDC TAC Diff
Entries 200570
VPD TAC Diff
ZDC TAC Diff

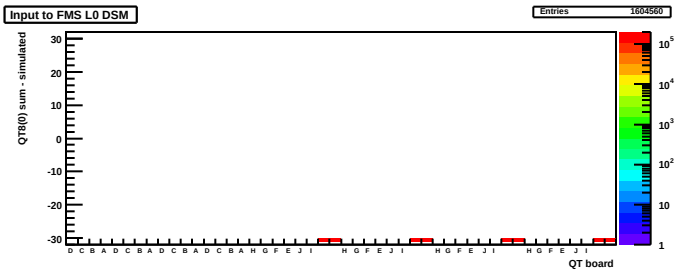

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

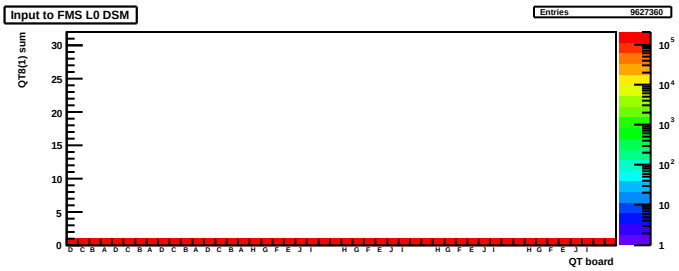

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

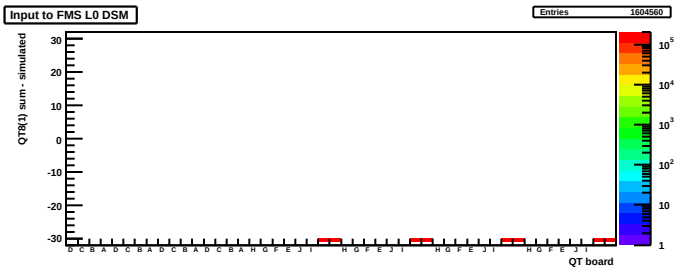

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

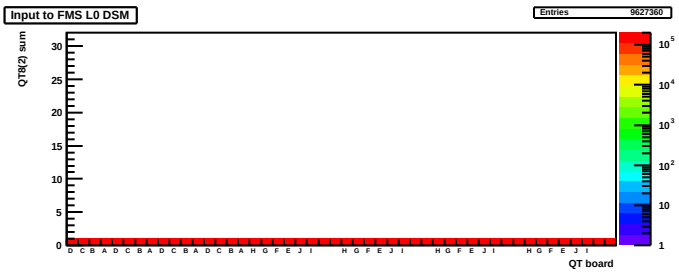

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

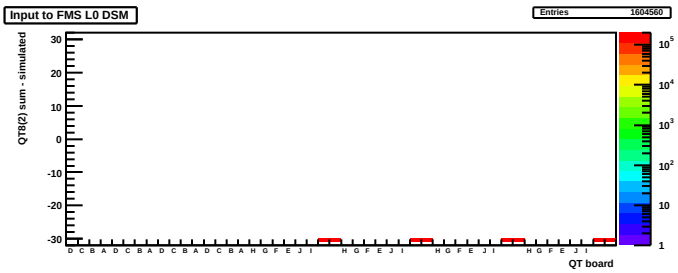

Input to FMS L0 DSM
Entries 9627360
QT8(0) sum
QT board

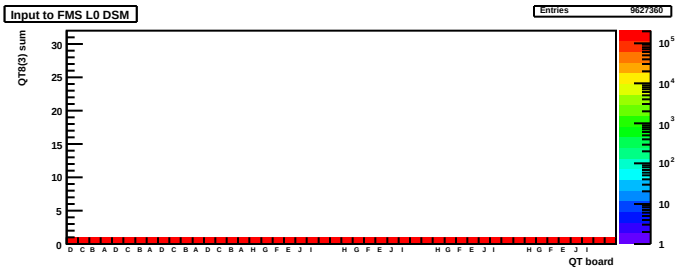

Input to FMS L0 DSM
Entries 1604560
QT8(0) sum - simulated
QT board

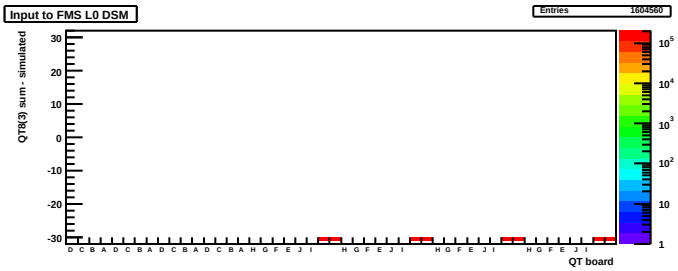

Input to FMS L0 DSM
Entries 9627360
QT8(1) sum
QT board

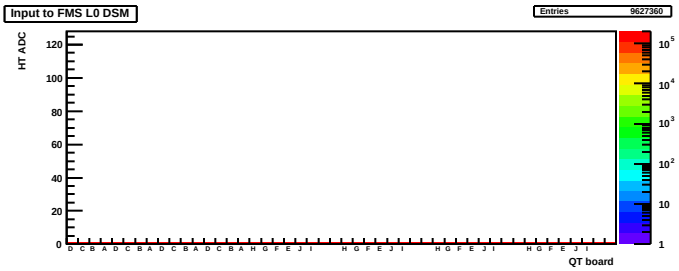

Input to FMS L0 DSM
Entries 1604560
QT8(1) sum - simulated
QT board

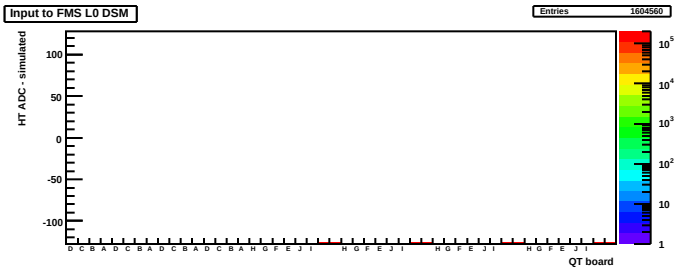

Input to FMS L0 DSM
Entries 9627360
QT8(2) sum
QT board

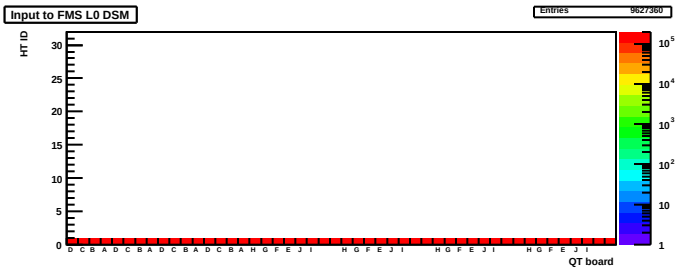

Input to FMS L0 DSM
Entries 1604560
QT8(2) sum - simulated
QT board

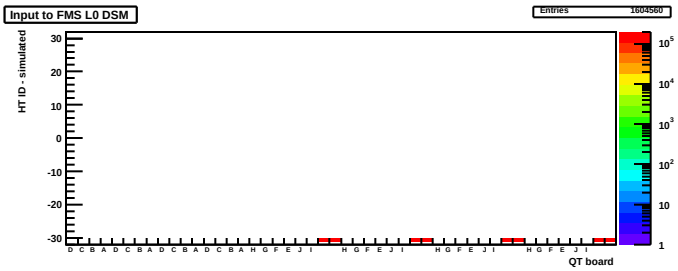

Input to FMS L0 DSM
Entries 9627360
QT8(3) sum
QT board

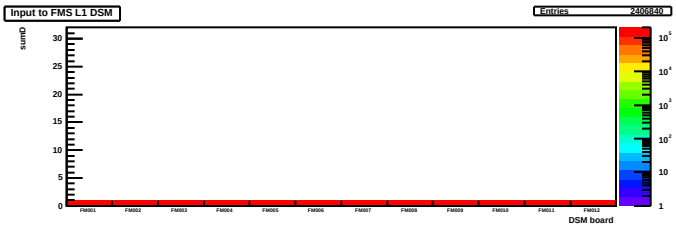

Input to FMS L0 DSM
Entries 1604560
QT8(3) sum - simulated
QT board

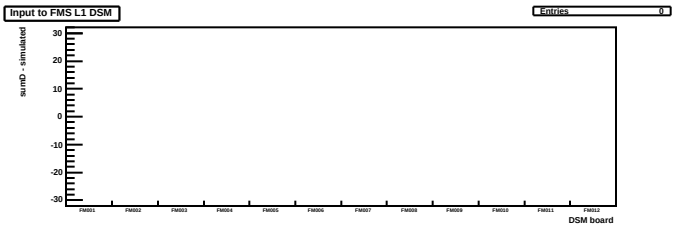

Input to FMS L0 DSM
Entries 9627360
HT ADC
QT board

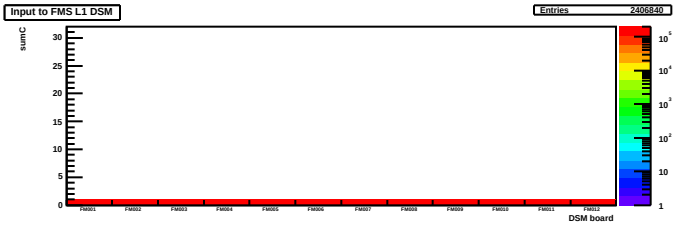

Input to FMS L0 DSM
Entries 1604560
HT ADC - simulated
QT board

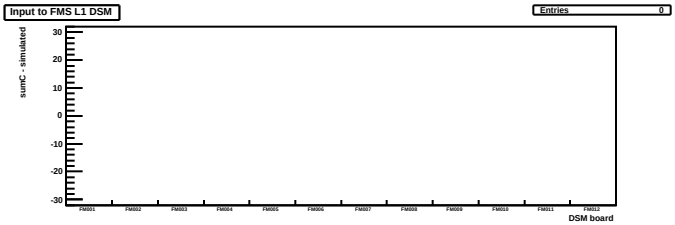

Input to FMS L0 DSM
Entries 9627360
HT ID
QT board

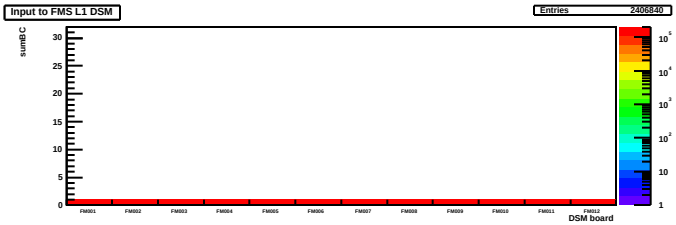

Input to FMS L0 DSM
Entries 1604560
HT ID - simulated
QT board

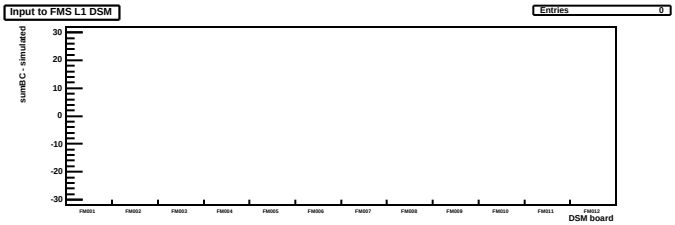

Input to FMS L1 DSM
Entries 2406840
sumD
DSM board

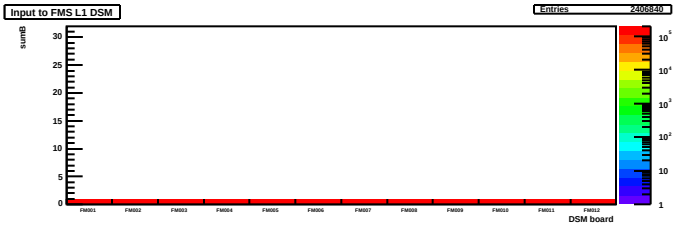

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

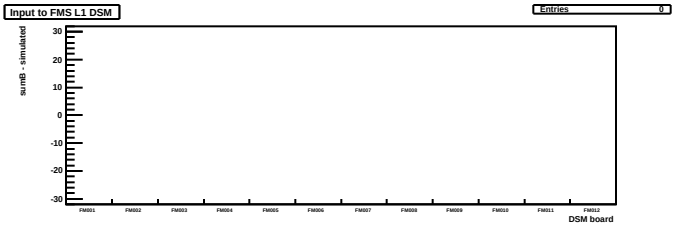

Input to FMS L1 DSM
Entries 2406840
sumC
DSM board

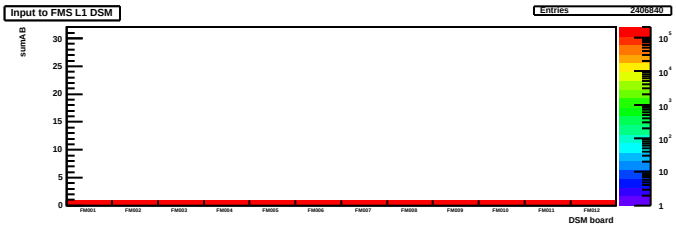

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

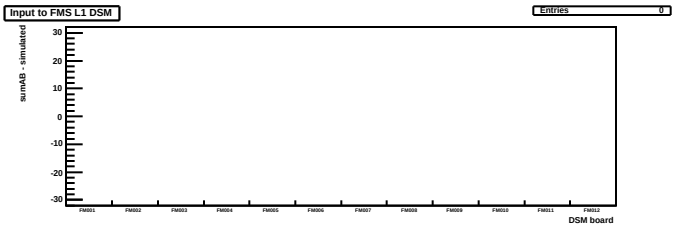

Input to FMS L1 DSM
Entries 2406840
sumBC
DSM board

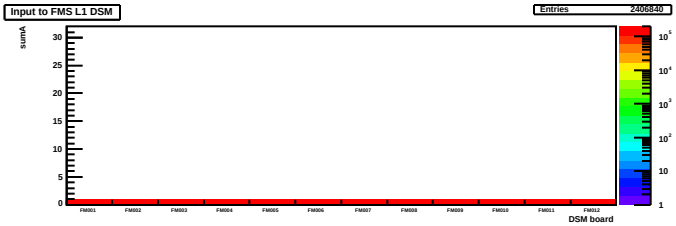

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

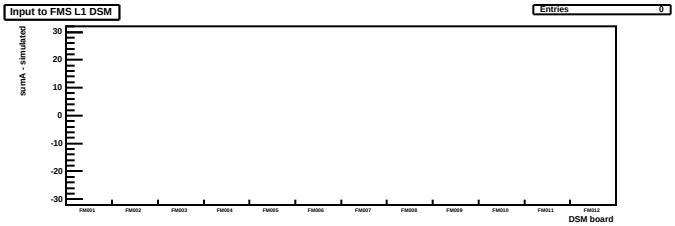

Input to FMS L1 DSM
Entries 2406840
sumB
DSM board

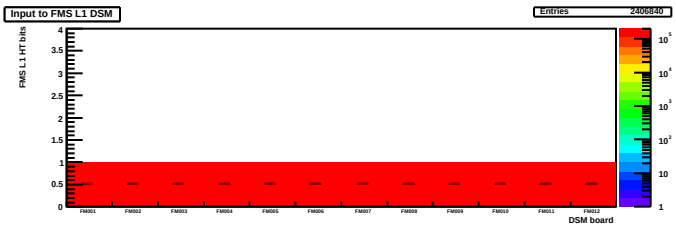

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

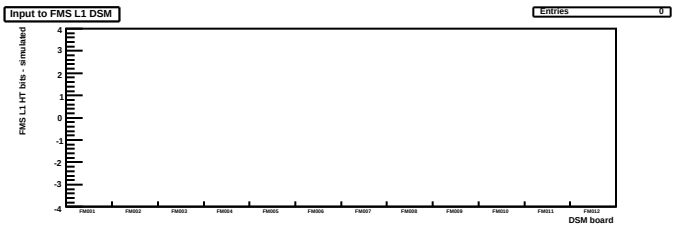

Input to FMS L1 DSM
Entries 2406840
sumAB
DSM board

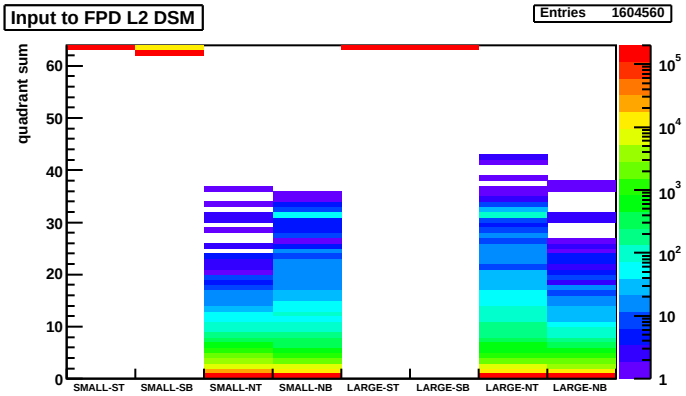

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

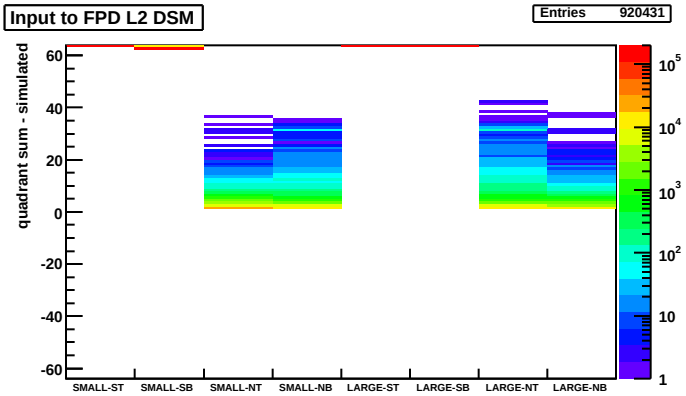

Input to FMS L1 DSM
Entries 2406840
sumA
DSM board

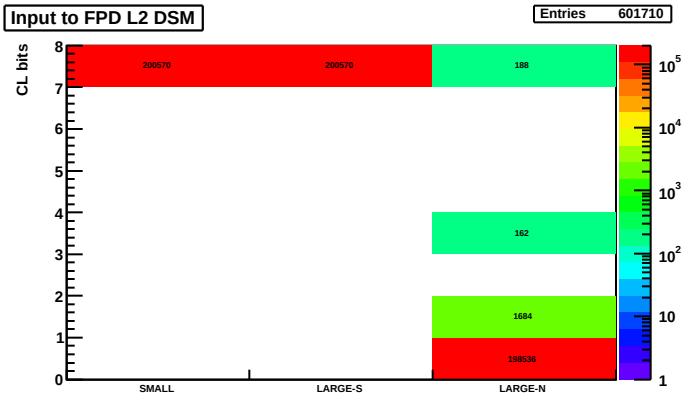

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

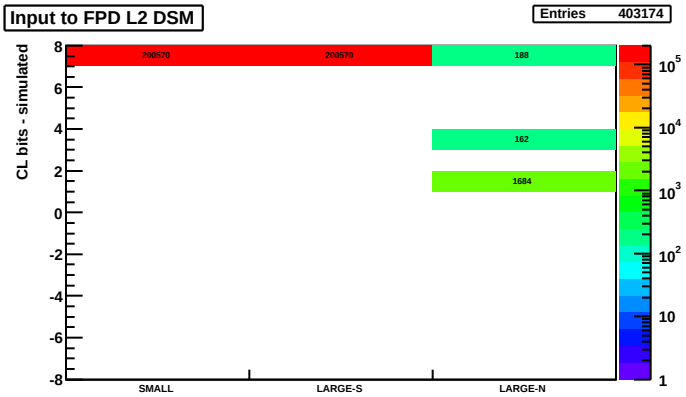

Input to FMS L1 DSM
Entries 2406840
FMS L1 HT bits
DSM board

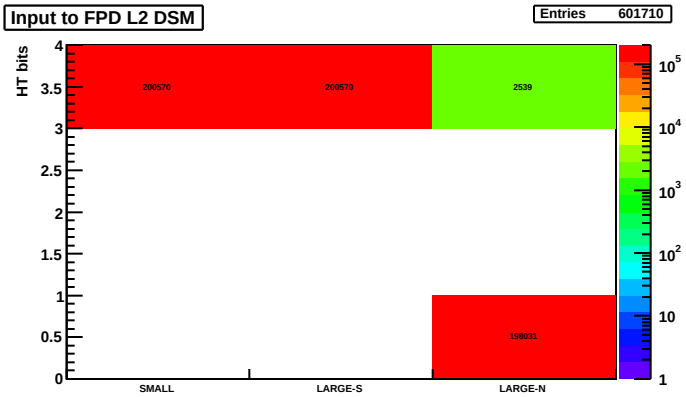

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

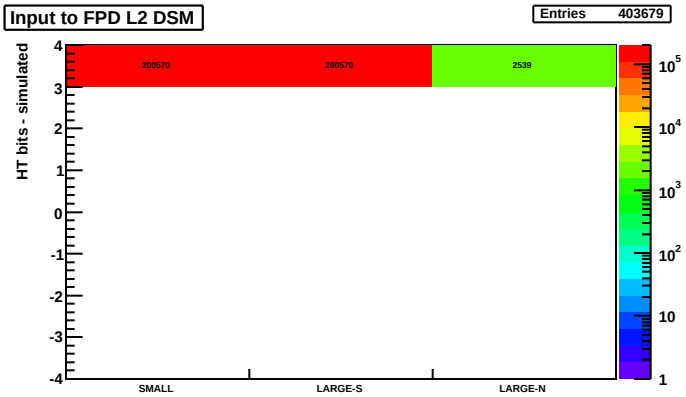

Input to FPD L2 DSM
Entries 1604560
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

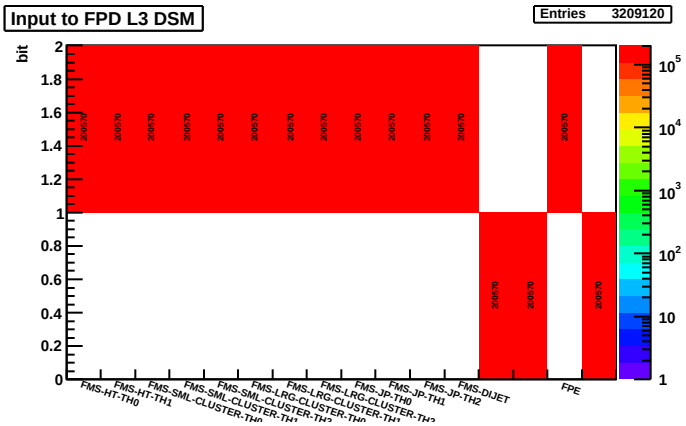

Input to FPD L2 DSM
Entries 920431
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

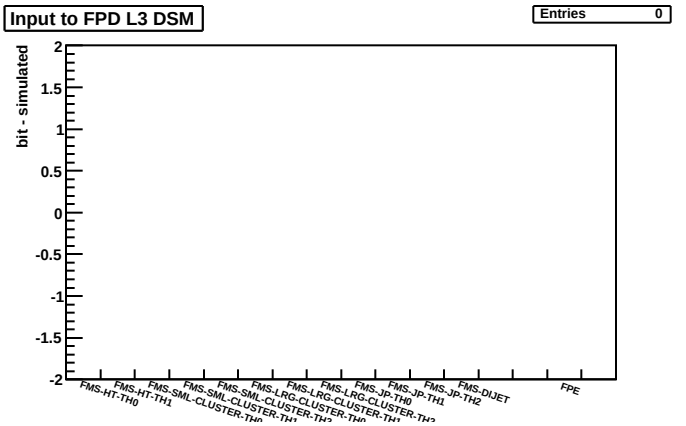

Input to FPD L2 DSM
Entries 601710
CL bits
200570
200570
188
162
1684
198536
SMALL
LARGE-S
LARGE-N

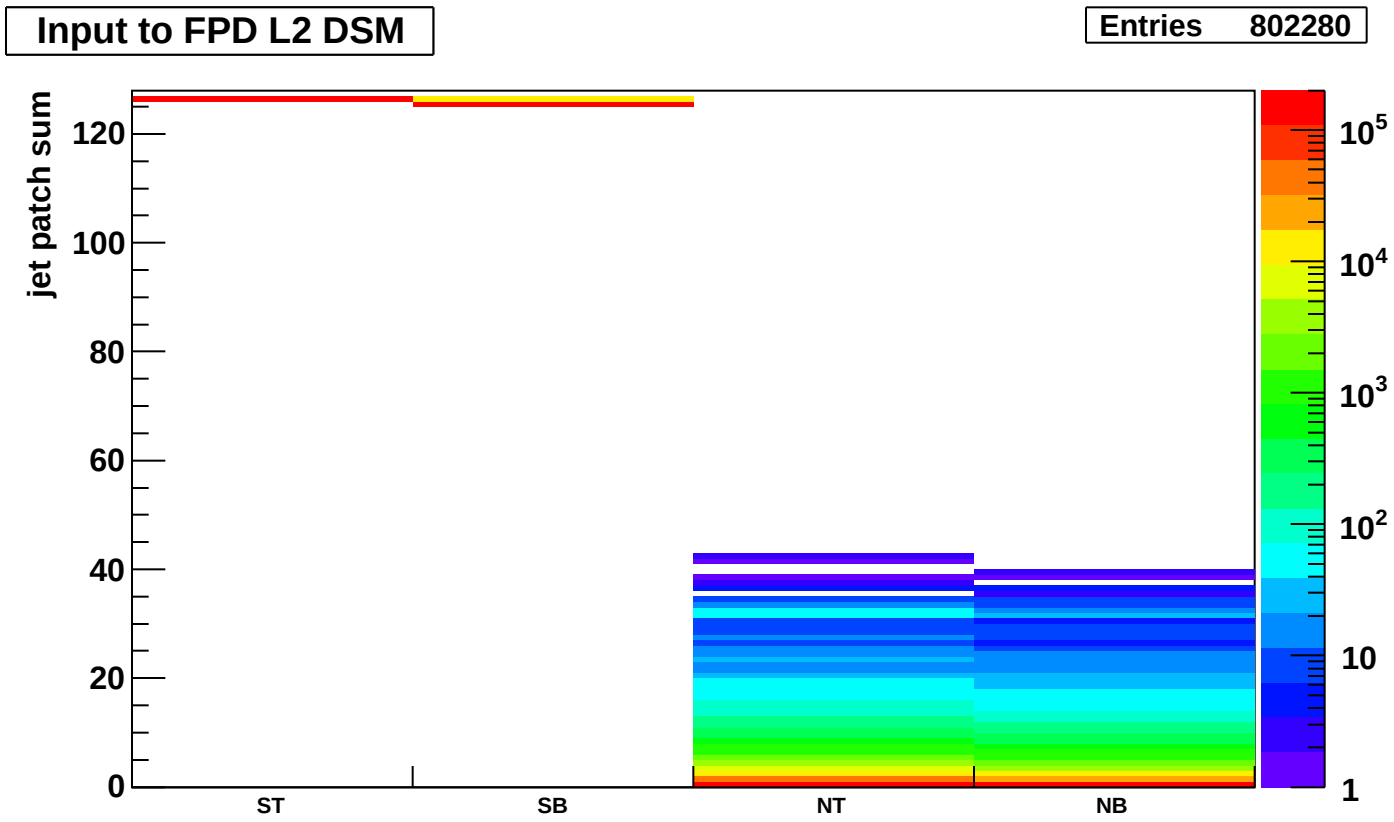

Input to FPD L2 DSM
Entries 403174
CL bits - simulated
200570
200570
188
162
1684
SMALL
LARGE-S
LARGE-N

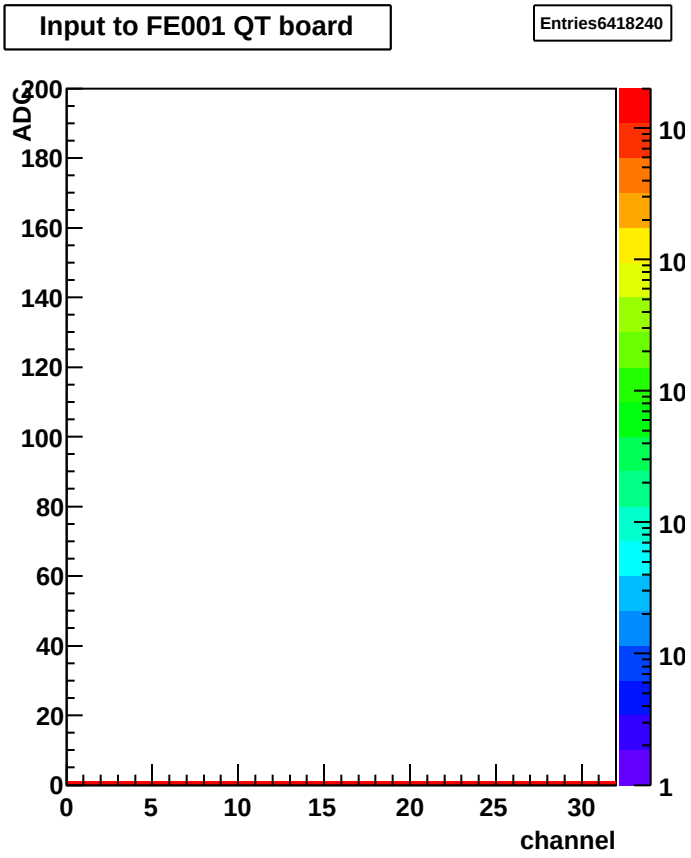

Input to FPD L2 DSM
Entries 601710
HT bits
200570
200570
2539
198031
SMALL
LARGE-S
LARGE-N

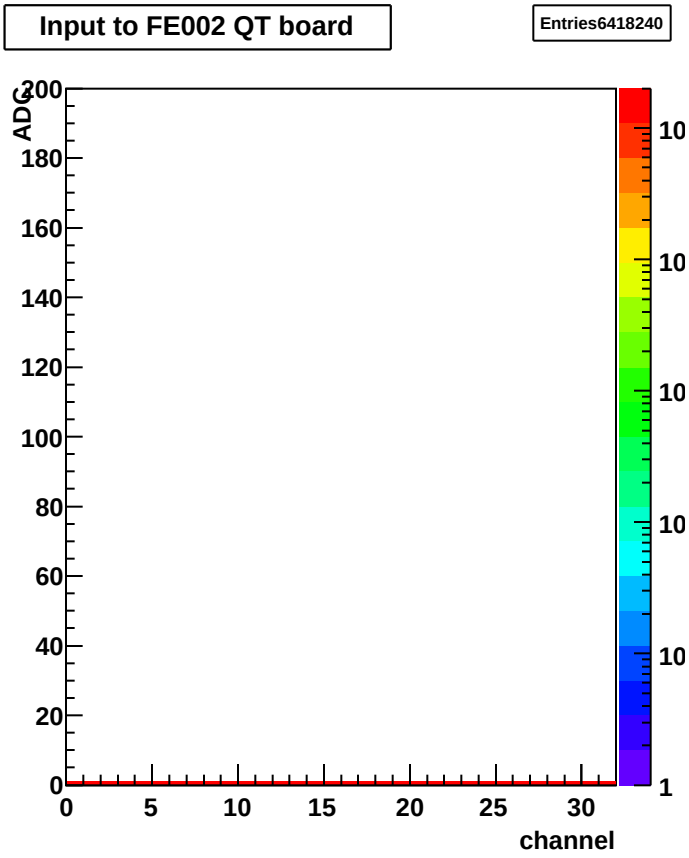

Input to FPD L2 DSM
Entries 403679
HT bits - simulated
200570
200570
2539
SMALL
LARGE-S
LARGE-N

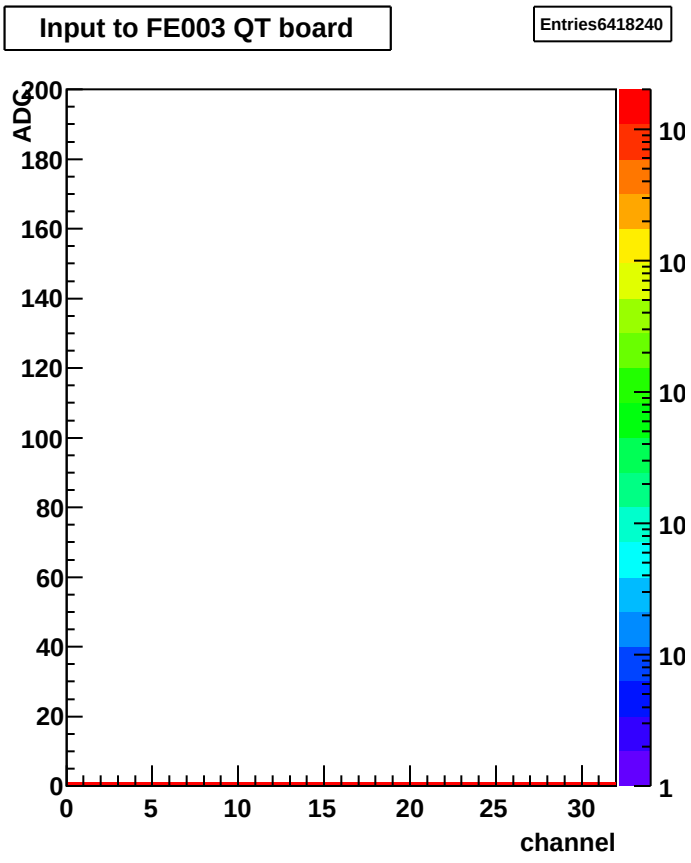

Input to FPD L3 DSM
Entries 3209120
bit
200570
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

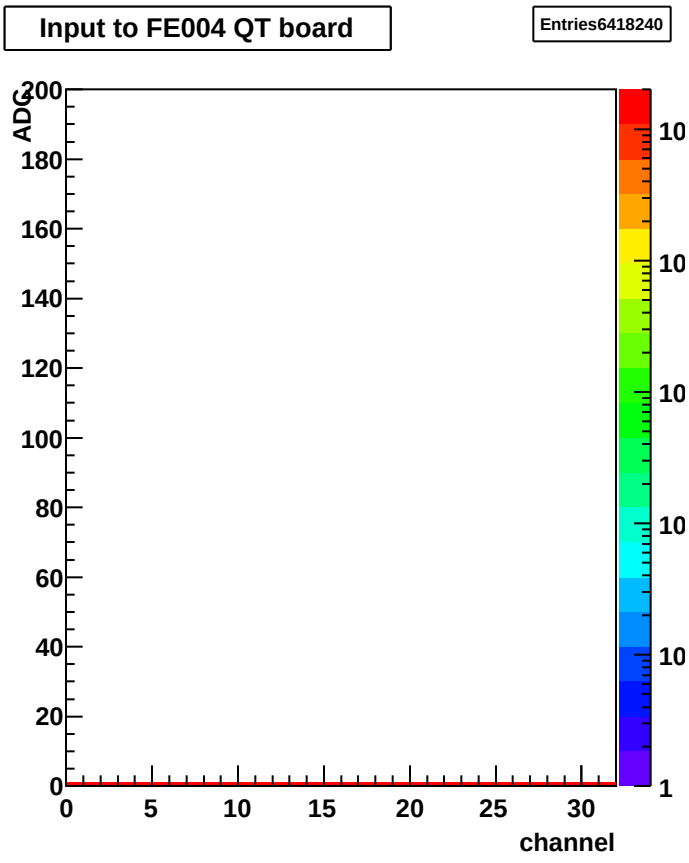

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

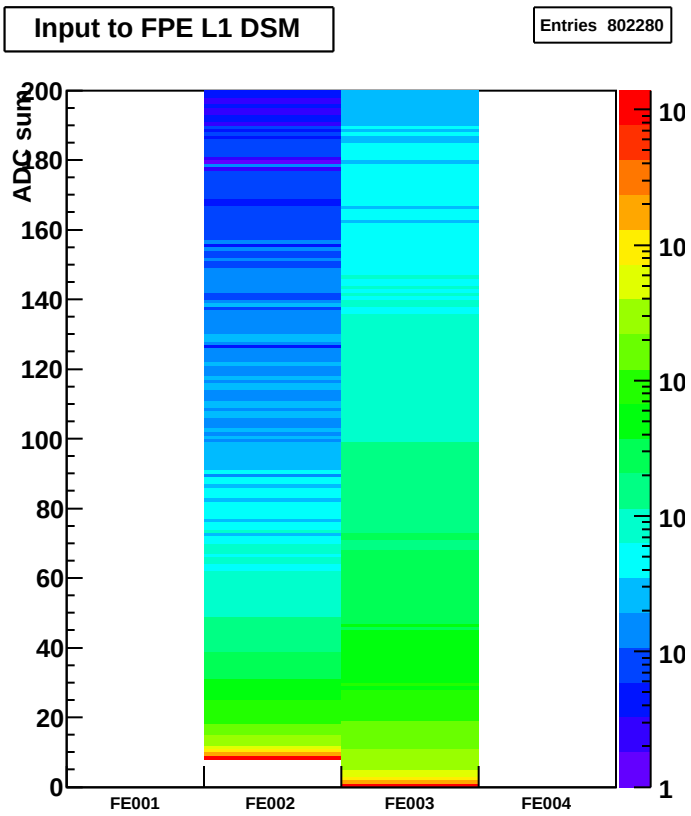

Input to FPD L2 DSM
Entries 802280
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

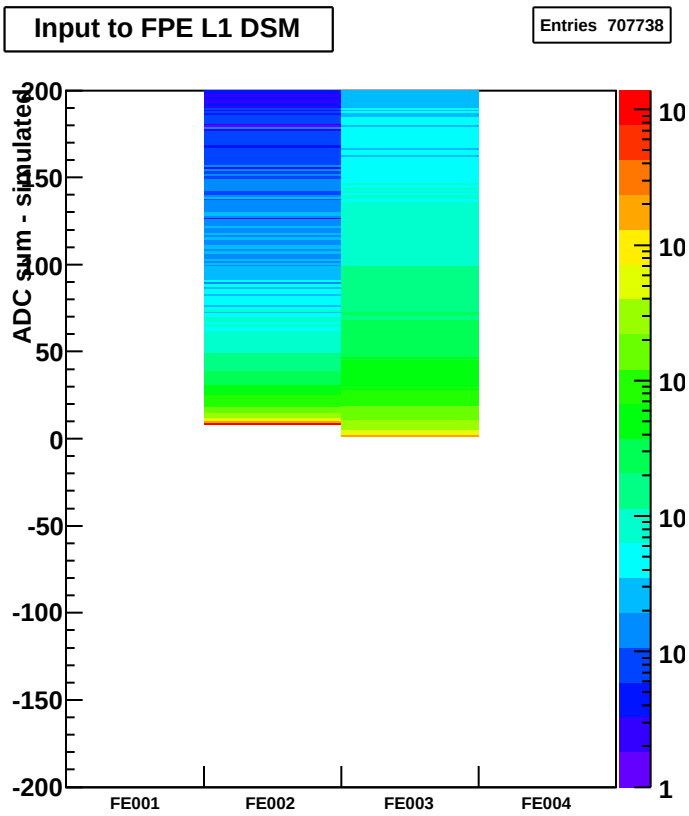

Input to FE001 QT board
Entries6418240
ADC
channel

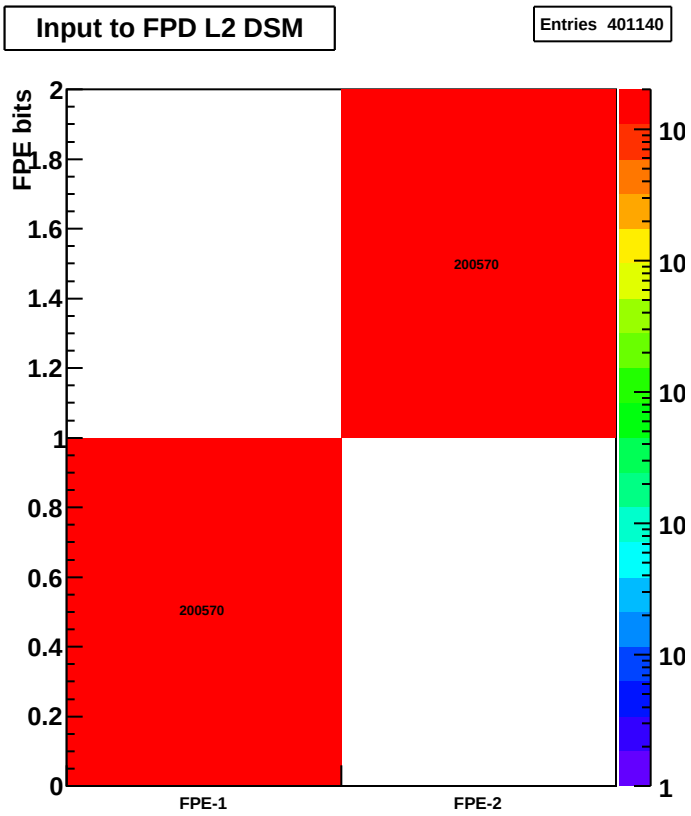

Input to FE002 QT board
Entries6418240
ADC
channel

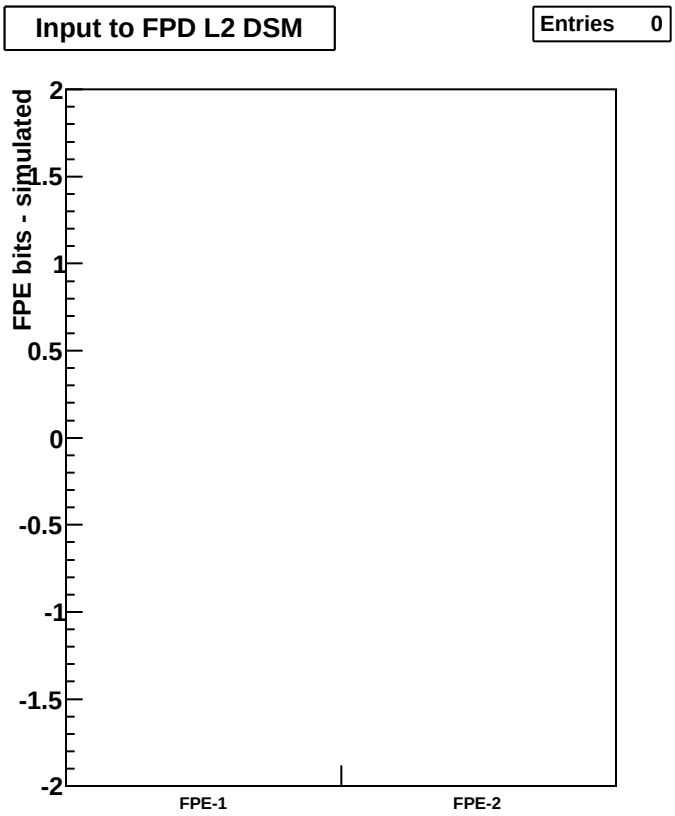

Input to FE003 QT board
Entries6418240
ADC
channel


Input to FE004 QT board
Entries6418240
ADC
channel


Input to FPE L1 DSM
Entries 802280
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 707738
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 401140
FPE bits
200570
200570
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2