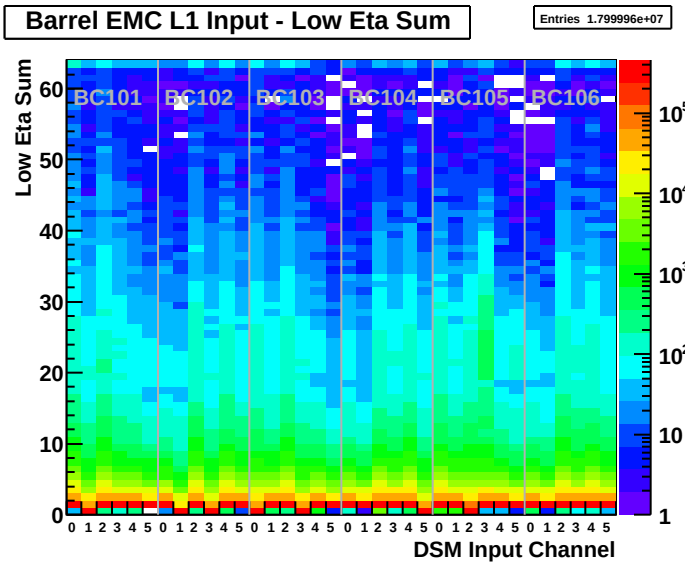
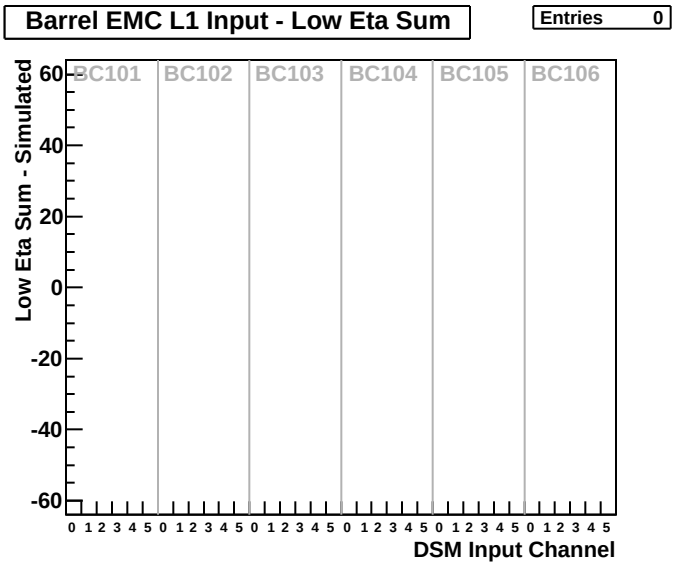
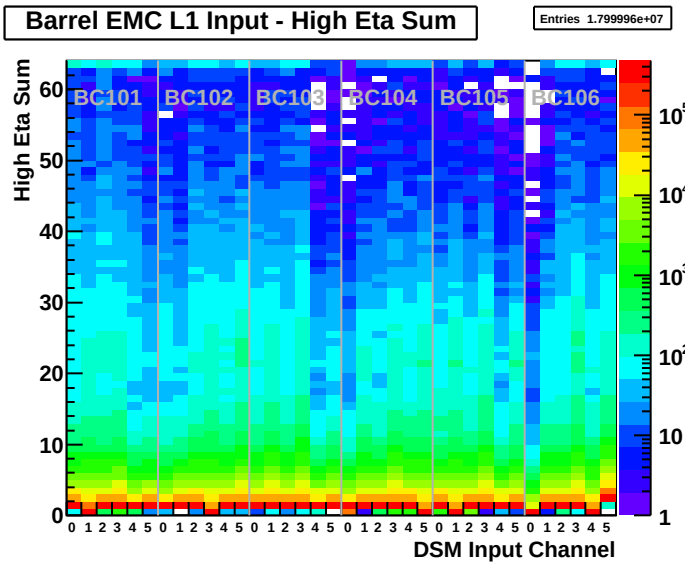
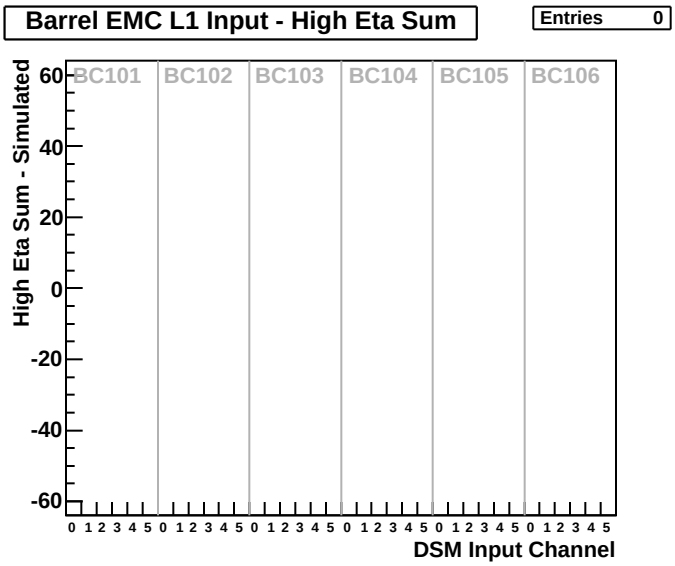
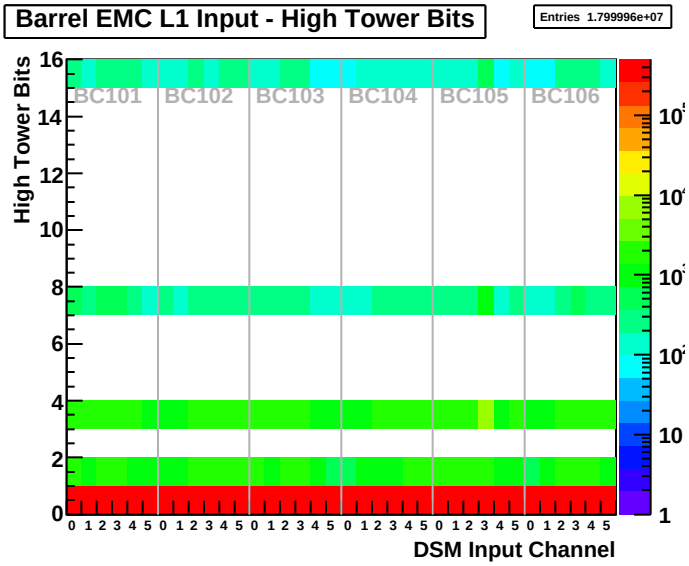
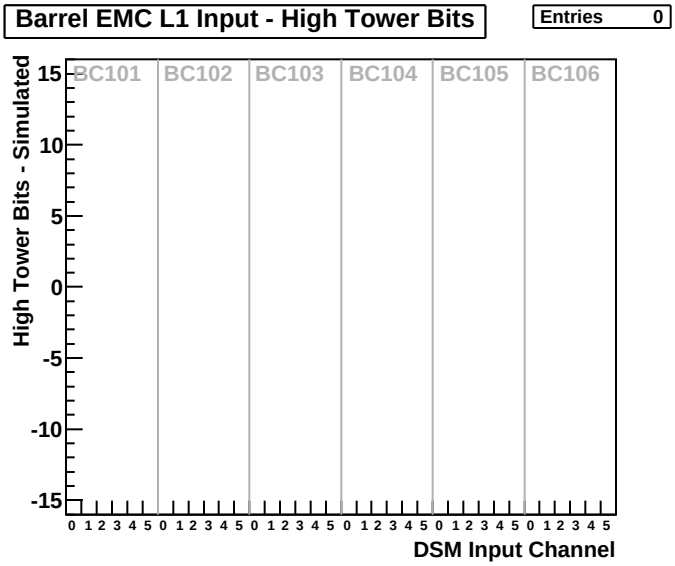

Barrel EMC L1 Input - Low Eta Sum
Entries 1.799996e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

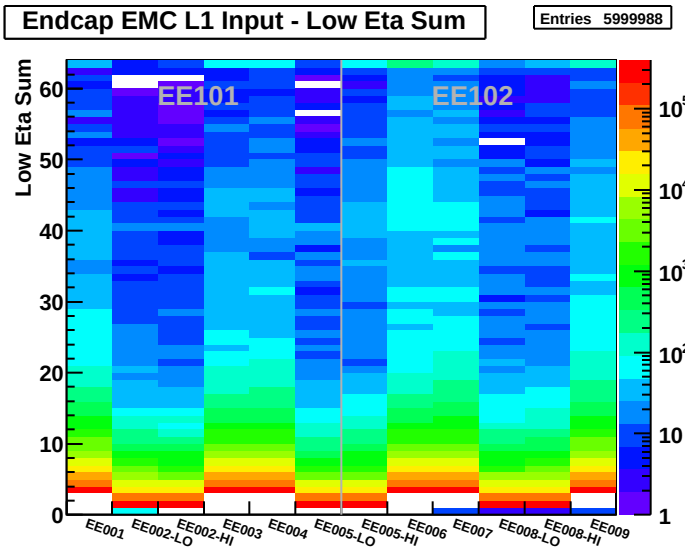

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

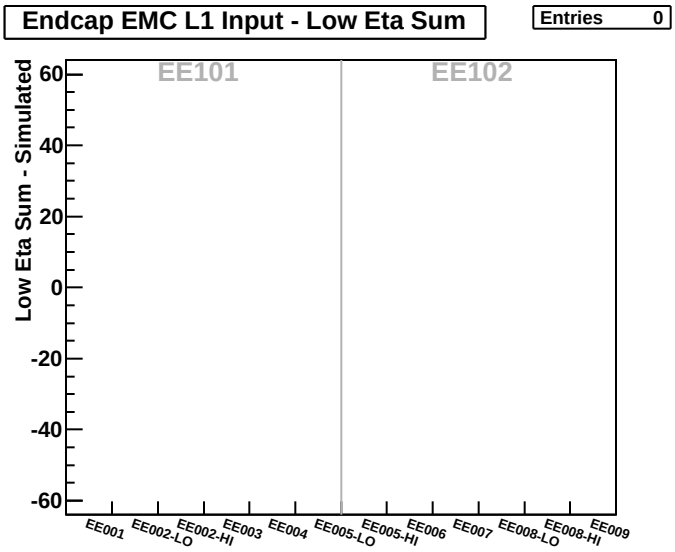

Barrel EMC L1 Input - High Eta Sum
Entries 1.799996e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

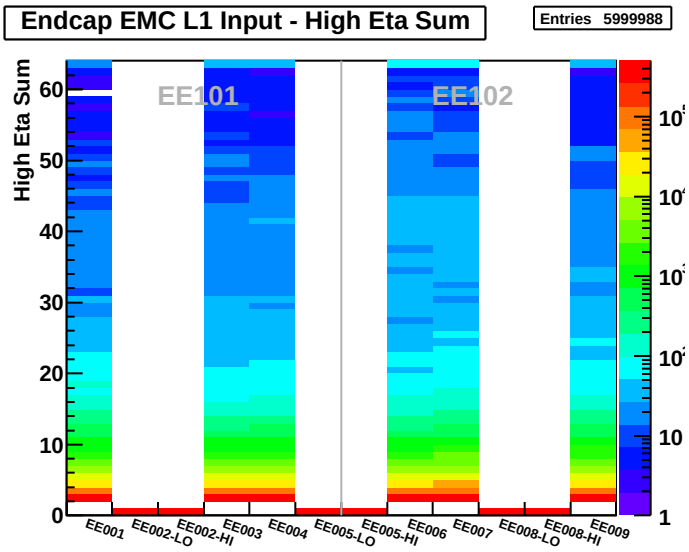

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

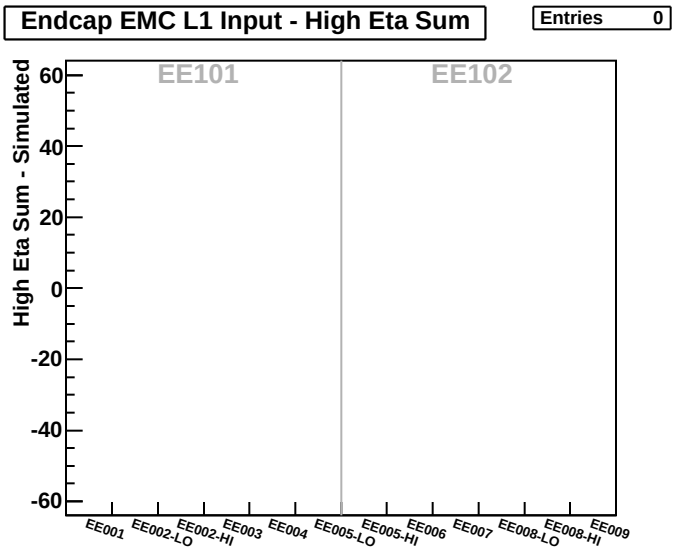

Barrel EMC L1 Input - High Tower Bits
Entries 1.799996e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

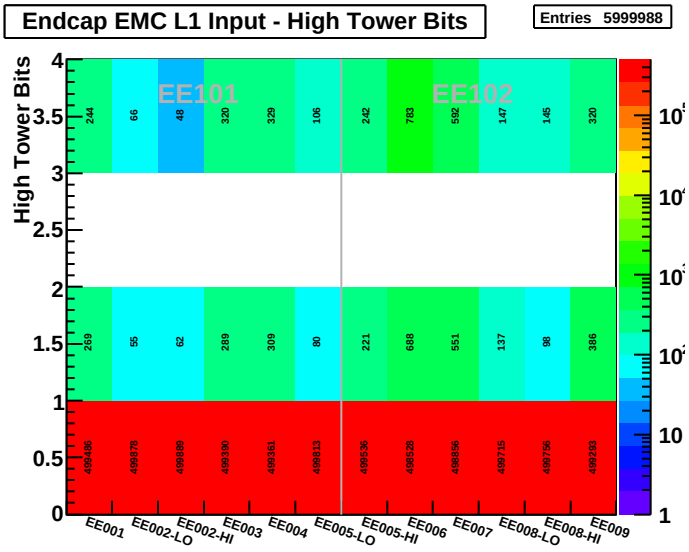

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

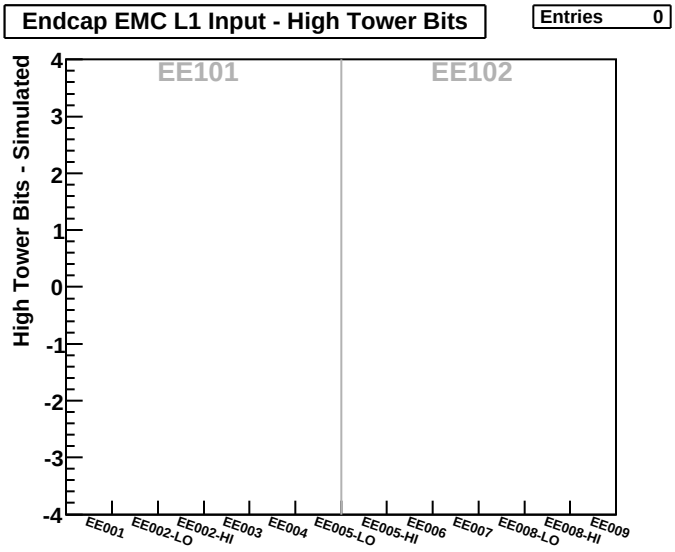

Endcap EMC L1 Input - Low Eta Sum
Entries 5999988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

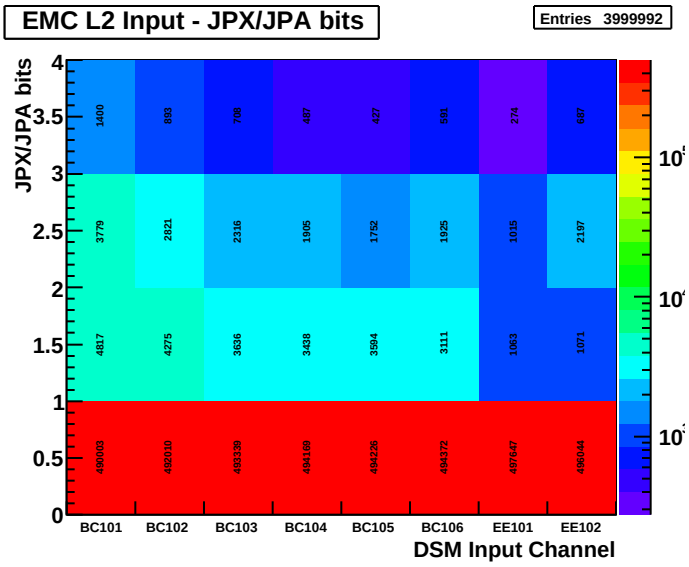

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

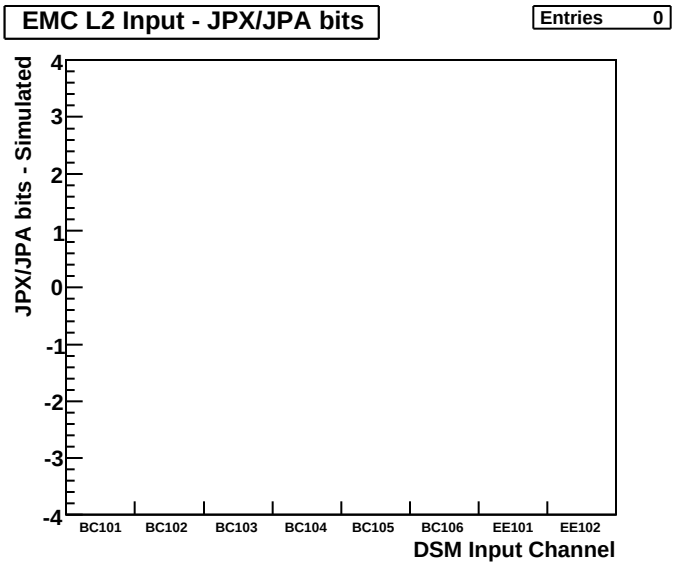

Endcap EMC L1 Input - High Eta Sum
Entries 5999988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

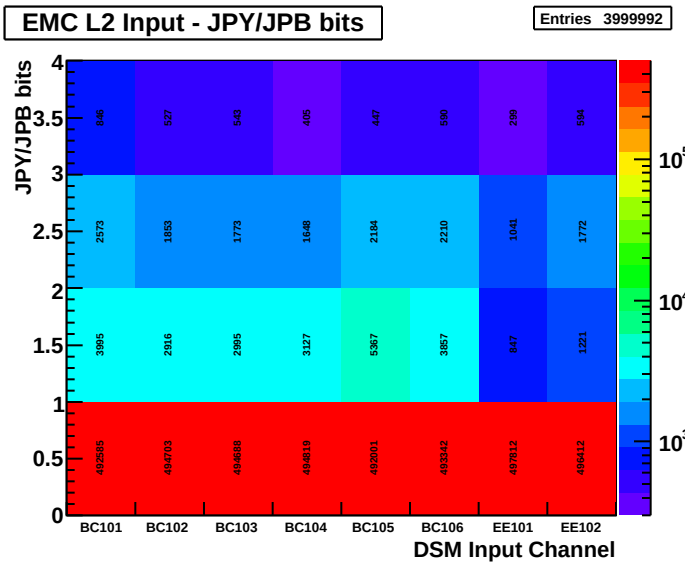

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

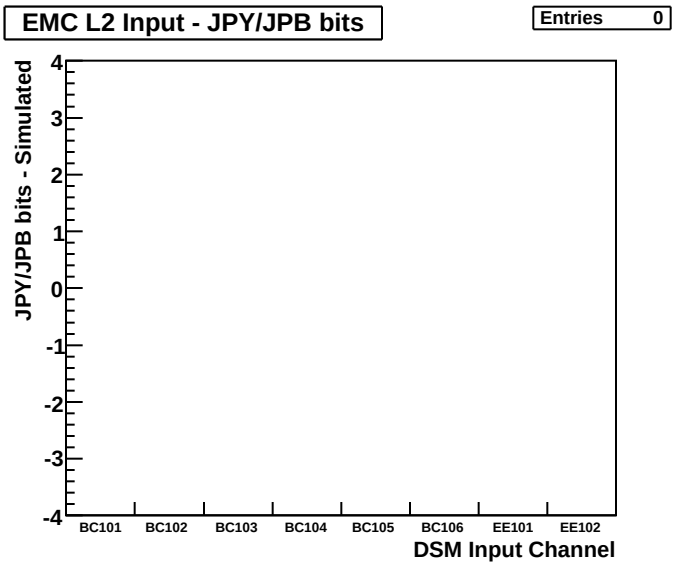

Endcap EMC L1 Input - High Tower Bits
Entries 5999988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

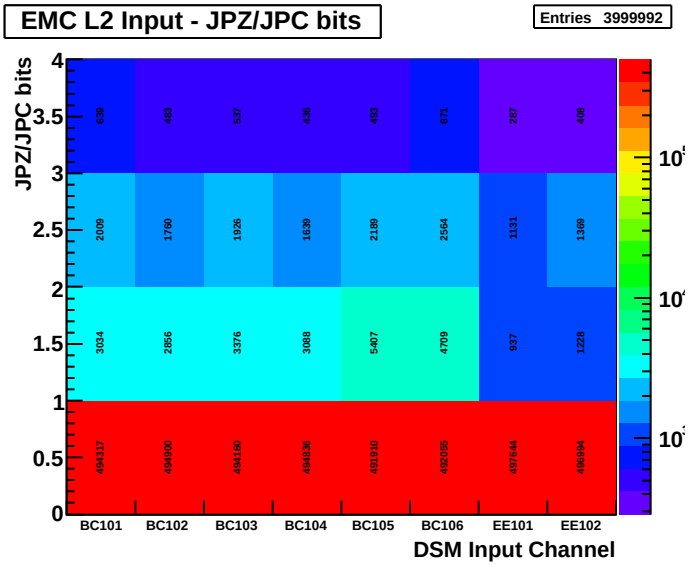

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

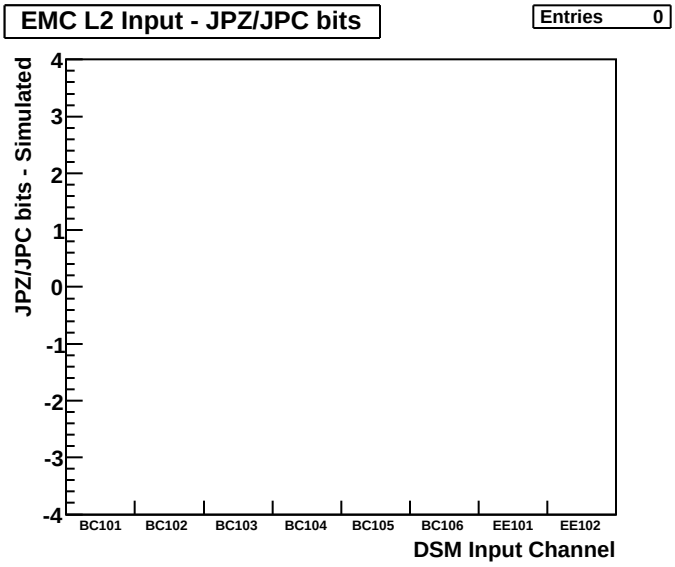

EMC L2 Input - JPX/JPA bits
Entries 3999992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1400 893 708 487 427 591 274 687
3779 2821 2316 1905 1752 1925 1015 2197
4817 4275 3636 3438 3594 3111 1063 1071
490003 492010 493339 494169 494226 494372 497647 496044

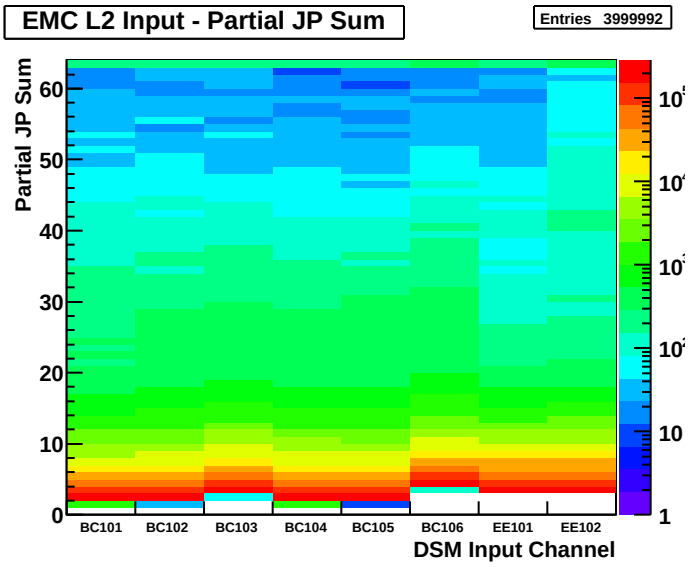

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

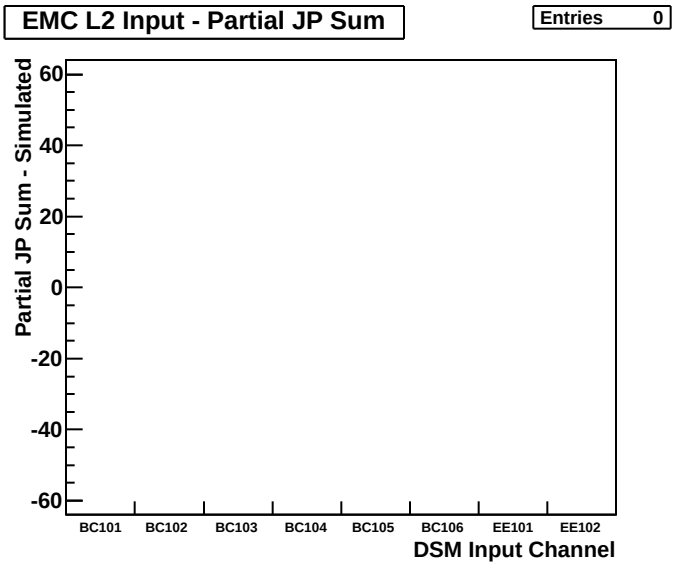

EMC L2 Input - JPY/JPB bits
Entries 3999992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
846 527 543 405 447 590 299 594
2573 1853 1773 1648 2184 2210 1041 1772
3995 2916 2995 3127 5367 3857 847 1221
492585 494703 494688 494819 492001 493342 497812 496412

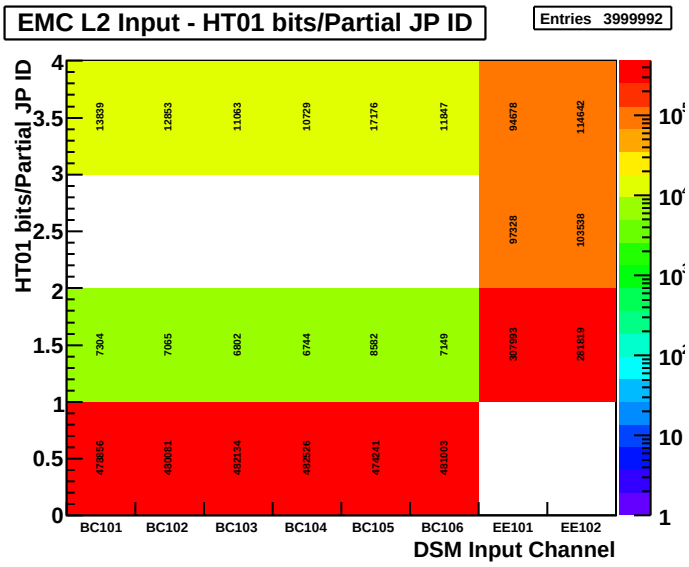

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

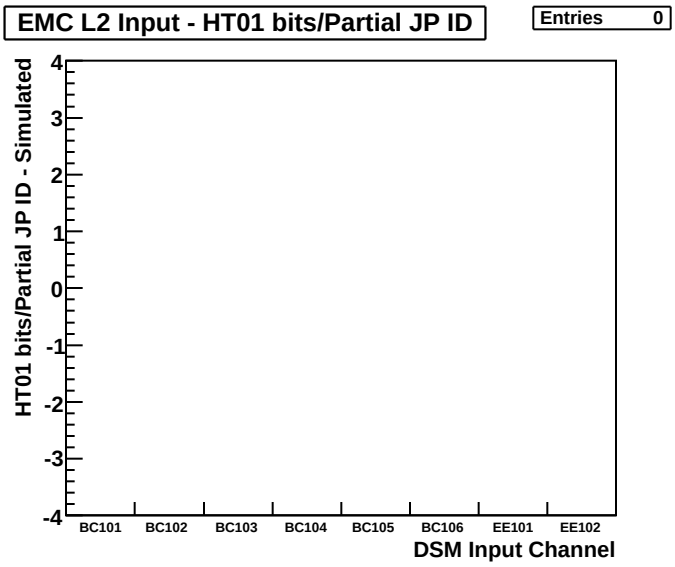

EMC L2 Input - JPZ/JPC bits
Entries 3999992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
639 483 537 436 493 671 237 408
2009 1760 1926 1639 2189 2564 1131 1369
3034 2856 3376 3088 5407 4709 937 1228
494317 494300 494160 494836 491910 492055 497644 496994

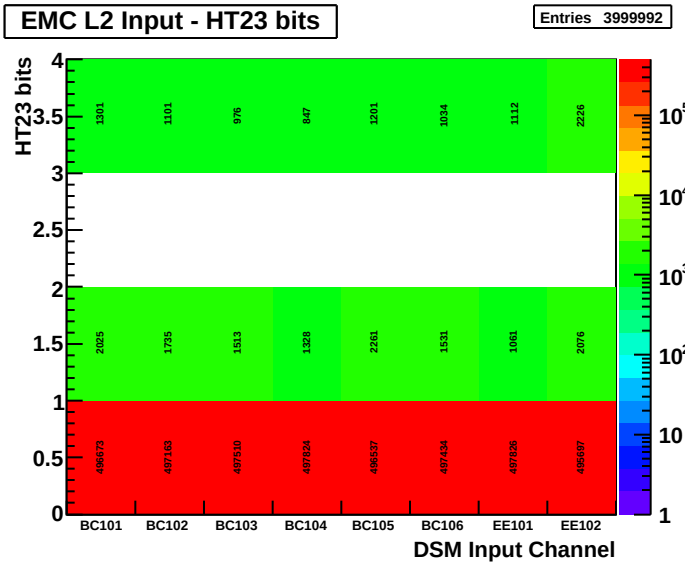

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

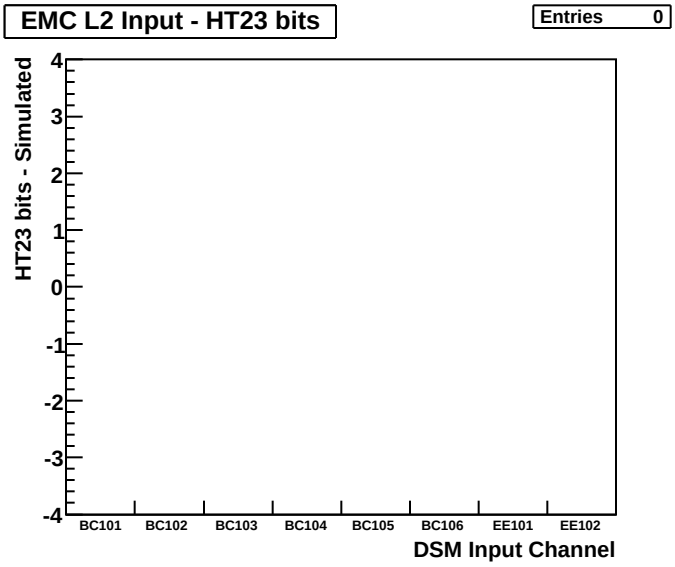

EMC L2 Input - Partial JP Sum
Entries 3999992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

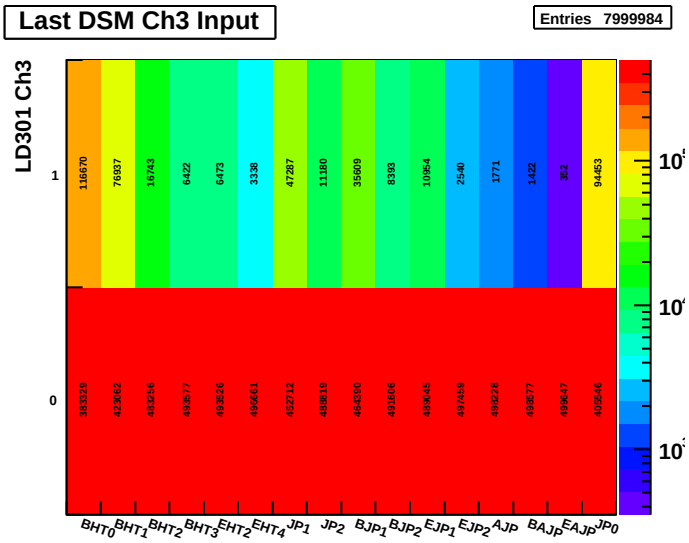

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

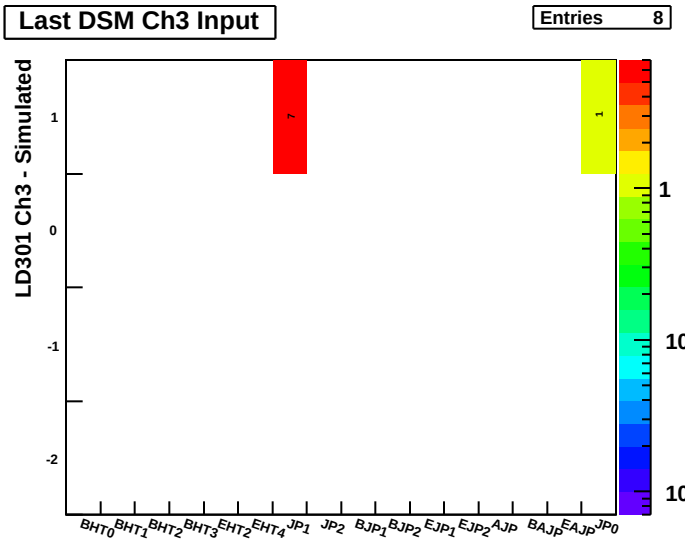

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3999992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

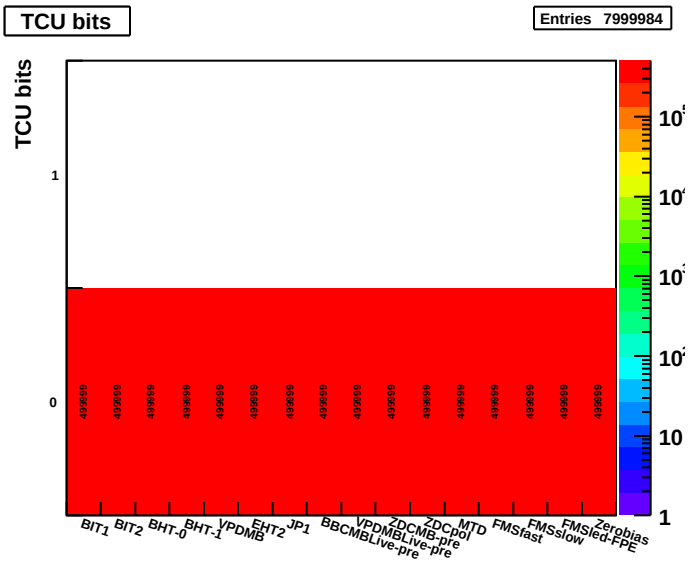

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

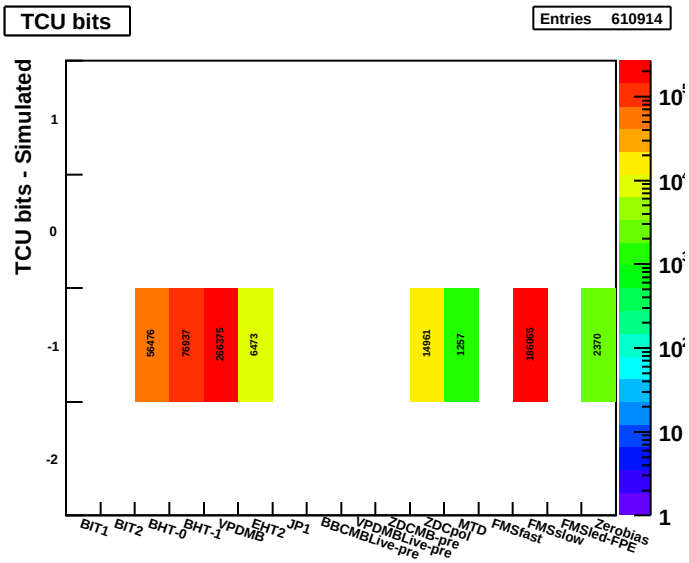

EMC L2 Input - HT23 bits
Entries 3999992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

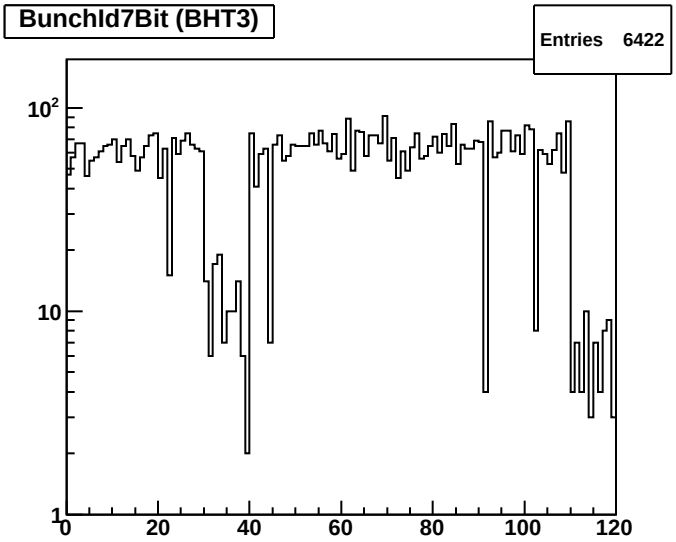

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

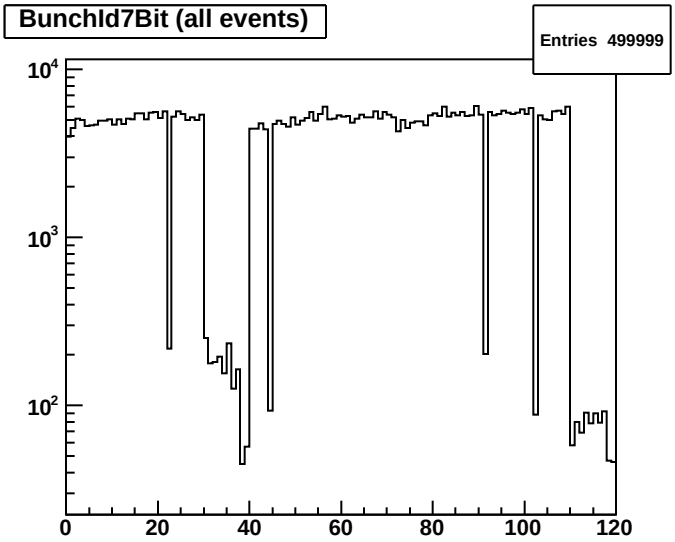

Last DSM Ch3 Input
Entries 7999984
LD301 Ch3
116670 76937 16743 6422 6473 3338 47287 11180 35609 8393 10954 2540 1771 1422 352 94453
383329 423062 483256 493577 493526 496661 452712 488819 464390 491606 489045 497459 498228 498577 499647 405546
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

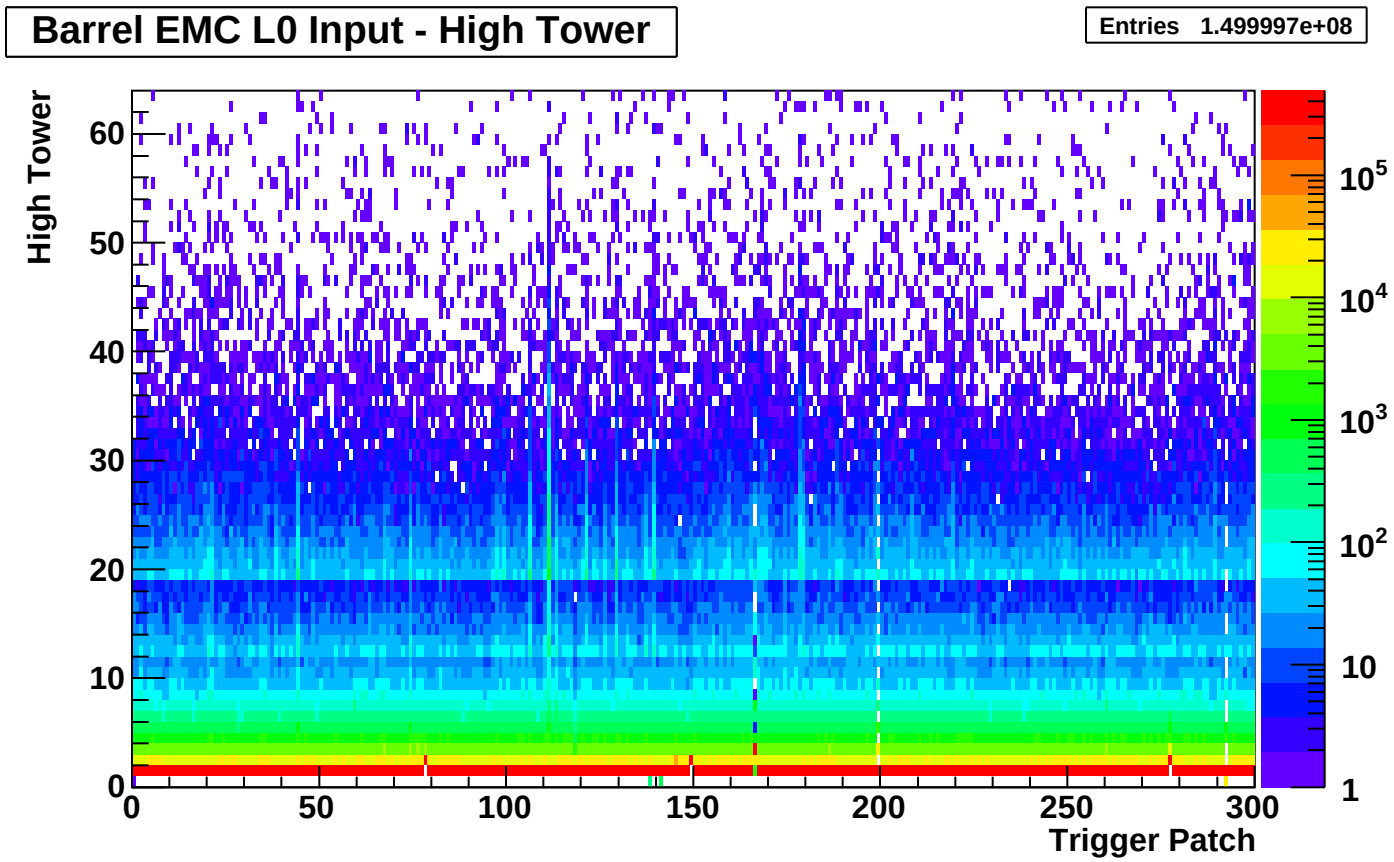

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
7
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

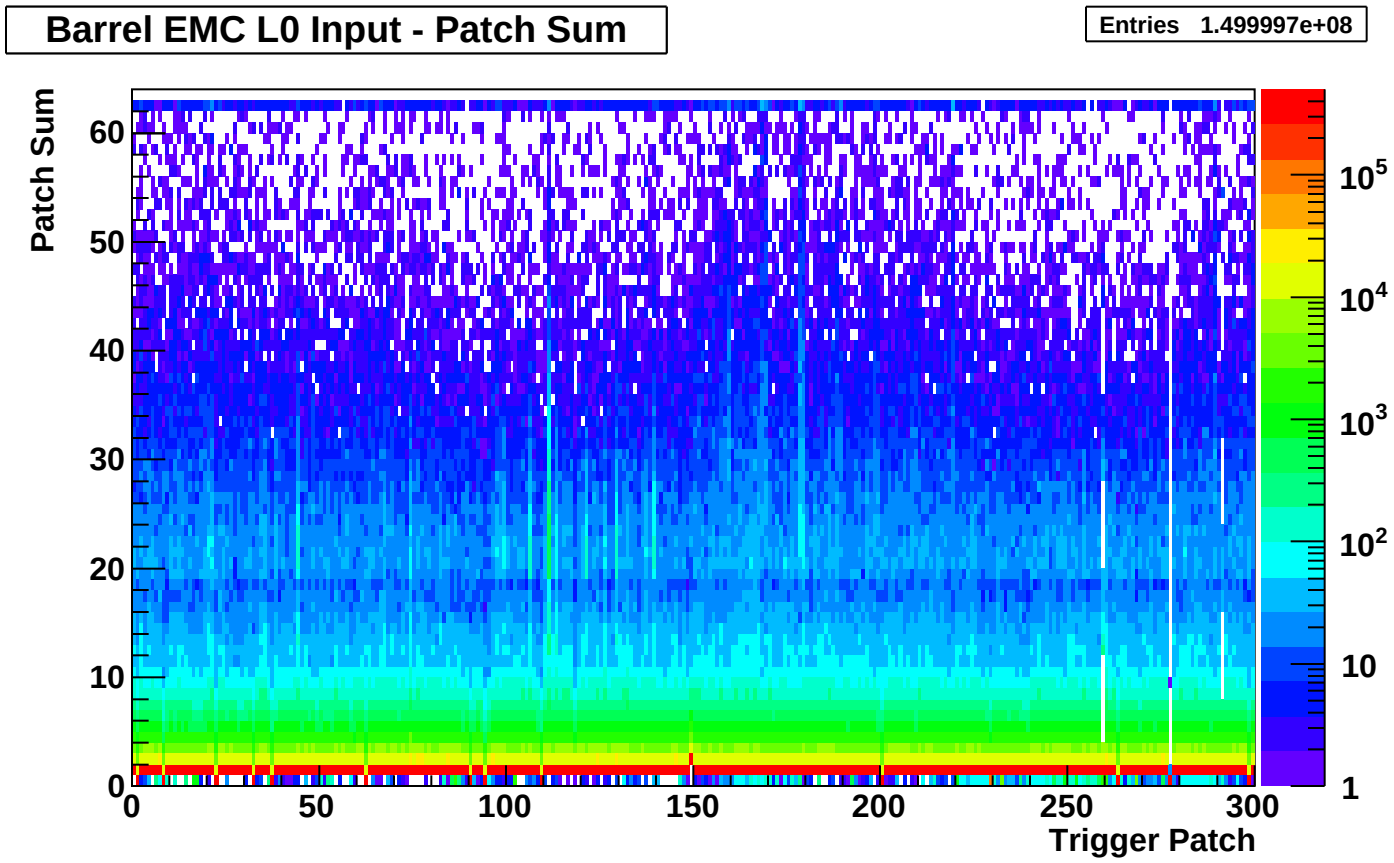

TCU bits
Entries 7999984
TCU bits
499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999 499999
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

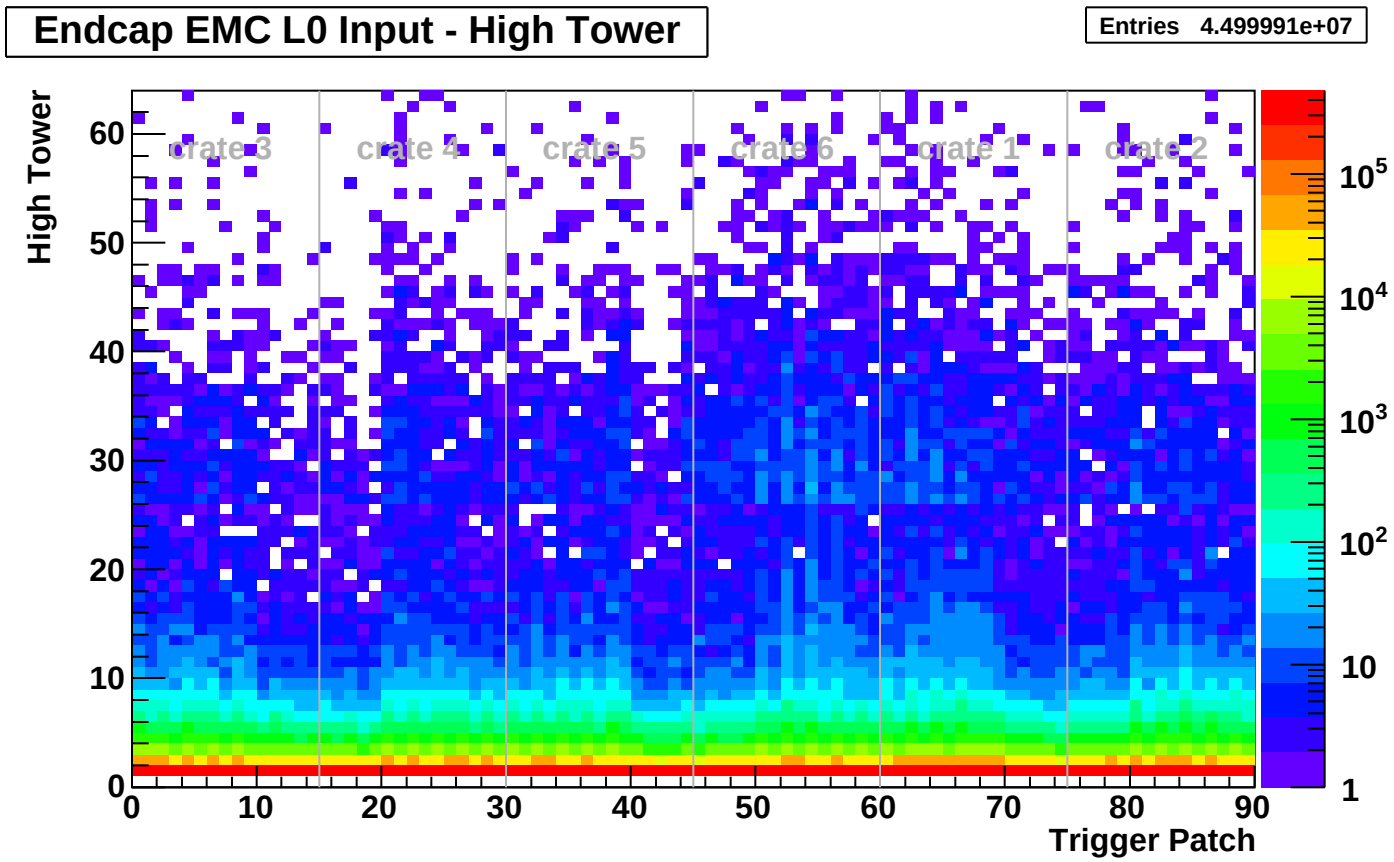

TCU bits
Entries 610914
TCU bits - Simulated
56476 76937 268375 6473 14961 1257 186065 2370
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

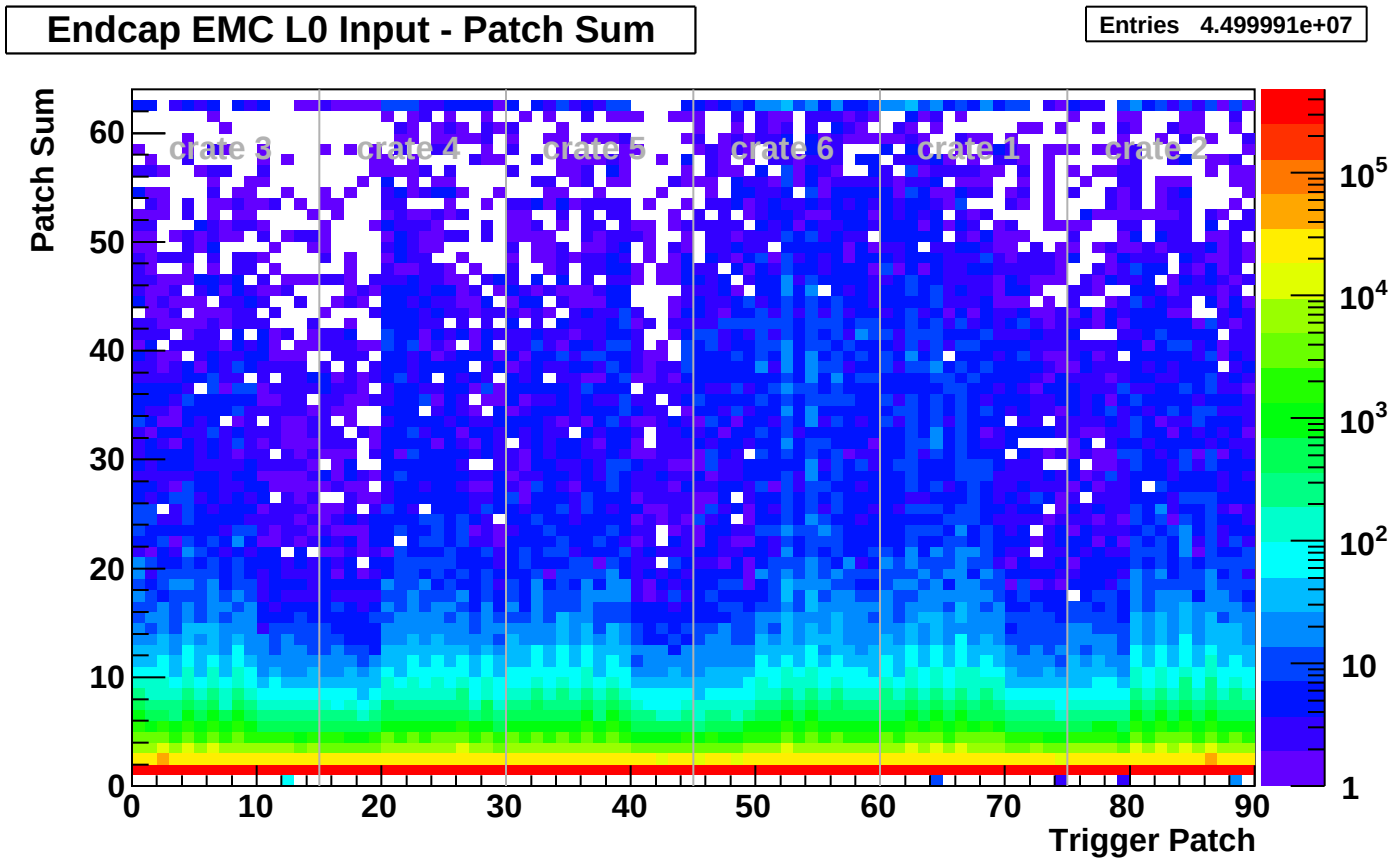

BunchId7Bit (BHT3)
Entries 6422

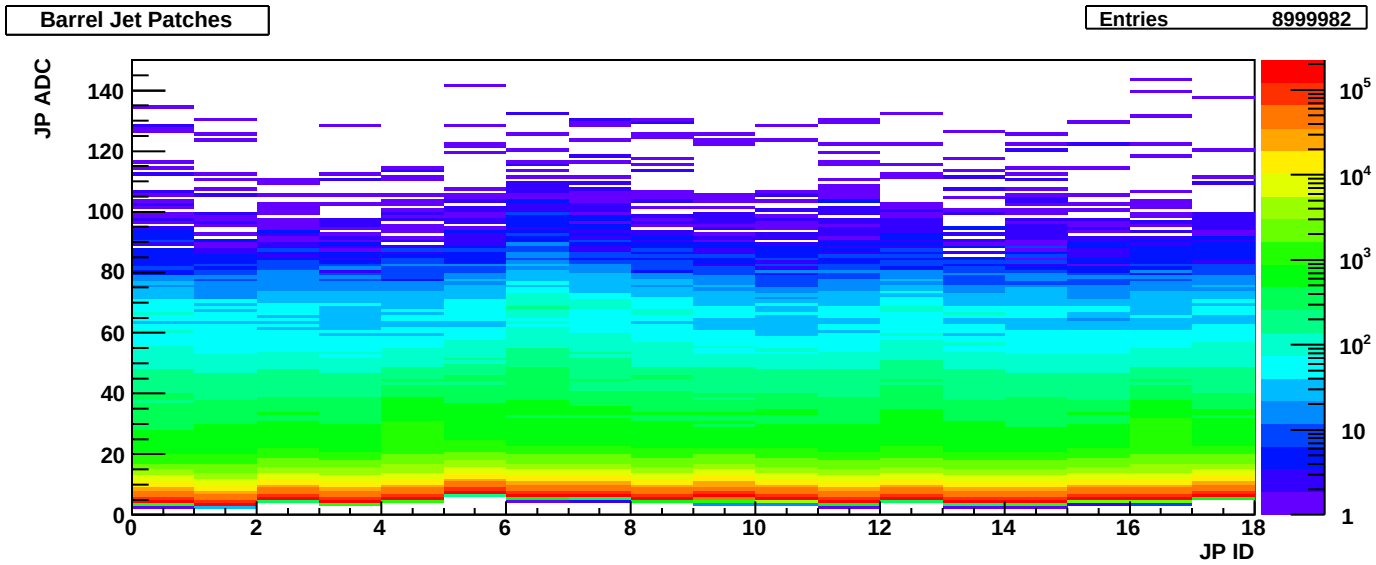

BunchId7Bit (all events)
Entries 499999

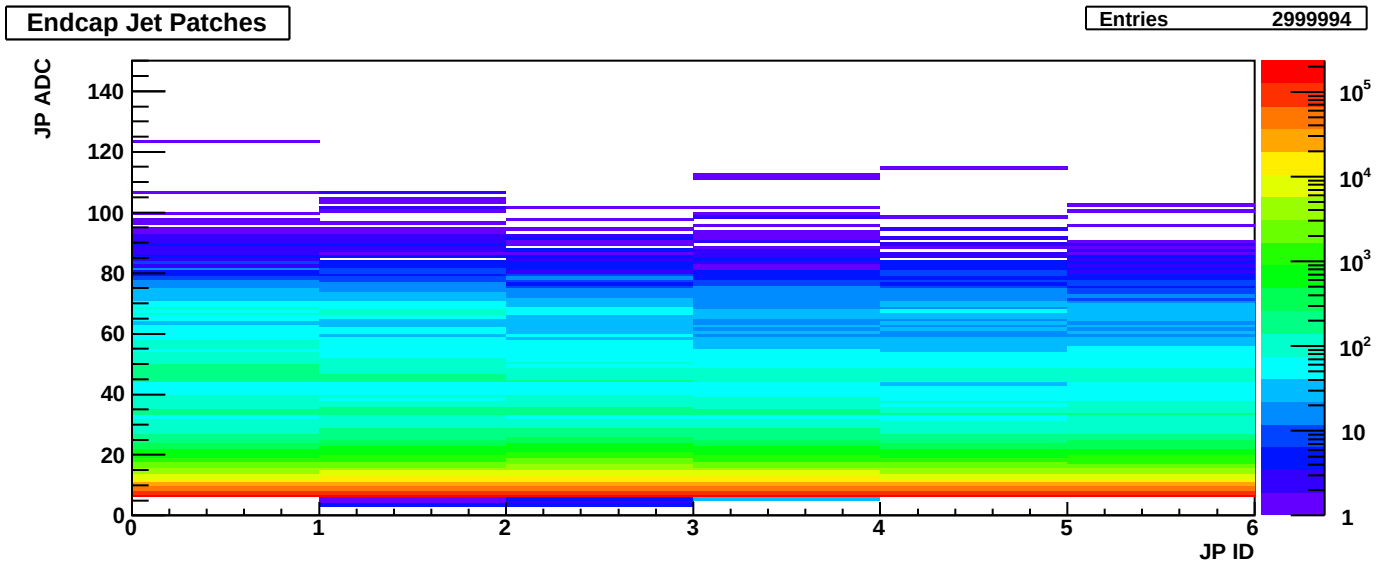

Barrel EMC L0 Input - High Tower
Entries 1.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

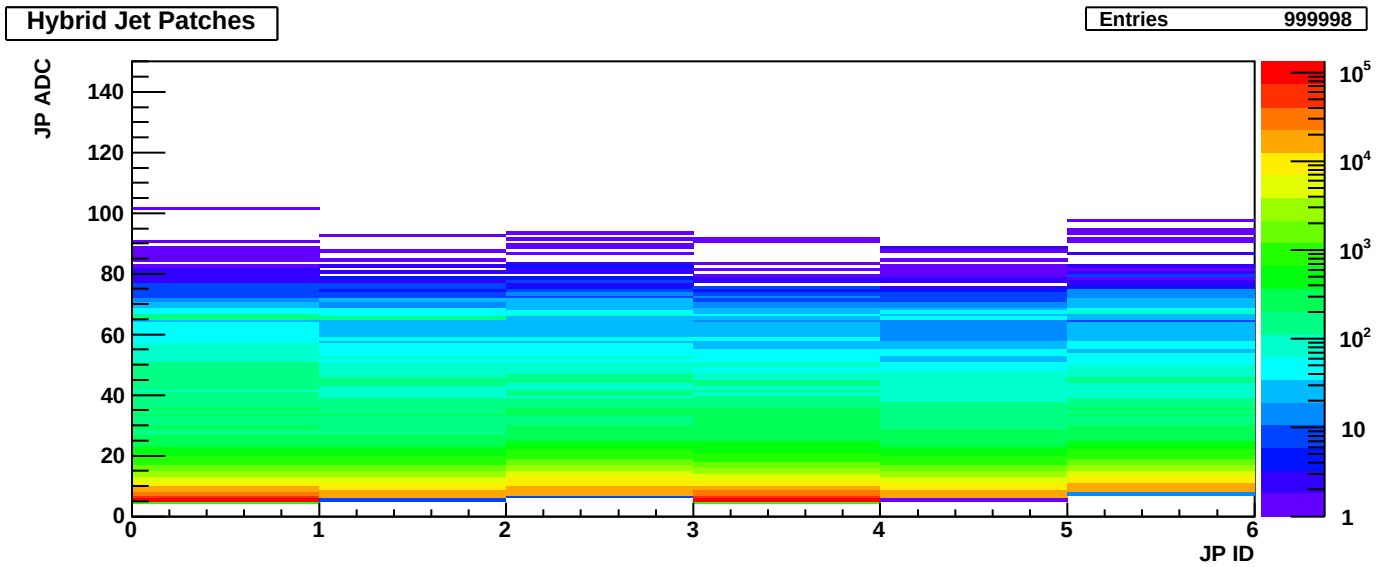

Barrel EMC L0 Input - Patch Sum
Entries 1.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

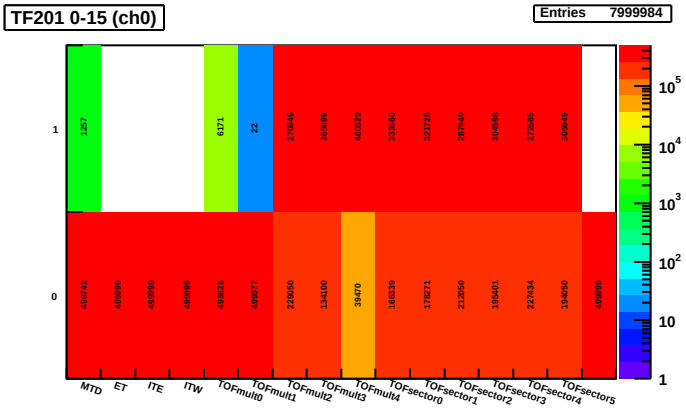

Endcap EMC L0 Input - High Tower
Entries 4.499991e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

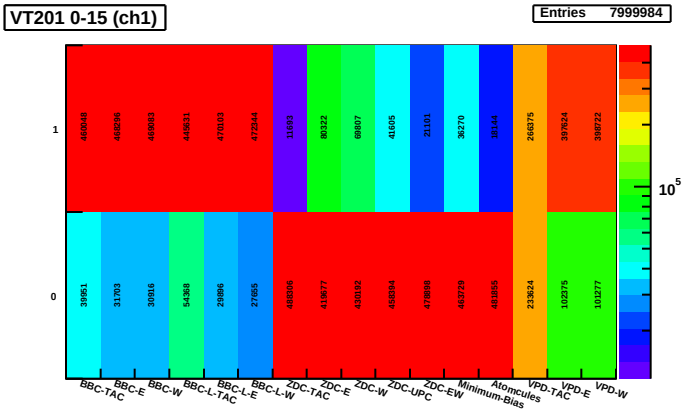

Endcap EMC L0 Input - Patch Sum
Entries 4.499991e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

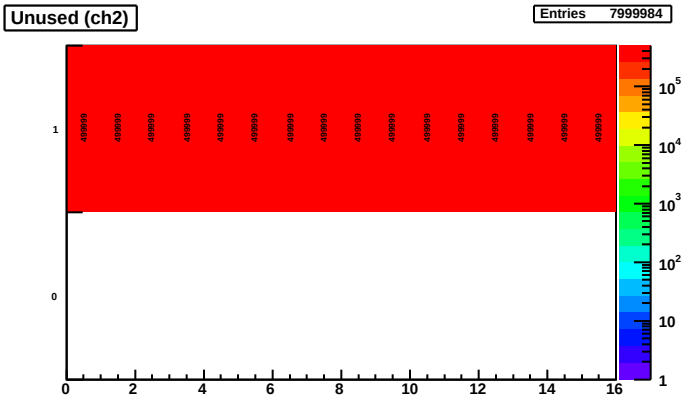

Barrel Jet Patches
Entries 8999982
JP ADC
JP ID

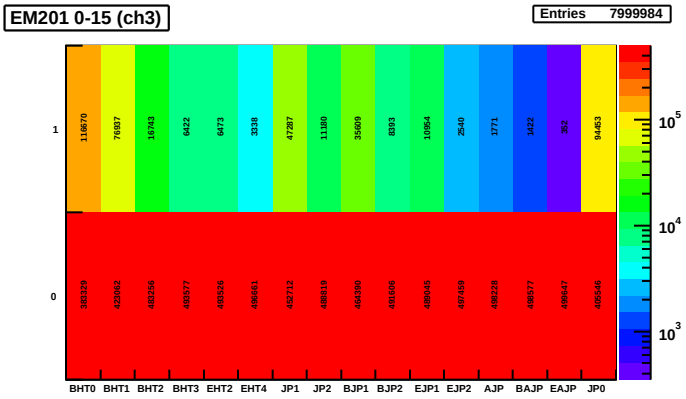

Endcap Jet Patches
Entries 2999994
JP ADC
JP ID

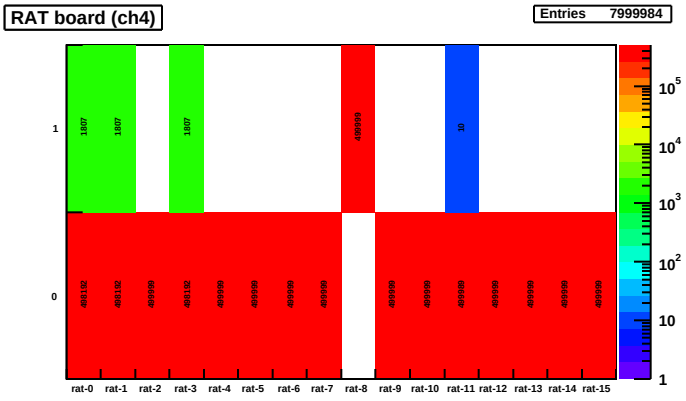

Hybrid Jet Patches
Entries 999998
JP ADC
JP ID

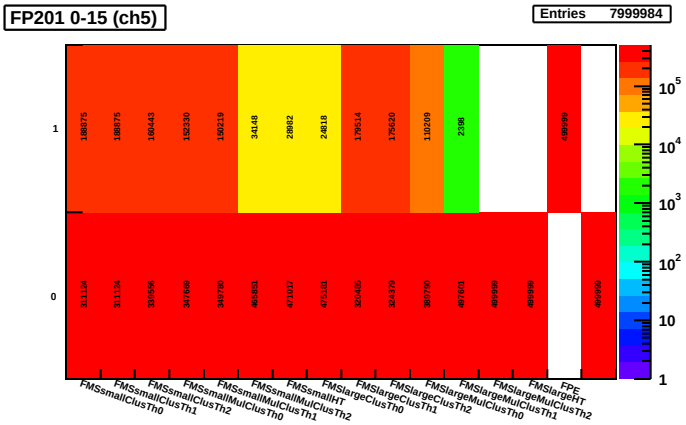

TF201 0-15 (ch0)
Entries 7999984
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

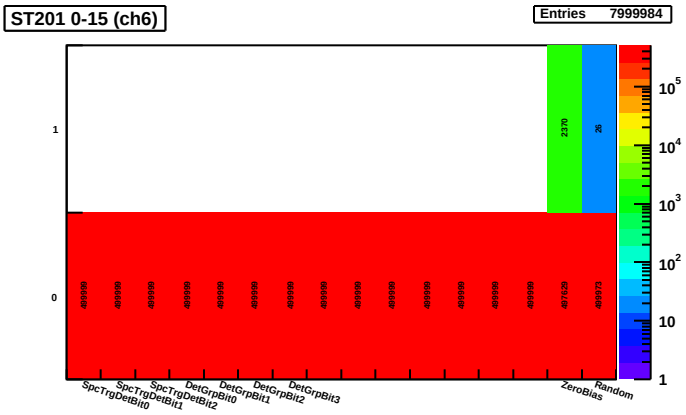

VT201 0-15 (ch1)
Entries 7999984
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

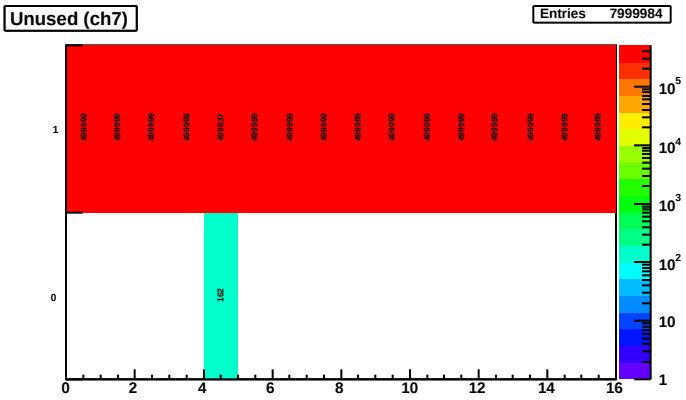

Unused (ch2)
Entries 7999984

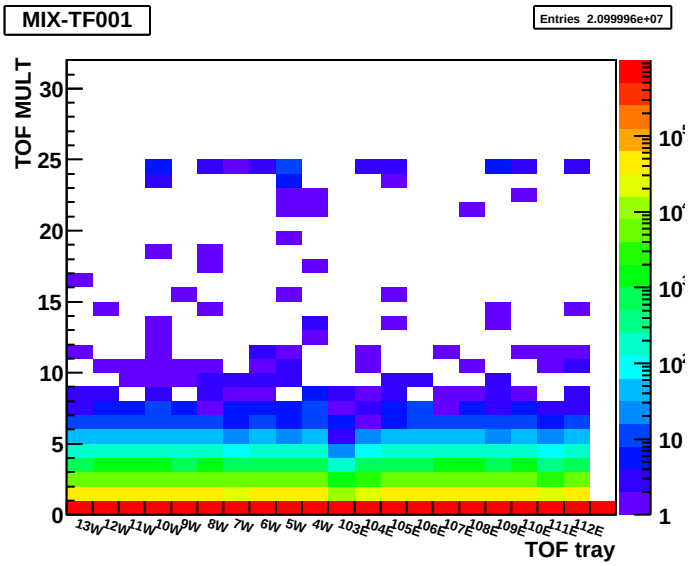

EM201 0-15 (ch3)
Entries 7999984
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

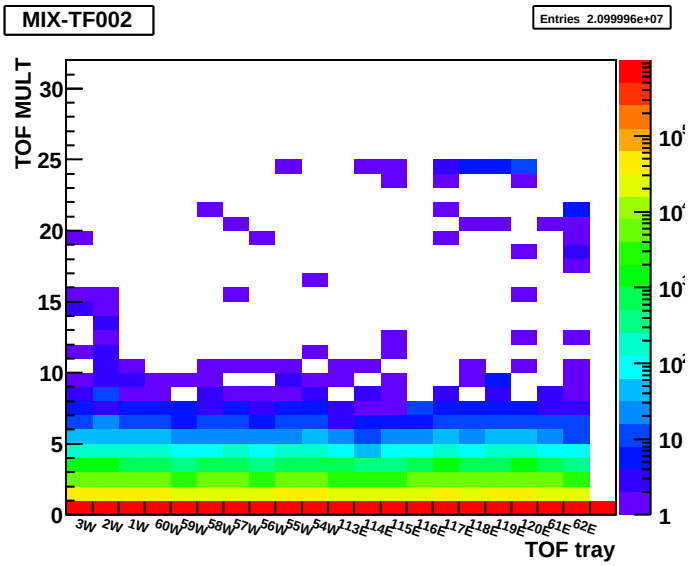

RAT board (ch4)
Entries 7999984
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

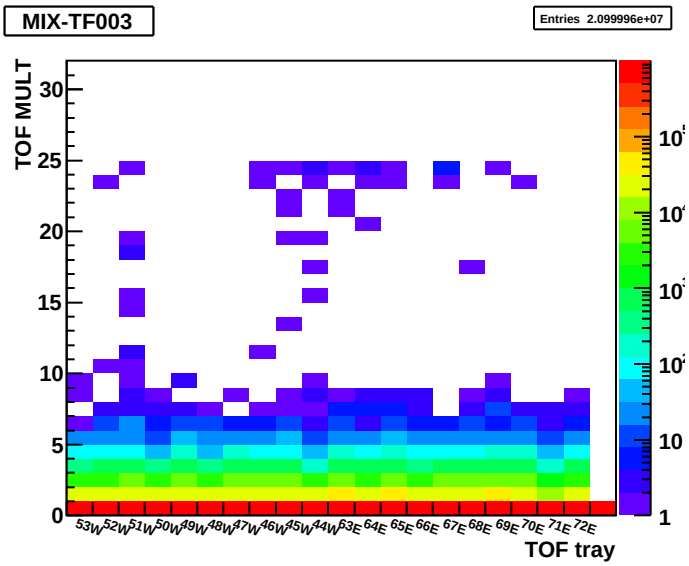

FP201 0-15 (ch5)
Entries 7999984
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

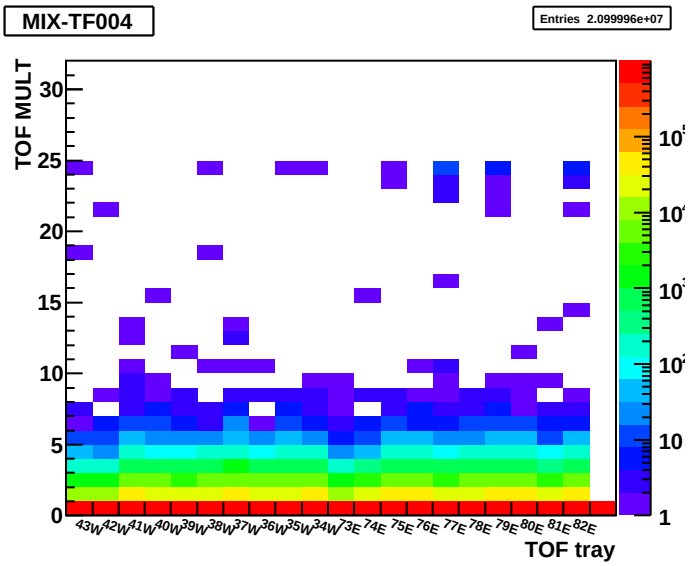

ST201 0-15 (ch6)
Entries 7999984
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

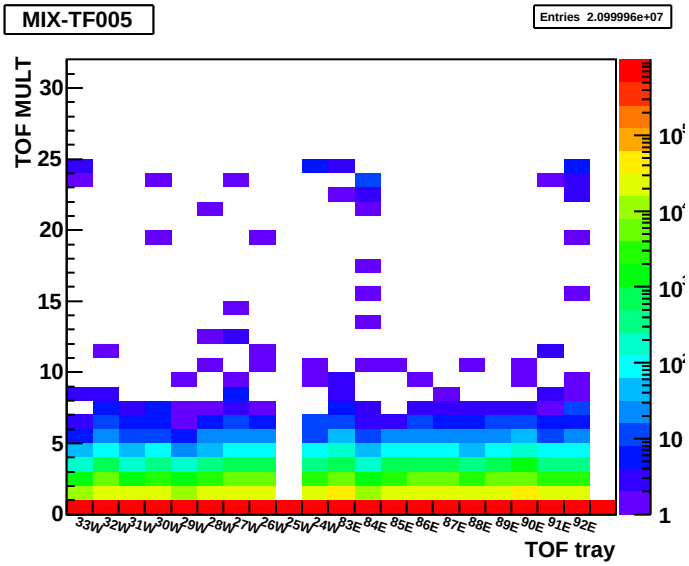

Unused (ch7)
Entries 7999984

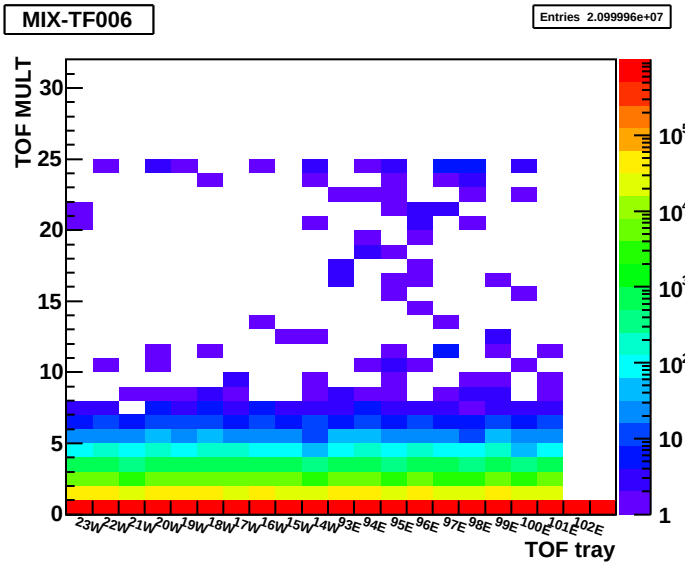

MIX-TF001
Entries 2.099996e+07
TOF MULT
TOF tray

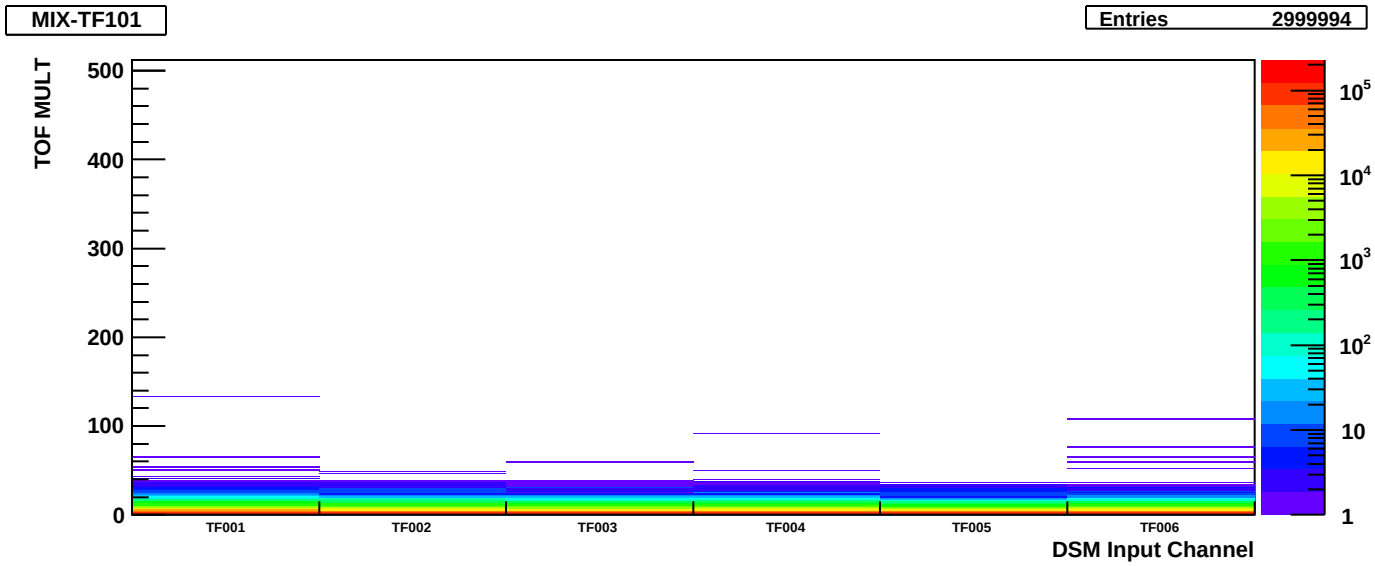

MIX-TF002
Entries 2.099996e+07
TOF MULT
TOF tray

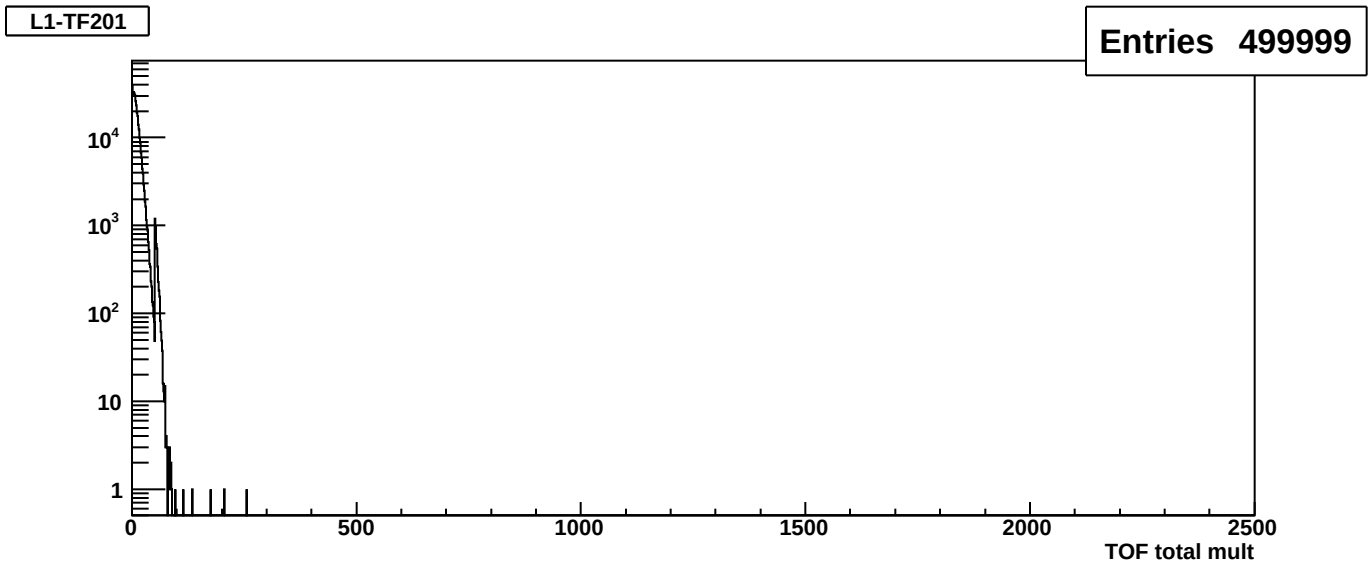

MIX-TF003
Entries 2.099996e+07
TOF MULT
TOF tray

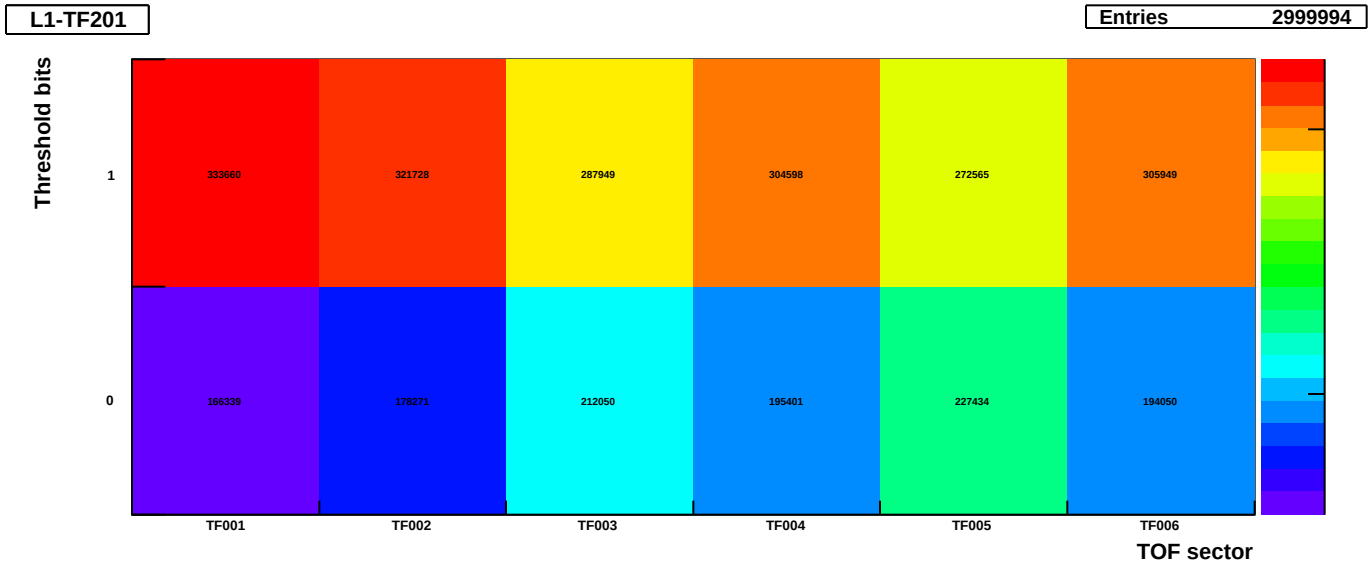

MIX-TF004
Entries 2.099996e+07
TOF MULT
TOF tray

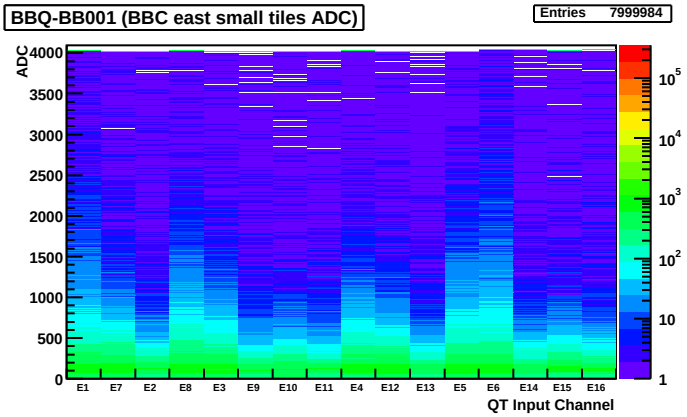

MIX-TF005
Entries 2.099996e+07
TOF MULT
TOF tray

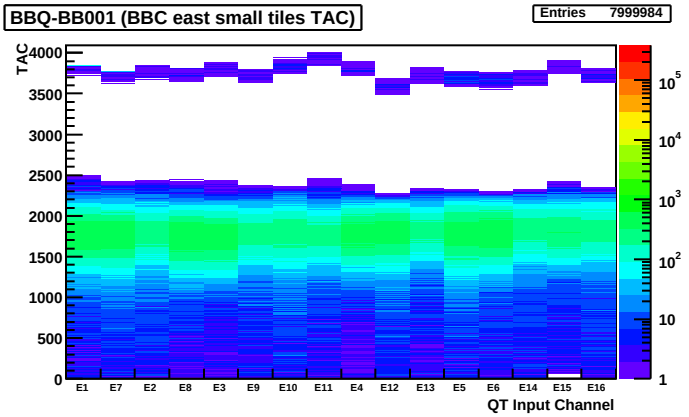

MIX-TF006
Entries 2.099996e+07
TOF MULT
TOF tray

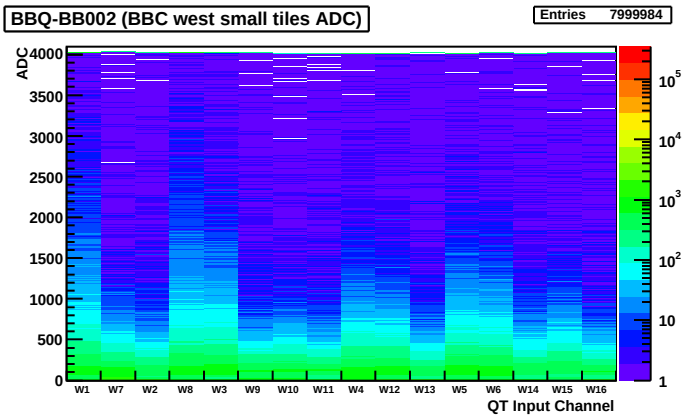

MIX-TF101
Entries 2999994
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

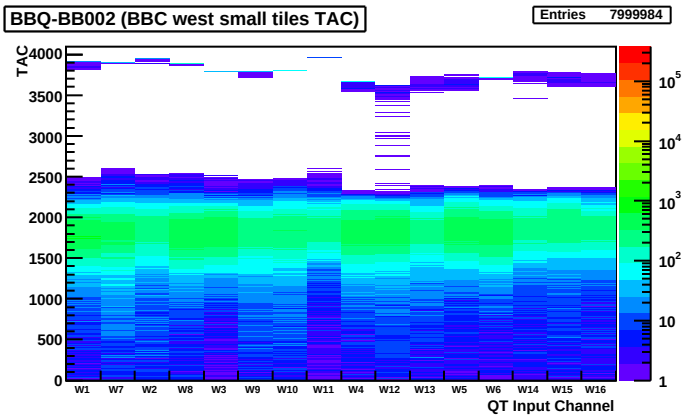

L1-TF201
Entries 499999
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

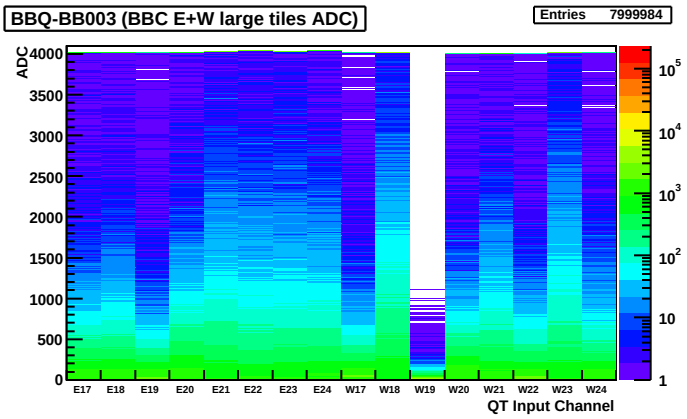

L1-TF201
Entries 2999994
Threshold bits
1
0
333660
321728
287949
304598
272565
305949
166339
178271
212050
195401
227434
194050
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

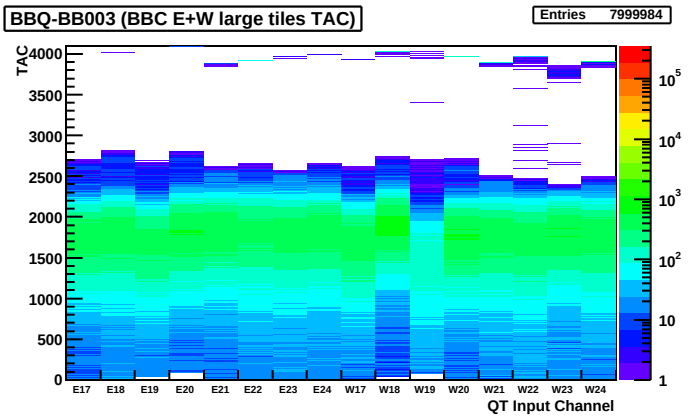

BBQ-BB001 (BBC east small tiles ADC)
Entries 7999984
ADC
QT Input Channel

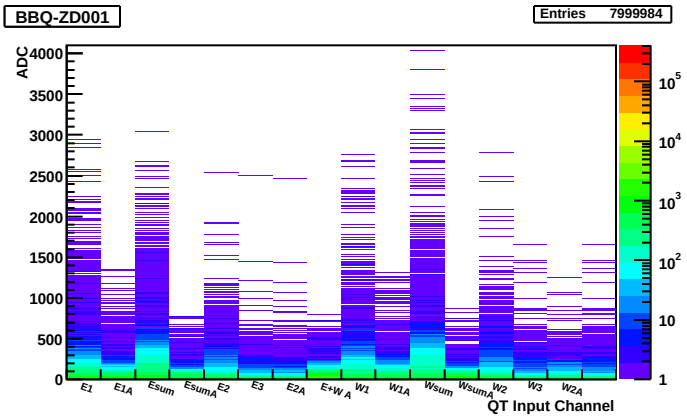

BBQ-BB001 (BBC east small tiles TAC)
Entries 7999984
TAC
QT Input Channel

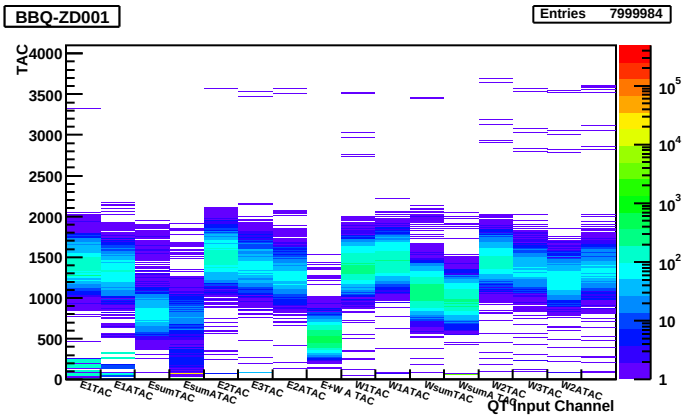

BBQ-BB002 (BBC west small tiles ADC)
Entries 7999984
ADC
QT Input Channel

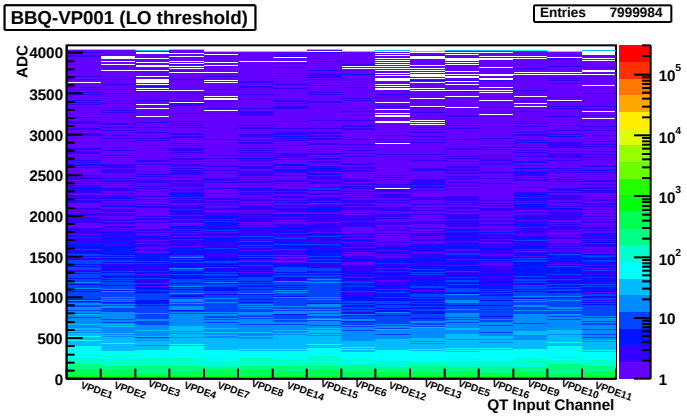

BBQ-BB002 (BBC west small tiles TAC)
Entries 7999984
TAC
QT Input Channel

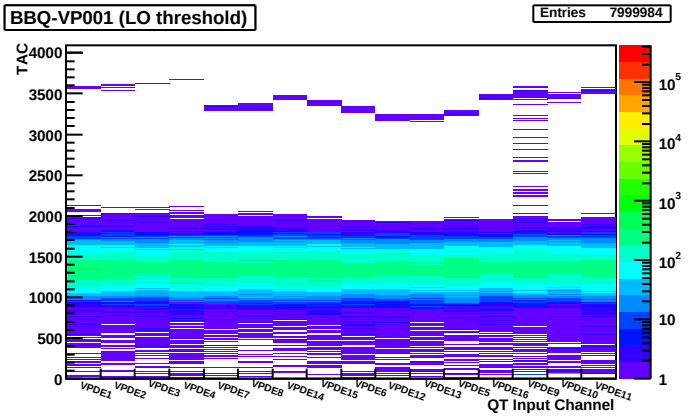

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7999984
ADC
QT Input Channel

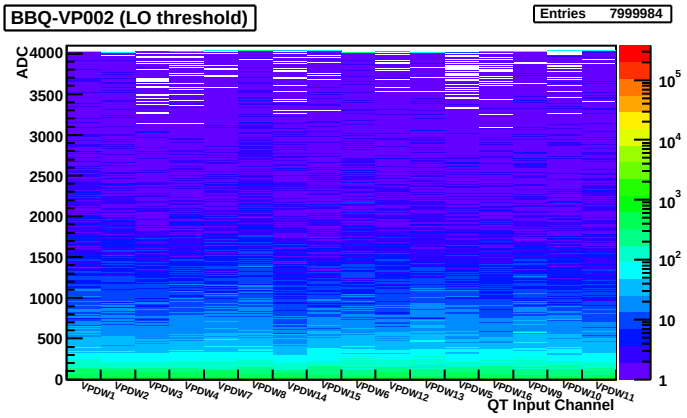

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7999984
TAC
QT Input Channel

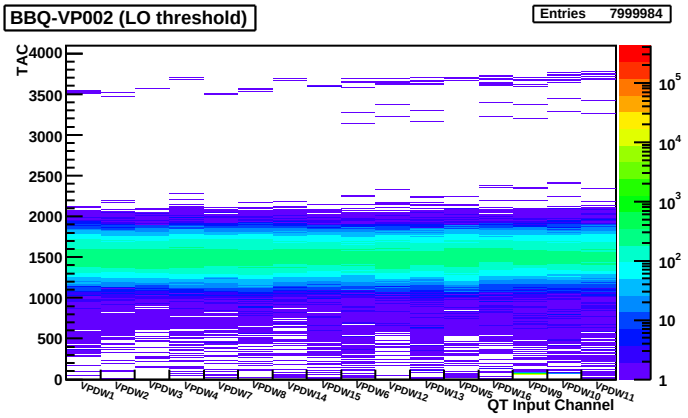

BBQ-ZD001
Entries 7999984
ADC
QT Input Channel

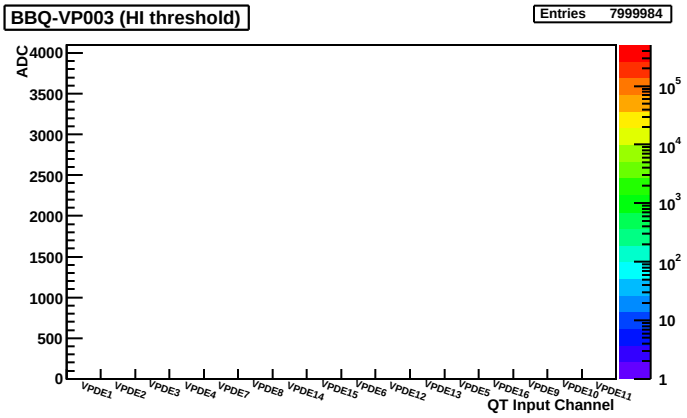

BBQ-ZD001
Entries 7999984
TAC
QT Input Channel

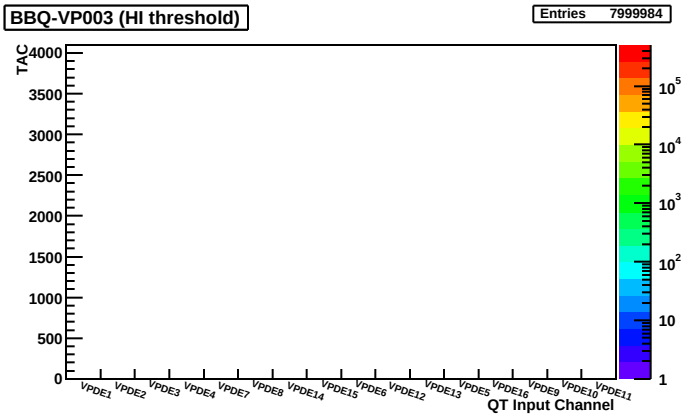

BBQ-VP001 (LO threshold)
Entries 7999984
ADC
QT Input Channel

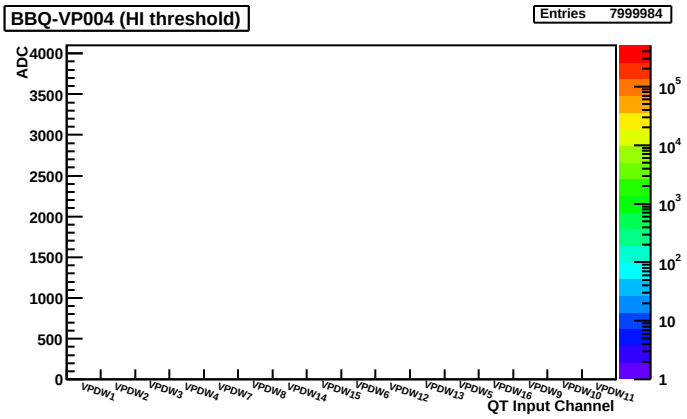

BBQ-VP001 (LO threshold)
Entries 7999984
TAC
QT Input Channel

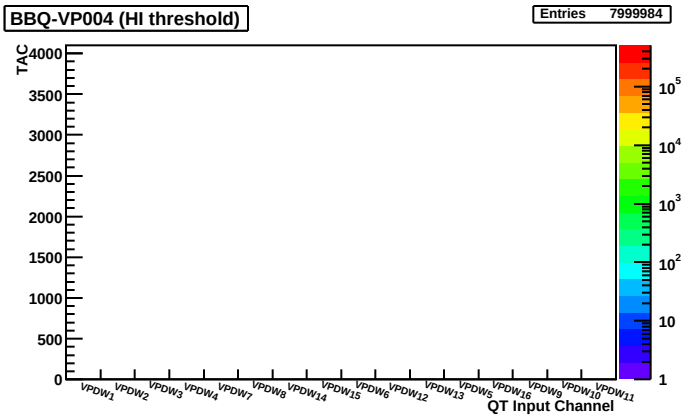

BBQ-VP002 (LO threshold)
Entries 7999984
ADC
QT Input Channel

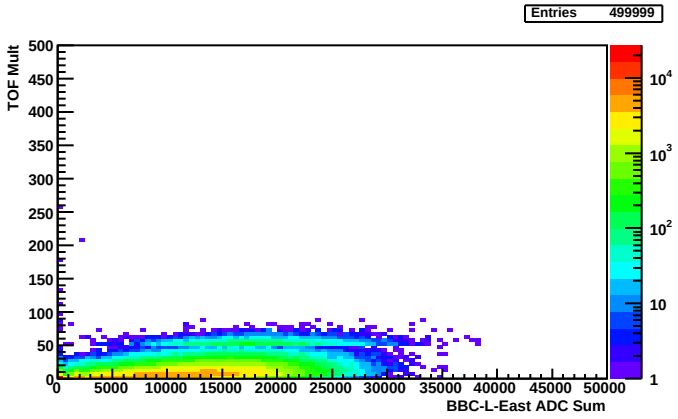

BBQ-VP002 (LO threshold)
Entries 7999984
TAC
QT Input Channel

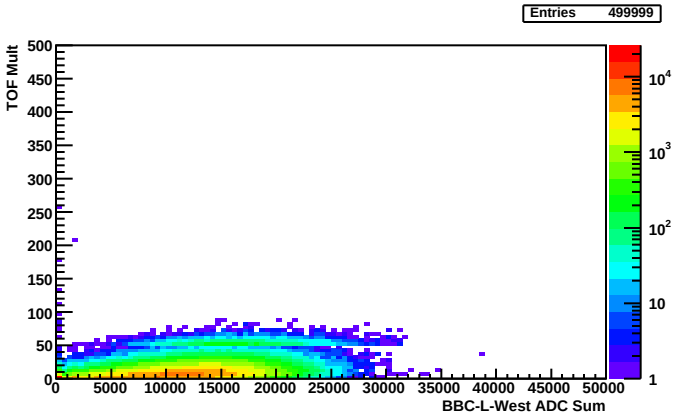

BBQ-VP003 (HI threshold)
Entries 7999984
ADC
QT Input Channel

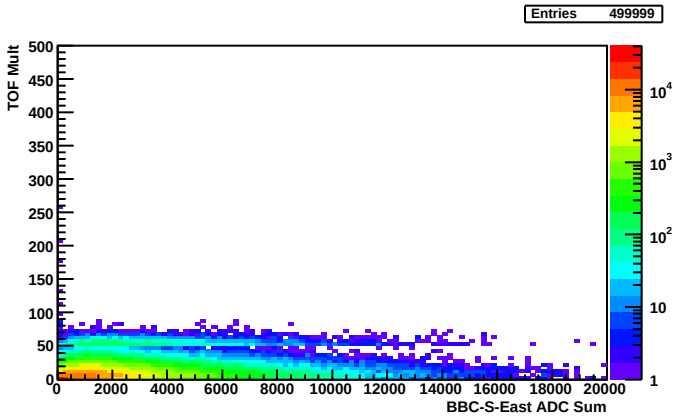

BBQ-VP003 (HI threshold)
Entries 7999984
TAC
QT Input Channel

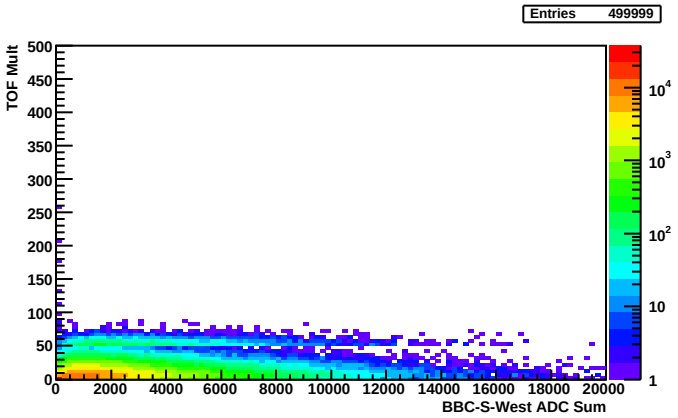

BBQ-VP004 (HI threshold)
Entries 7999984
ADC
QT Input Channel

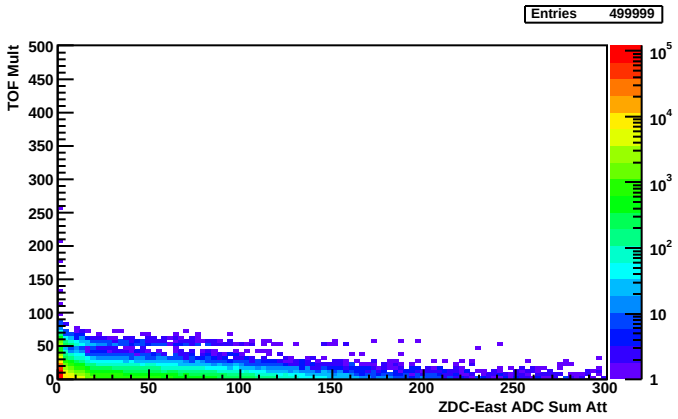

BBQ-VP004 (HI threshold)
Entries 7999984
TAC
QT Input Channel

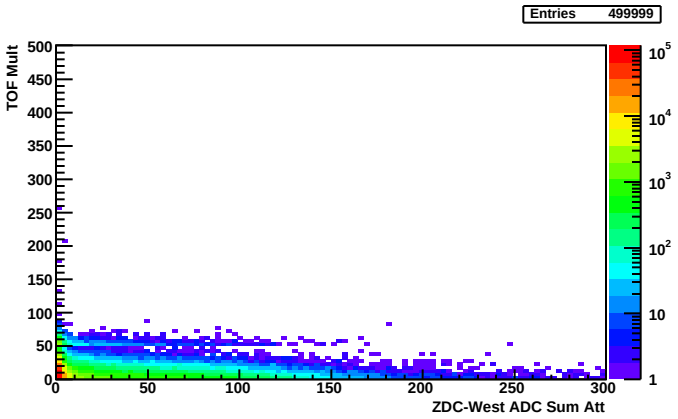

Entries 499999
TOF Mult
BBC-L-East ADC Sum

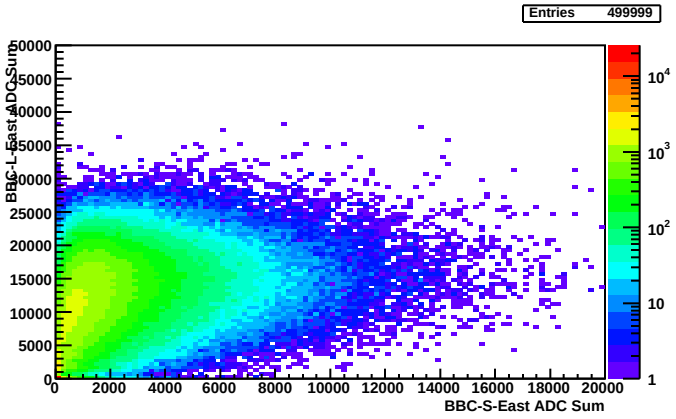

Entries 499999
TOF Mult
BBC-L-West ADC Sum

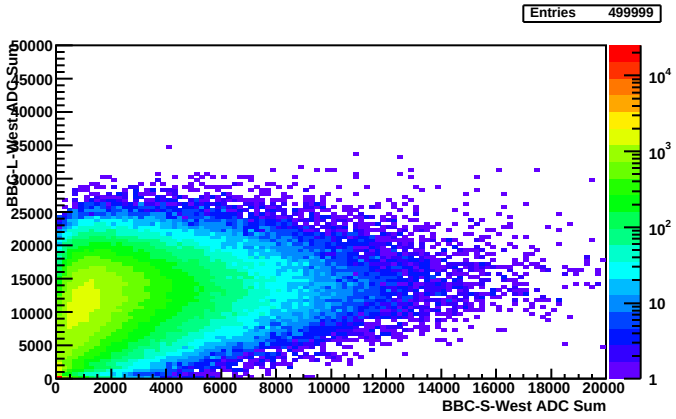

Entries 499999
TOF Mult
BBC-S-East ADC Sum

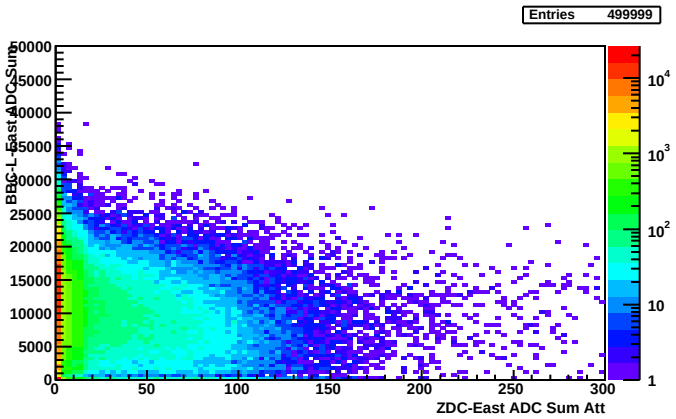

Entries 499999
TOF Mult
BBC-S-West ADC Sum

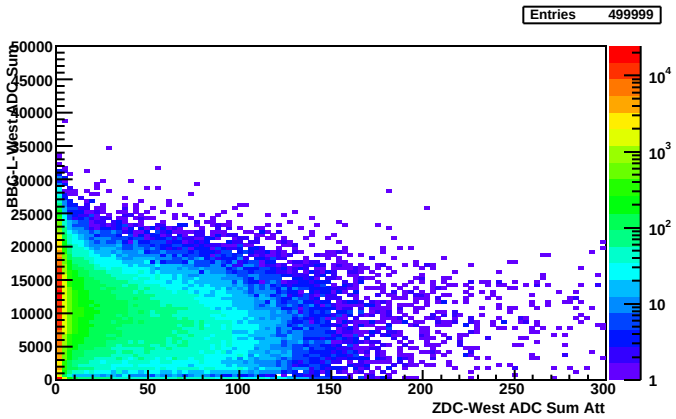

Entries 499999
TOF Mult
ZDC-East ADC Sum Att

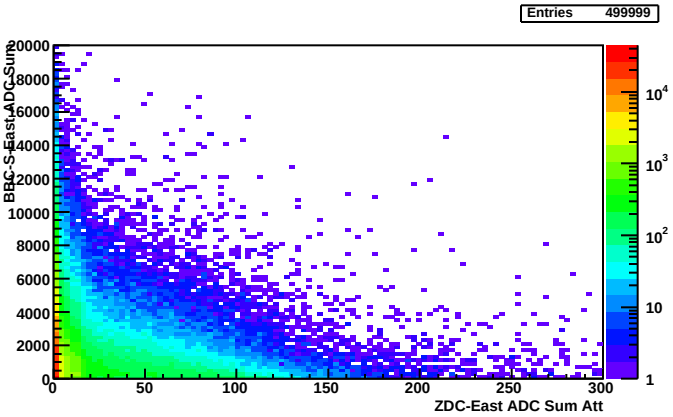

Entries 499999
TOF Mult
ZDC-West ADC Sum Att

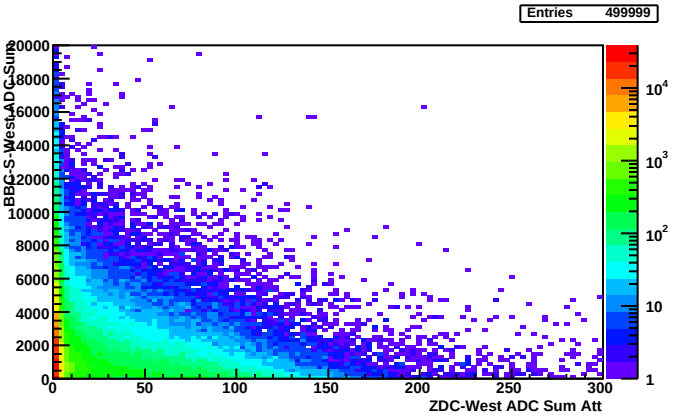

Entries 499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

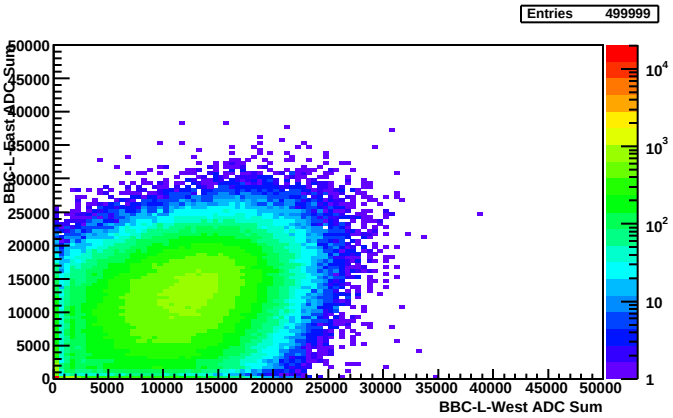

Entries 499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

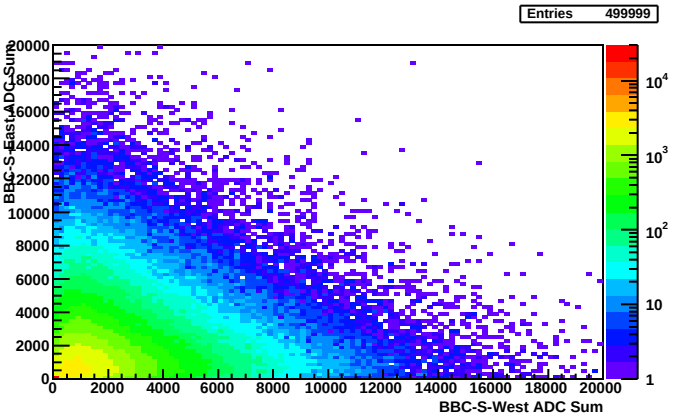

Entries 499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

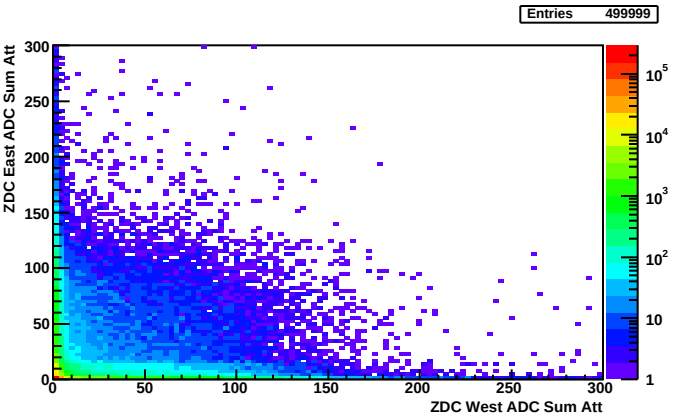

Entries 499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

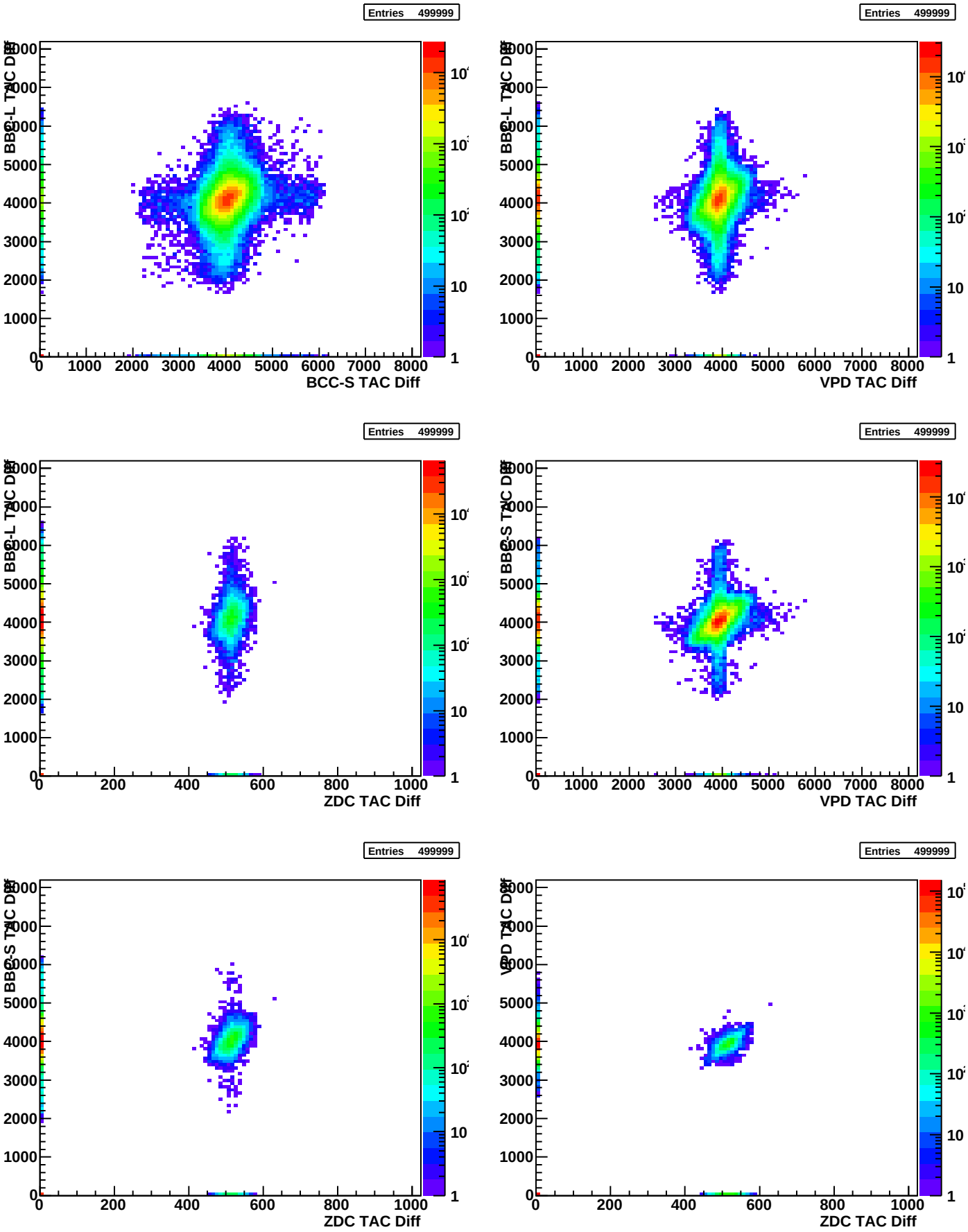

Entries 499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

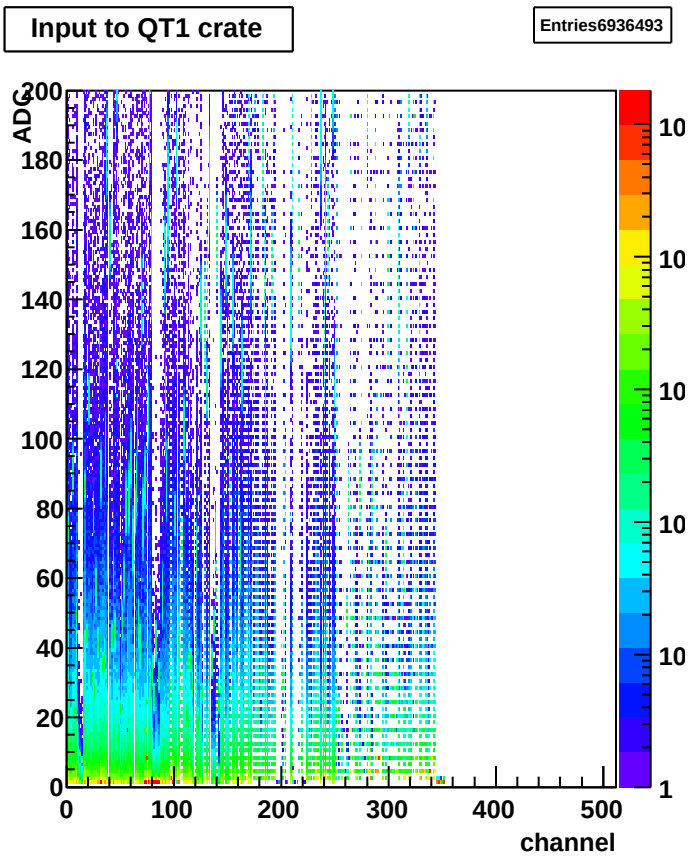

Entries 499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

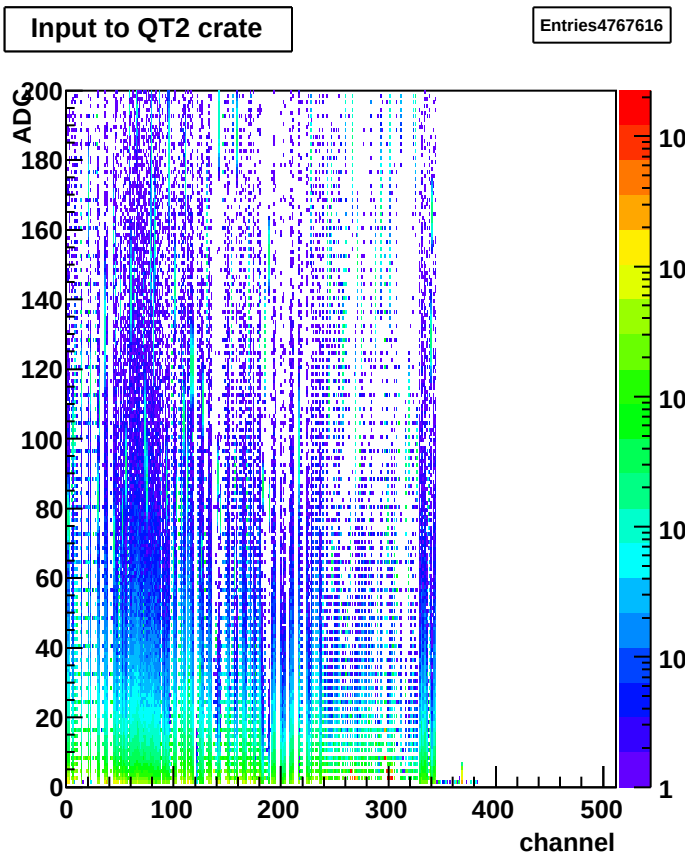

Entries 499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

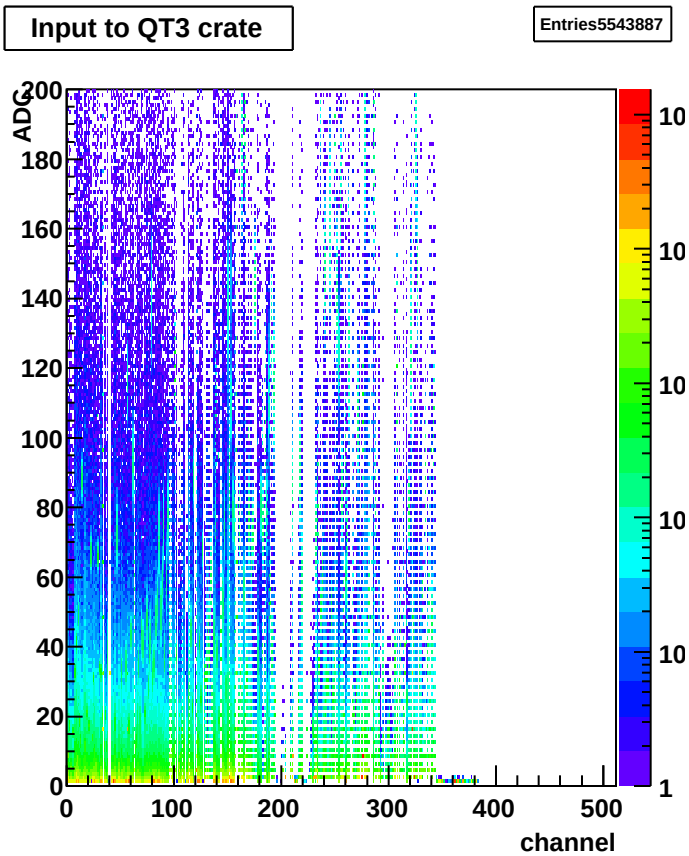

Entries 499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

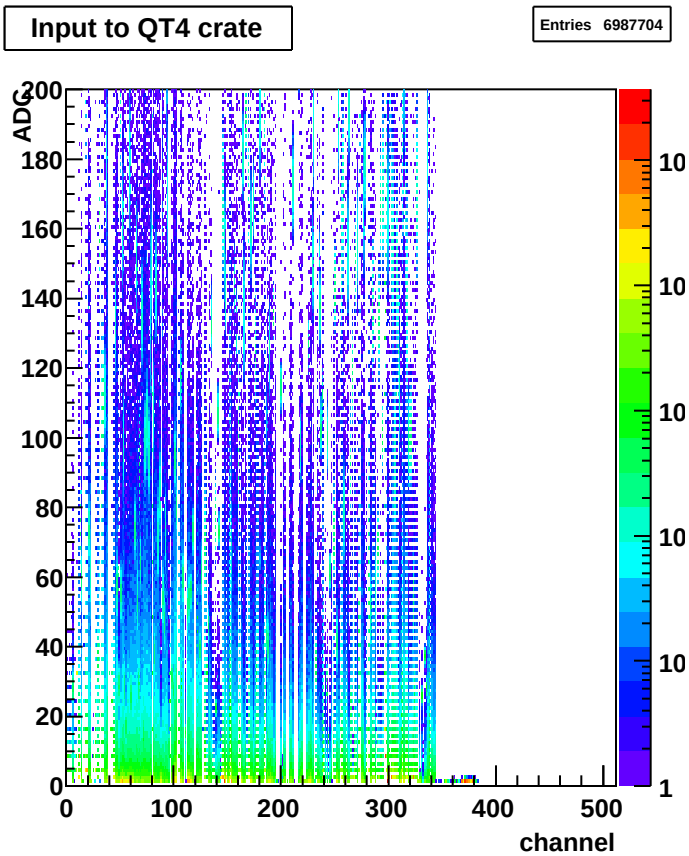

Entries 499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

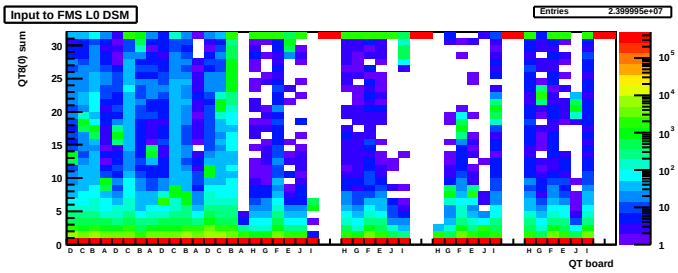

Entries 499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 499999
BBC-L TAC Diff
VPD TAC Diff
Entries 499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 499999
BBC-S TAC Diff
VPD TAC Diff
Entries 499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 499999
VPD TAC Diff
ZDC TAC Diff

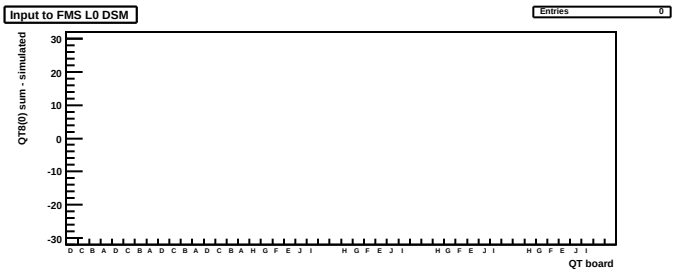

Input to QT1 crate
Entries6936493
ADC
channel

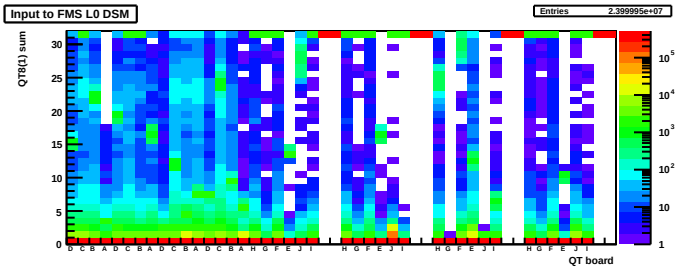

Input to QT2 crate
Entries4767616
ADC
channel

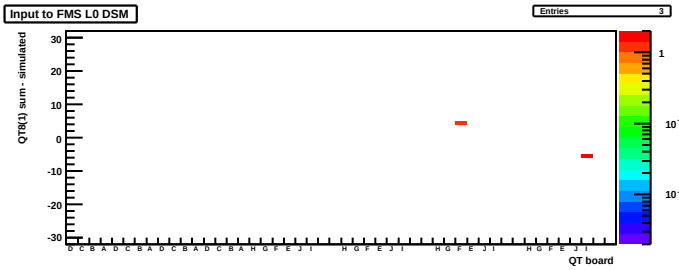

Input to QT3 crate
Entries5543887
ADC
channel

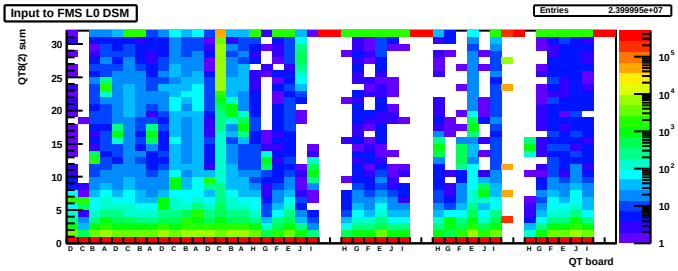

Input to QT4 crate
Entries 6987704
ADC
channel

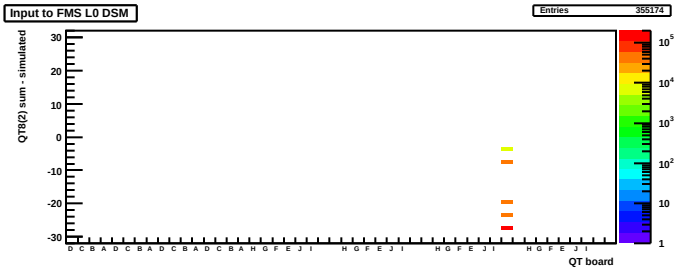

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(0) sum
QT board

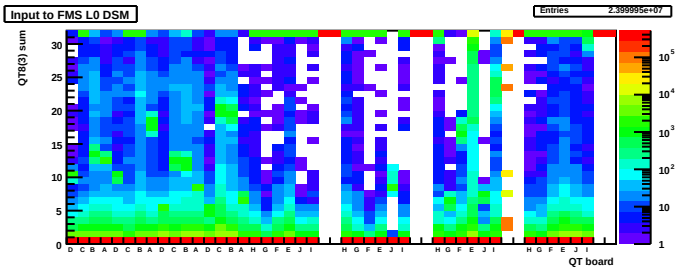

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

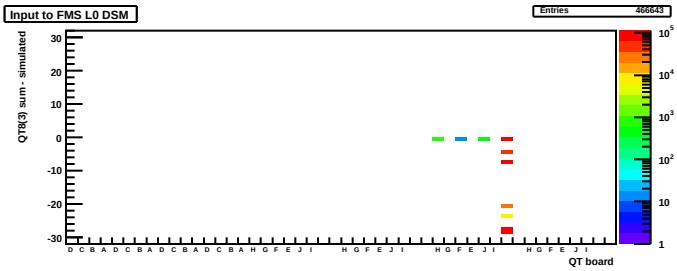

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(1) sum
QT board

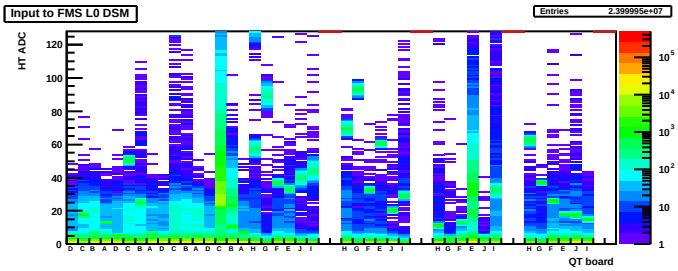

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

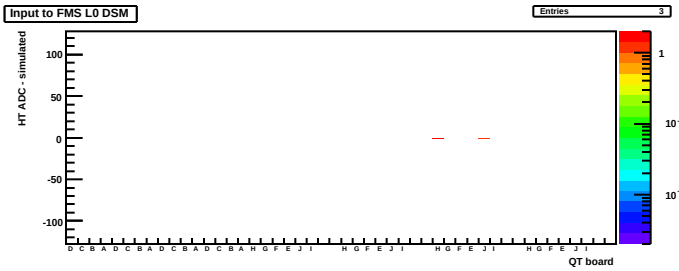

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(2) sum
QT board

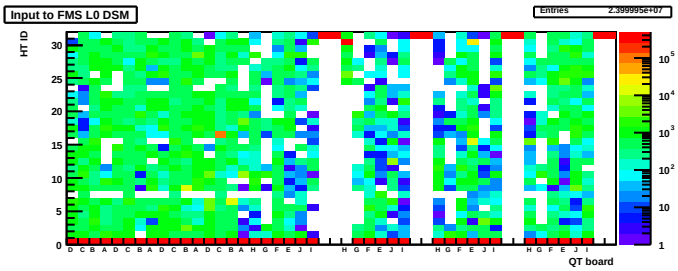

Input to FMS L0 DSM
Entries 355174
QT8(2) sum - simulated
QT board

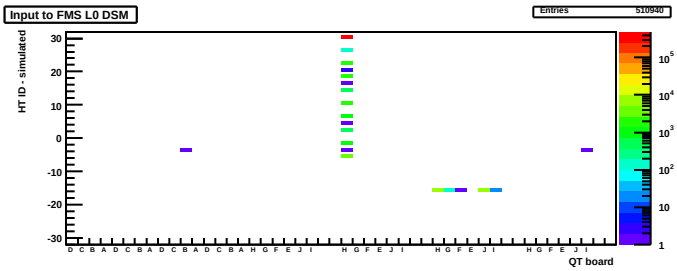

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(3) sum
QT board

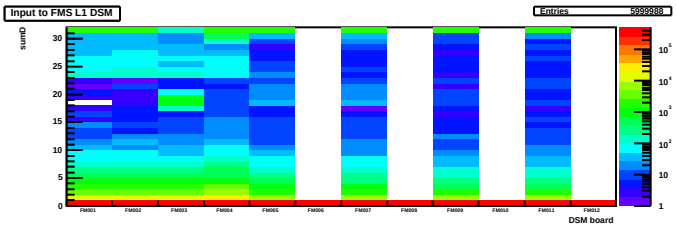

Input to FMS L0 DSM
Entries 466643
QT8(3) sum - simulated
QT board

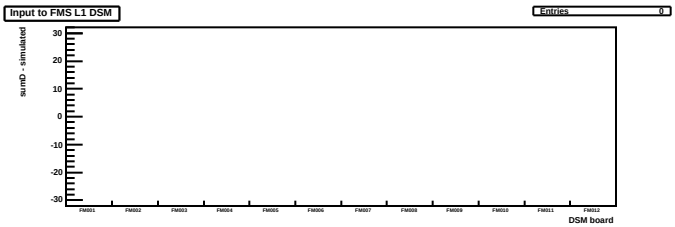

Input to FMS L0 DSM
Entries 2.399995e+07
HT ADC
QT board

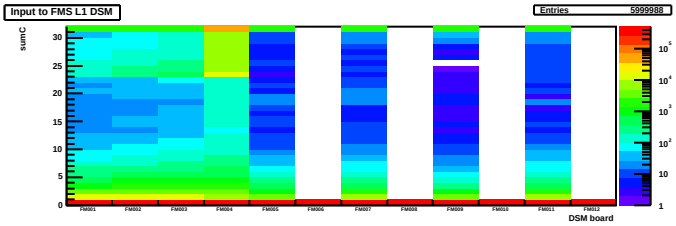

Input to FMS L0 DSM
Entries 3
HT ADC - simulated
QT board

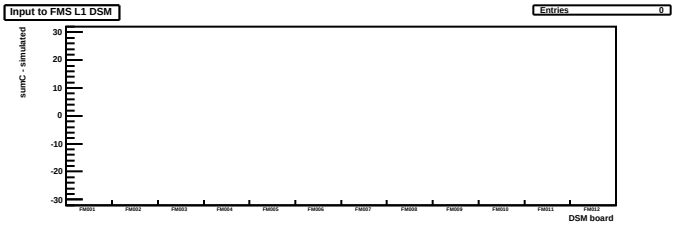

Input to FMS L0 DSM
Entries 2.399995e+07
HT ID
QT board

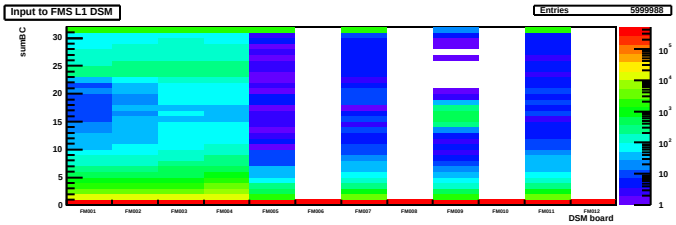

Input to FMS L0 DSM
Entries 510940
HT ID - simulated
QT board

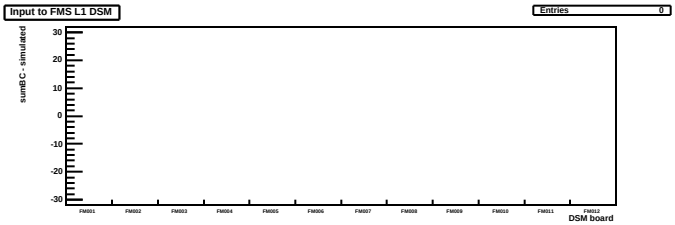

Input to FMS L1 DSM
Entries 5999988
sumD
DSM board

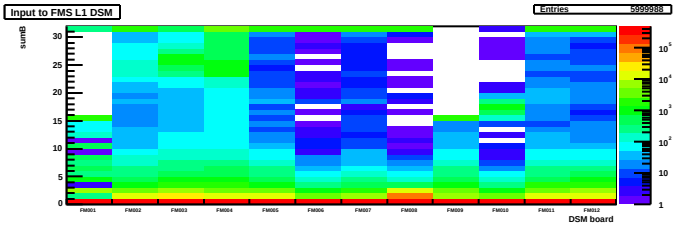

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

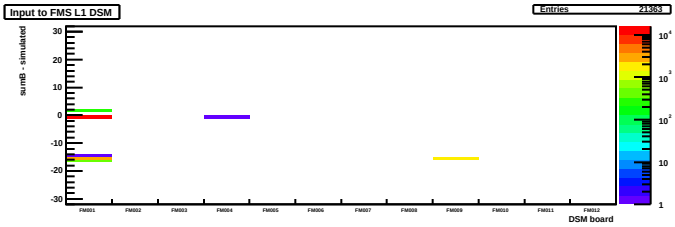

Input to FMS L1 DSM
Entries 5999988
sumC
DSM board

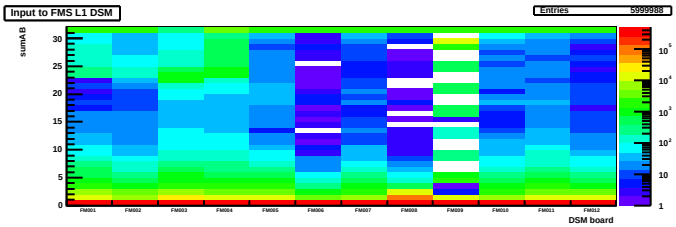

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

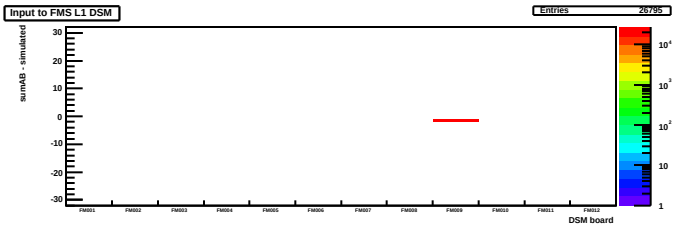

Input to FMS L1 DSM
Entries 5999988
sumBC
DSM board

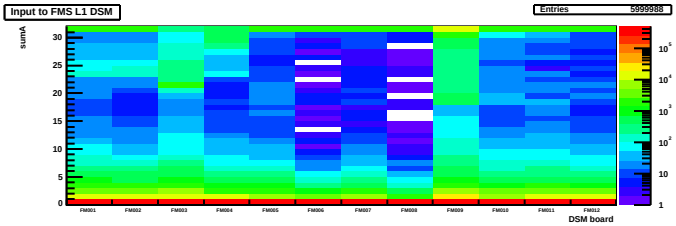

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

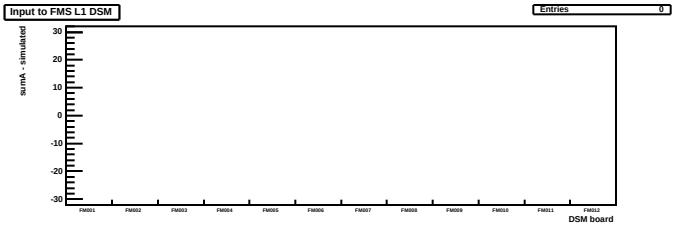

Input to FMS L1 DSM
Entries 5999988
sumB
DSM board

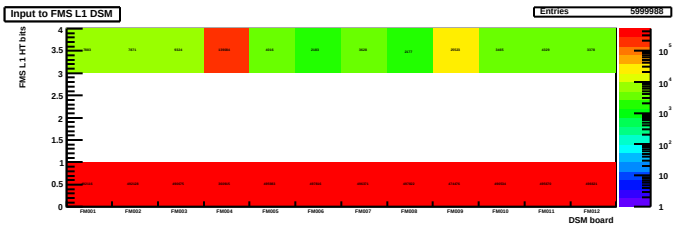

Input to FMS L1 DSM
Entries 21363
sumB - simulated
DSM board

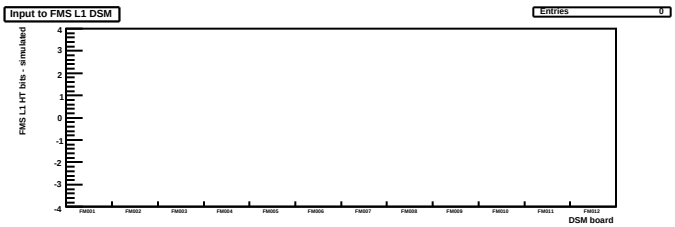

Input to FMS L1 DSM
Entries 5999988
sumAB
DSM board

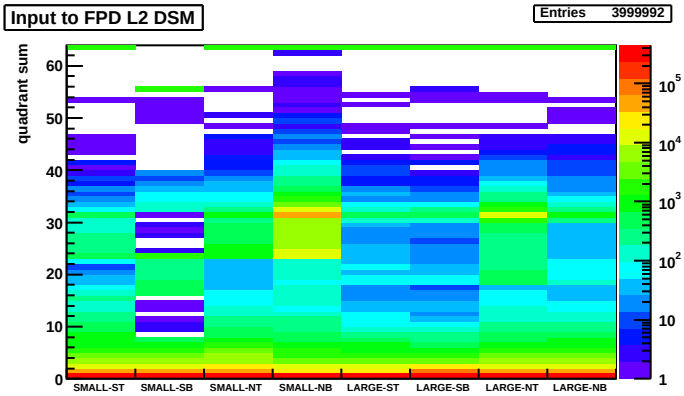

Input to FMS L1 DSM
Entries 26795
sumAB - simulated
DSM board

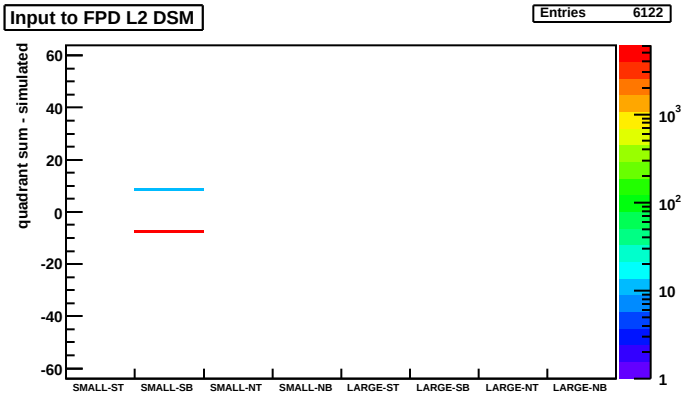

Input to FMS L1 DSM
Entries 5999988
sumA
DSM board

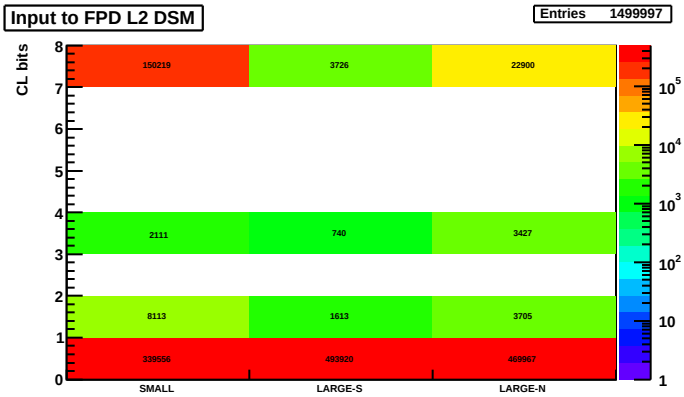

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

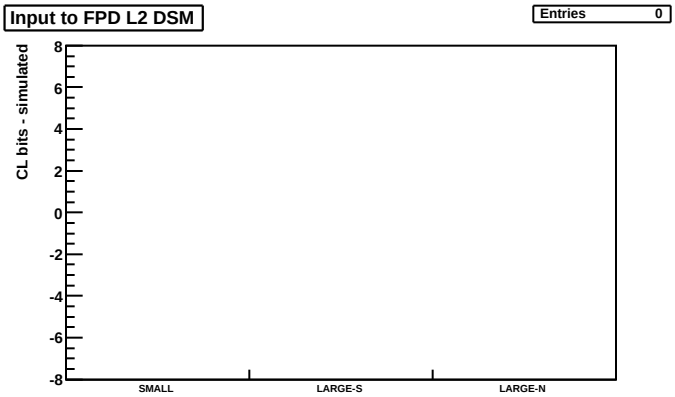

Input to FMS L1 DSM
Entries 5999988
FMS L1 HT bits
DSM board

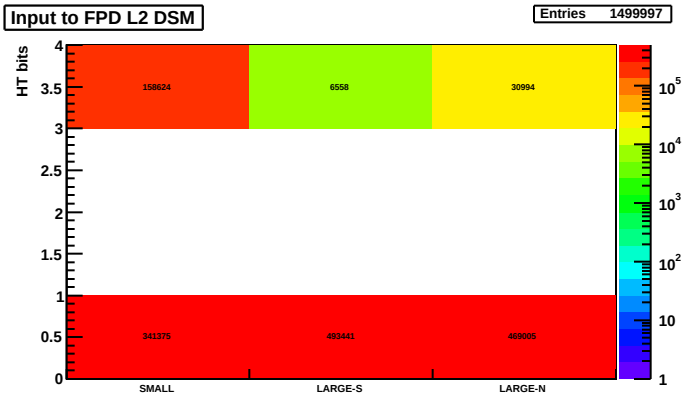

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

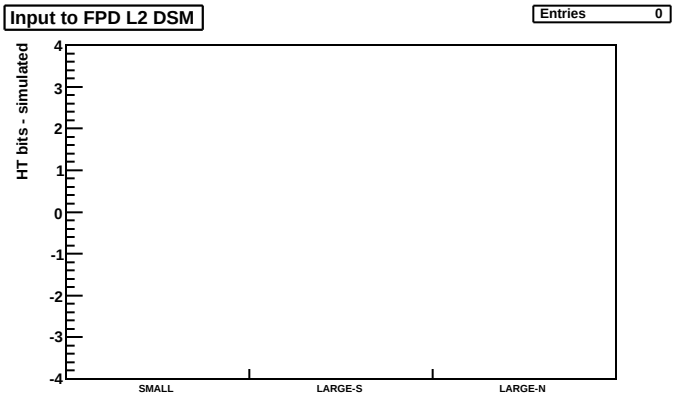

Input to FPD L2 DSM
Entries 3999992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

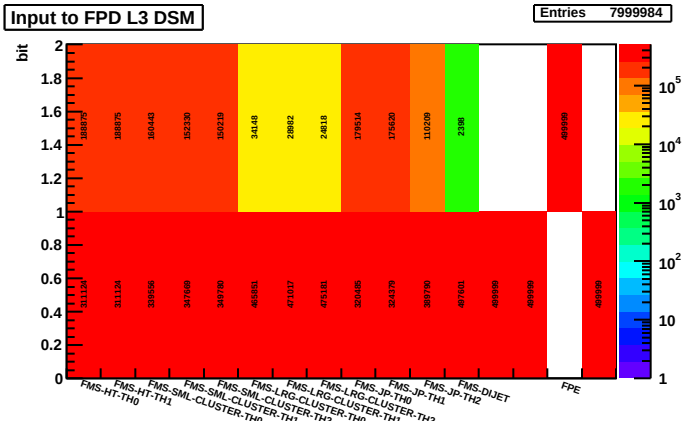

Input to FPD L2 DSM
Entries 6122
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

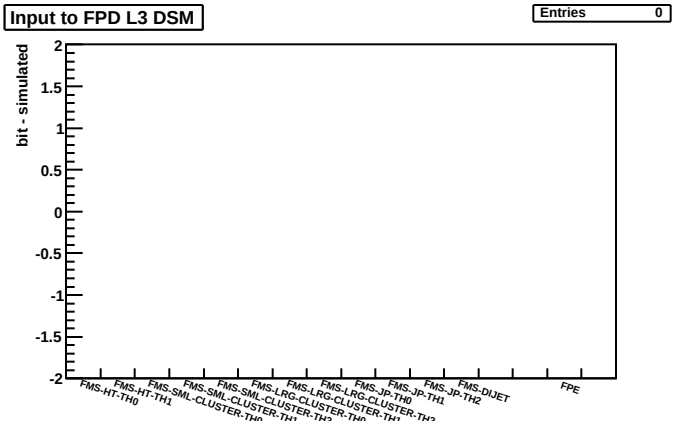

Input to FPD L2 DSM
Entries 1499997
CL bits
150219
3726
22900
2111
740
3427
8113
1613
3705
339556
493920
469967
SMALL
LARGE-S
LARGE-N

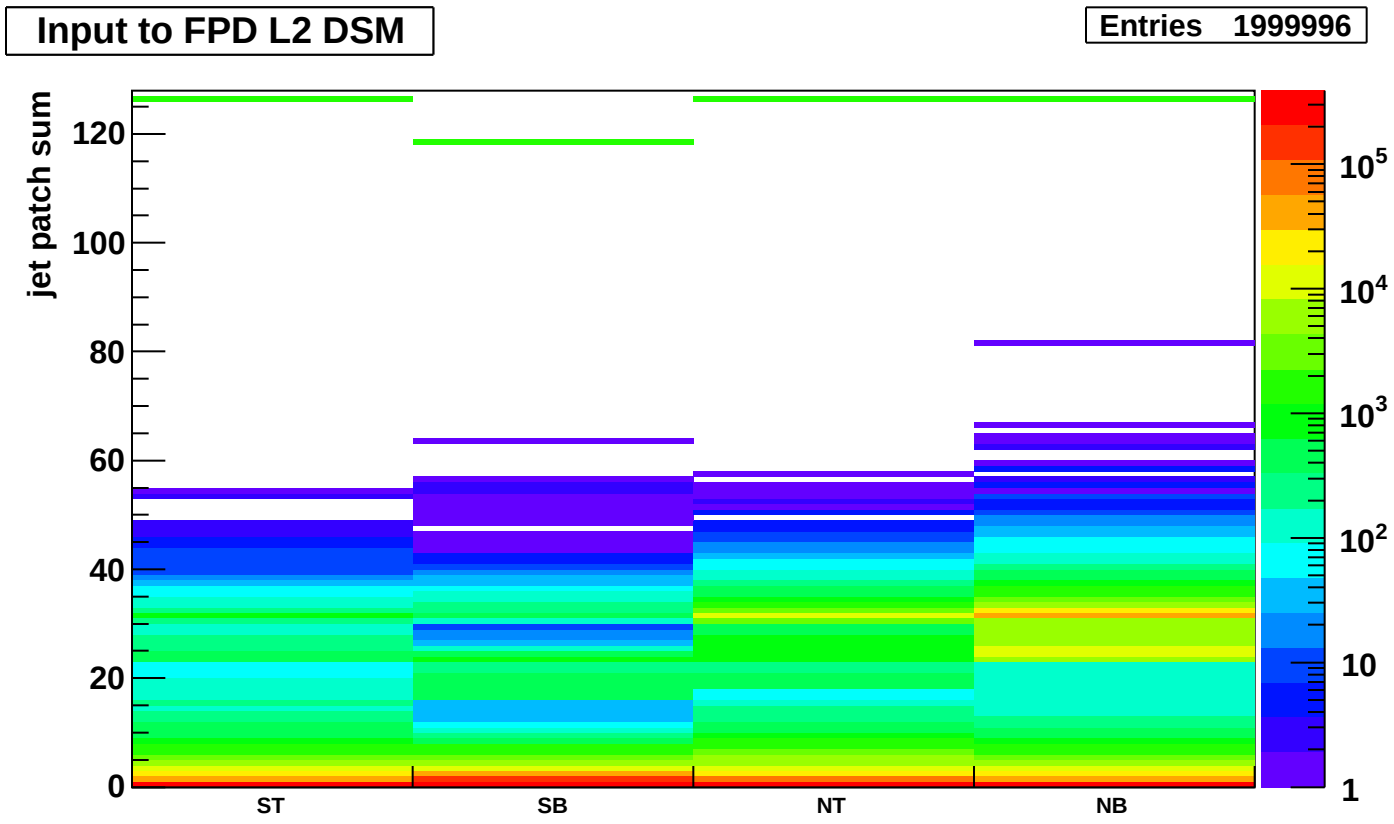

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

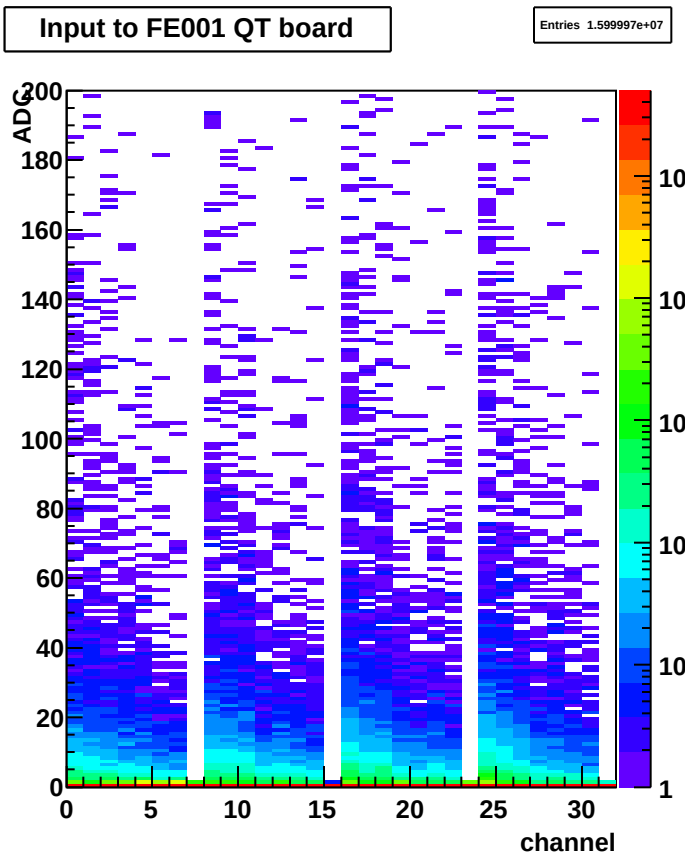

Input to FPD L2 DSM
Entries 1499997
HT bits
158624
6558
30994
341375
493441
469005
SMALL
LARGE-S
LARGE-N

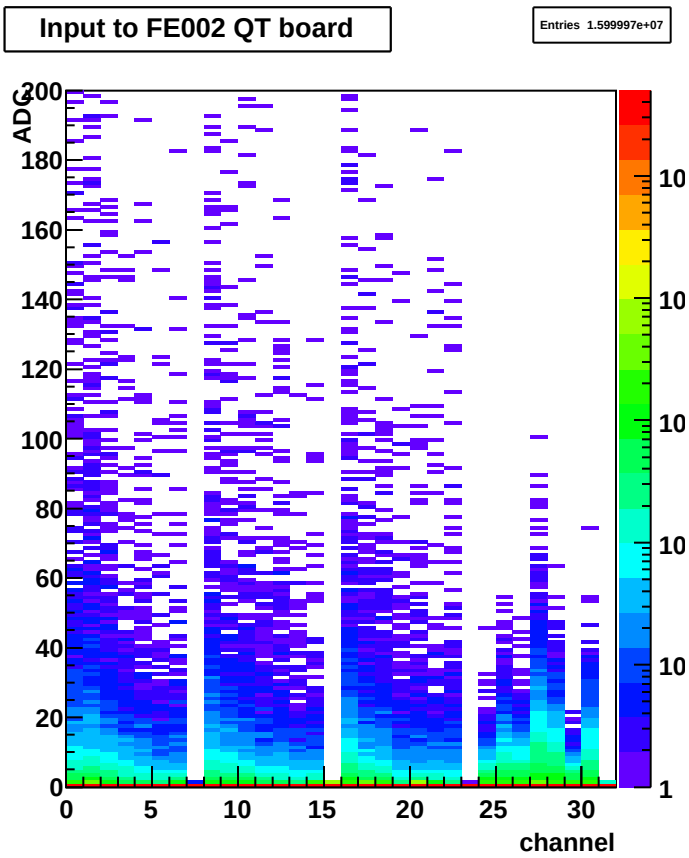

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

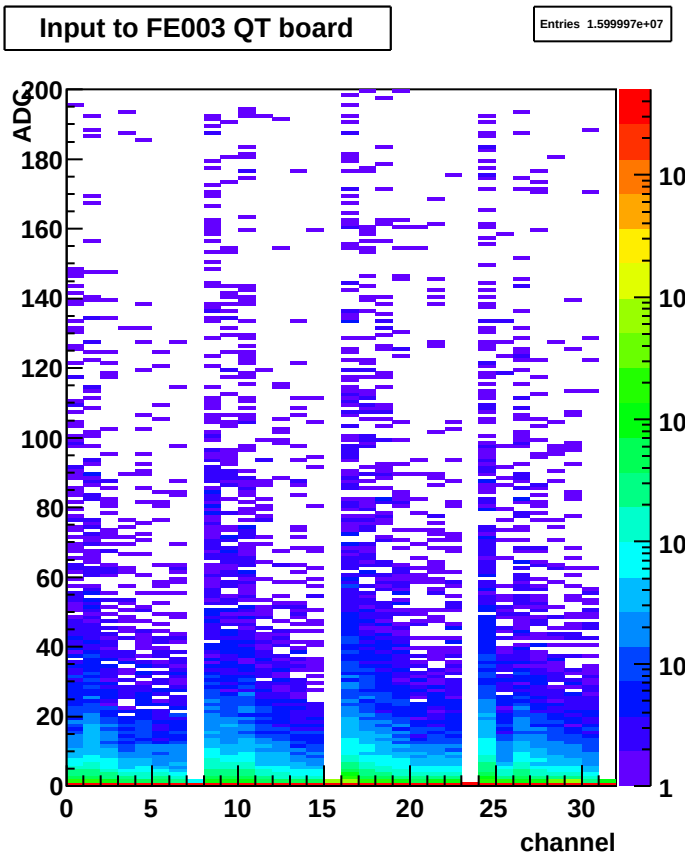

Input to FPD L3 DSM
Entries 7999984
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

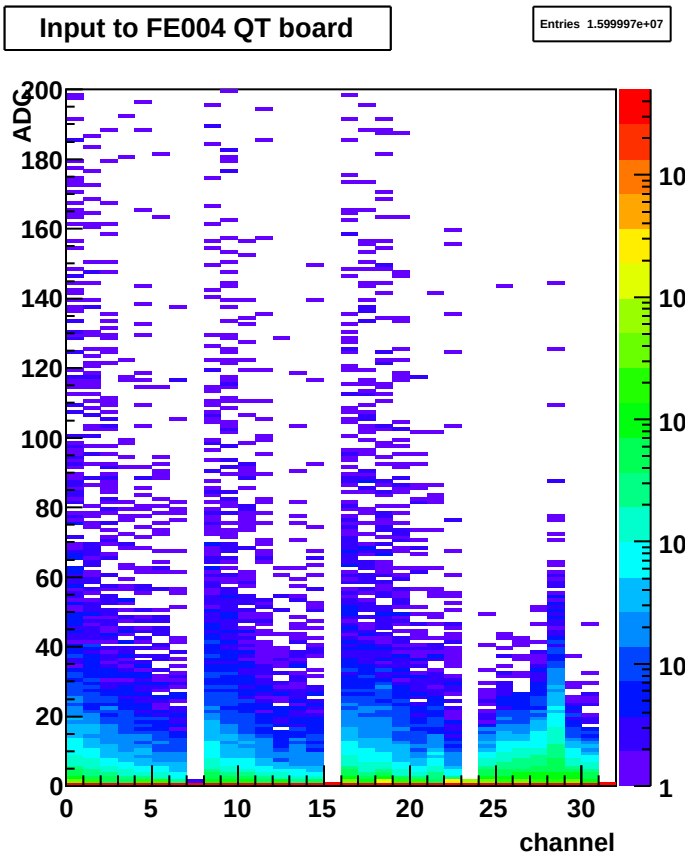

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

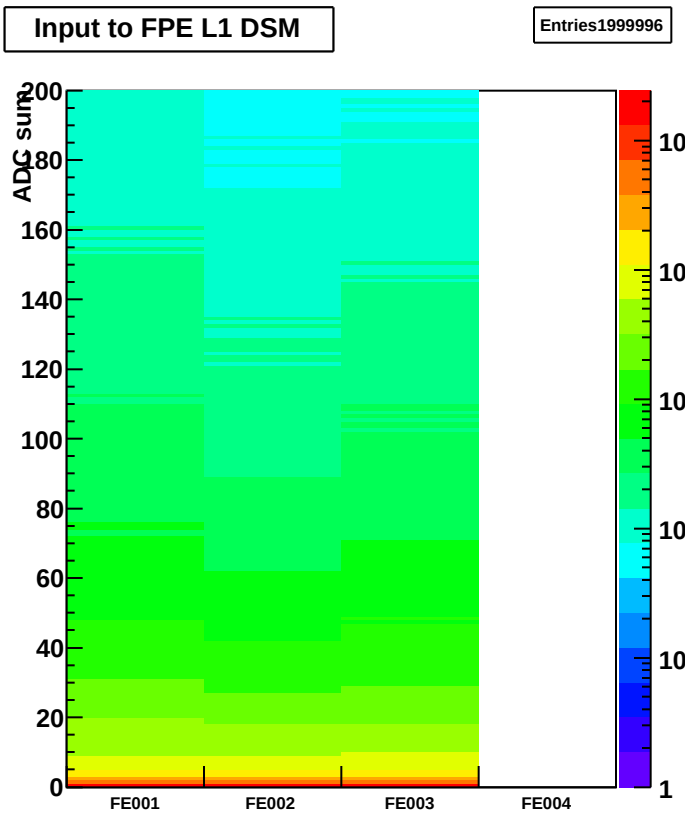

Input to FPD L2 DSM
Entries 1999996
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

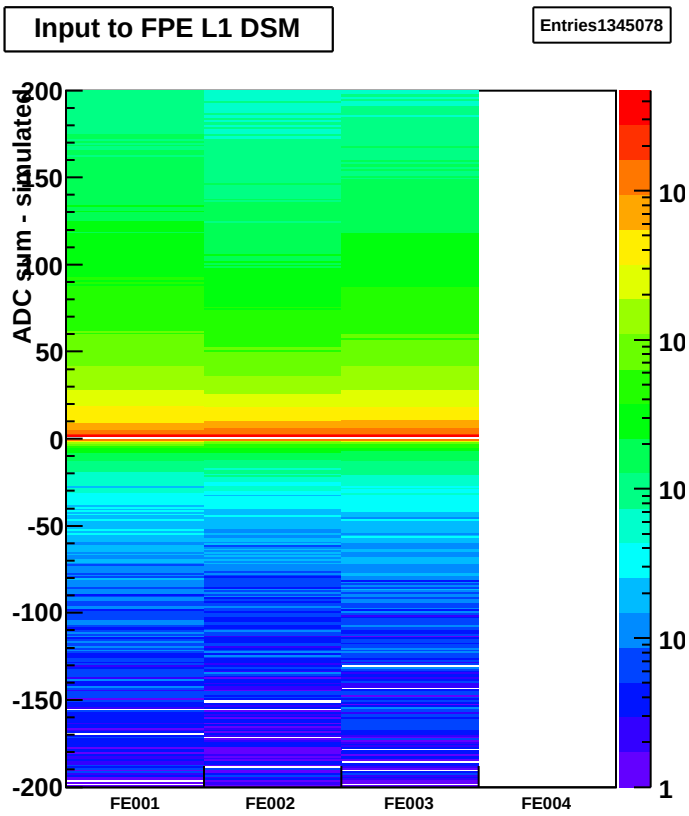

Input to FE001 QT board
Entries 1.599997e+07
ADC
channel

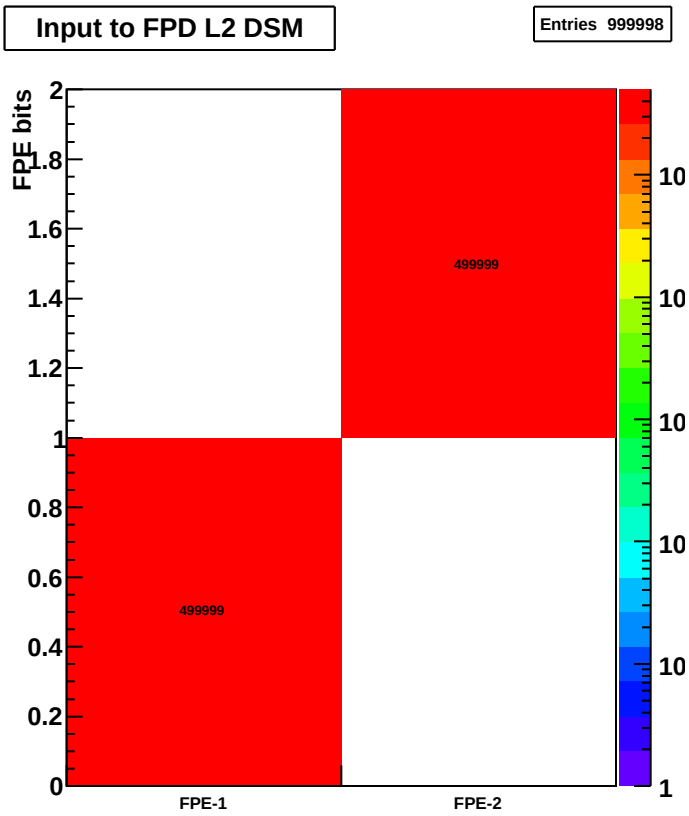

Input to FE002 QT board
Entries 1.599997e+07
ADC
channel

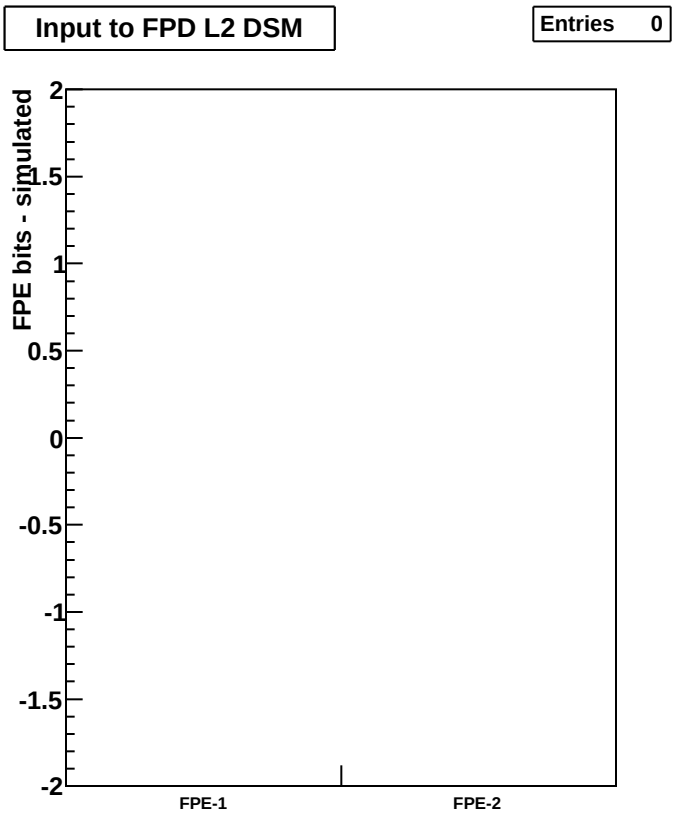

Input to FE003 QT board
Entries 1.599997e+07
ADC
channel


Input to FE004 QT board
Entries 1.599997e+07
ADC
channel


Input to FPE L1 DSM
Entries1999996
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1345078
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 999998
FPE bits
499999
499999
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2