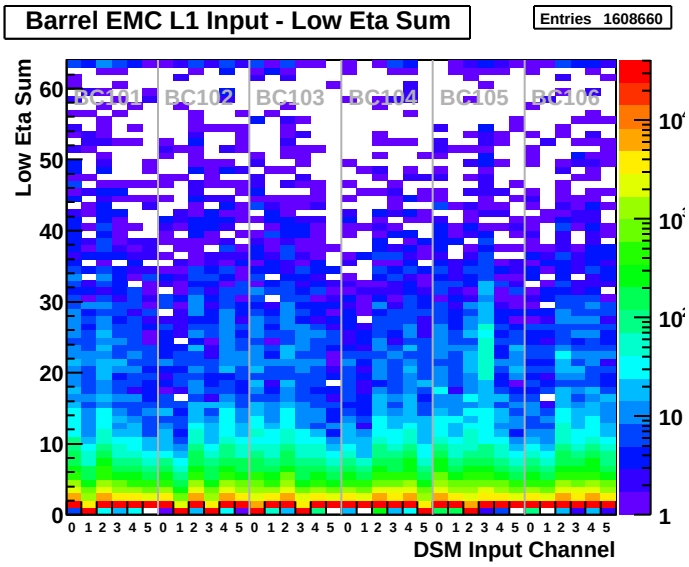
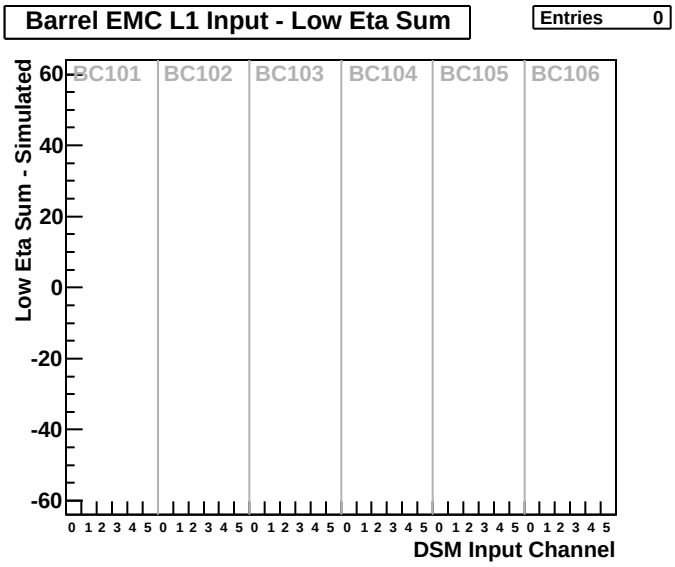
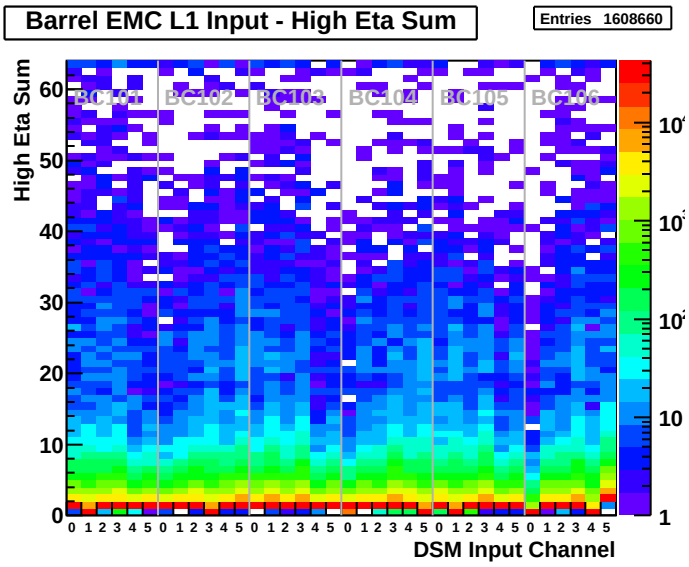
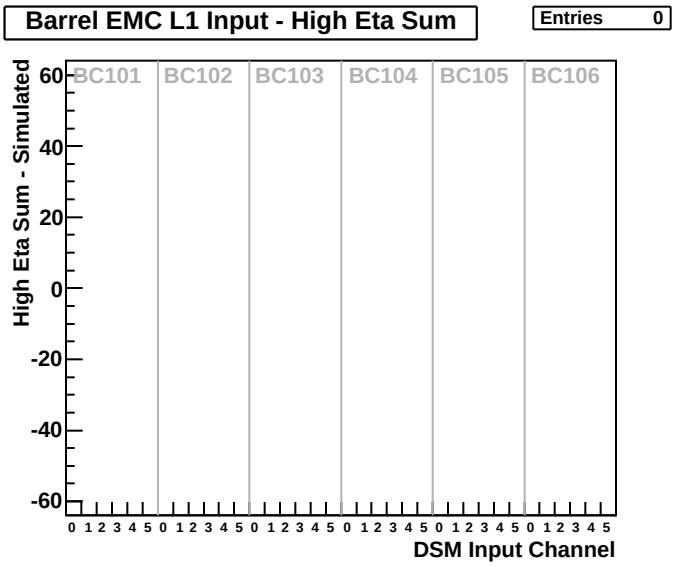
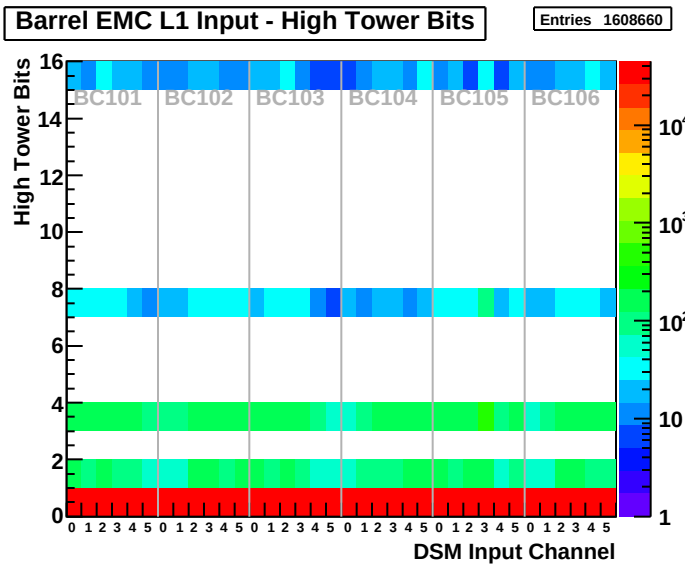
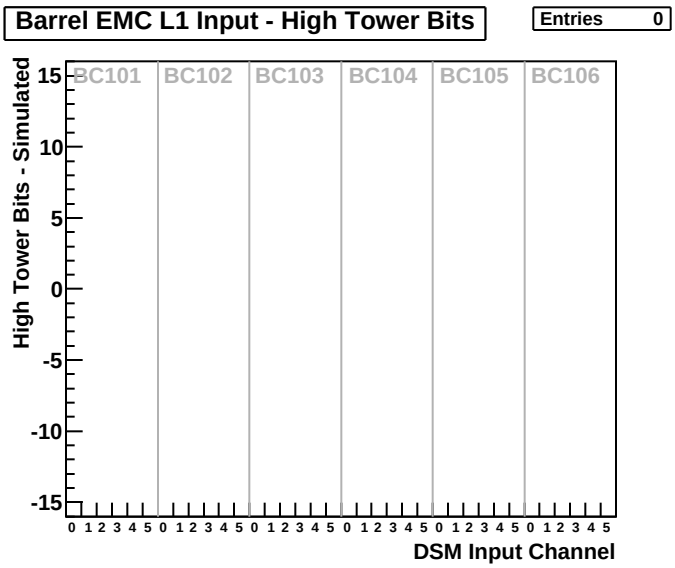

Barrel EMC L1 Input - Low Eta Sum
Entries 1608660
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

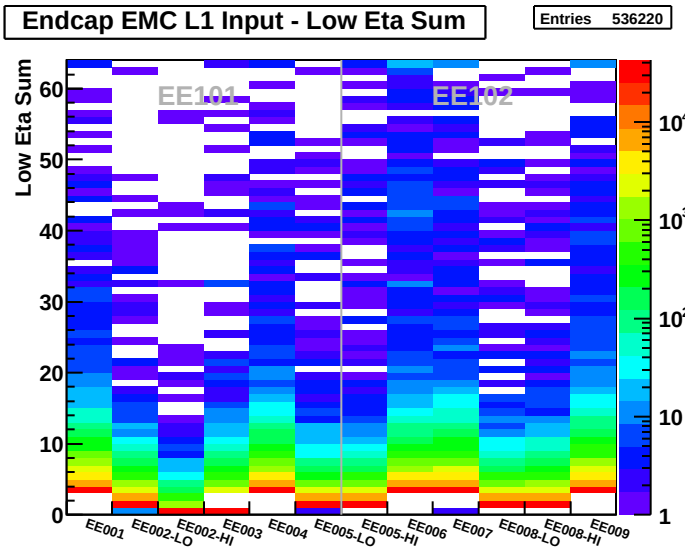

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

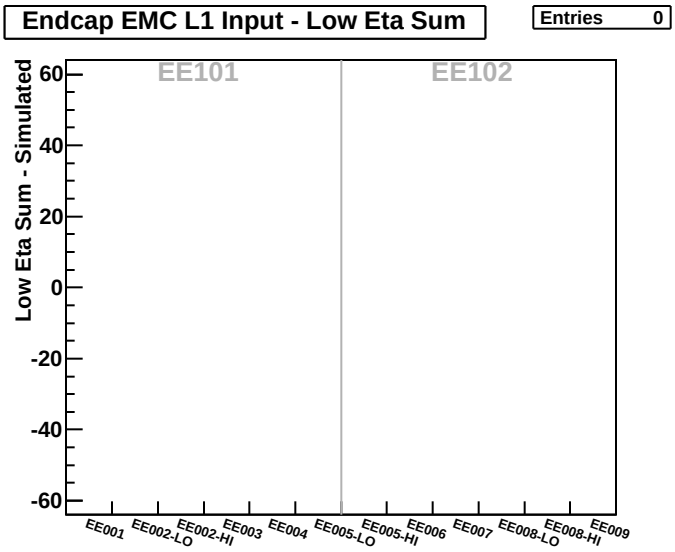

Barrel EMC L1 Input - High Eta Sum
Entries 1608660
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

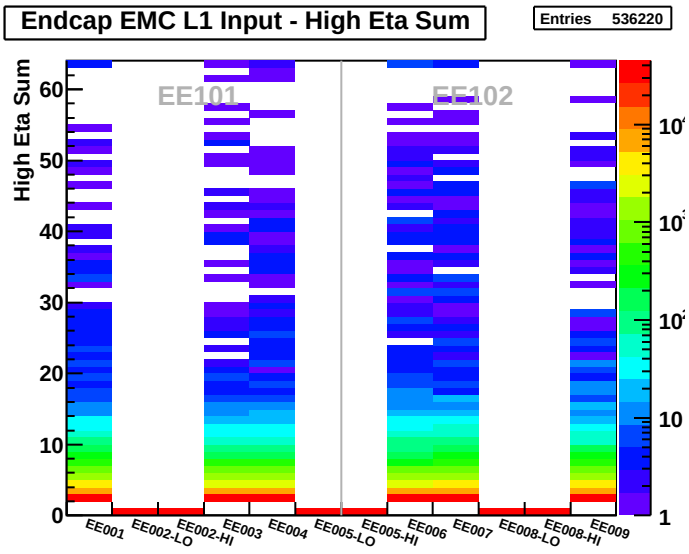

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

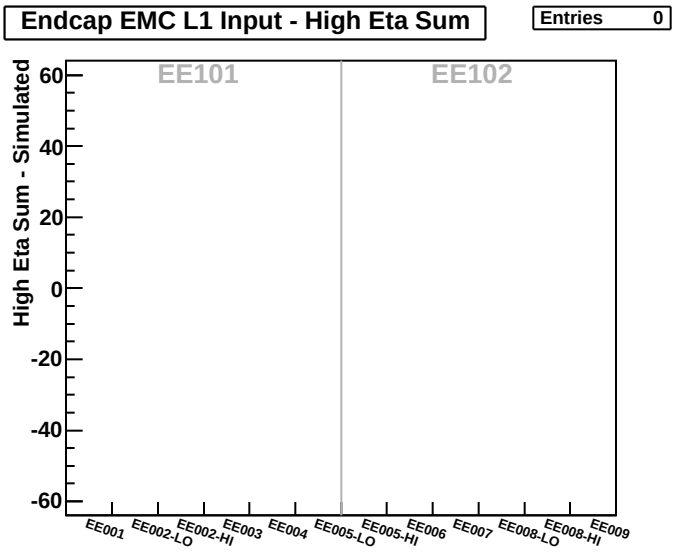

Barrel EMC L1 Input - High Tower Bits
Entries 1608660
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

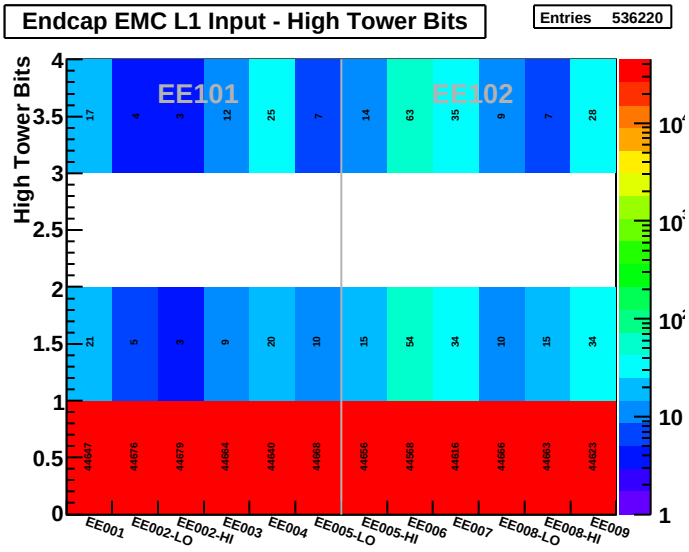

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

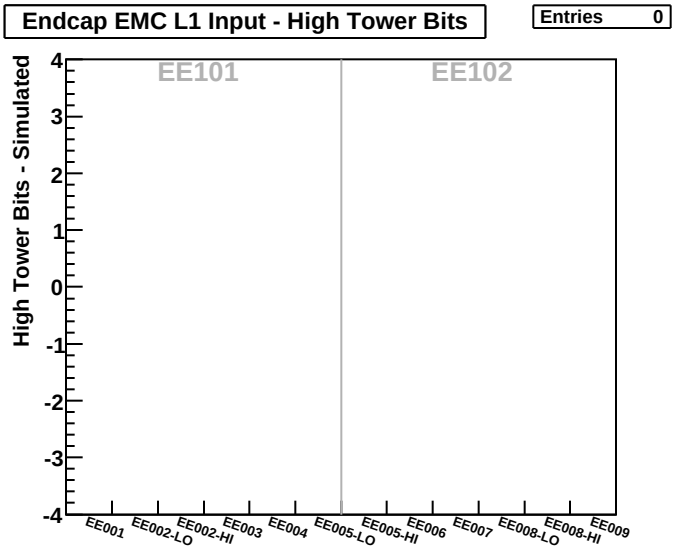

Endcap EMC L1 Input - Low Eta Sum
Entries 536220
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

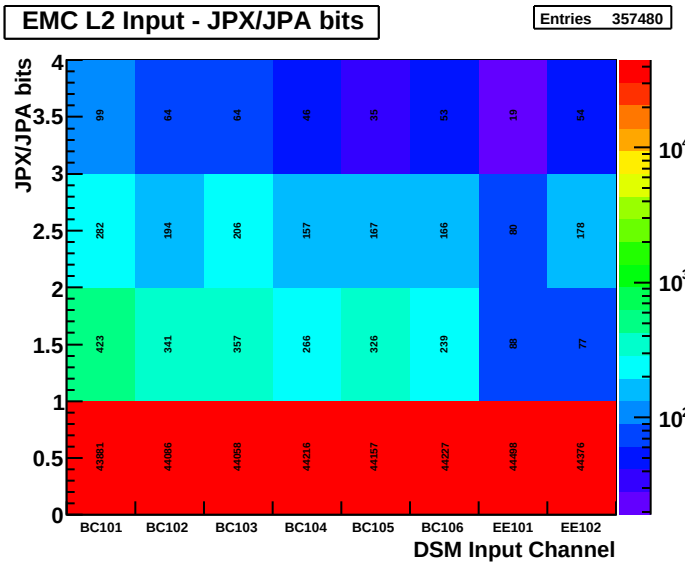

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

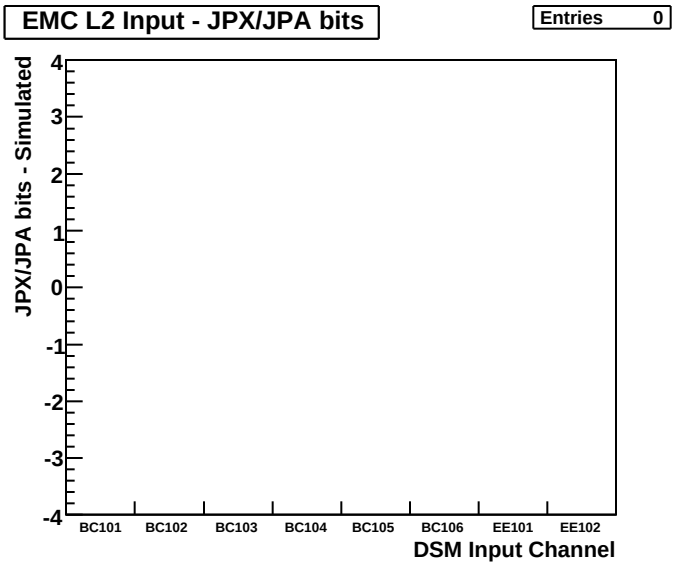

Endcap EMC L1 Input - High Eta Sum
Entries 536220
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

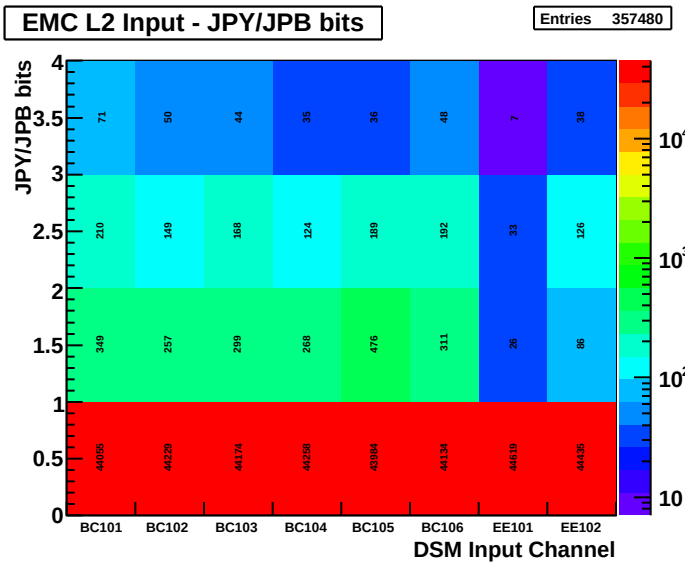

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

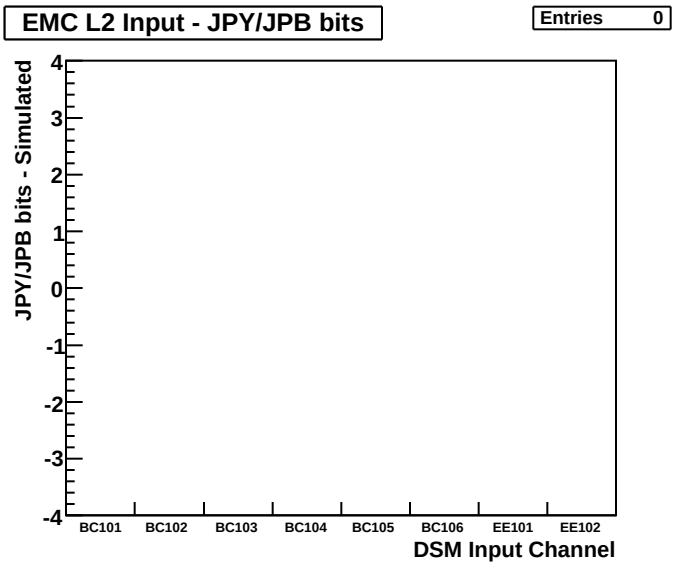

Endcap EMC L1 Input - High Tower Bits
Entries 536220
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

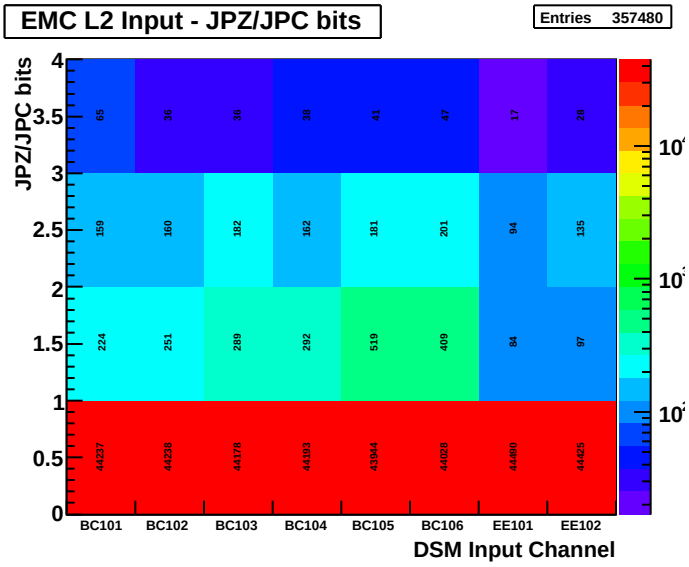

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

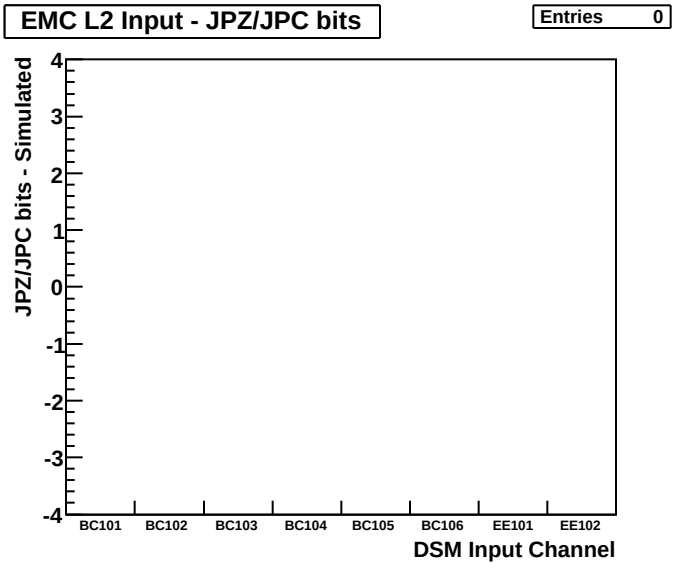

EMC L2 Input - JPX/JPA bits
Entries 357480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
99 64 64 46 35 53 19 54
282 194 206 157 167 166 80 178
423 341 357 266 326 239 88 77
43881 44086 44058 44216 44157 44227 44498 44376

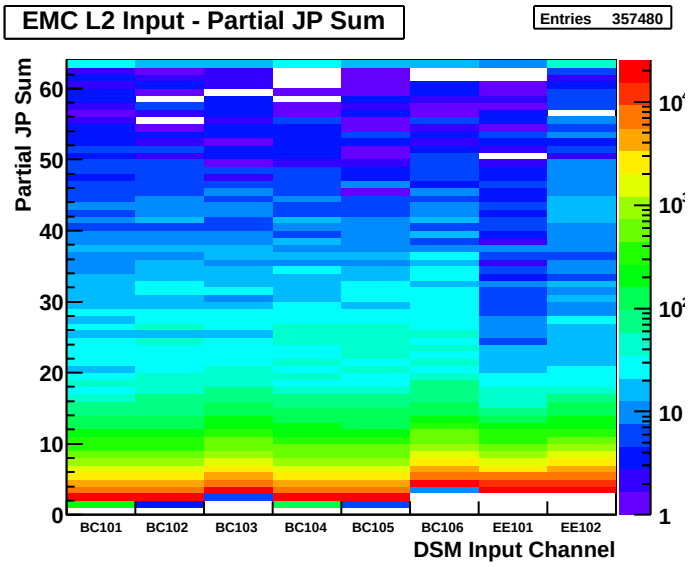

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

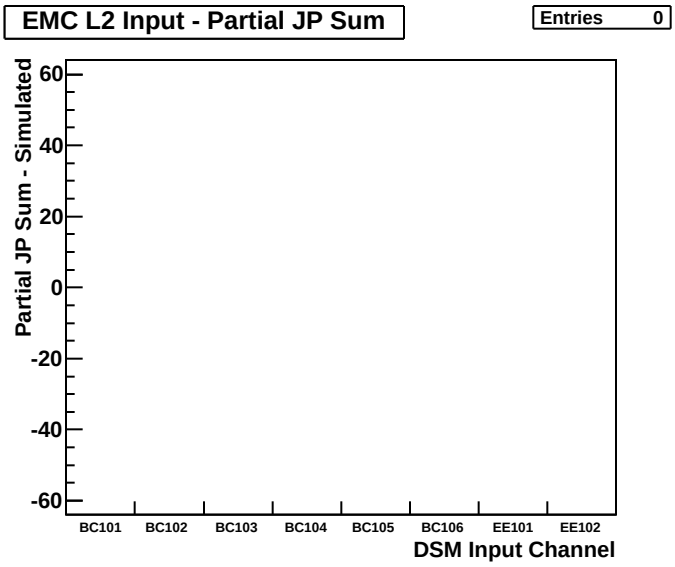

EMC L2 Input - JPY/JPB bits
Entries 357480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
71 50 44 35 36 48 7 38
210 149 168 124 189 192 33 126
349 257 299 268 476 311 26 86
44055 44229 44174 44258 43984 44134 44619 44435

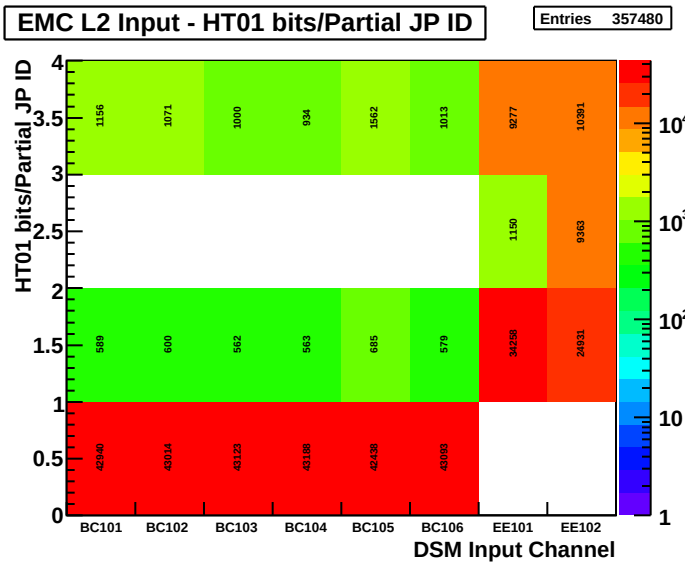

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

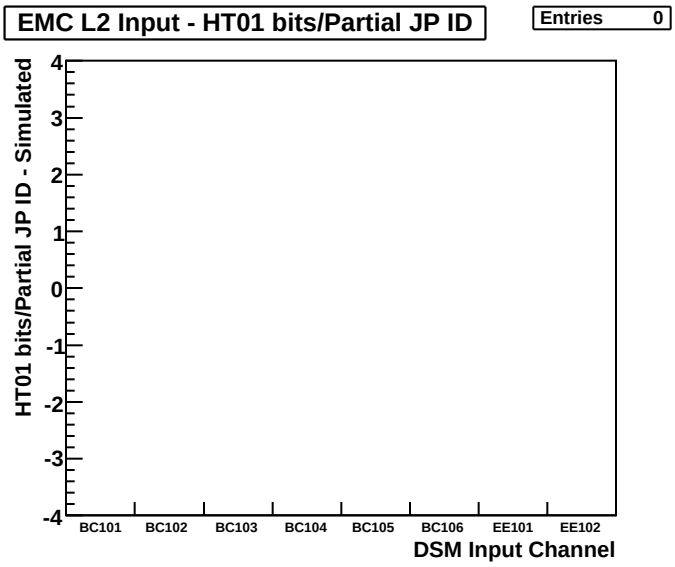

EMC L2 Input - JPZ/JPC bits
Entries 357480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
65 36 36 38 41 47 17 28
159 160 182 162 181 201 94 135
224 251 289 292 519 409 84 97
44237 44238 44178 44193 43944 44028 44480 44425

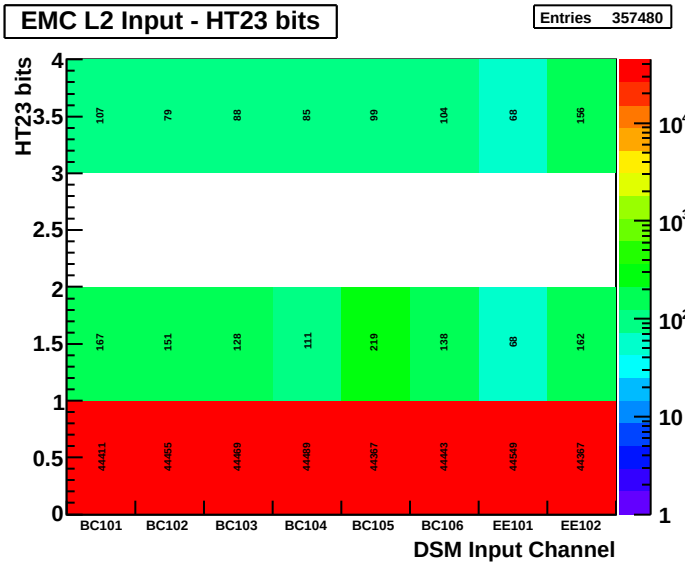

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

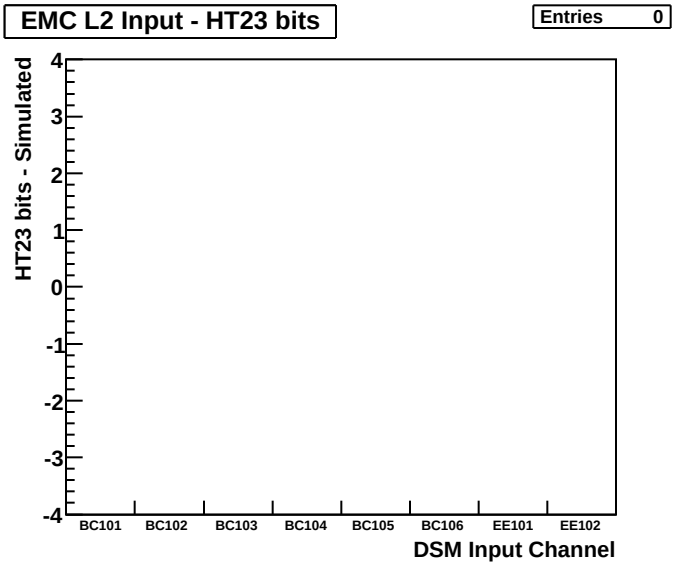

EMC L2 Input - Partial JP Sum
Entries 357480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

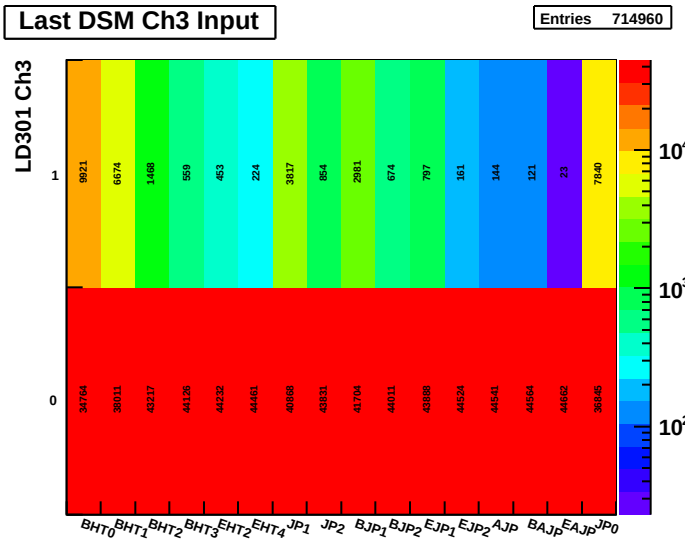

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

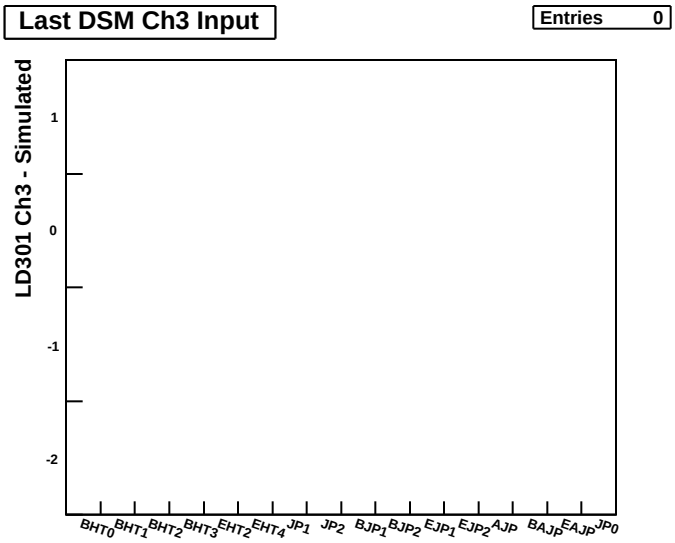

EMC L2 Input - HT01 bits/Partial JP ID
Entries 357480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

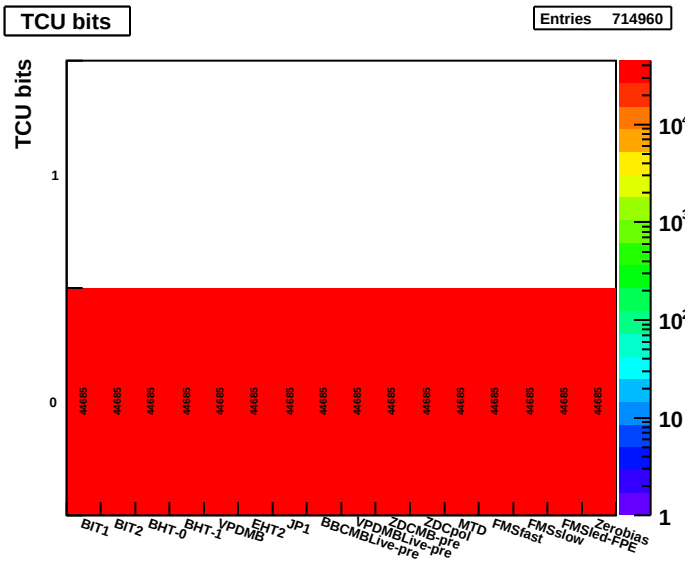

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

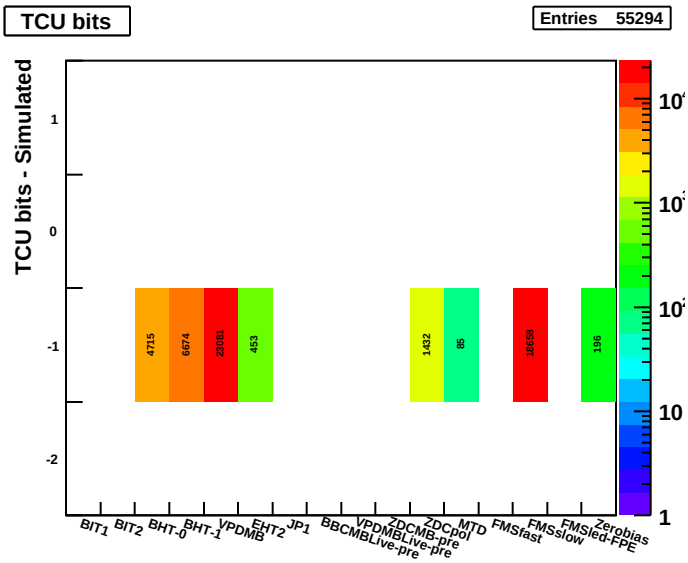

EMC L2 Input - HT23 bits
Entries 357480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

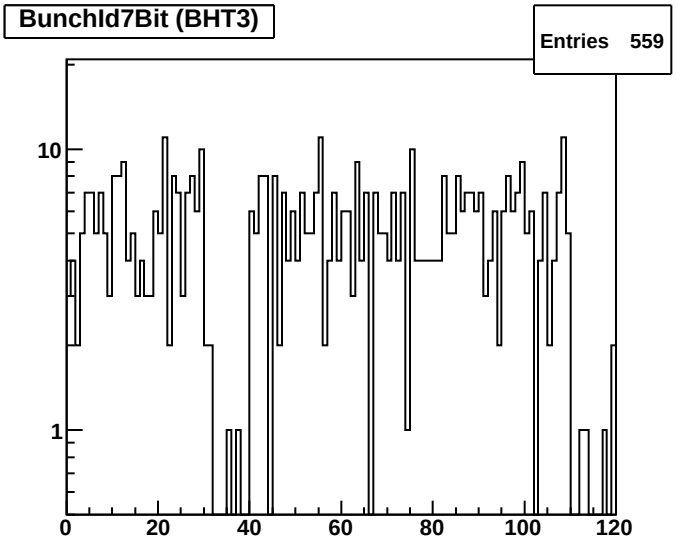

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

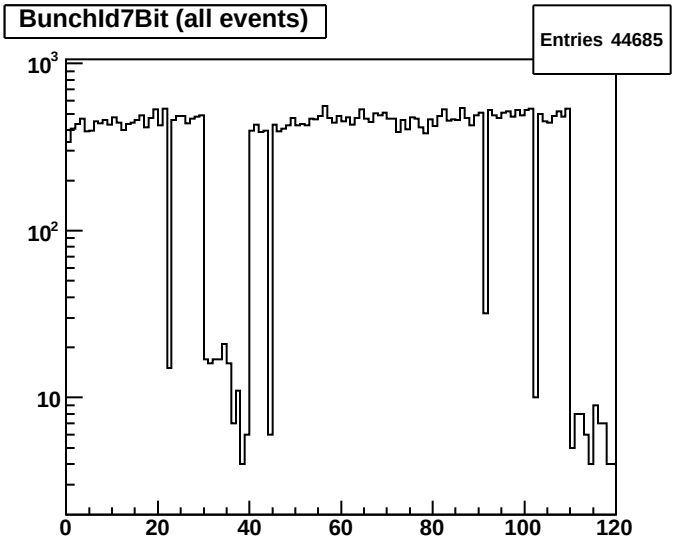

Last DSM Ch3 Input
Entries 714960
LD301 Ch3
9921 6674 1468 559 453 224 3817 854 2981 674 797 161 144 121 23 7840
34764 38011 43217 44126 44232 44461 40868 43931 41704 44011 43888 44524 44541 44564 44662 36845
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

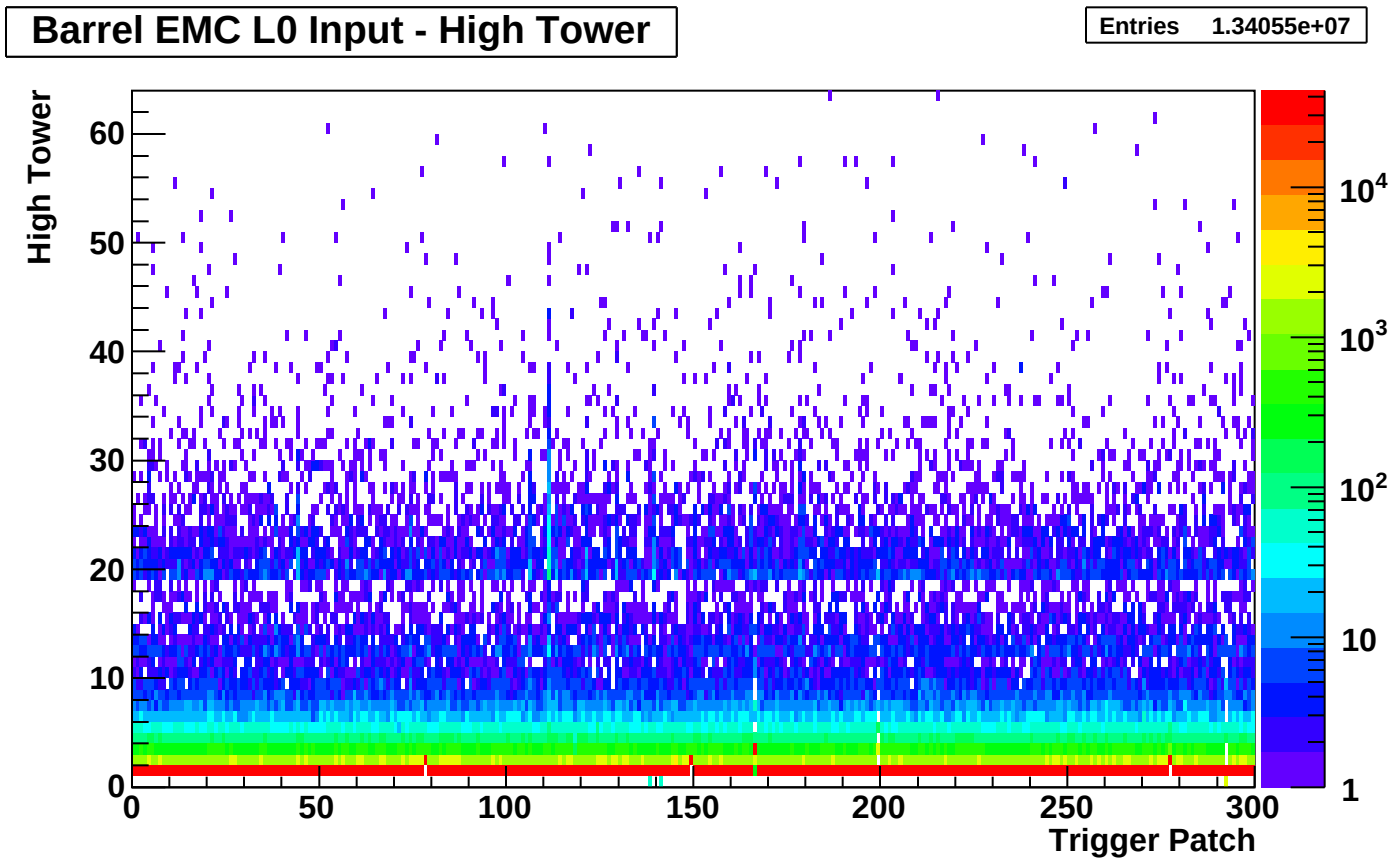

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

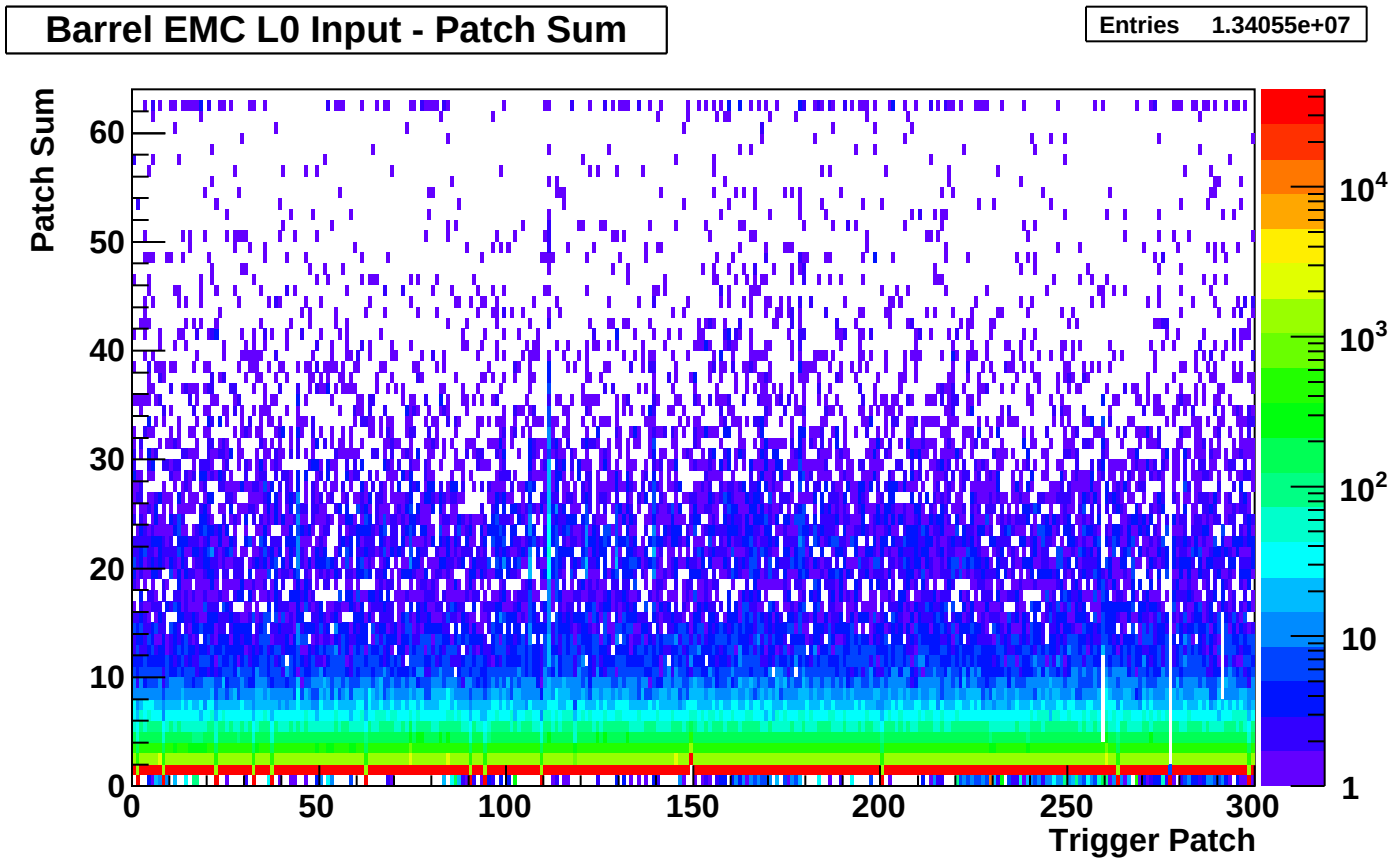

TCU bits
Entries 714960
TCU bits
44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685 44685
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

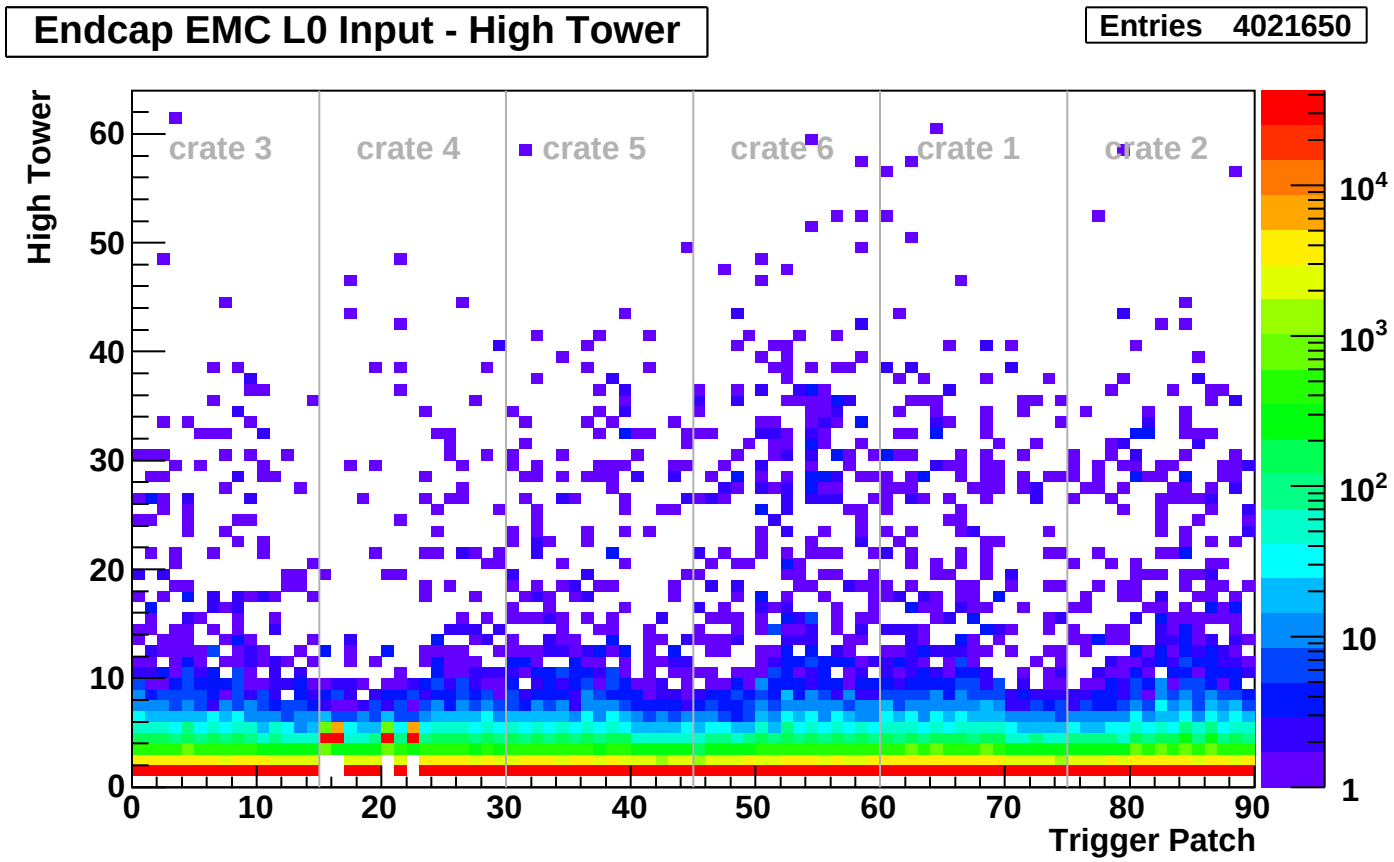

TCU bits
Entries 55294
TCU bits - Simulated
4715 6674 23081 453 1432 85 18658 196
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

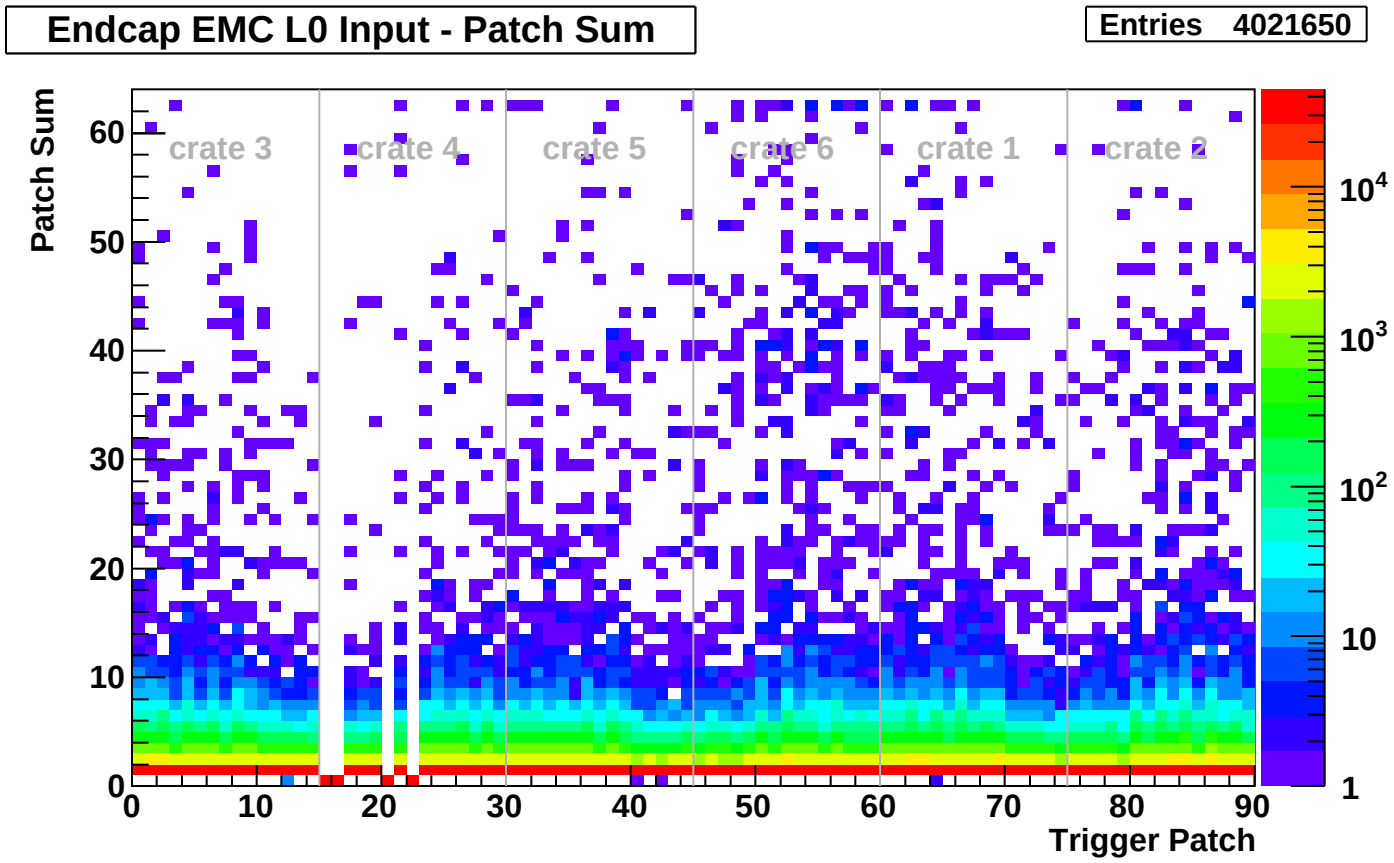

BunchId7Bit (BHT3)
Entries 559

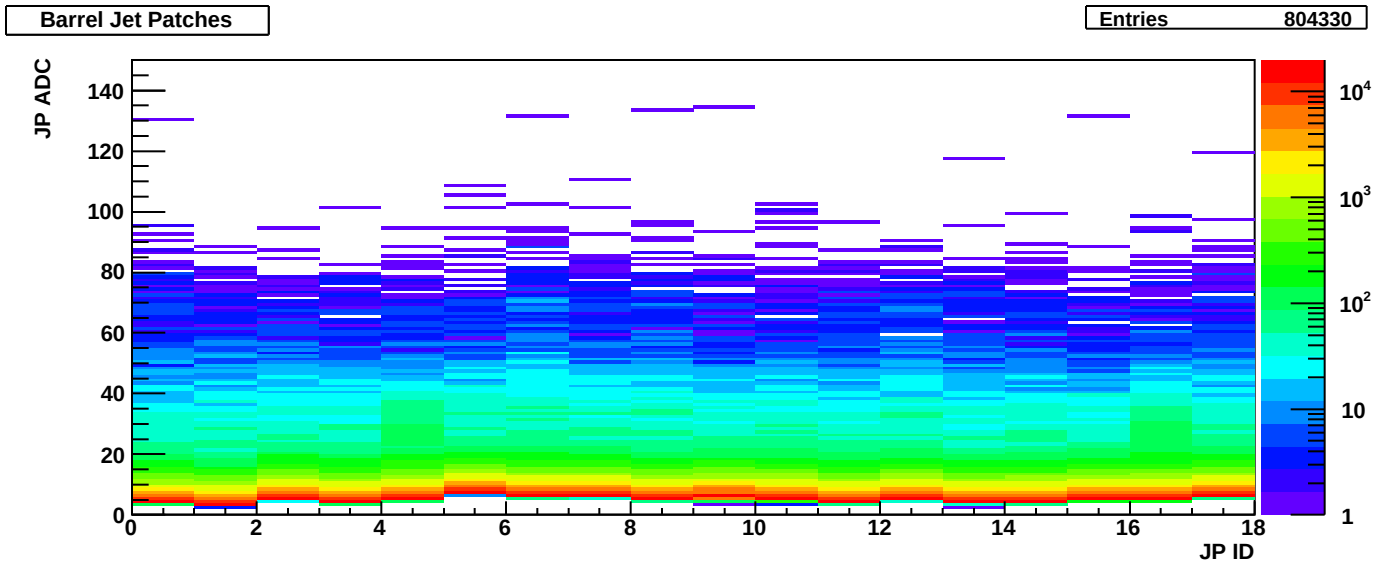

BunchId7Bit (all events)
Entries 44685

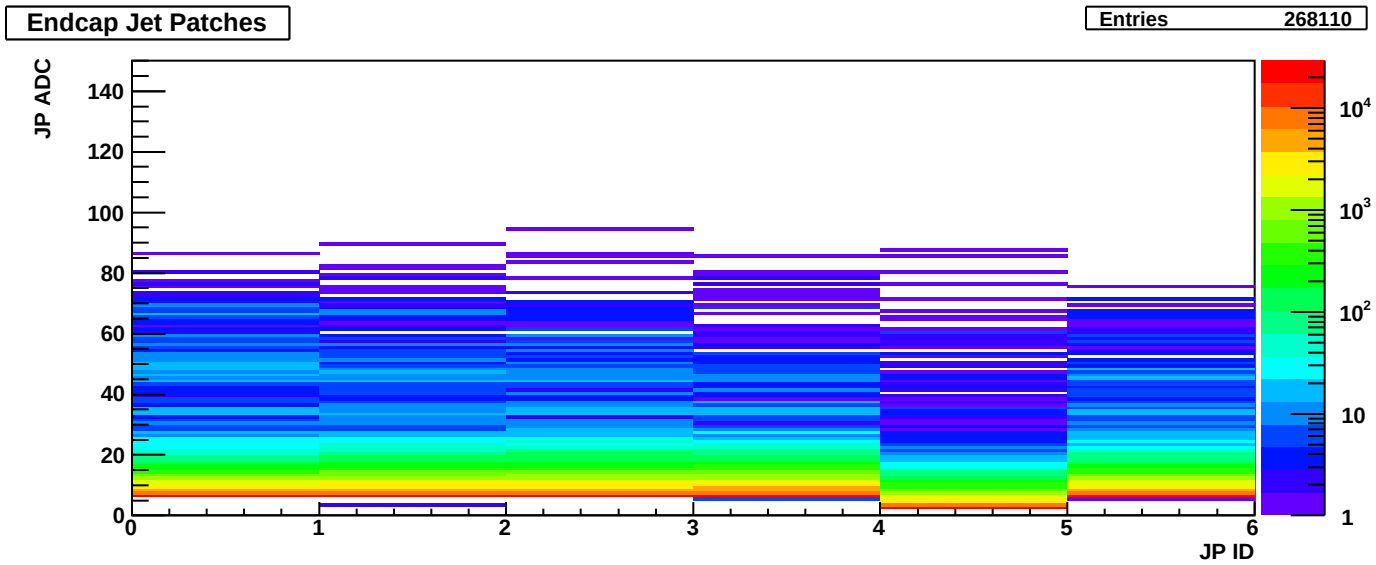

Barrel EMC L0 Input - High Tower
Entries 1.34055e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

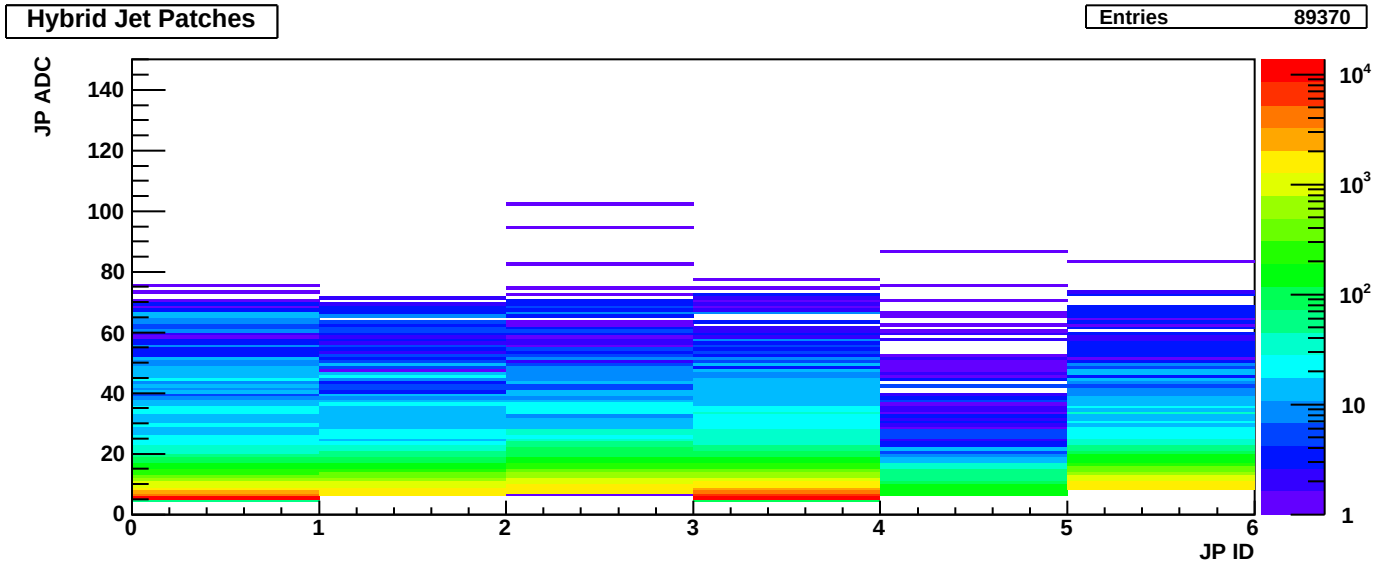

Barrel EMC L0 Input - Patch Sum
Entries 1.34055e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

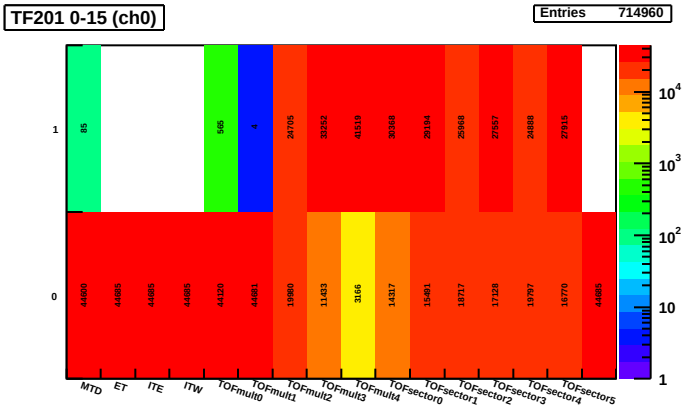

Endcap EMC L0 Input - High Tower
Entries 4021650
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

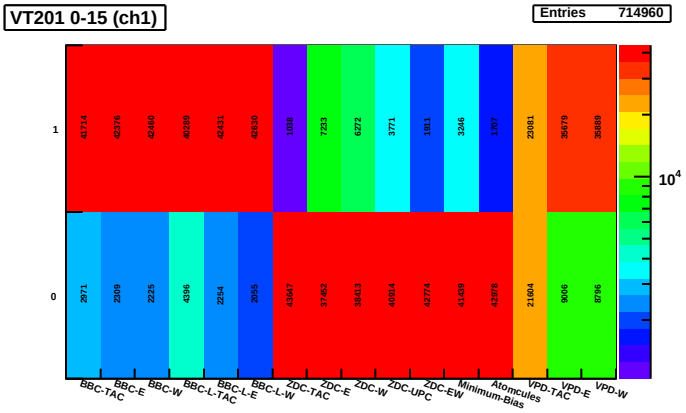

Endcap EMC L0 Input - Patch Sum
Entries 4021650
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

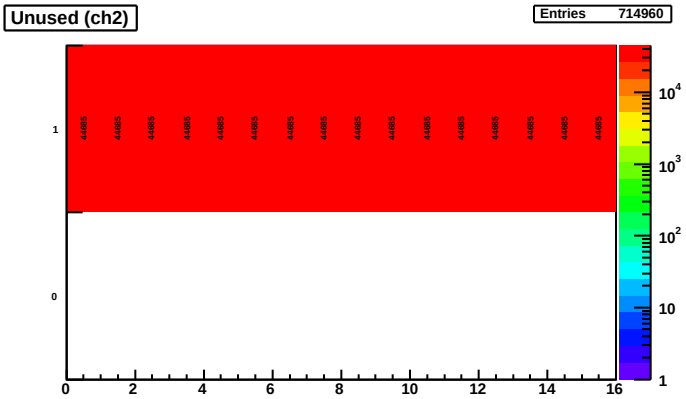

Barrel Jet Patches
Entries 804330
JP ADC
JP ID

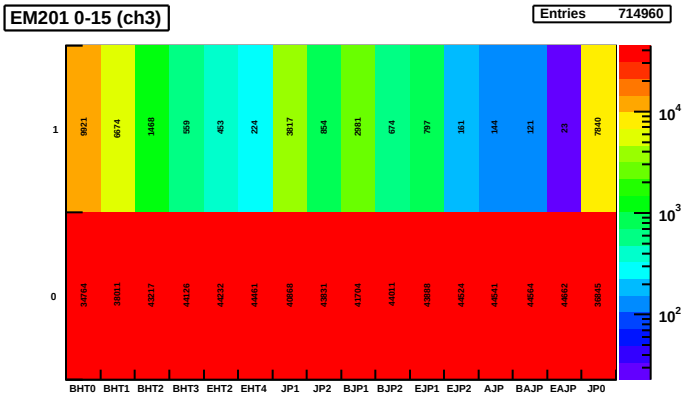

Endcap Jet Patches
Entries 268110
JP ADC
JP ID

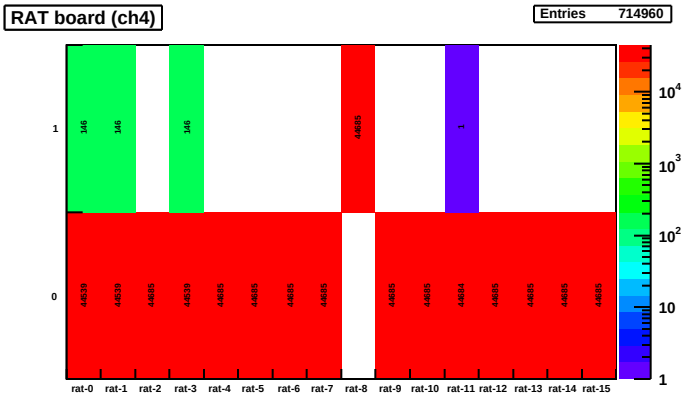

Hybrid Jet Patches
Entries 89370
JP ADC
JP ID

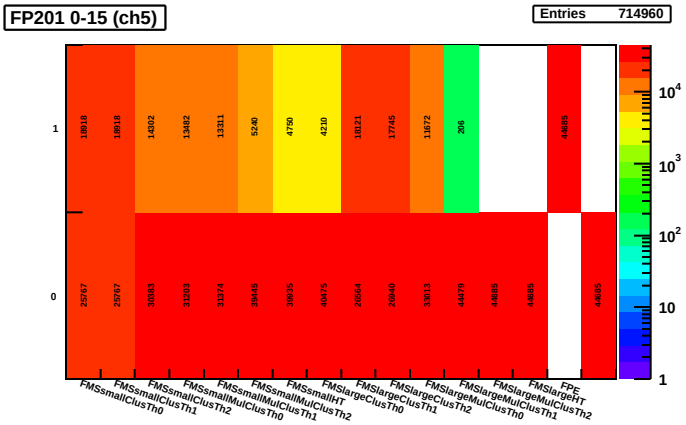

TF201 0-15 (ch0)
Entries 714960
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

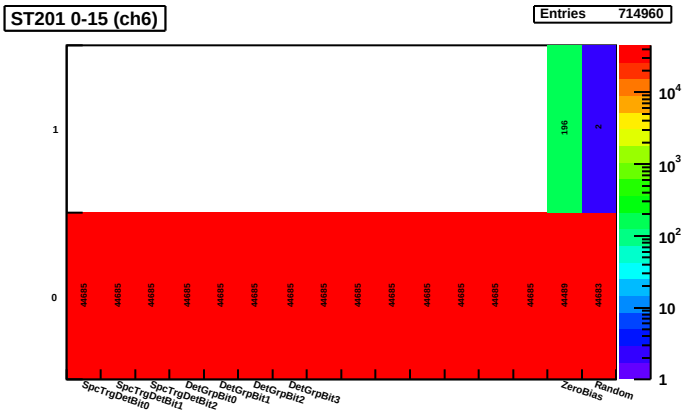

VT201 0-15 (ch1)
Entries 714960
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

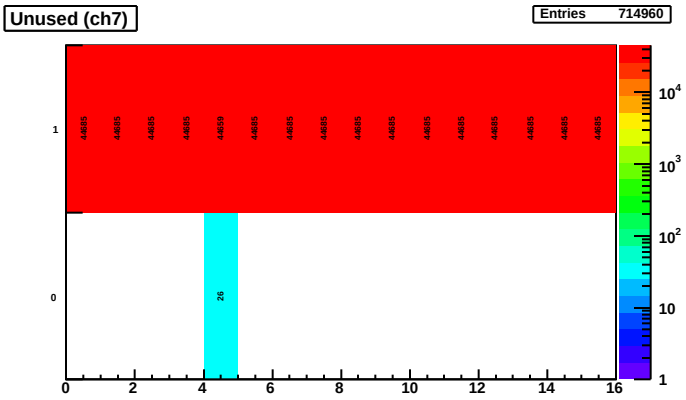

Unused (ch2)
Entries 714960

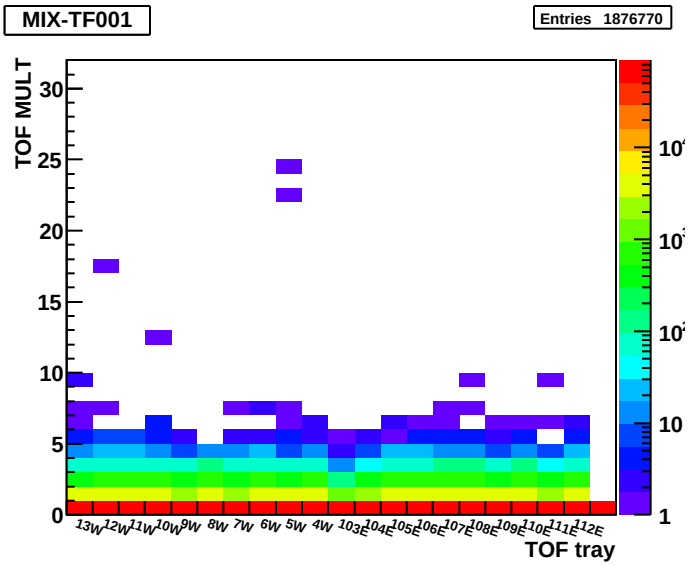

EM201 0-15 (ch3)
Entries 714960
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

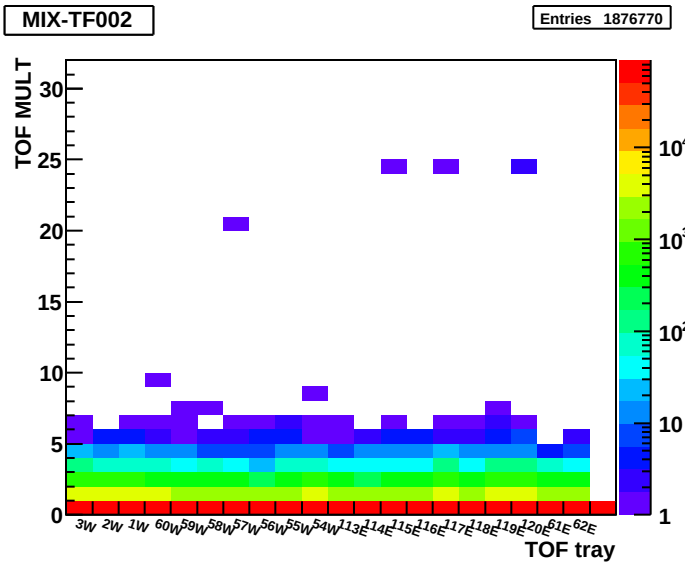

RAT board (ch4)
Entries 714960
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

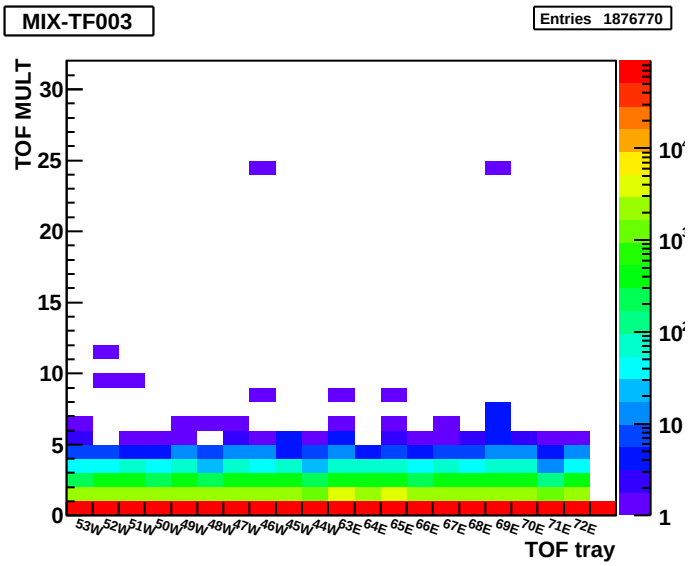

FP201 0-15 (ch5)
Entries 714960
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

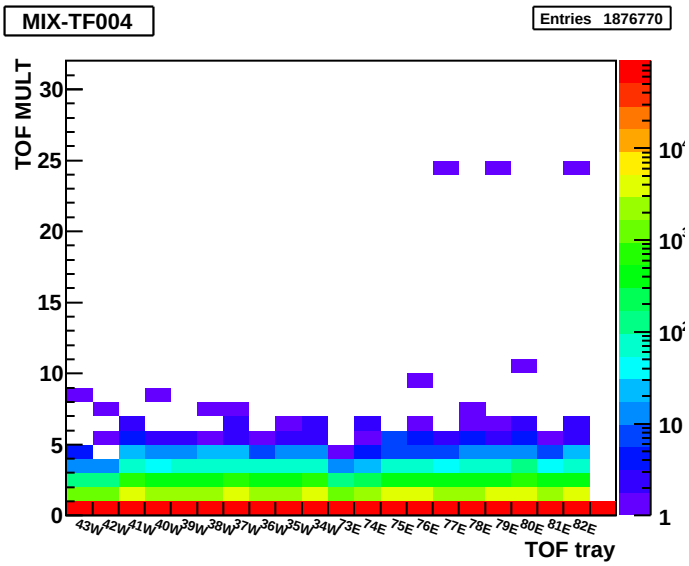

ST201 0-15 (ch6)
Entries 714960
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

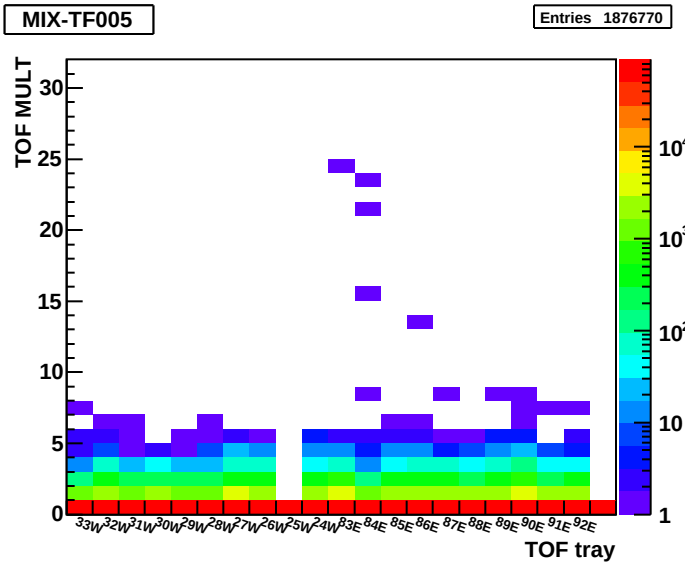

Unused (ch7)
Entries 714960

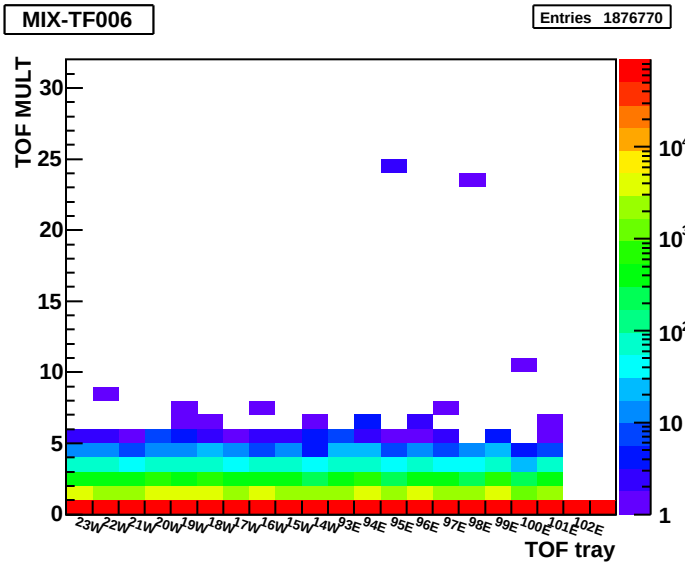

MIX-TF001
Entries 1876770
TOF MULT
TOF tray

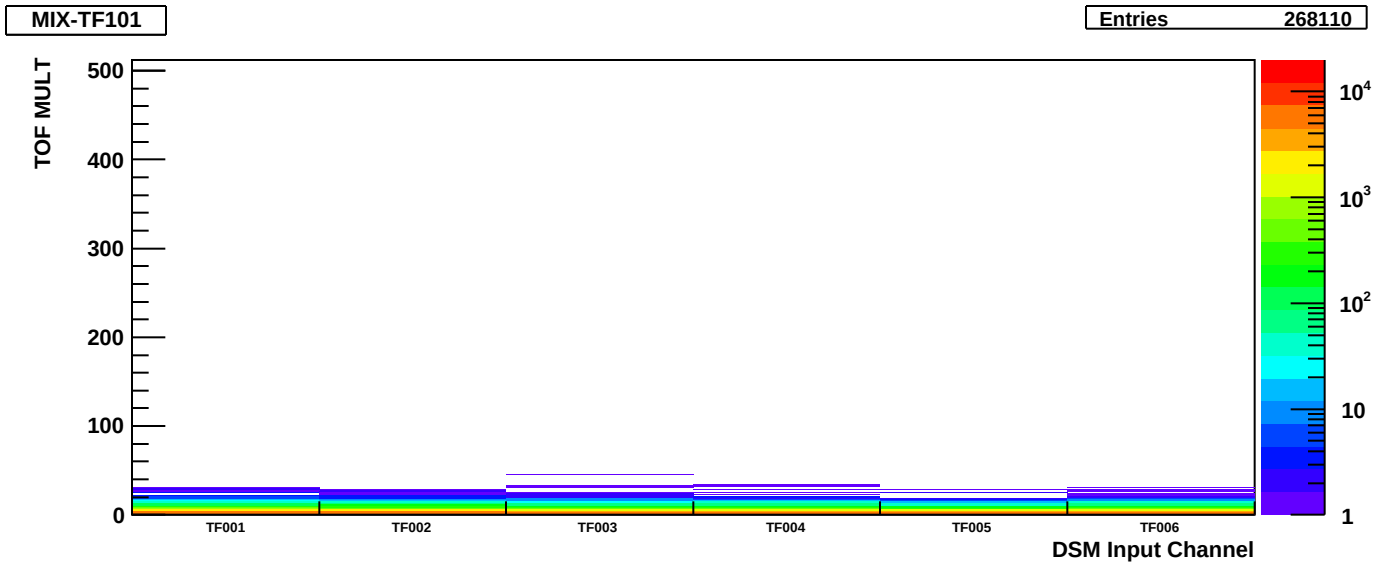

MIX-TF002
Entries 1876770
TOF MULT
TOF tray

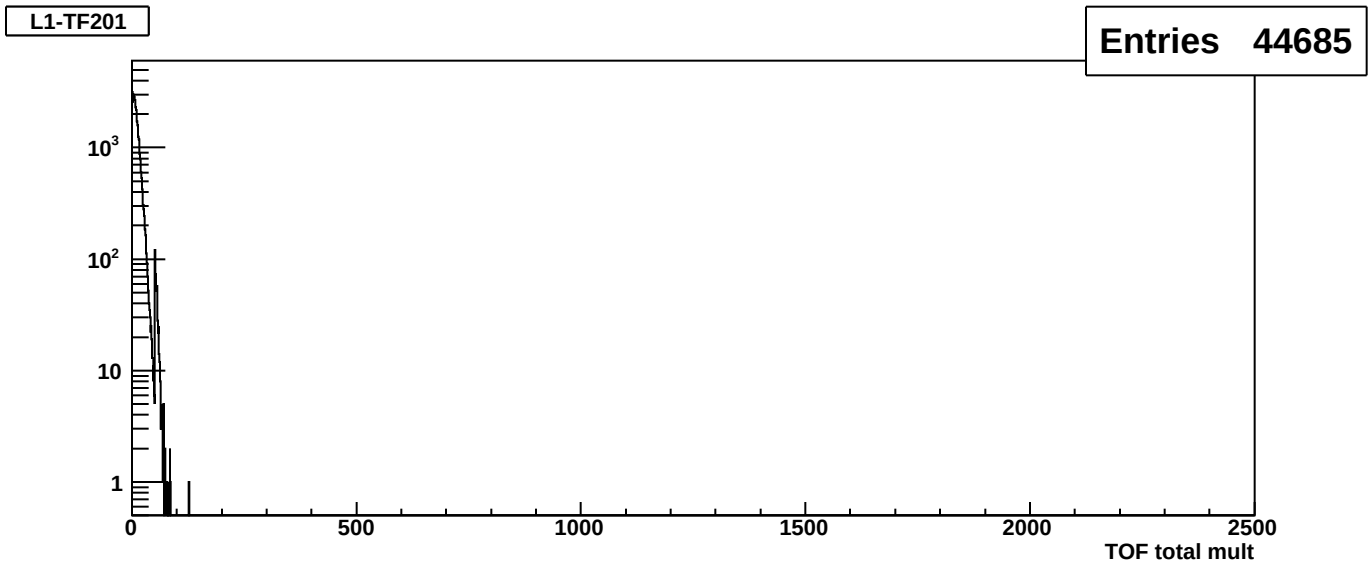

MIX-TF003
Entries 1876770
TOF MULT
TOF tray

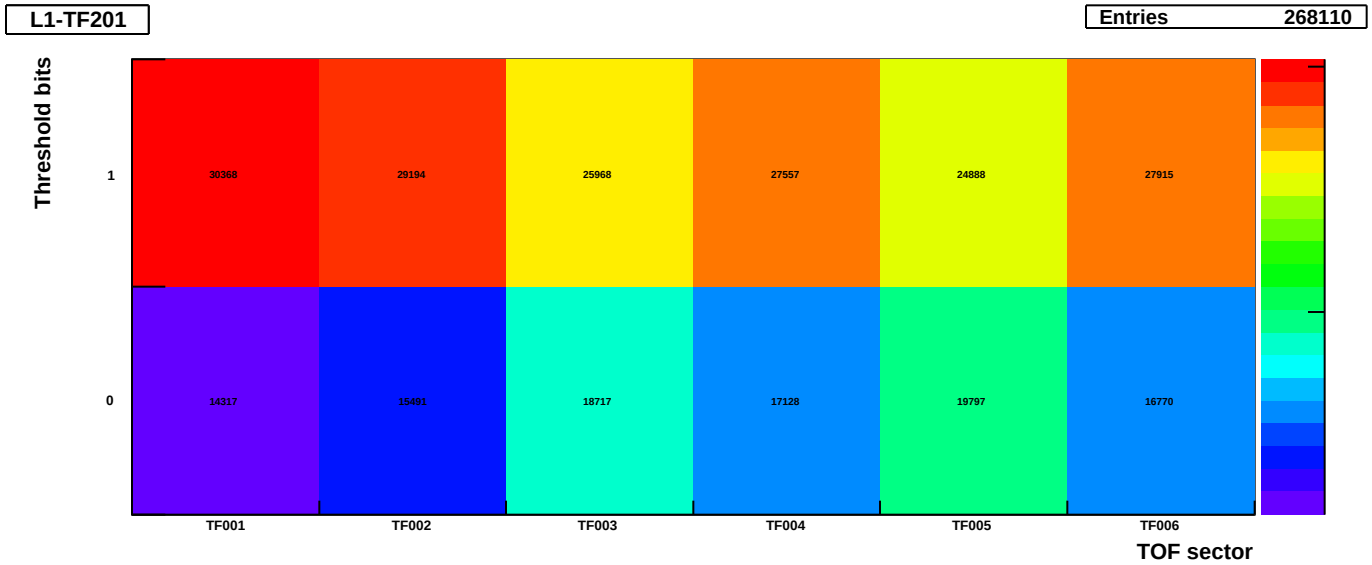

MIX-TF004
Entries 1876770
TOF MULT
TOF tray

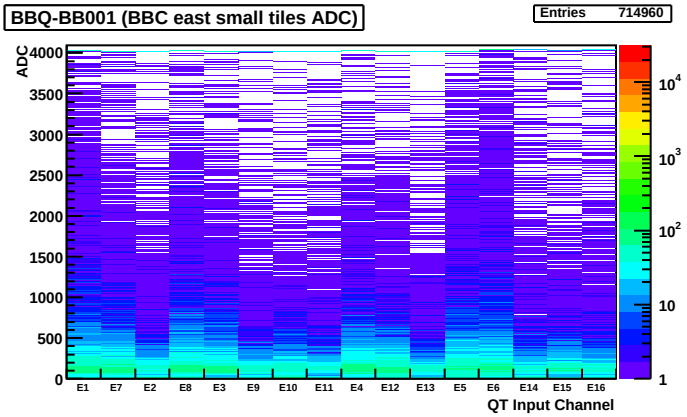

MIX-TF005
Entries 1876770
TOF MULT
TOF tray

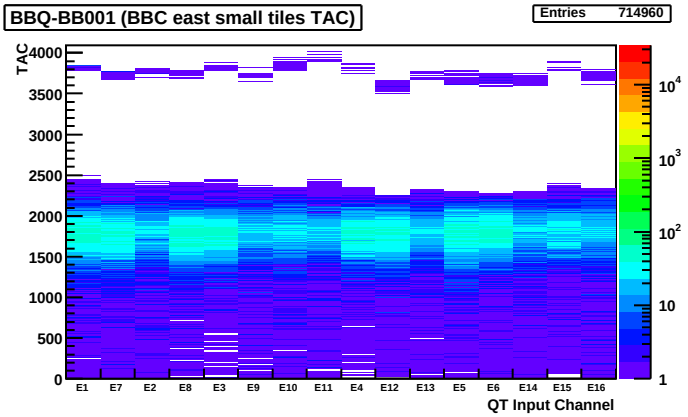

MIX-TF006
Entries 1876770
TOF MULT
TOF tray

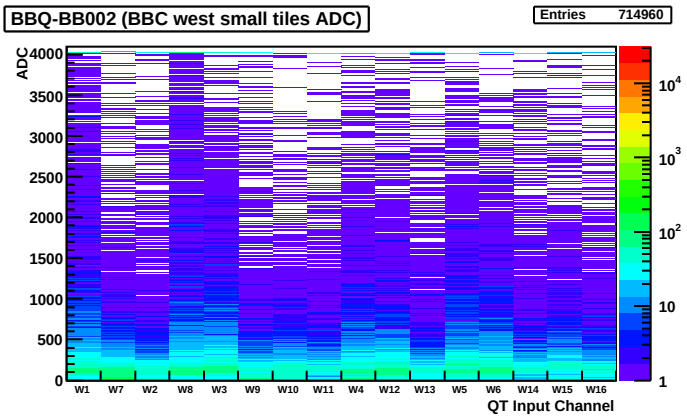

MIX-TF101
Entries 268110
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

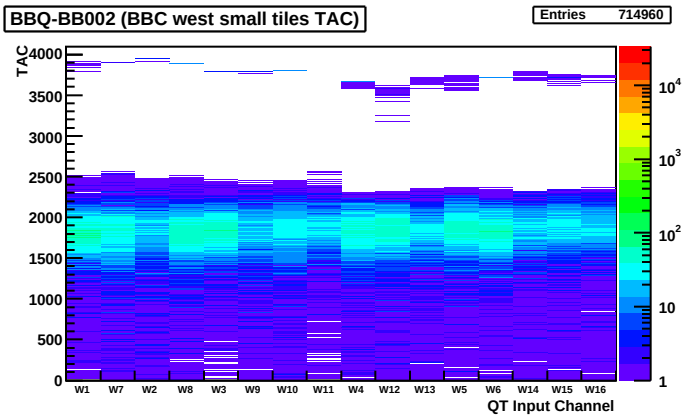

L1-TF201
Entries 44685
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

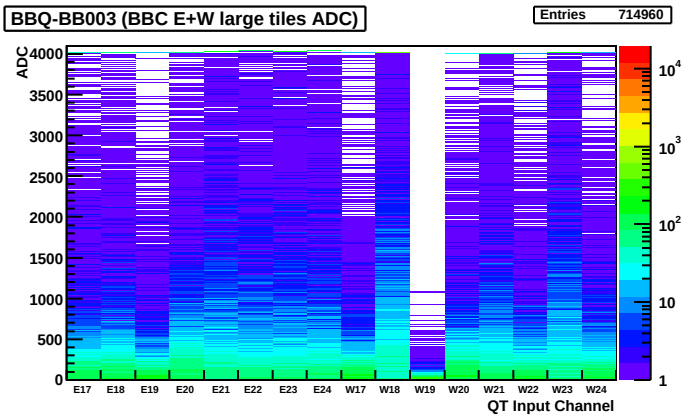

L1-TF201
Entries 268110
Threshold bits
1
0
30368
29194
25968
27557
24888
27915
14317
15491
18717
17128
19797
16770
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

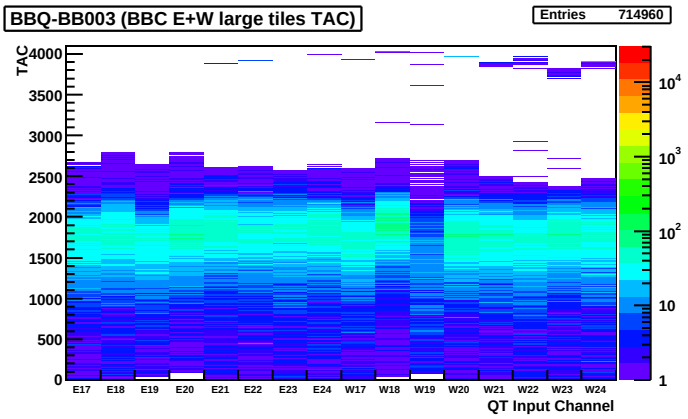

BBQ-BB001 (BBC east small tiles ADC)
Entries 714960
ADC
QT Input Channel

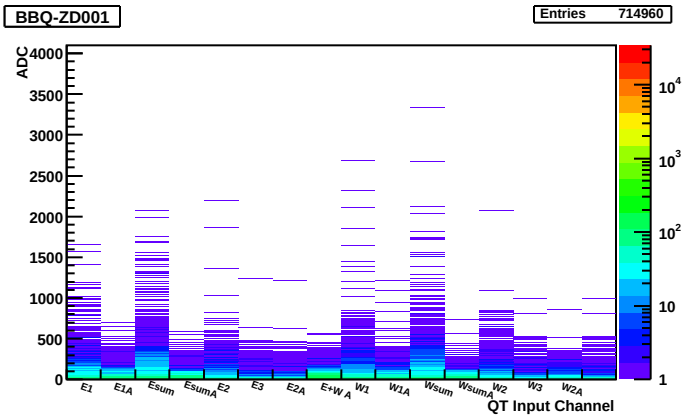

BBQ-BB001 (BBC east small tiles TAC)
Entries 714960
TAC
QT Input Channel

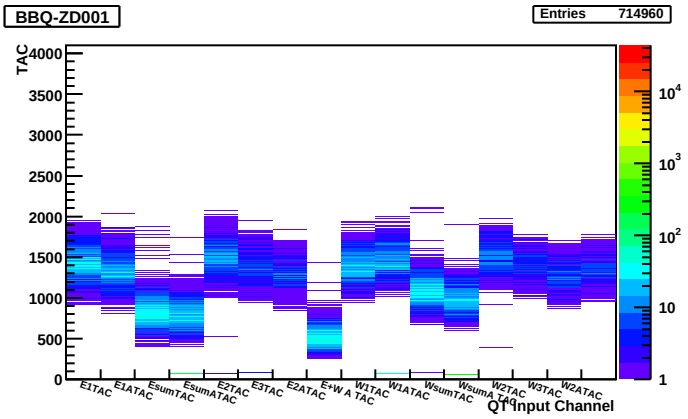

BBQ-BB002 (BBC west small tiles ADC)
Entries 714960
ADC
QT Input Channel

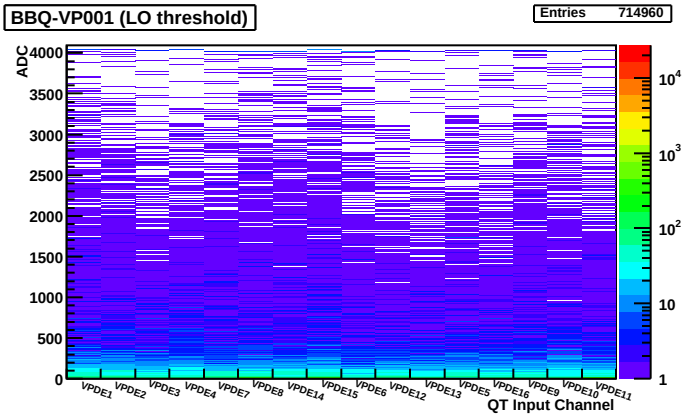

BBQ-BB002 (BBC west small tiles TAC)
Entries 714960
TAC
QT Input Channel

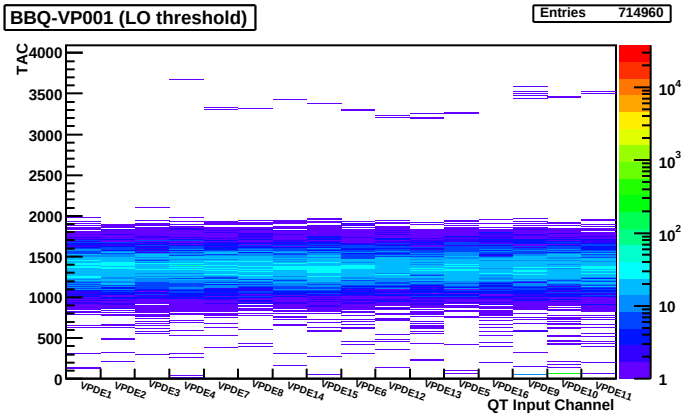

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 714960
ADC
QT Input Channel

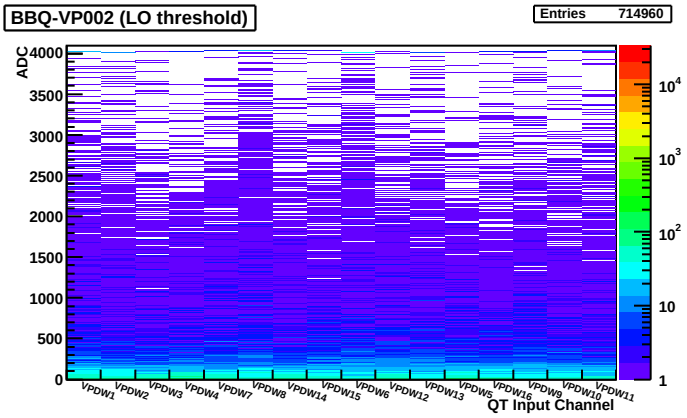

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 714960
TAC
QT Input Channel

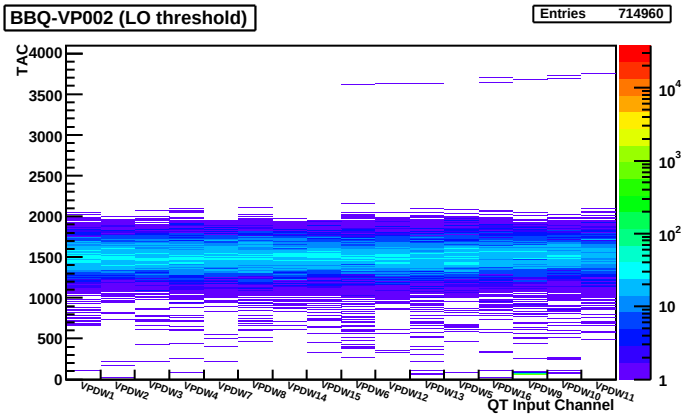

BBQ-ZD001
Entries 714960
ADC
QT Input Channel

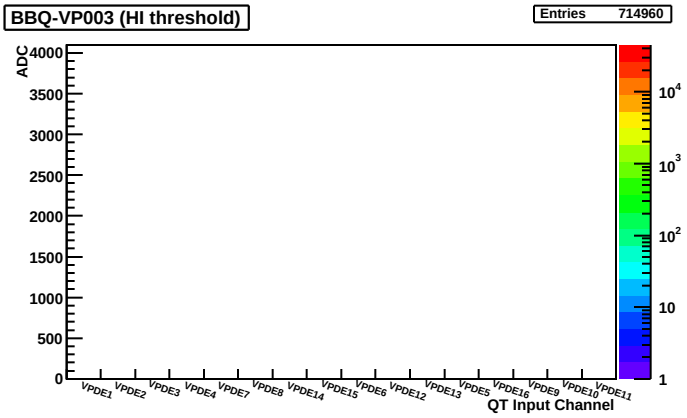

BBQ-ZD001
Entries 714960
TAC
QT Input Channel

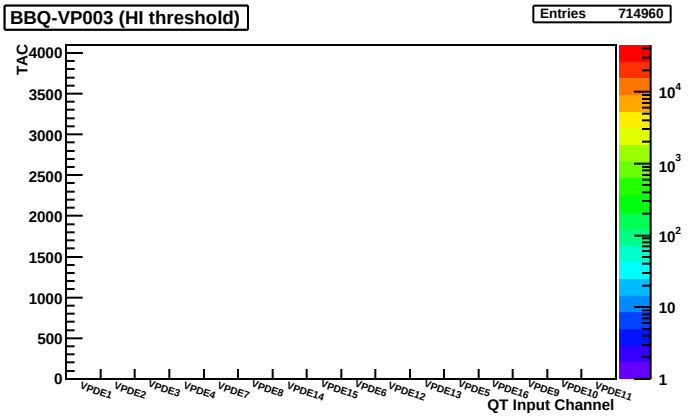

BBQ-VP001 (LO threshold)
Entries 714960
ADC
QT Input Channel

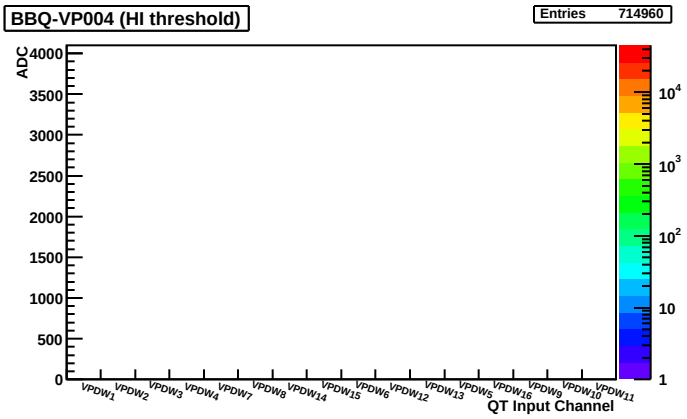

BBQ-VP001 (LO threshold)
Entries 714960
TAC
QT Input Channel

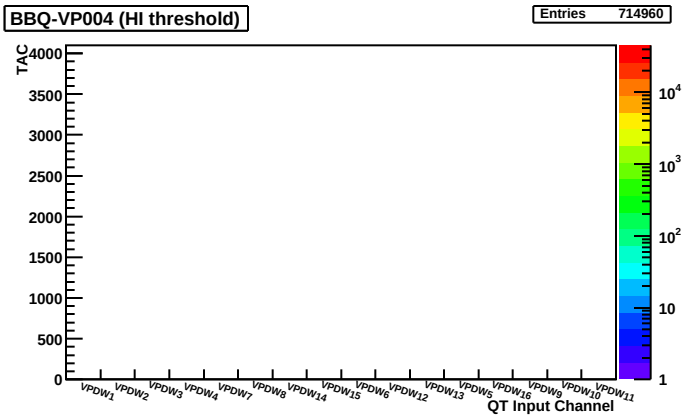

BBQ-VP002 (LO threshold)
Entries 714960
ADC
QT Input Channel

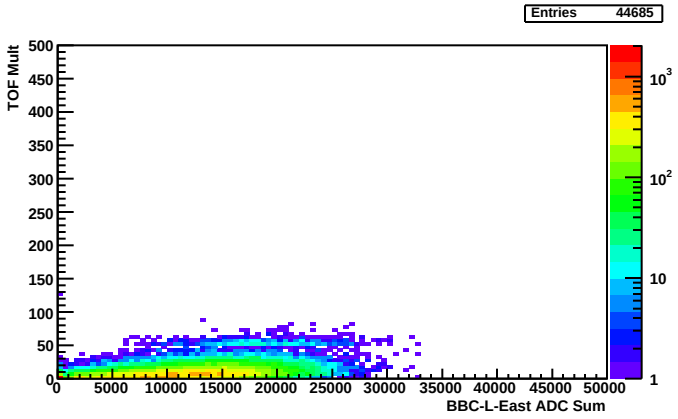

BBQ-VP002 (LO threshold)
Entries 714960
TAC
QT Input Channel

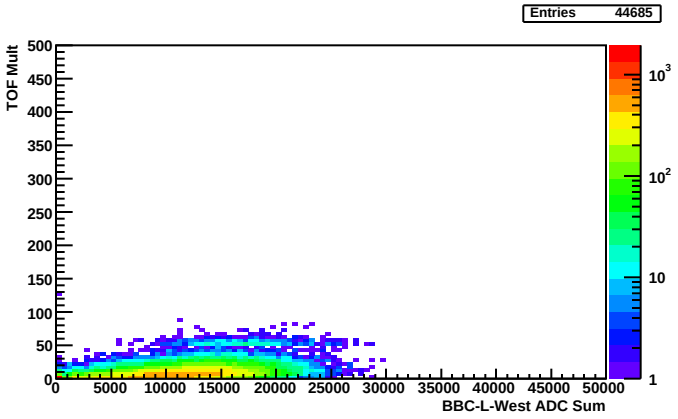

BBQ-VP003 (HI threshold)
Entries 714960
ADC
QT Input Channel

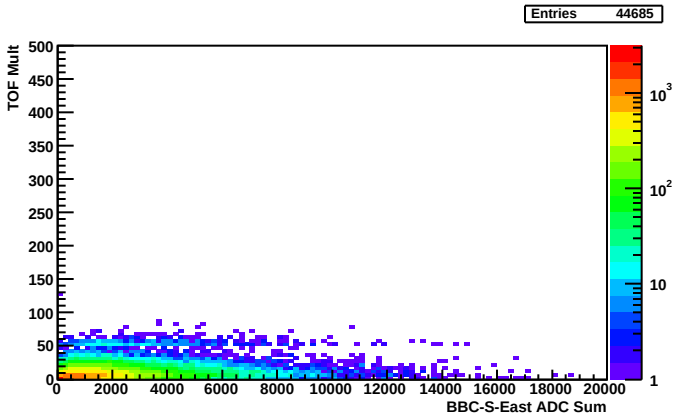

BBQ-VP003 (HI threshold)
Entries 714960
TAC
QT Input Channel

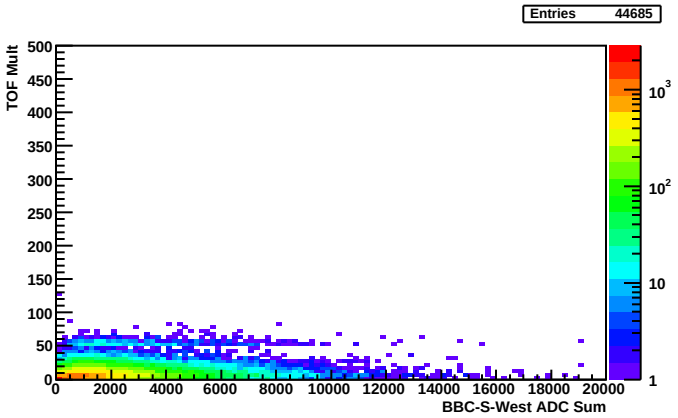

BBQ-VP004 (HI threshold)
Entries 714960
ADC
QT Input Channel

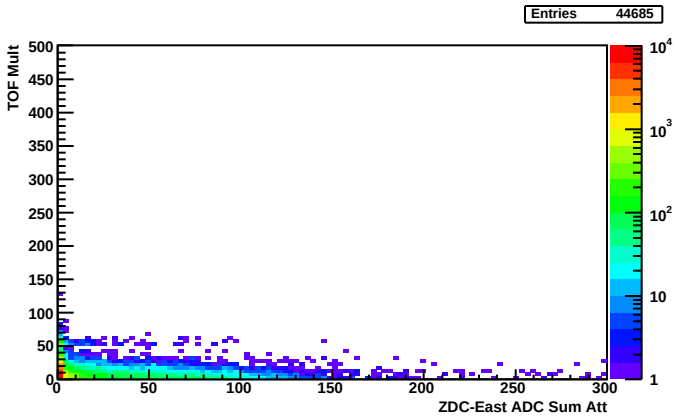

BBQ-VP004 (HI threshold)
Entries 714960
TAC
QT Input Channel

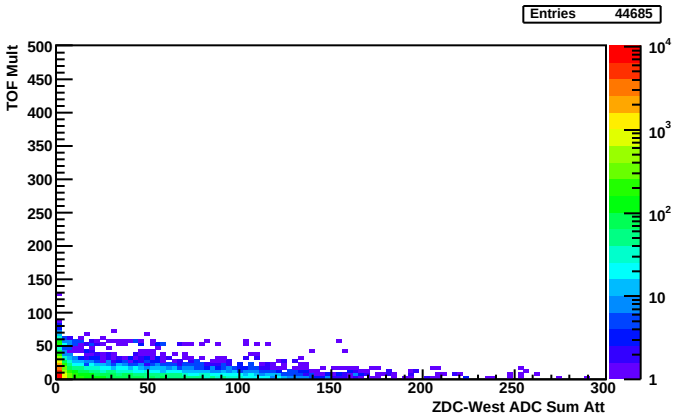

Entries 44685
TOF Mult
BBC-L-East ADC Sum

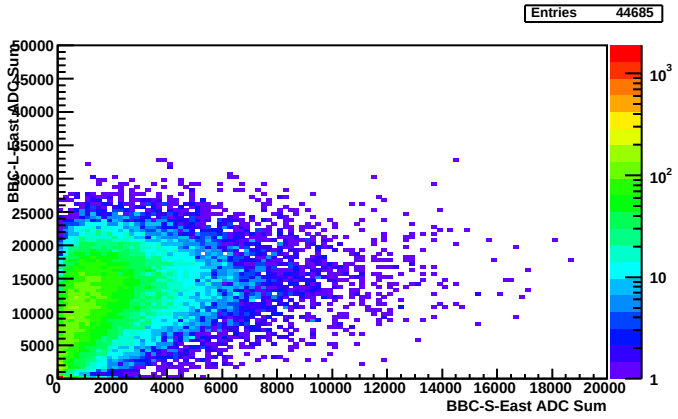

Entries 44685
TOF Mult
BBC-L-West ADC Sum

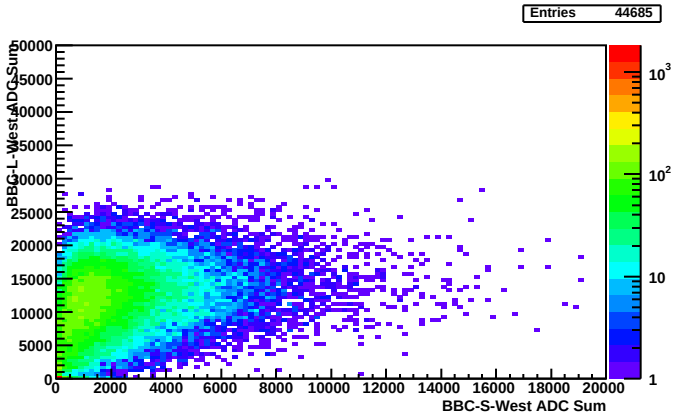

Entries 44685
TOF Mult
BBC-S-East ADC Sum

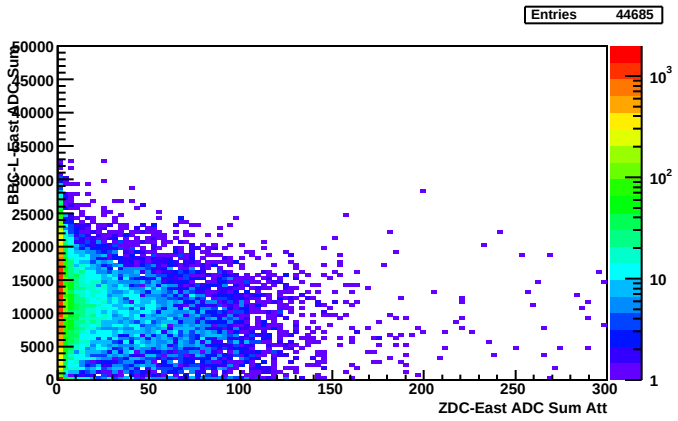

Entries 44685
TOF Mult
BBC-S-West ADC Sum

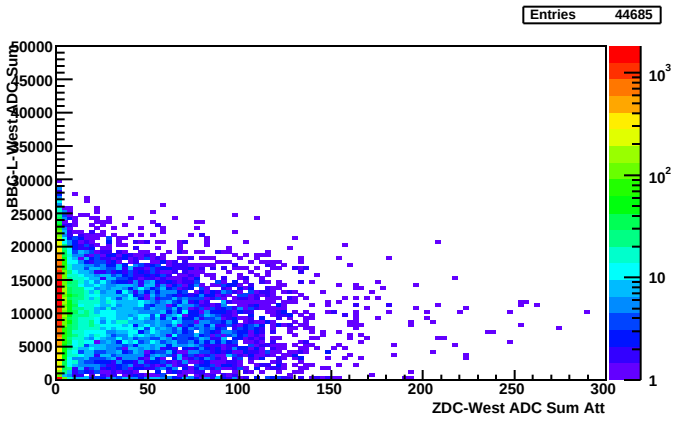

Entries 44685
TOF Mult
ZDC-East ADC Sum Att

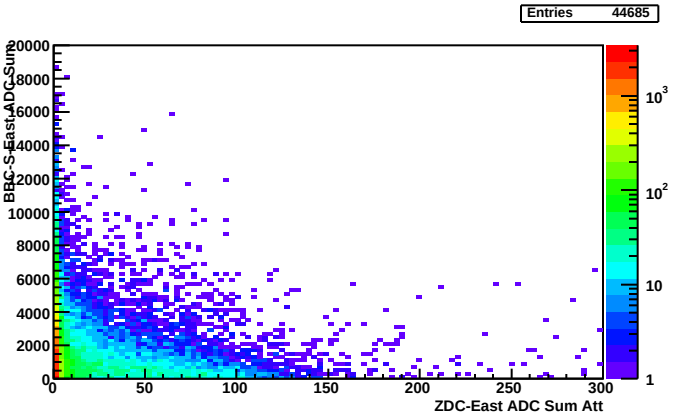

Entries 44685
TOF Mult
ZDC-West ADC Sum Att

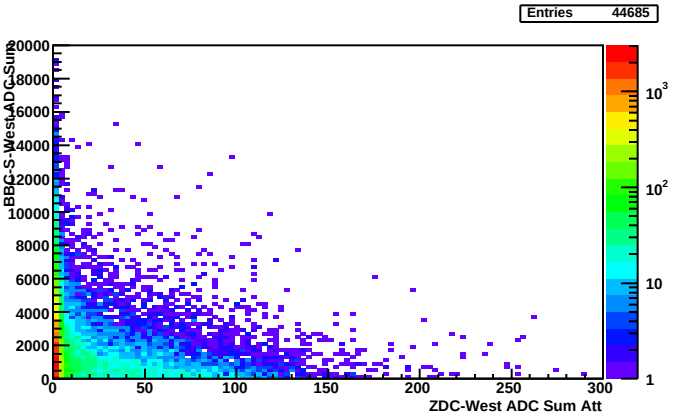

Entries 44685
BBC-L-East ADC Sum
BBC-S-East ADC Sum

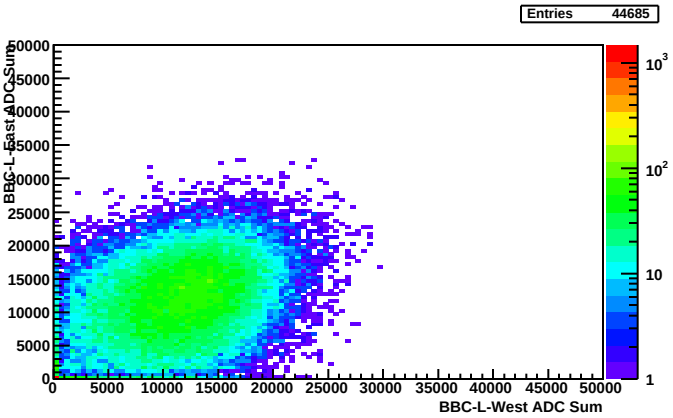

Entries 44685
BBC-L-West ADC Sum
BBC-S-West ADC Sum

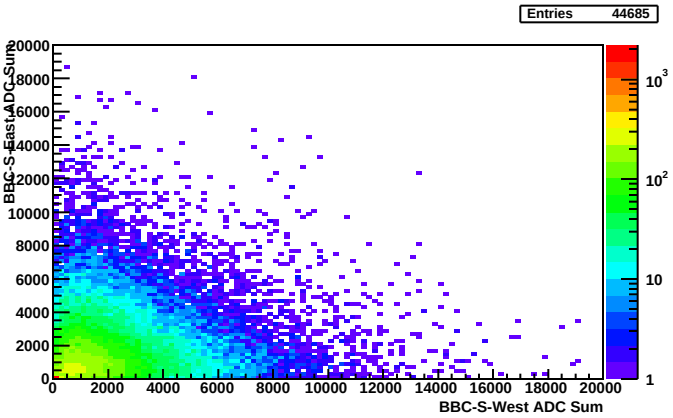

Entries 44685
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

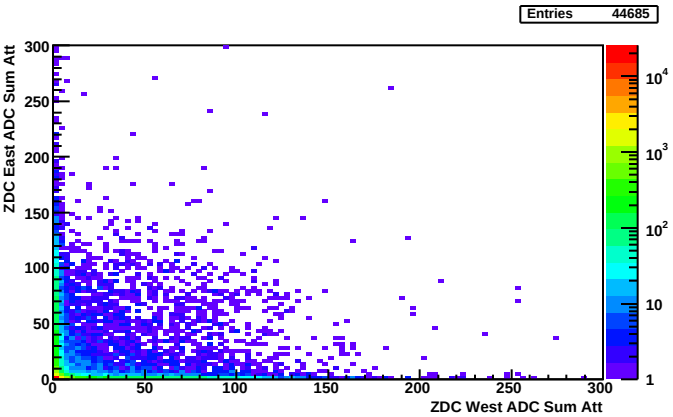

Entries 44685
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

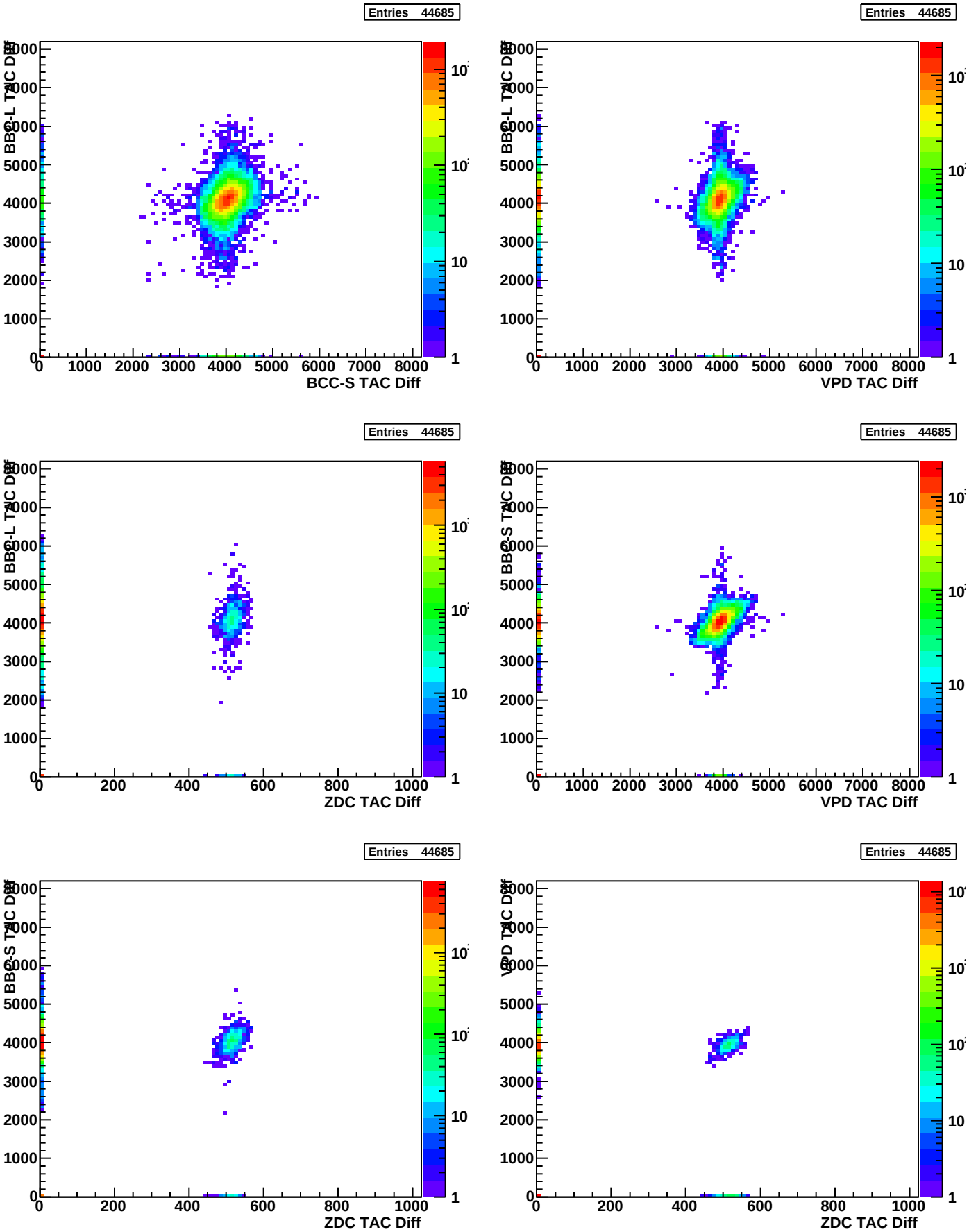

Entries 44685
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

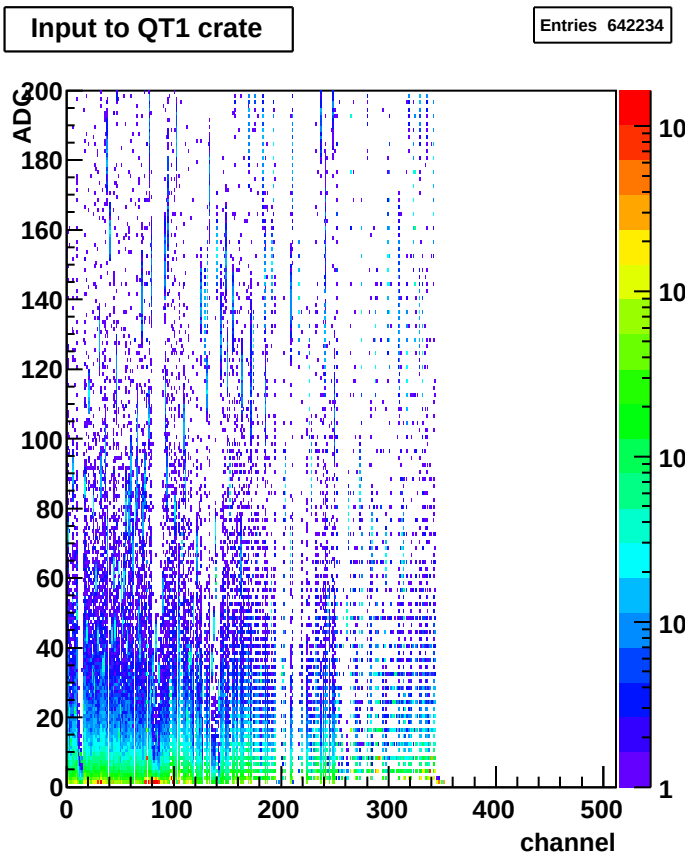

Entries 44685
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

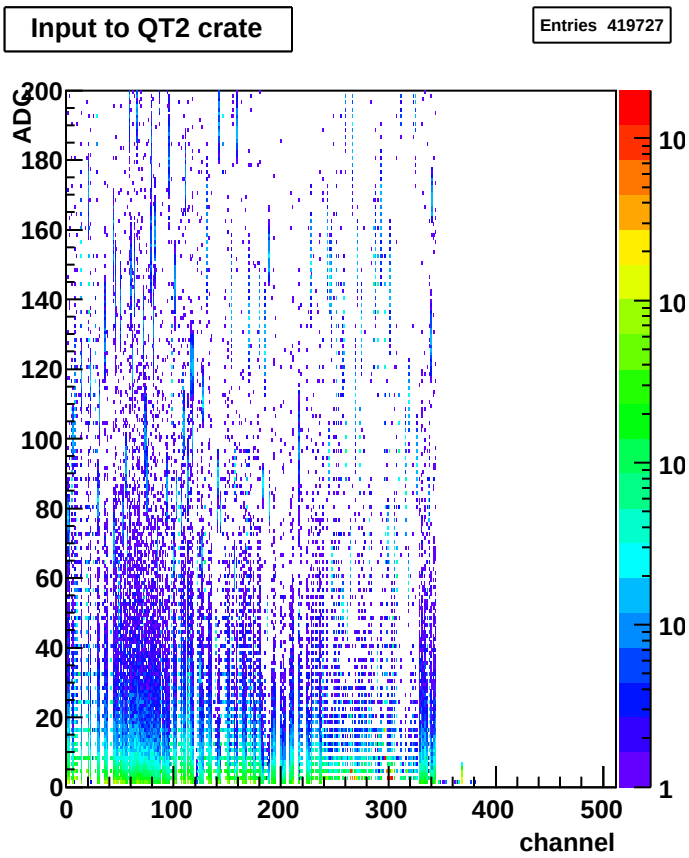

Entries 44685
BBC-L-East ADC Sum
BBC-L-West ADC Sum

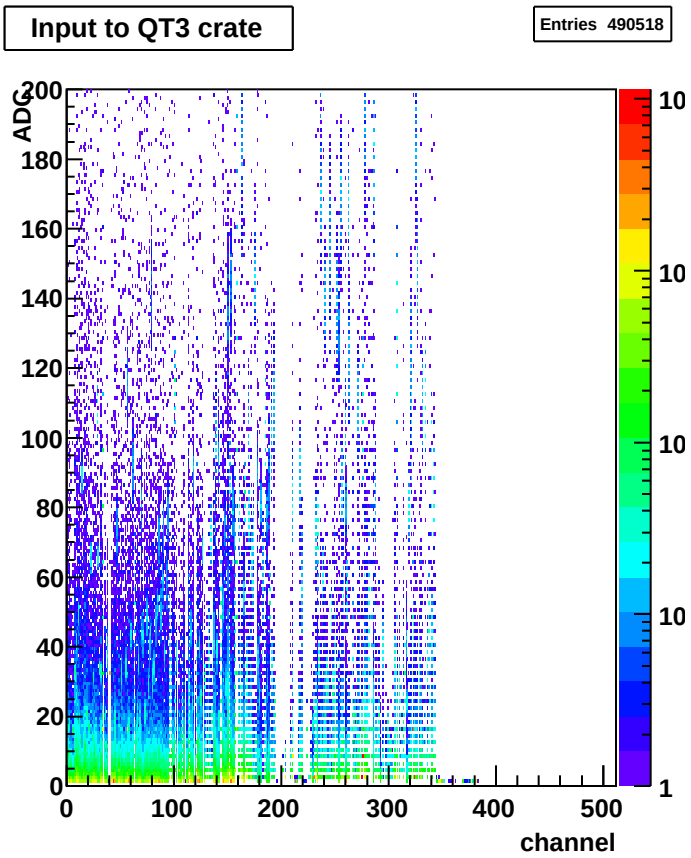

Entries 44685
BBC-S-East ADC Sum
BBC-S-West ADC Sum

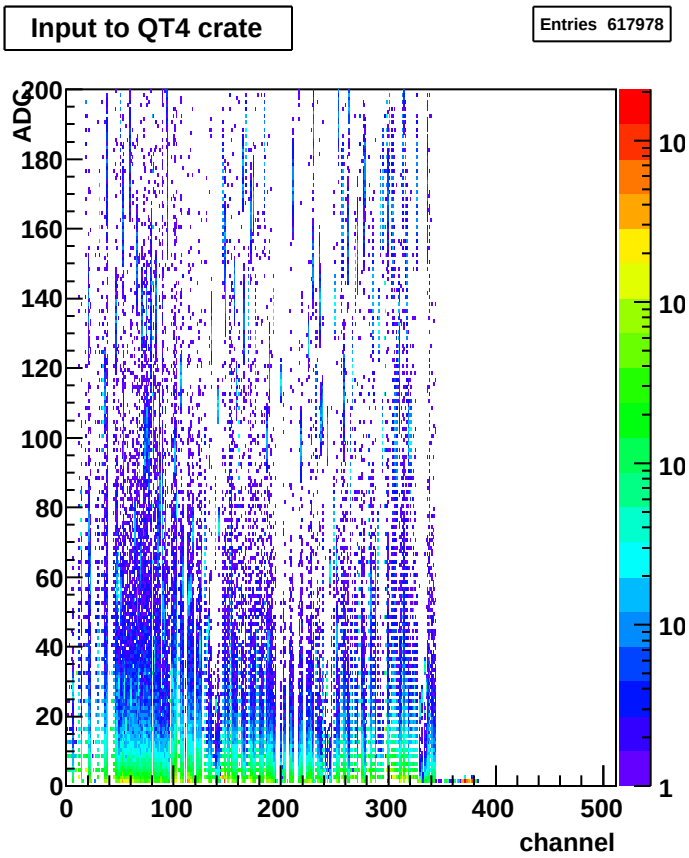

Entries 44685
ZDC East ADC Sum Att
ZDC West ADC Sum Att

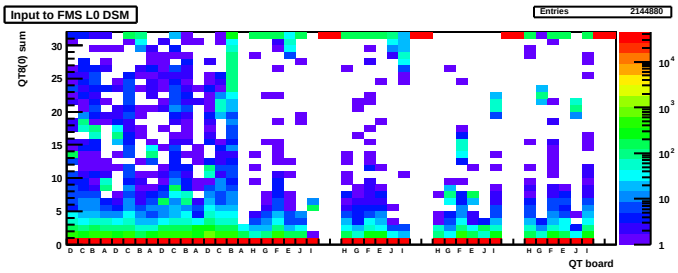

Entries 44685
BBC-L TAC Diff
BCC-S TAC Diff
Entries 44685
BBC-L TAC Diff
VPD TAC Diff
Entries 44685
BBC-L TAC Diff
ZDC TAC Diff
Entries 44685
BBC-S TAC Diff
VPD TAC Diff
Entries 44685
BBC-S TAC Diff
ZDC TAC Diff
Entries 44685
VPD TAC Diff
ZDC TAC Diff

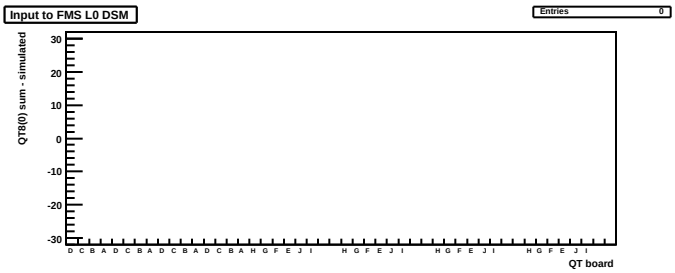

Input to QT1 crate
Entries 642234
ADC
channel

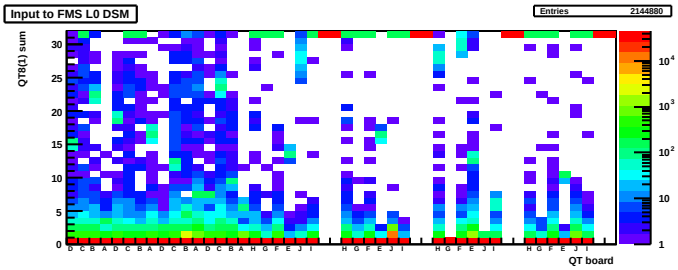

Input to QT2 crate
Entries 419727
ADC
channel

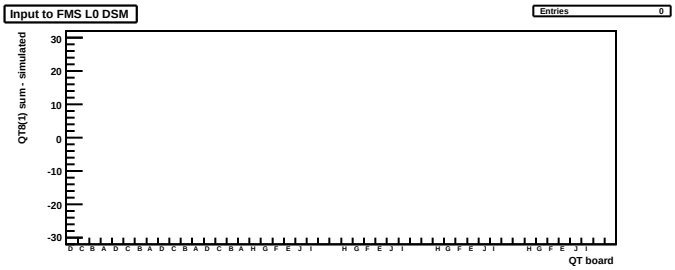

Input to QT3 crate
Entries 490518
ADC
channel

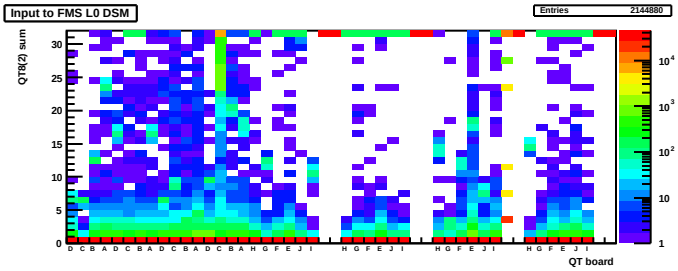

Input to QT4 crate
Entries 617978
ADC
channel

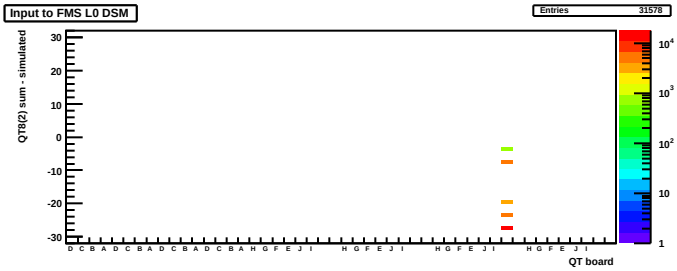

Input to FMS L0 DSM
Entries 2144880
QT8(0) sum
QT board

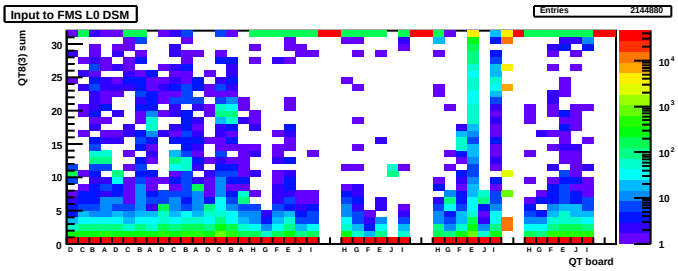

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

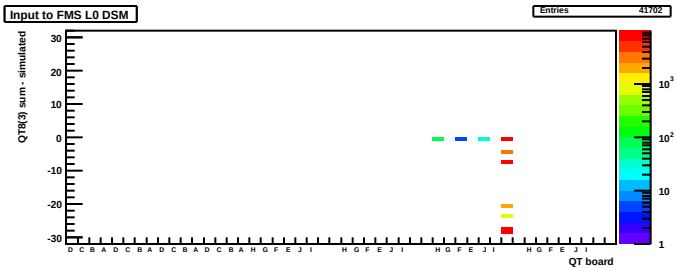

Input to FMS L0 DSM
Entries 2144880
QT8(1) sum
QT board

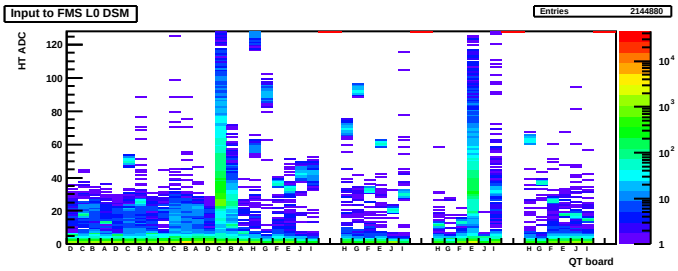

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

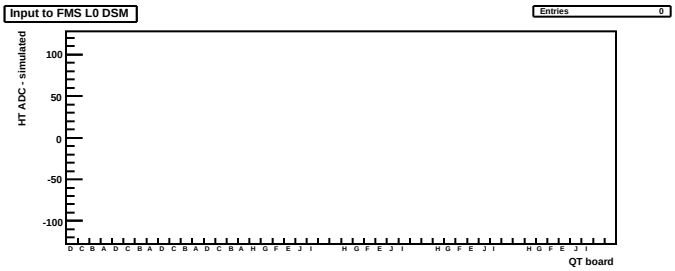

Input to FMS L0 DSM
Entries 2144880
QT8(2) sum
QT board

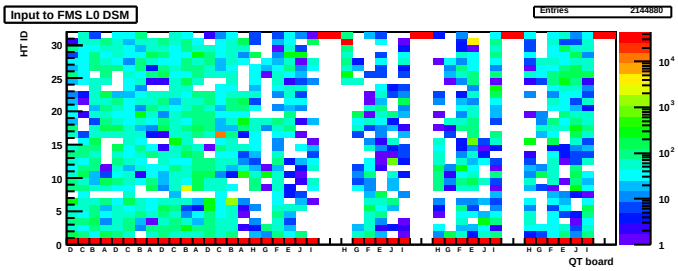

Input to FMS L0 DSM
Entries 31578
QT8(2) sum - simulated
QT board

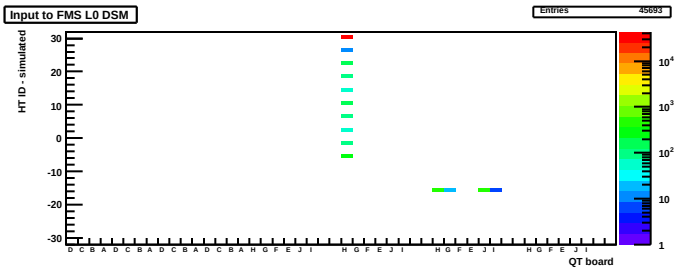

Input to FMS L0 DSM
Entries 2144880
QT8(3) sum
QT board

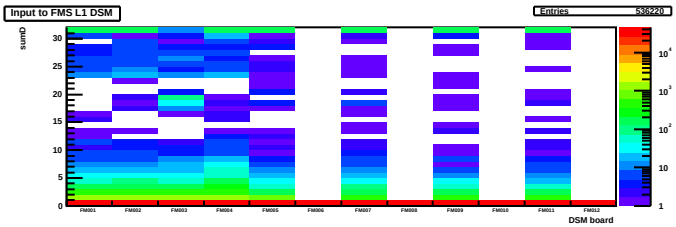

Input to FMS L0 DSM
Entries 41702
QT8(3) sum - simulated
QT board

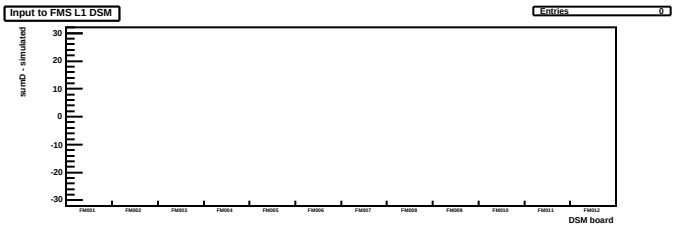

Input to FMS L0 DSM
Entries 2144880
HT ADC
QT board

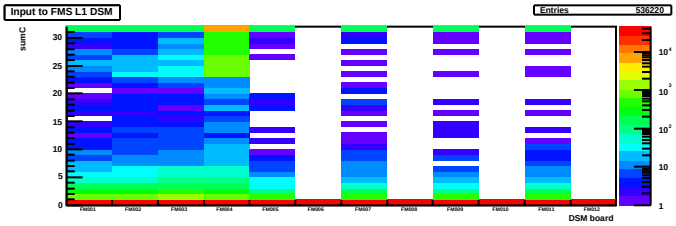

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

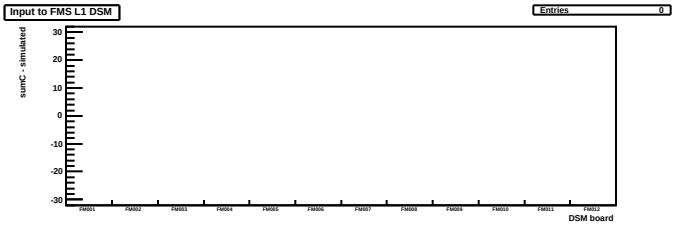

Input to FMS L0 DSM
Entries 2144880
HT ID
QT board

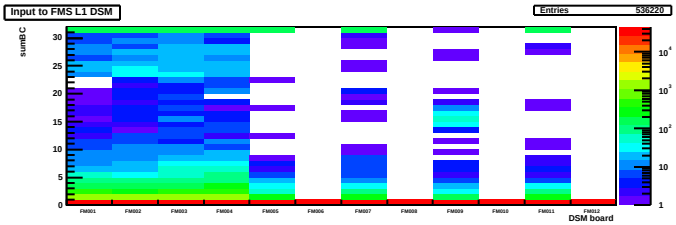

Input to FMS L0 DSM
Entries 45693
HT ID - simulated
QT board

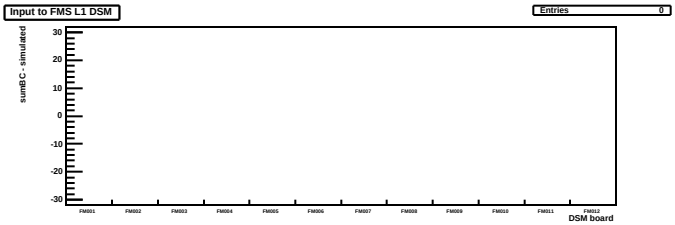

Input to FMS L1 DSM
Entries 536220
sumD
DSM board

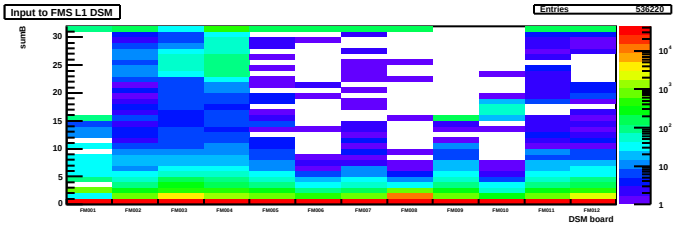

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

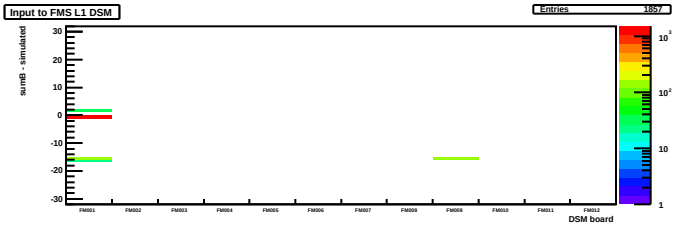

Input to FMS L1 DSM
Entries 536220
sumC
DSM board

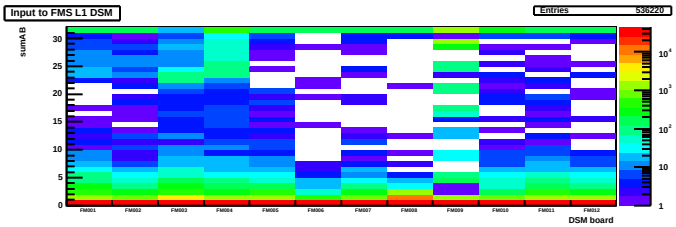

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

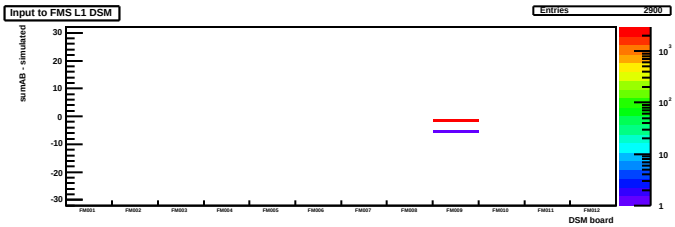

Input to FMS L1 DSM
Entries 536220
sumBC
DSM board

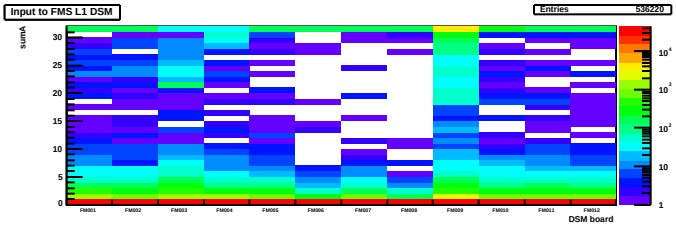

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

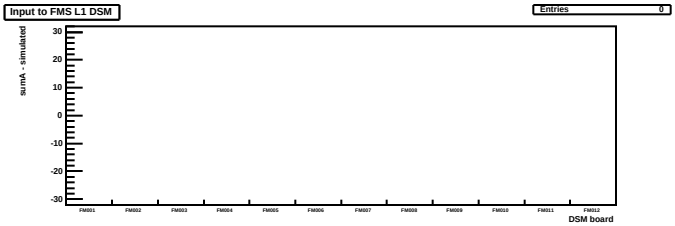

Input to FMS L1 DSM
Entries 536220
sumB
DSM board

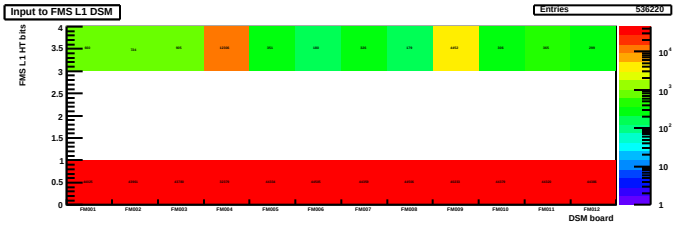

Input to FMS L1 DSM
Entries 1857
sumB - simulated
DSM board

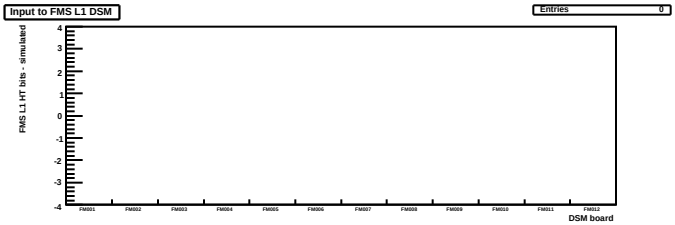

Input to FMS L1 DSM
Entries 536220
sumAB
DSM board

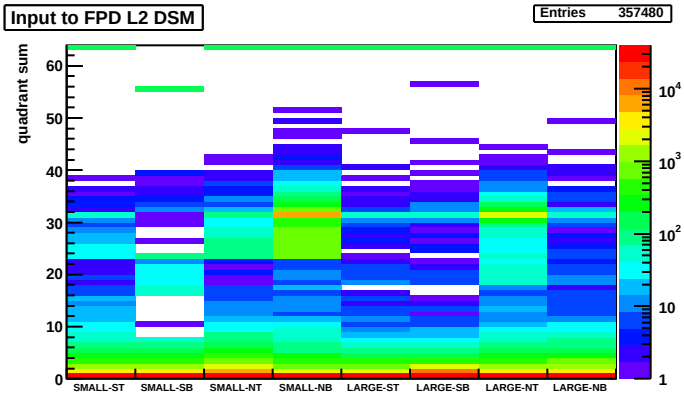

Input to FMS L1 DSM
Entries 2900
sumAB - simulated
DSM board

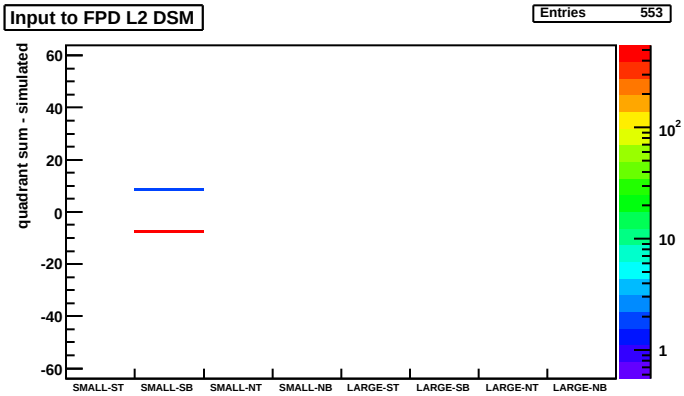

Input to FMS L1 DSM
Entries 536220
sumA
DSM board

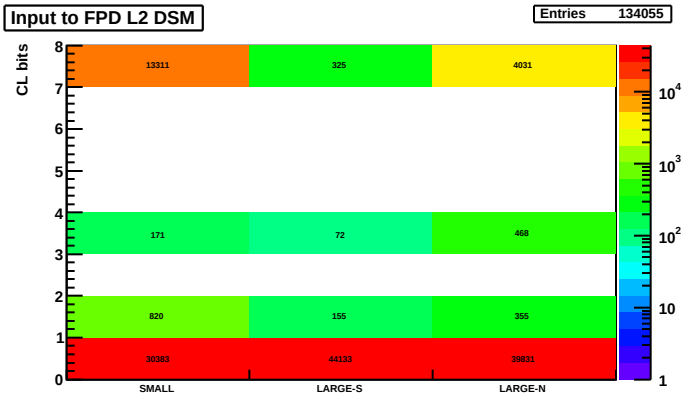

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

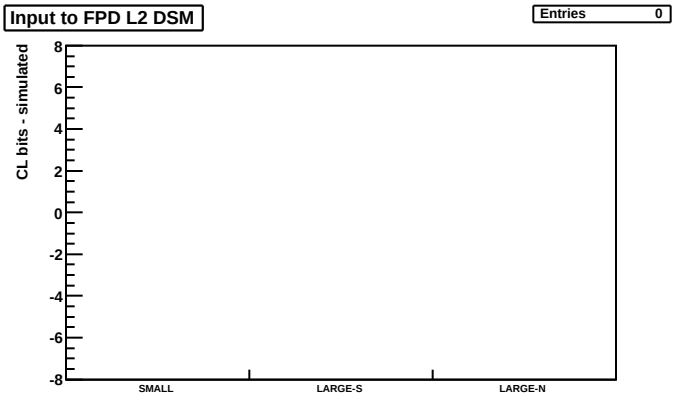

Input to FMS L1 DSM
Entries 536220
FMS L1 HT bits
DSM board

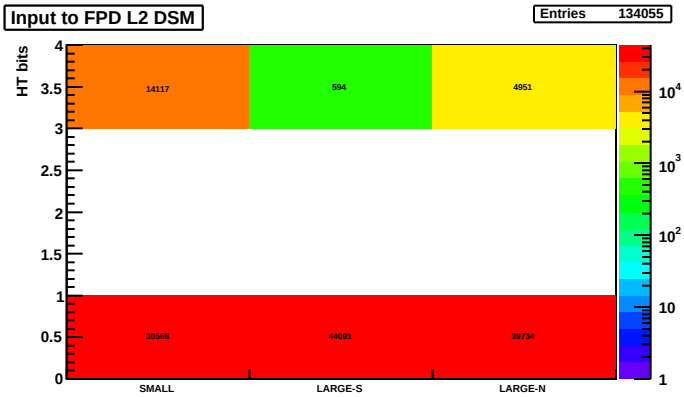

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

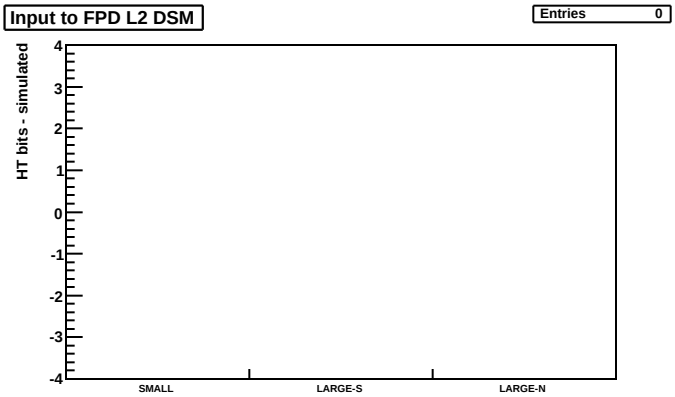

Input to FPD L2 DSM
Entries 357480
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

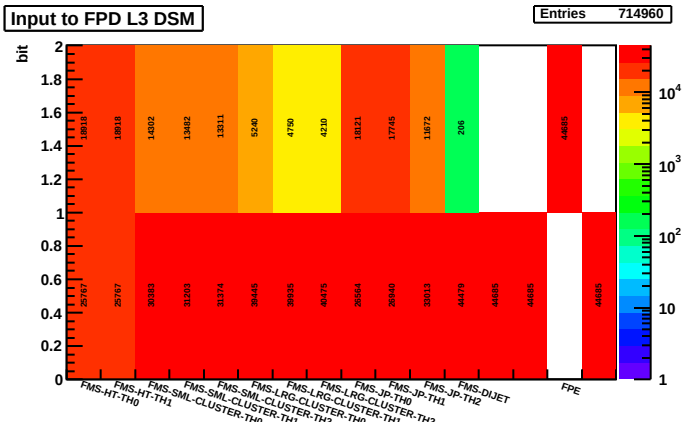

Input to FPD L2 DSM
Entries 553
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

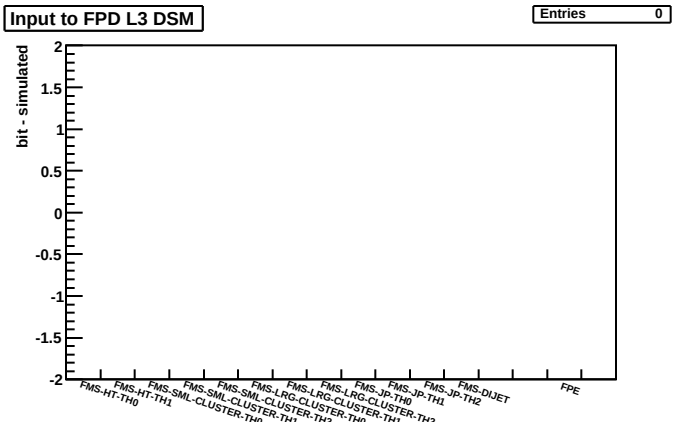

Input to FPD L2 DSM
Entries 134055
CL bits
13311
325
4031
171
72
468
820
155
355
30383
44133
39831
SMALL
LARGE-S
LARGE-N

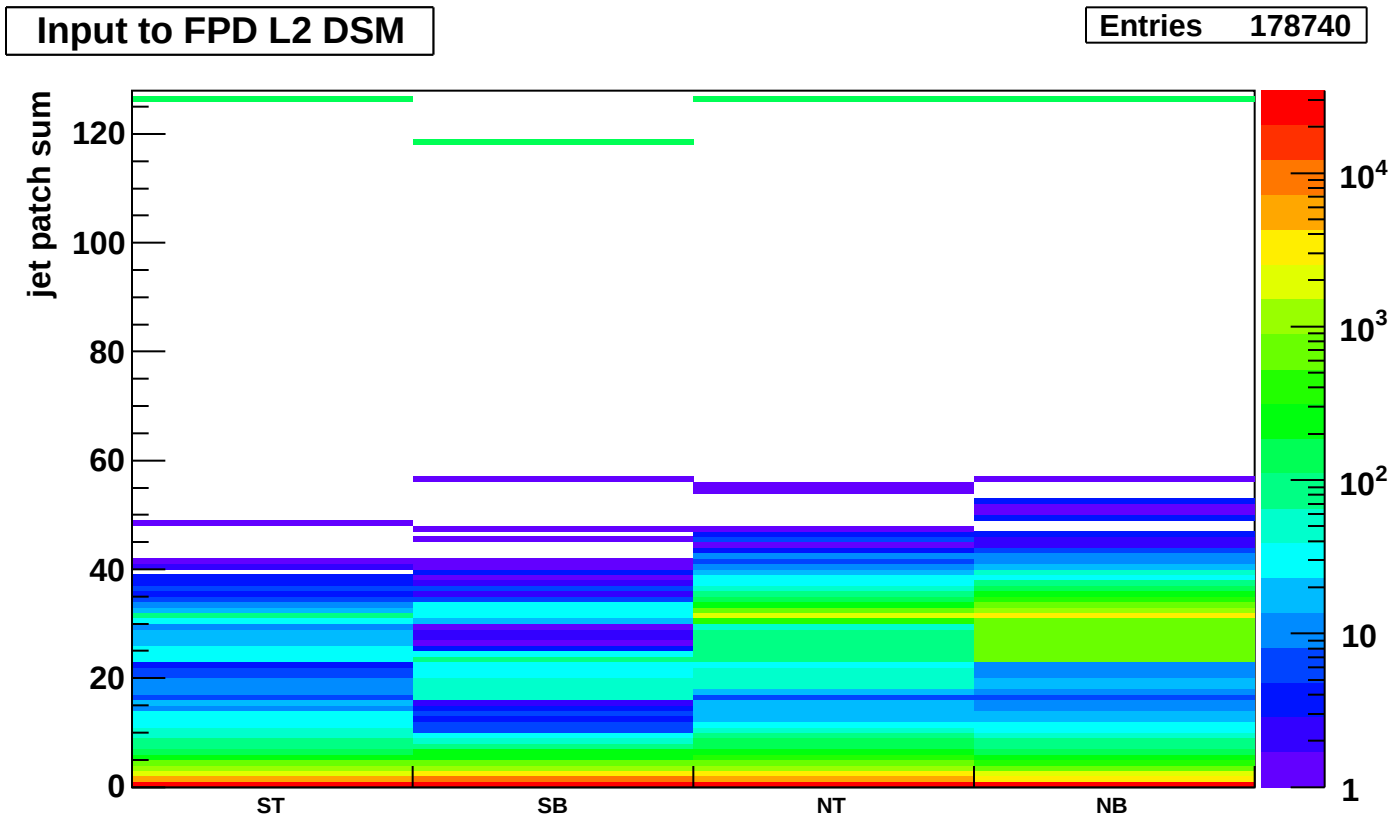

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

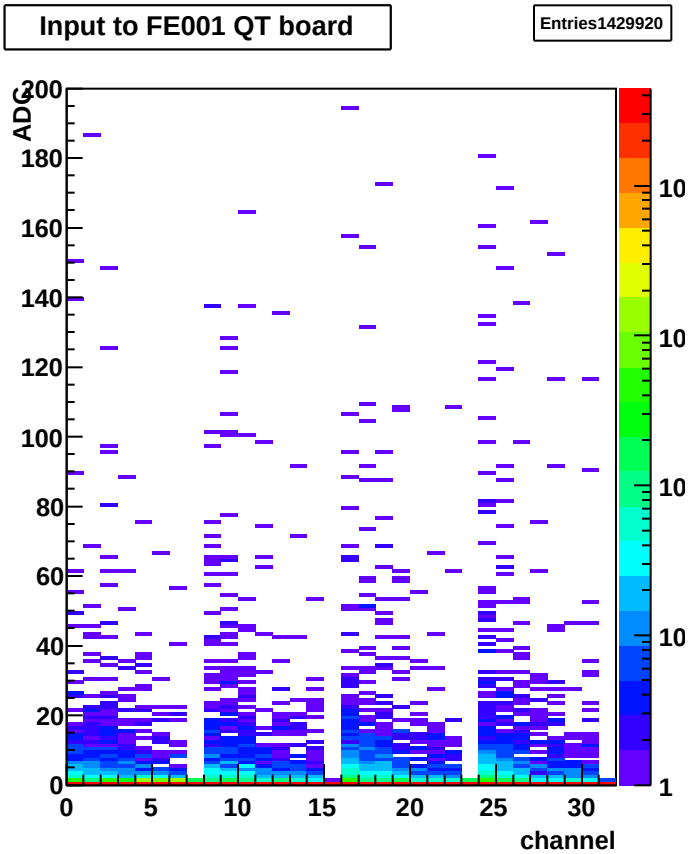

Input to FPD L2 DSM
Entries 134055
HT bits
14117
594
4951
30568
44091
39734
SMALL
LARGE-S
LARGE-N

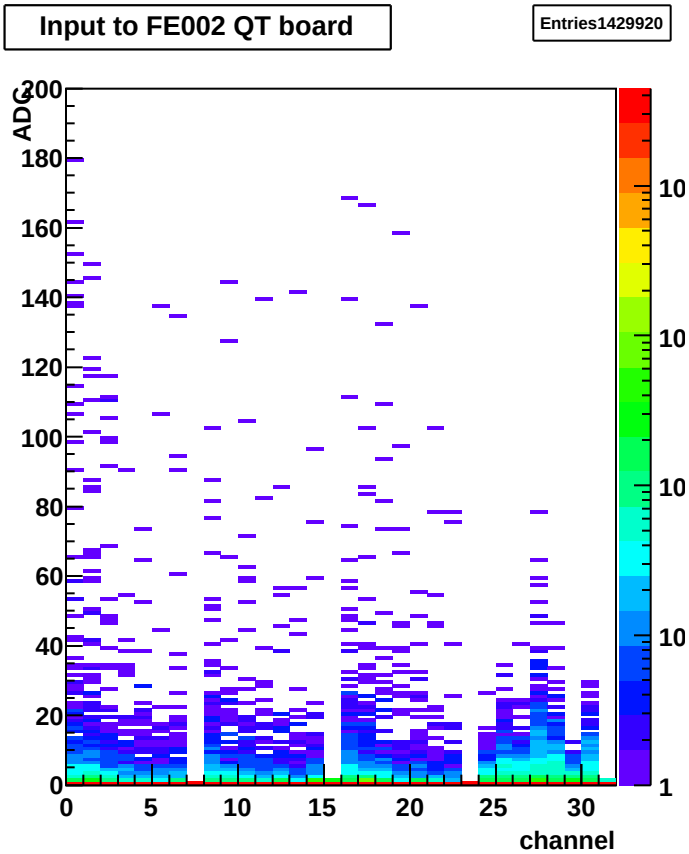

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

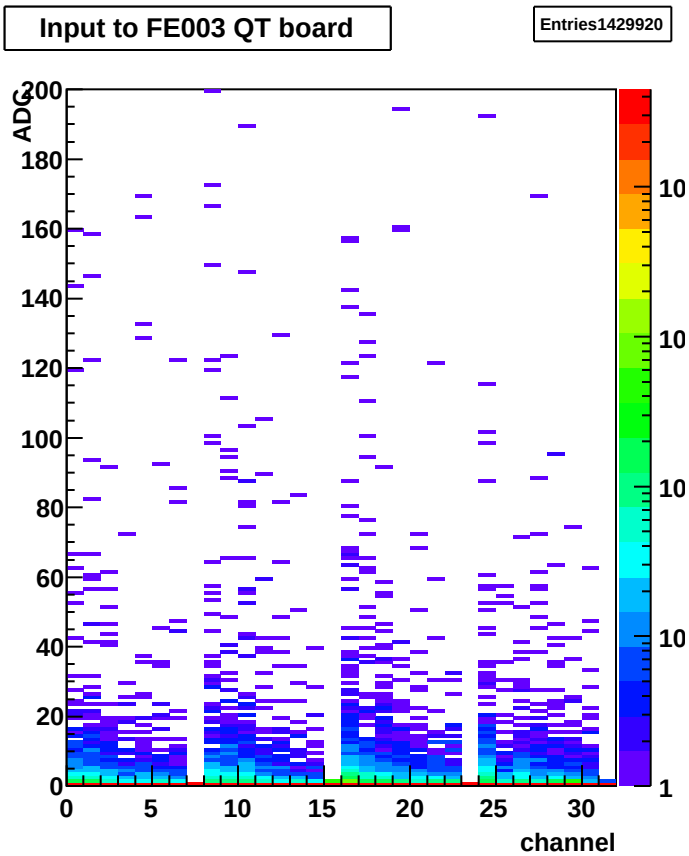

Input to FPD L3 DSM
Entries 714960
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

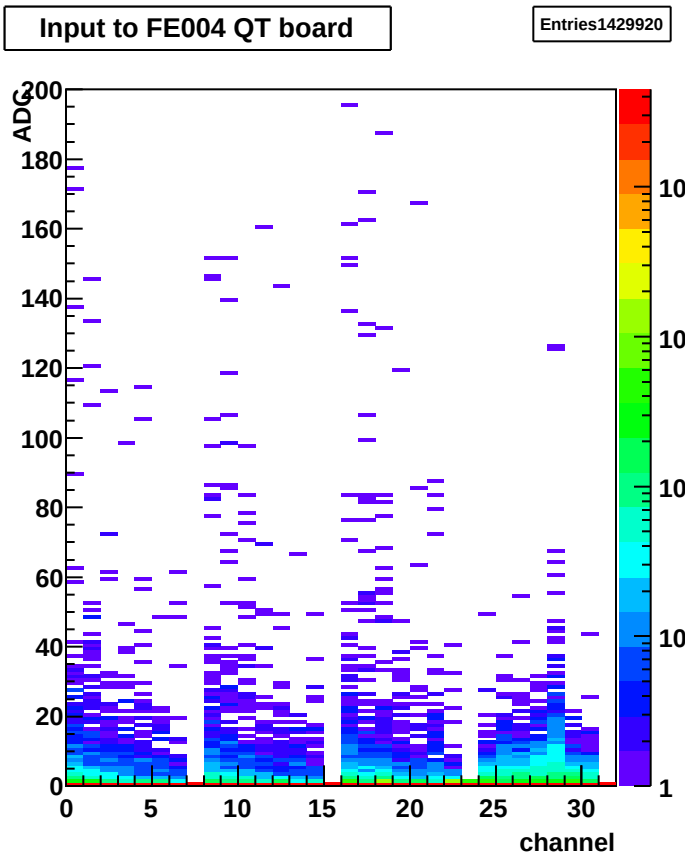

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

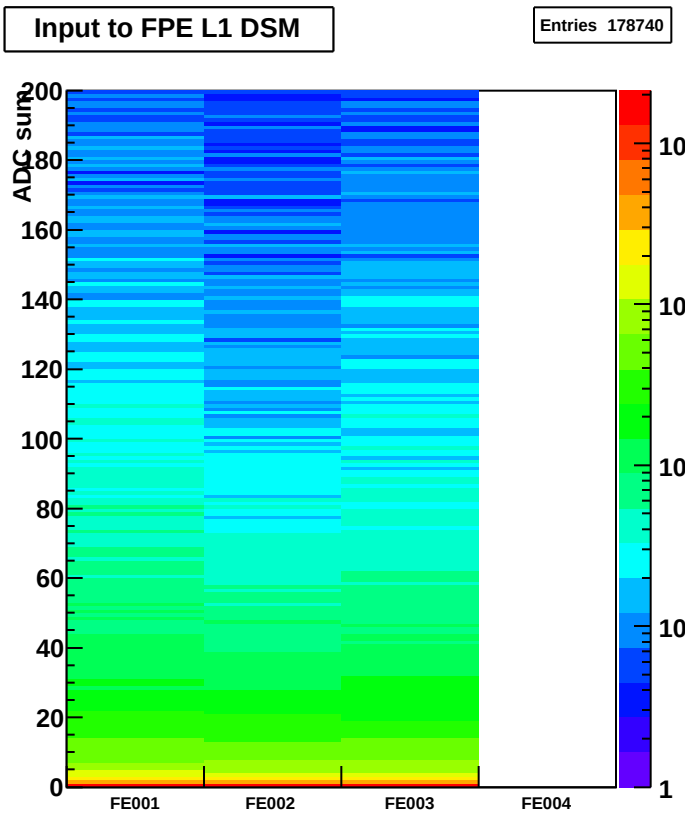

Input to FPD L2 DSM
Entries 178740
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

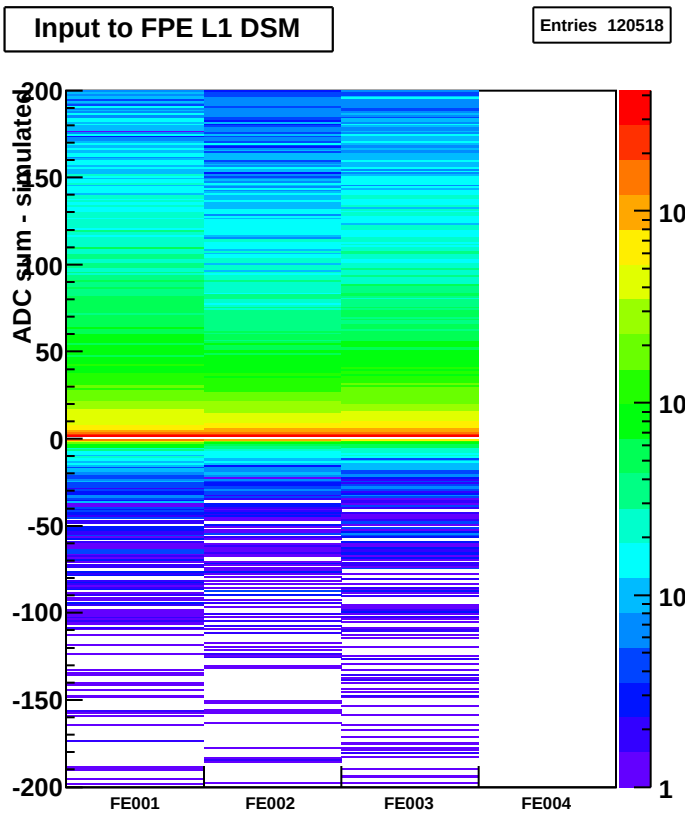

Input to FE001 QT board
Entries1429920
ADC
channel

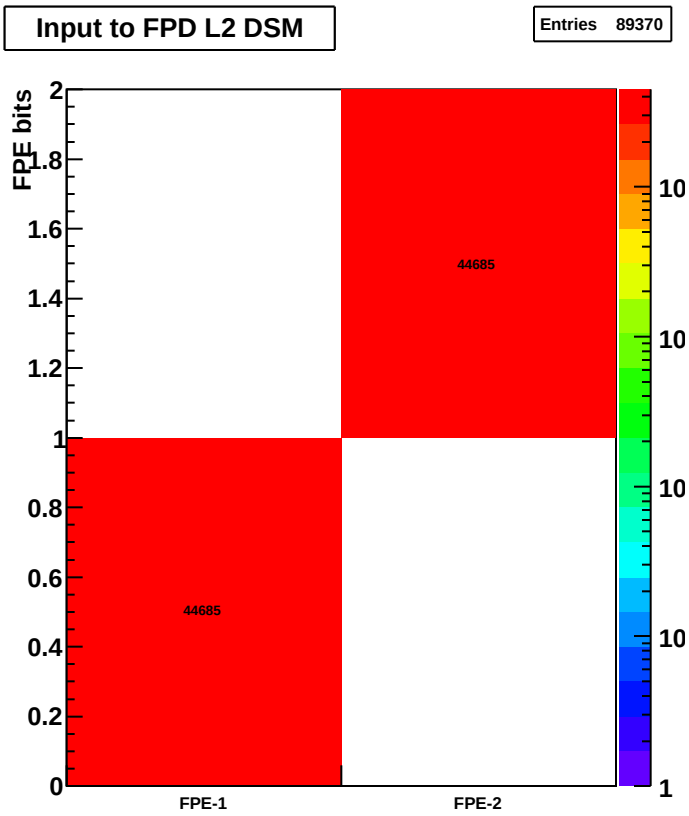

Input to FE002 QT board
Entries1429920
ADC
channel

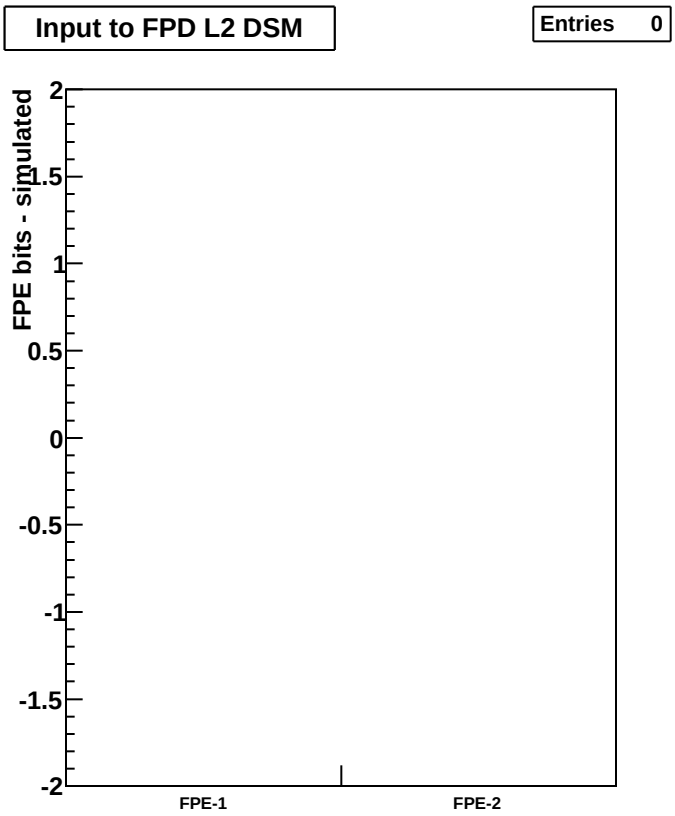

Input to FE003 QT board
Entries1429920
ADC
channel


Input to FE004 QT board
Entries1429920
ADC
channel


Input to FPE L1 DSM
Entries 178740
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 120518
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 89370
FPE bits
44685
44685
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2