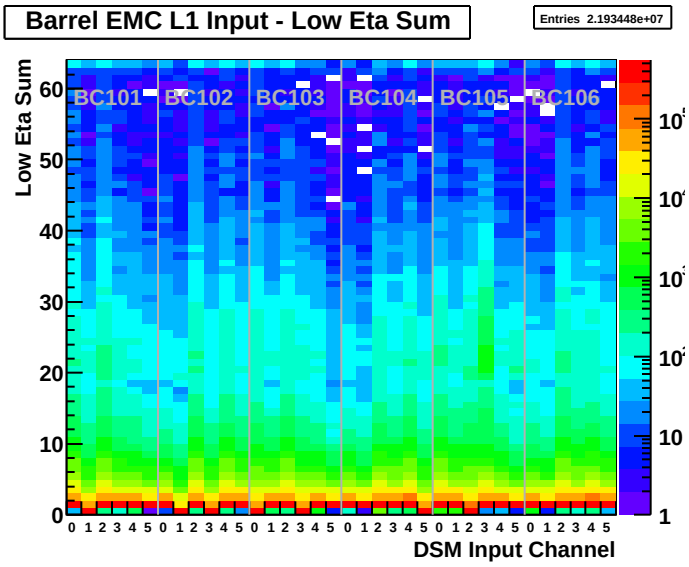
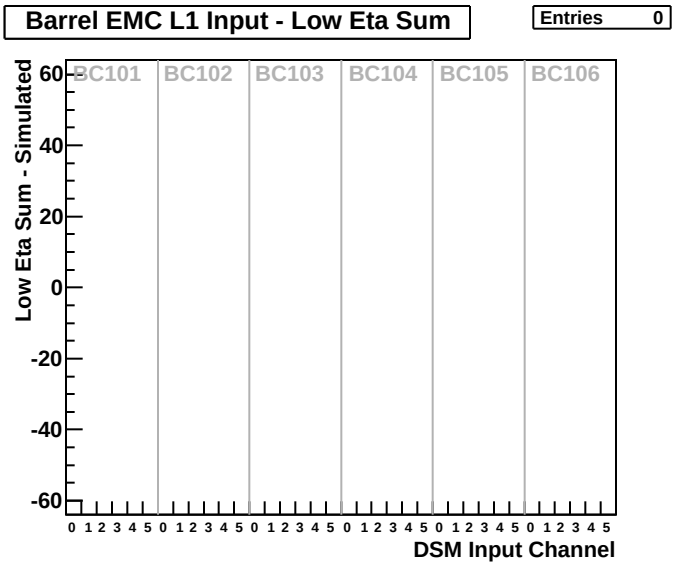
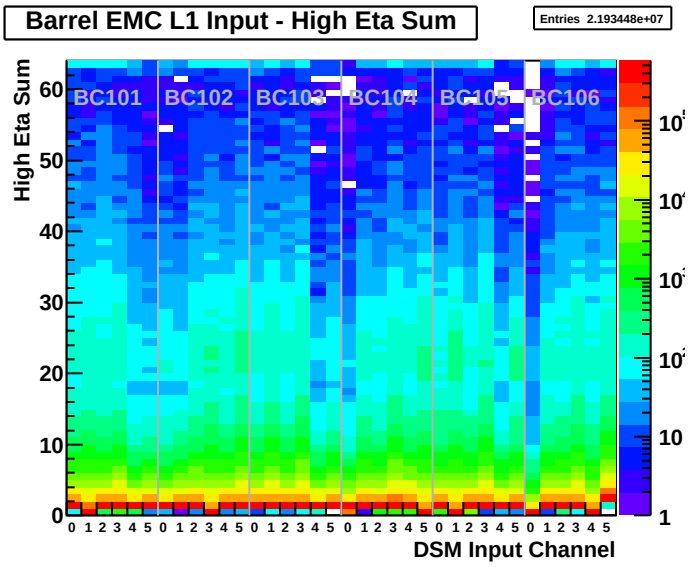
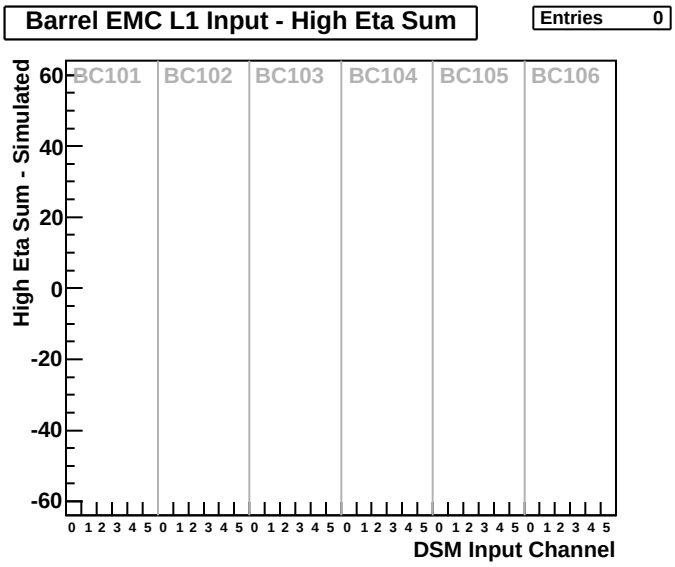
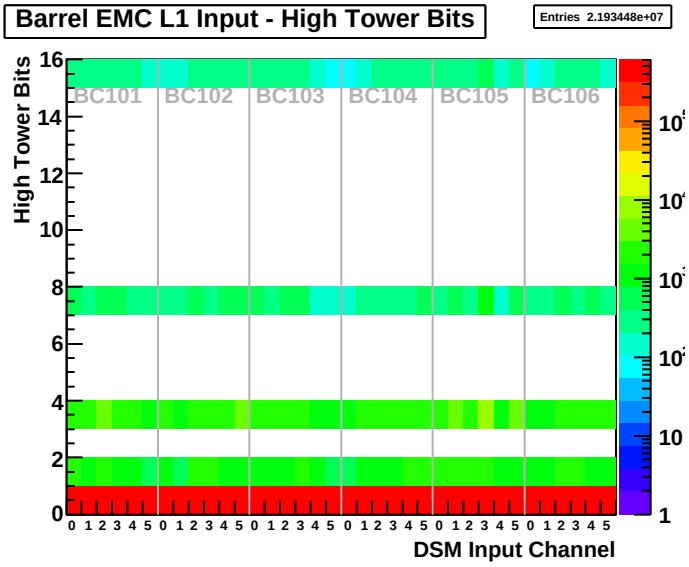
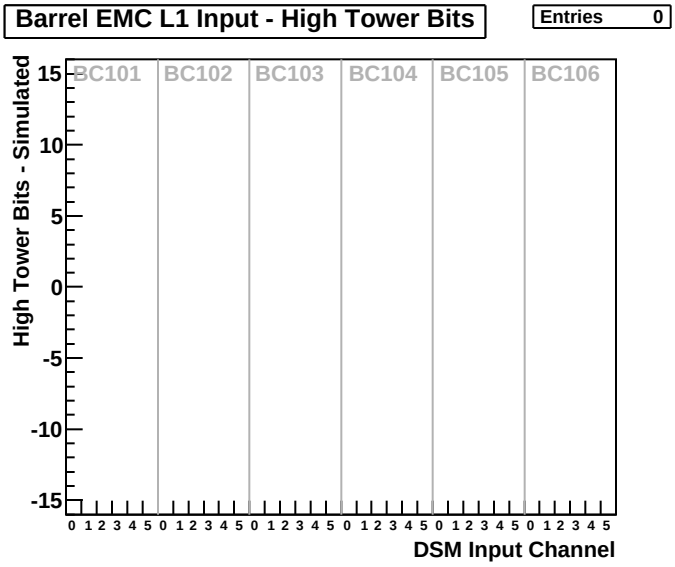

Barrel EMC L1 Input - Low Eta Sum
Entries 2.193448e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

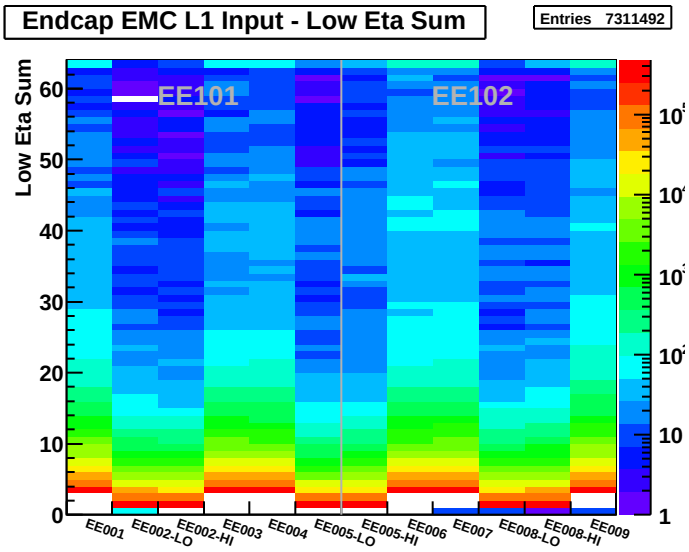

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

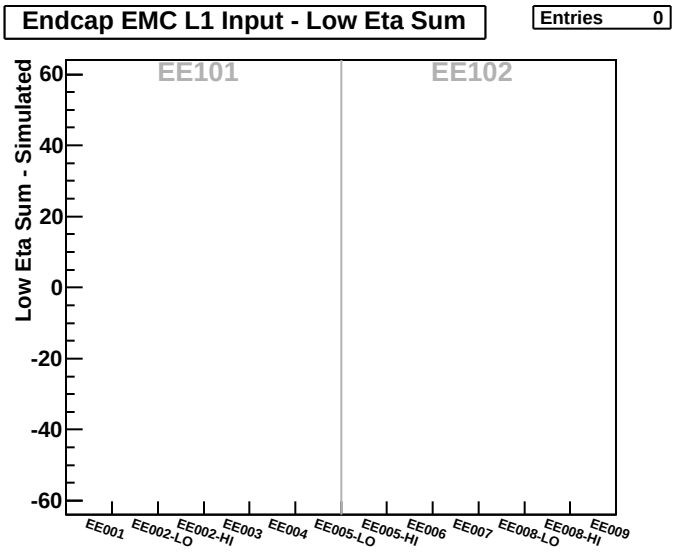

Barrel EMC L1 Input - High Eta Sum
Entries 2.193448e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

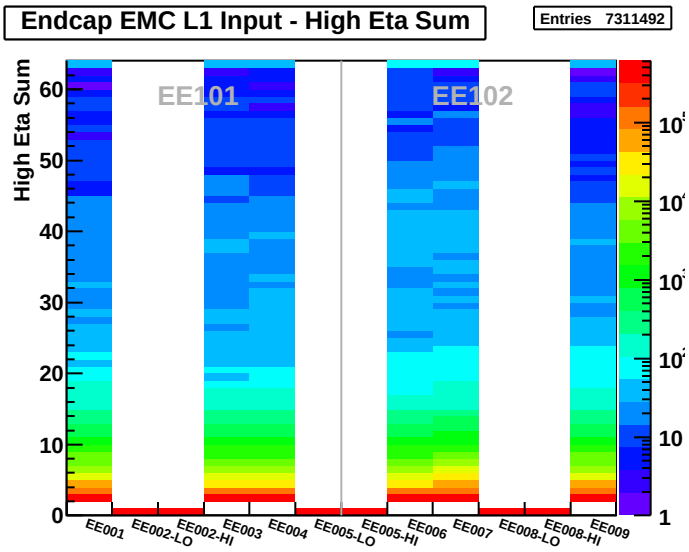

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

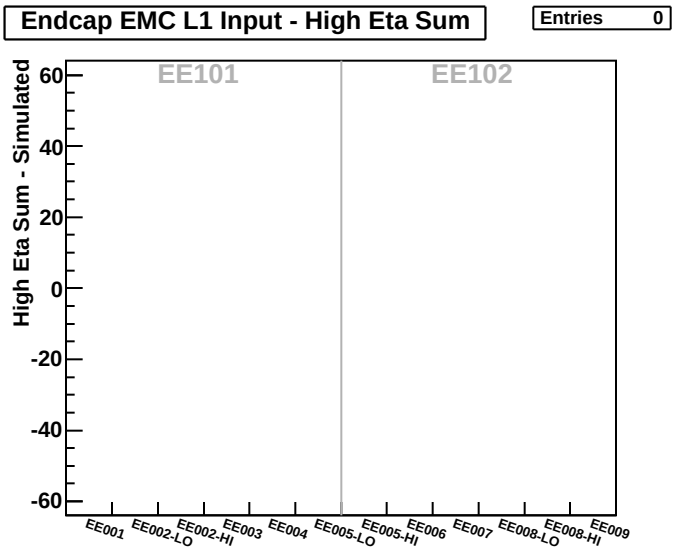

Barrel EMC L1 Input - High Tower Bits
Entries 2.193448e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

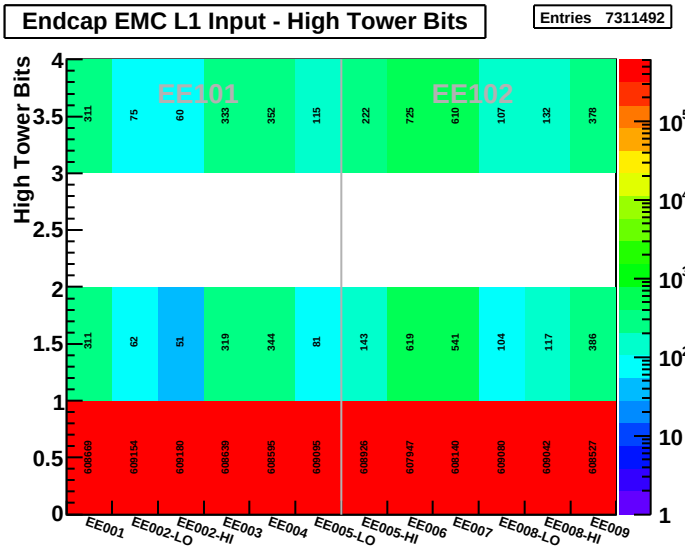

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

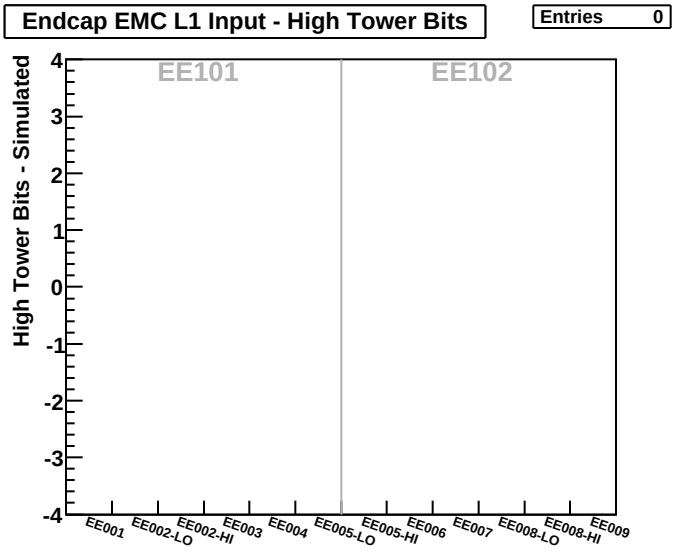

Endcap EMC L1 Input - Low Eta Sum
Entries 7311492
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

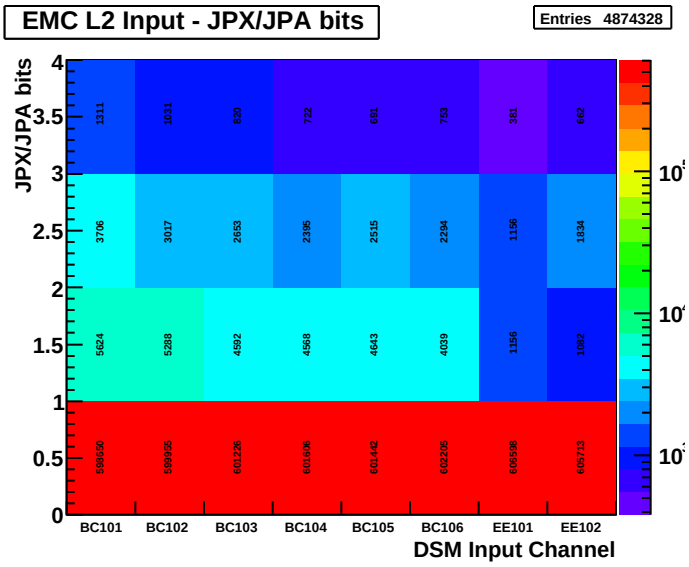

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

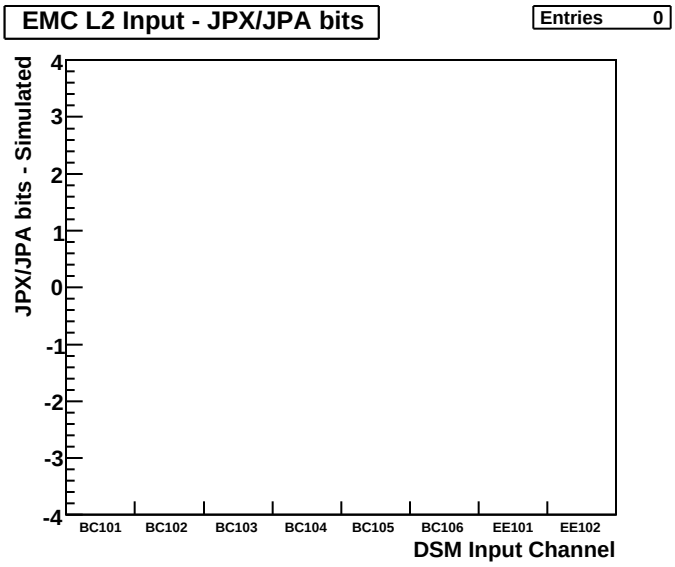

Endcap EMC L1 Input - High Eta Sum
Entries 7311492
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

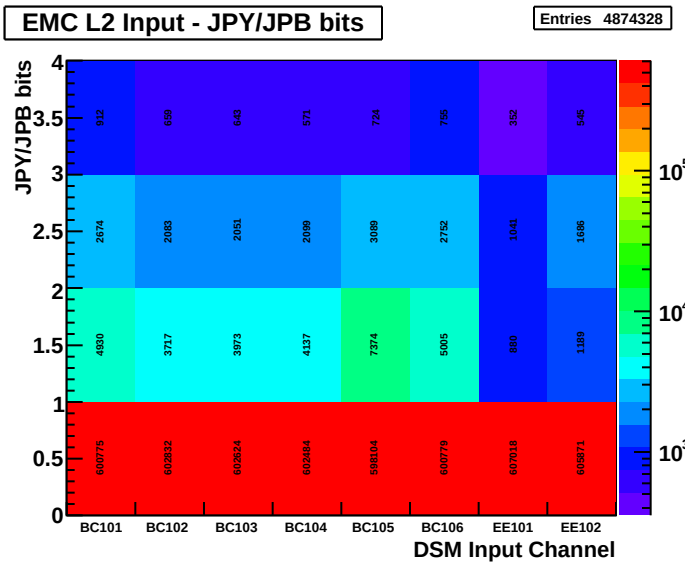

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

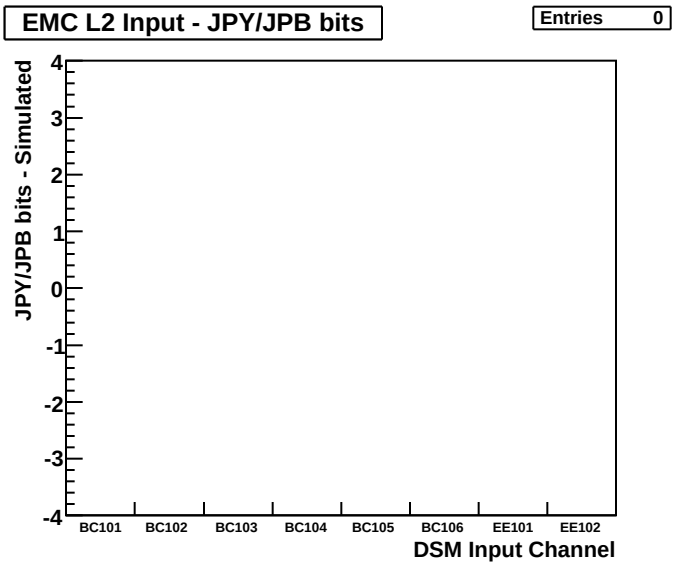

Endcap EMC L1 Input - High Tower Bits
Entries 7311492
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

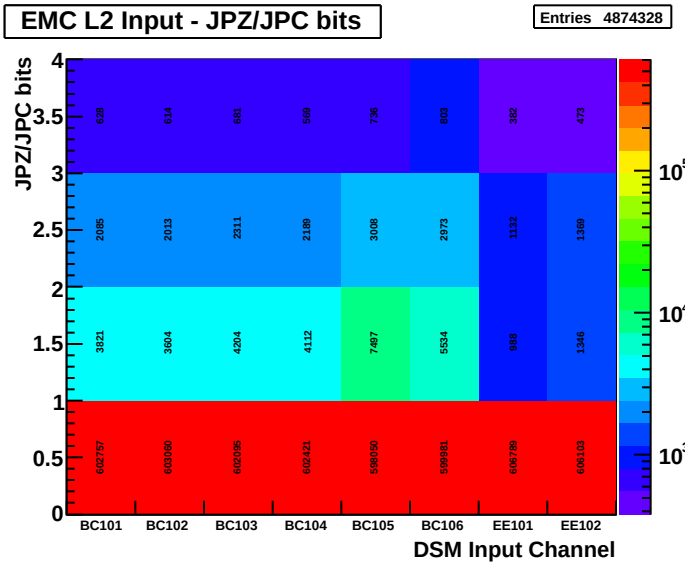

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

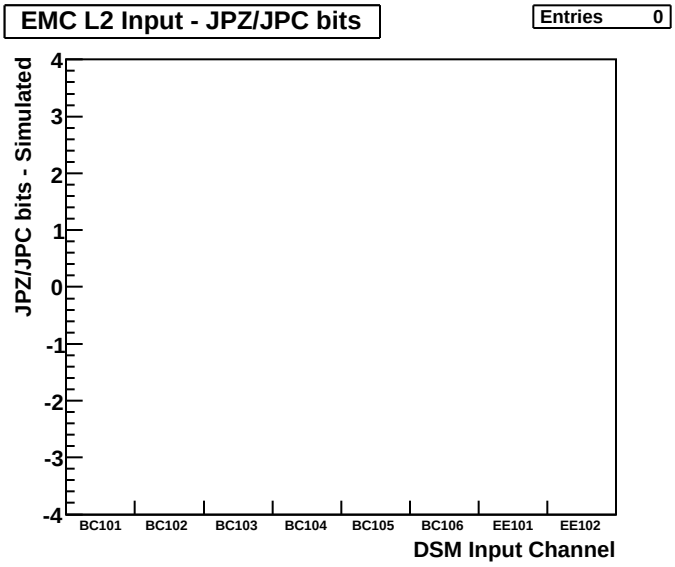

EMC L2 Input - JPX/JPA bits
Entries 4874328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1311 1031 820 722 691 753 381 662
3706 3017 2653 2395 2515 2294 1156 1834
5624 5288 4592 4568 4643 4039 1156 1082
598650 599955 601226 601606 601442 602205 605598 605713

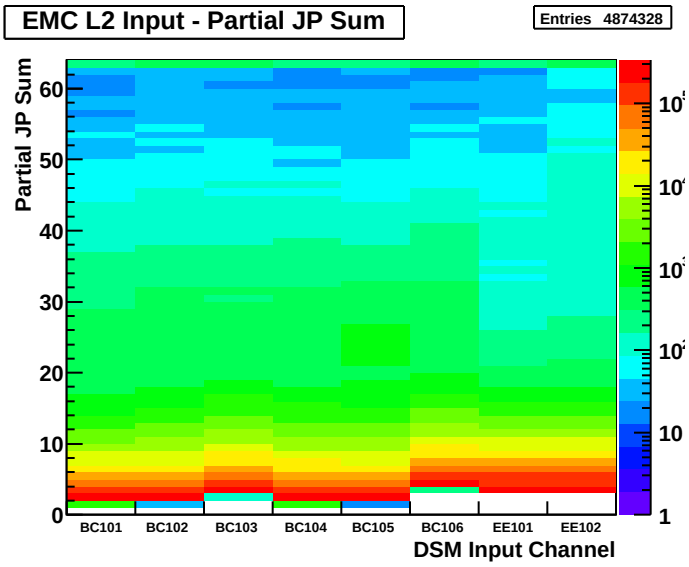

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

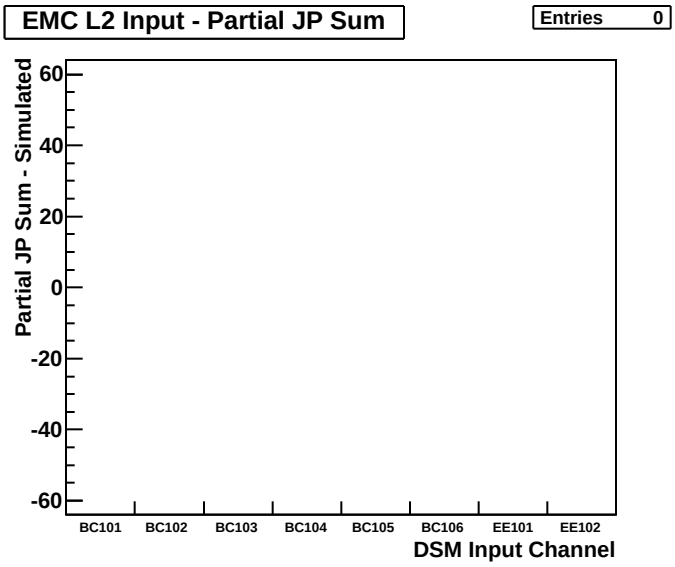

EMC L2 Input - JPY/JPB bits
Entries 4874328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
912 659 643 571 724 755 352 545
2674 2083 2051 2099 3089 2752 1041 1686
4930 3717 3973 4137 7374 5005 880 1189
600775 602332 602624 602484 598104 600779 607018 605871

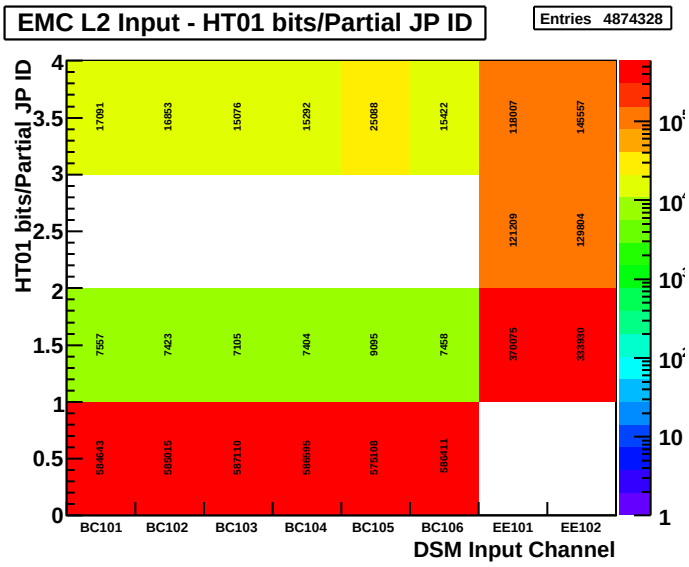

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

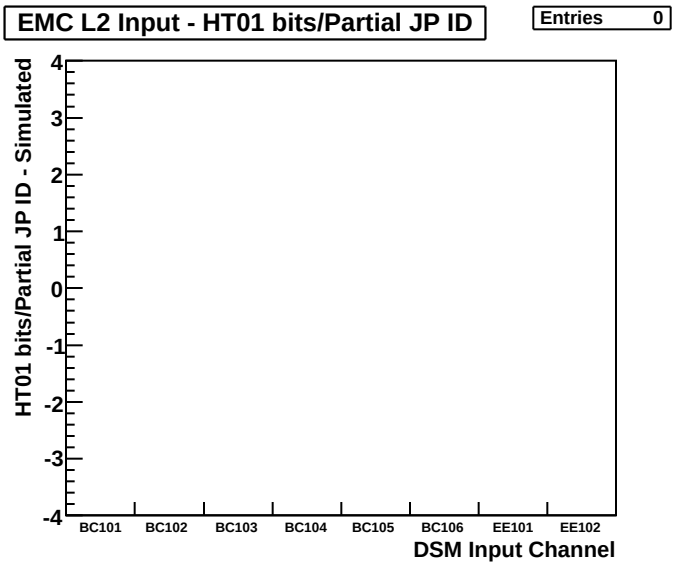

EMC L2 Input - JPZ/JPC bits
Entries 4874328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
628 614 681 569 736 803 382 473
2085 2013 2311 2189 3008 2973 1132 1369
3821 3604 4204 4112 7497 5534 988 1346
602757 603060 602095 602421 598050 599981 606789 606103

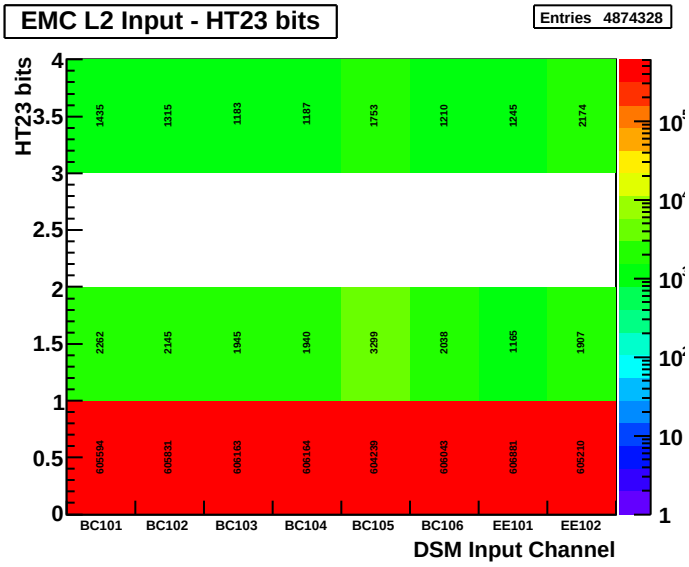

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

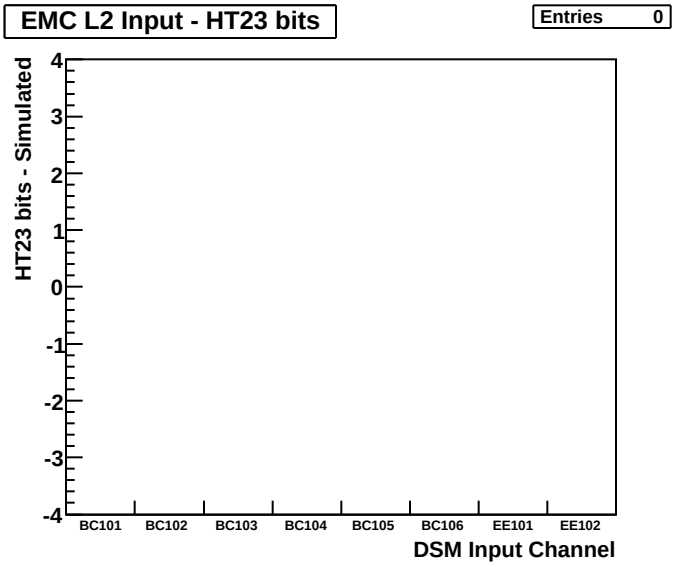

EMC L2 Input - Partial JP Sum
Entries 4874328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

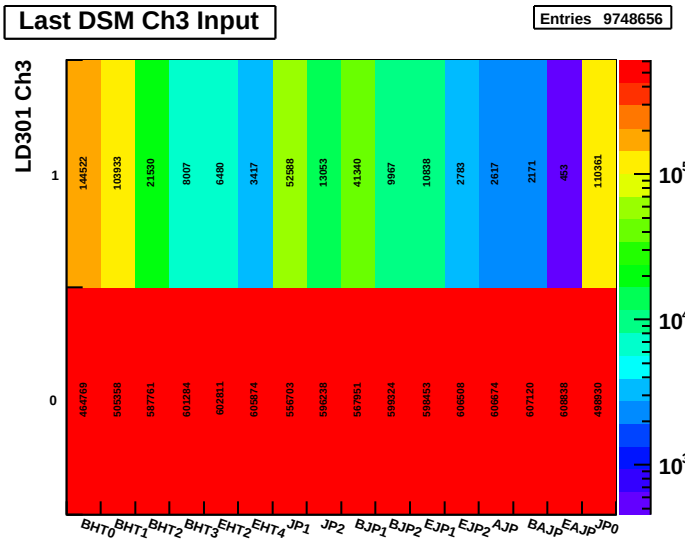

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

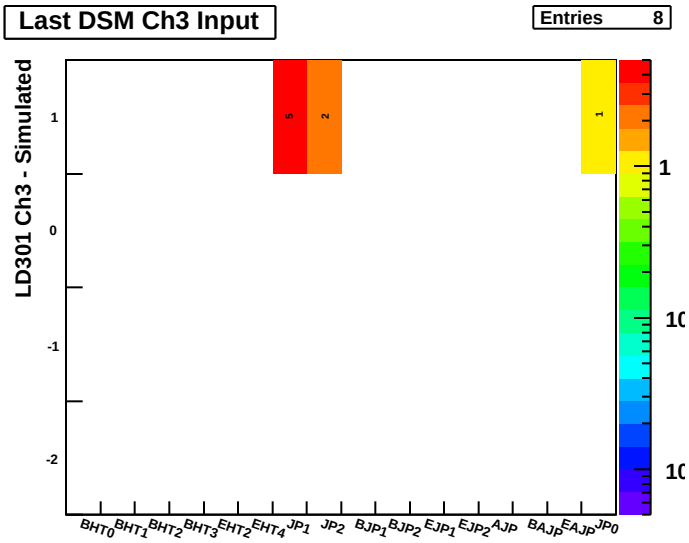

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4874328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

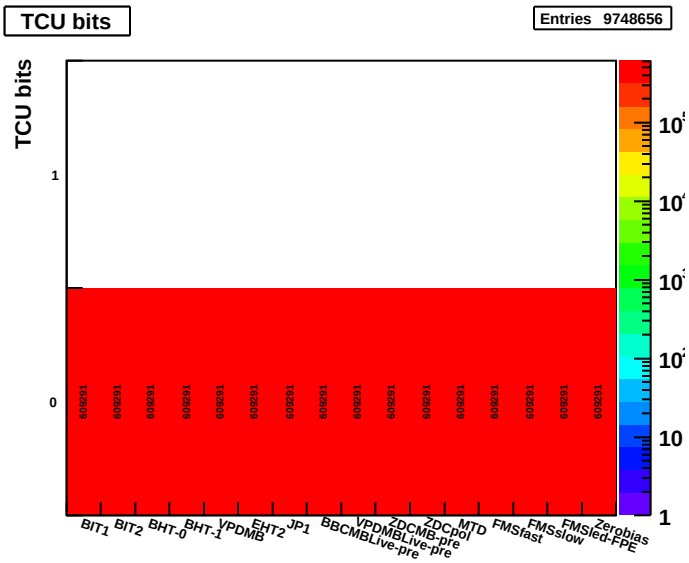

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

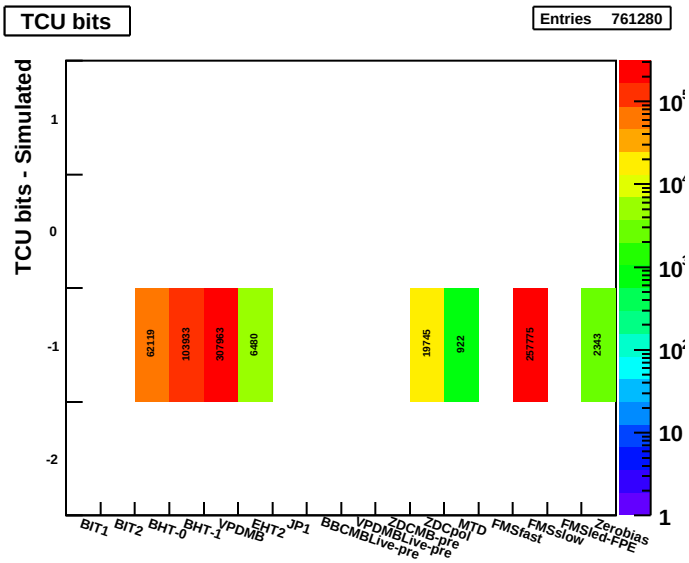

EMC L2 Input - HT23 bits
Entries 4874328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

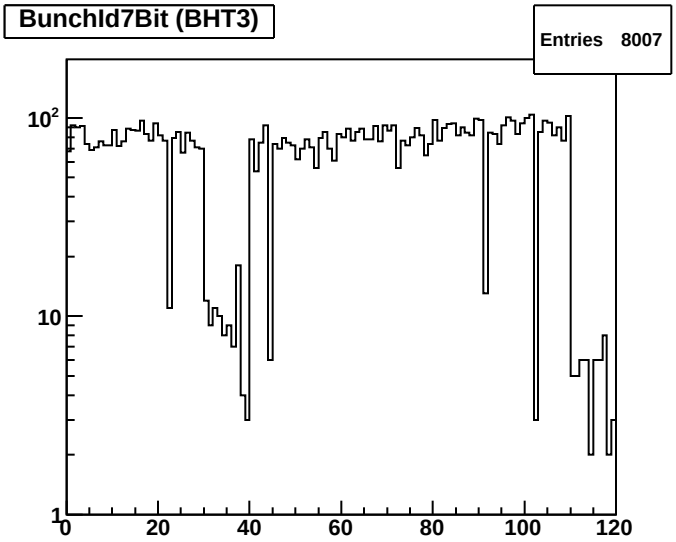

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

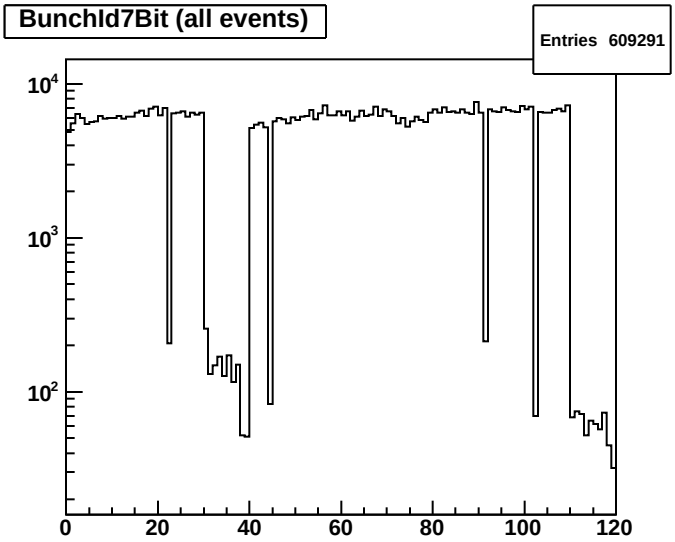

Last DSM Ch3 Input
Entries 9748656
LD301 Ch3
144522 103933 21530 8007 6480 3417 52598 13053 41340 9967 10838 2783 2817 2171 453 110361
464769 505358 587761 601284 602811 605874 556703 596238 567951 598324 598453 606508 606674 607120 608838 498930
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

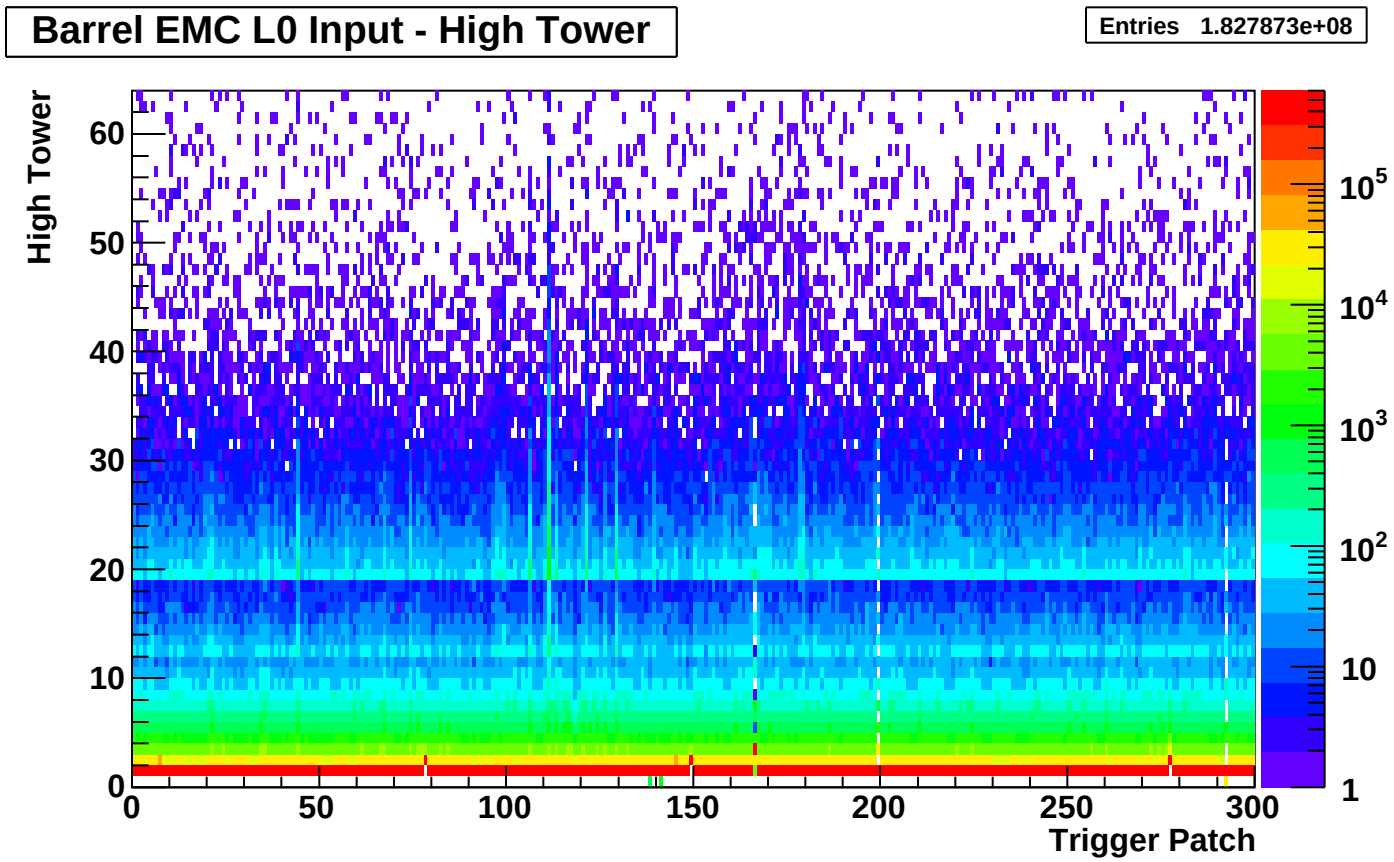

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
5 2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

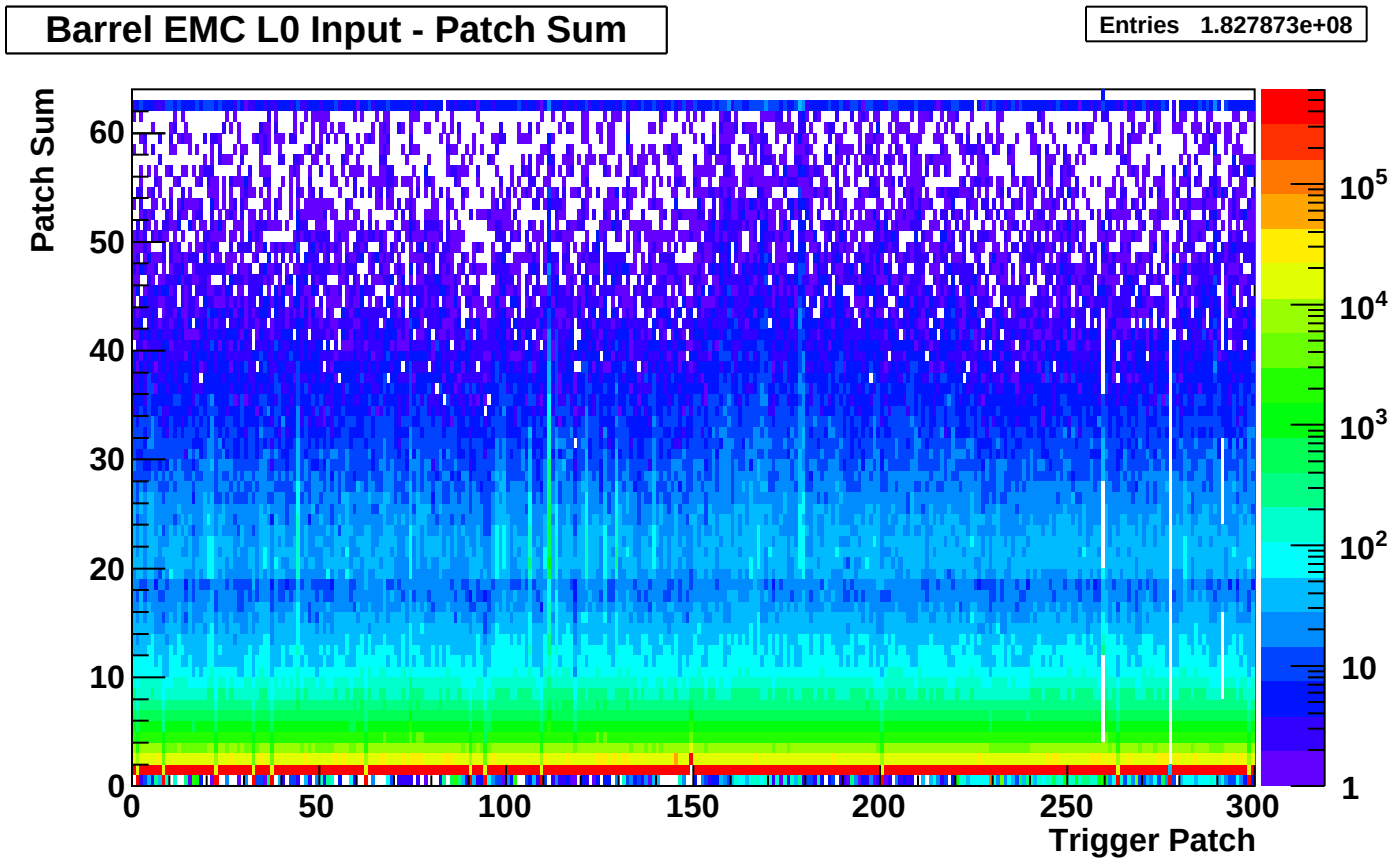

TCU bits
Entries 9748656
TCU bits
609291
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

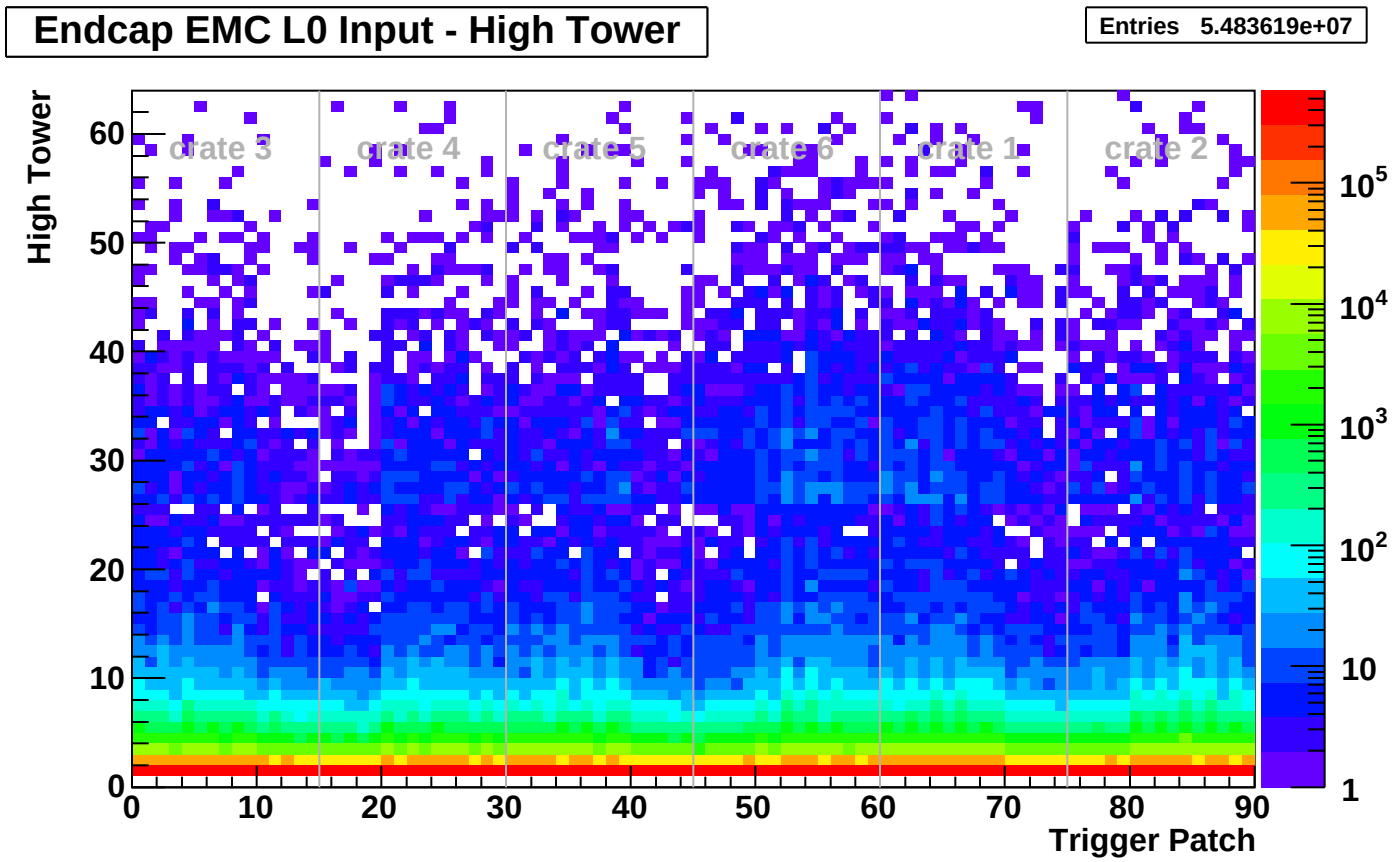

TCU bits
Entries 761280
TCU bits - Simulated
62119 103933 307963 6480 19745 922 257775 2343
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

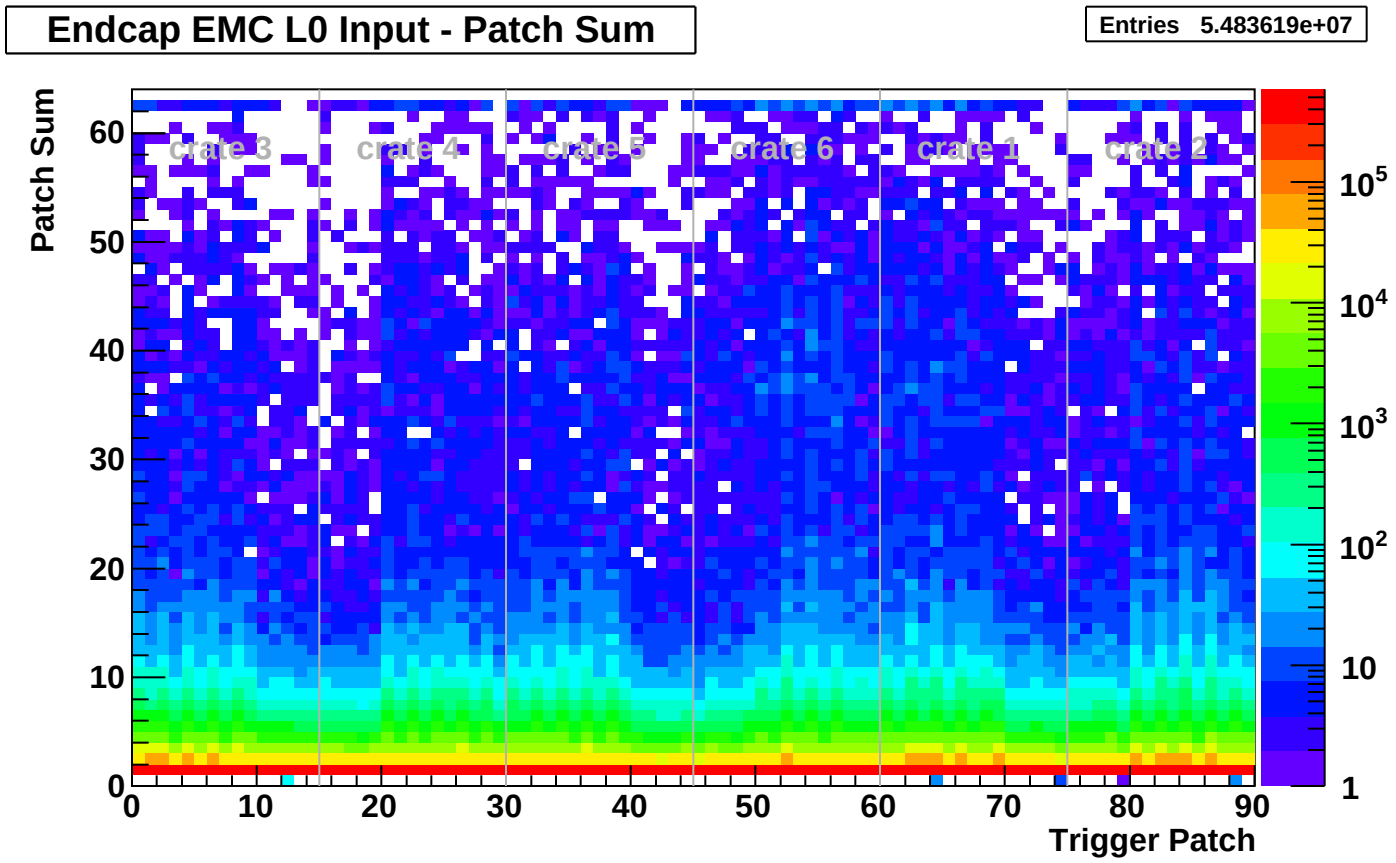

BunchId7Bit (BHT3)
Entries 8007

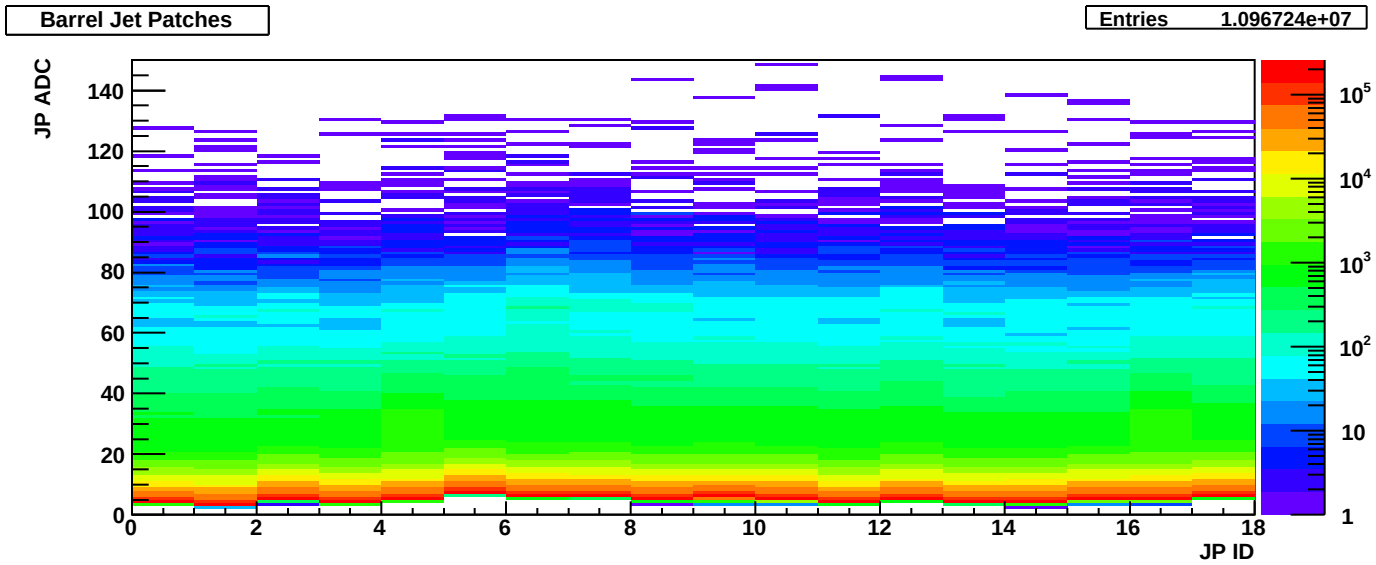

BunchId7Bit (all events)
Entries 609291

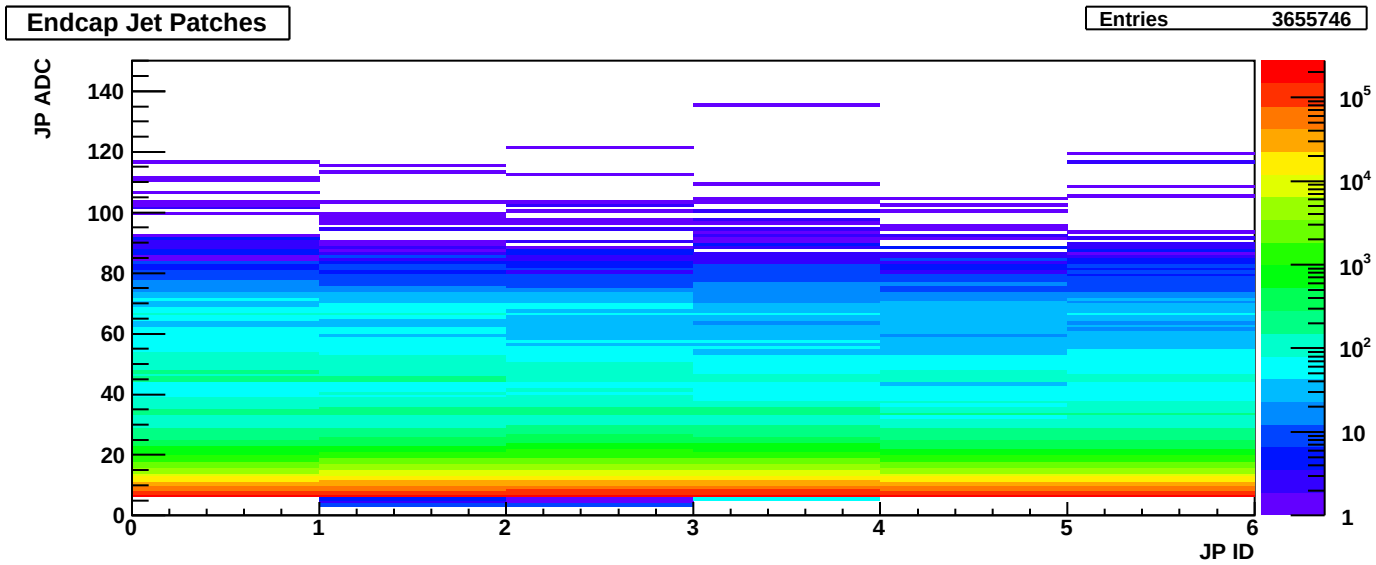

Barrel EMC L0 Input - High Tower
Entries 1.827873e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

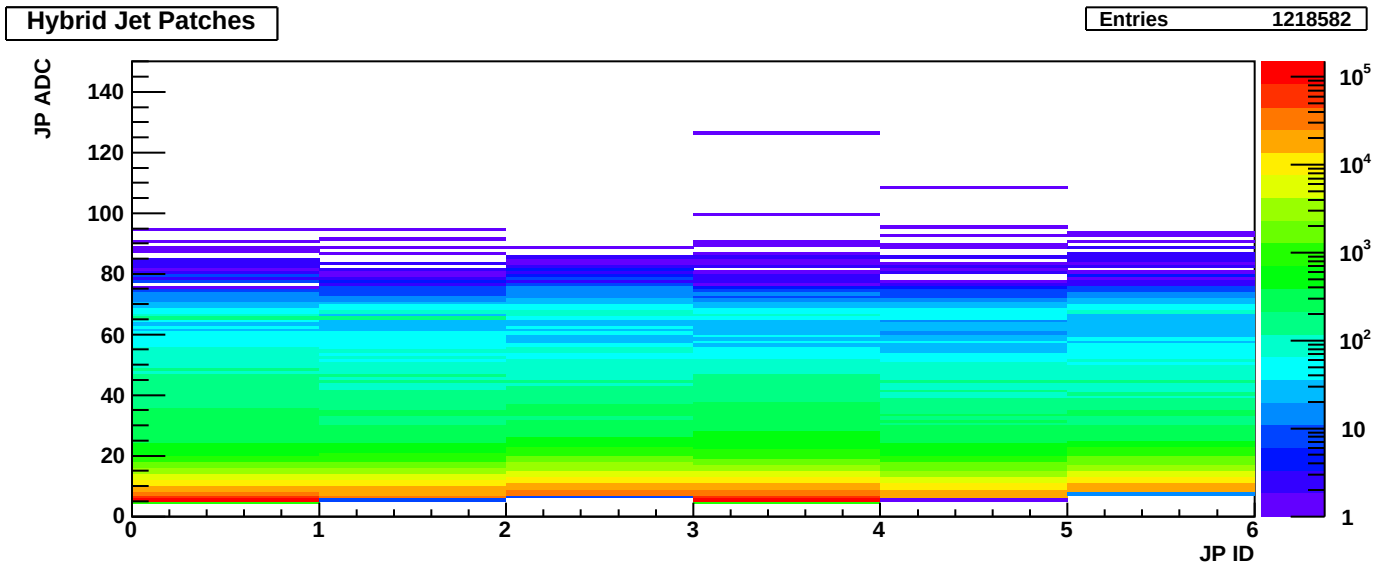

Barrel EMC L0 Input - Patch Sum
Entries 1.827873e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

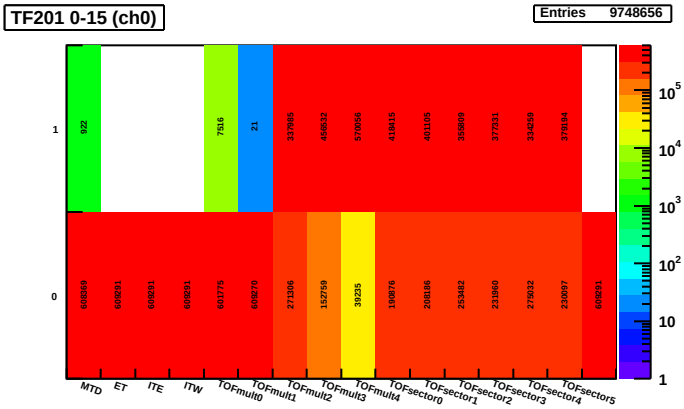

Endcap EMC L0 Input - High Tower
Entries 5.483619e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

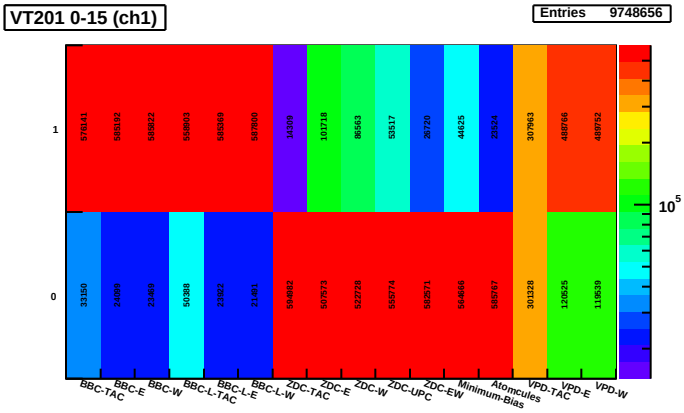

Endcap EMC L0 Input - Patch Sum
Entries 5.483619e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

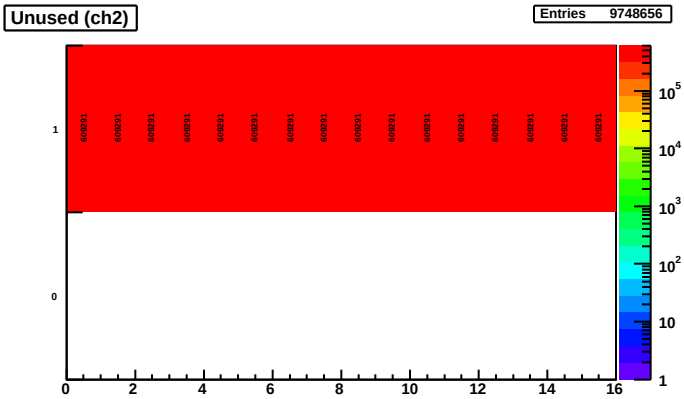

Barrel Jet Patches
Entries 1.096724e+07
JP ADC
JP ID

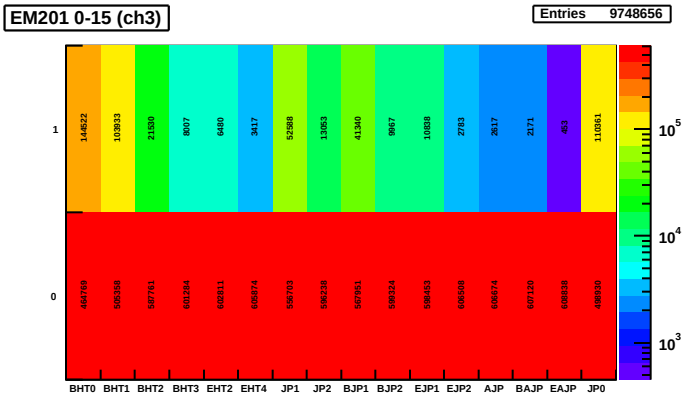

Endcap Jet Patches
Entries 3655746
JP ADC
JP ID

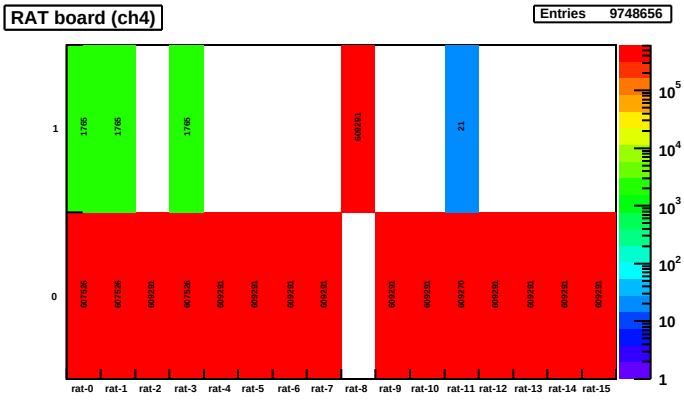

Hybrid Jet Patches
Entries 1218582
JP ADC
JP ID

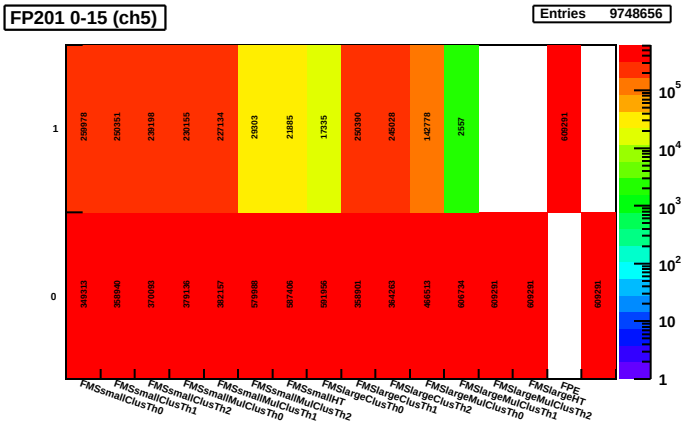

TF201 0-15 (ch0)
Entries 9748656
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

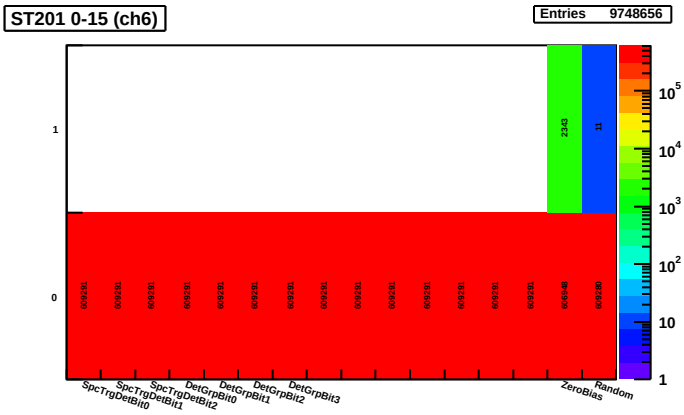

VT201 0-15 (ch1)
Entries 9748656
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

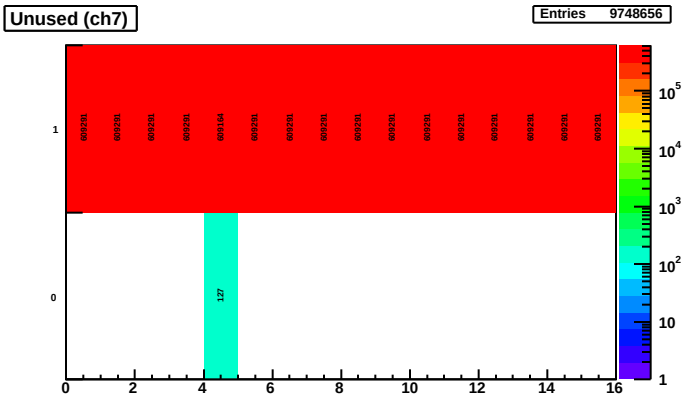

Unused (ch2)
Entries 9748656

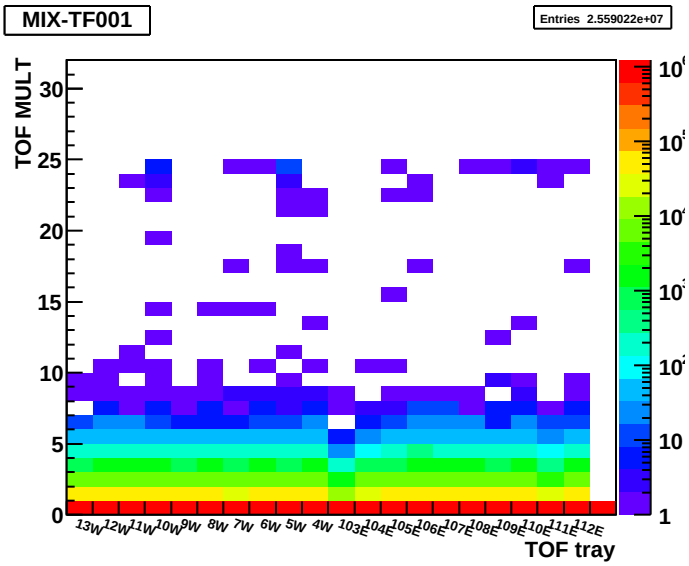

EM201 0-15 (ch3)
Entries 9748656
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

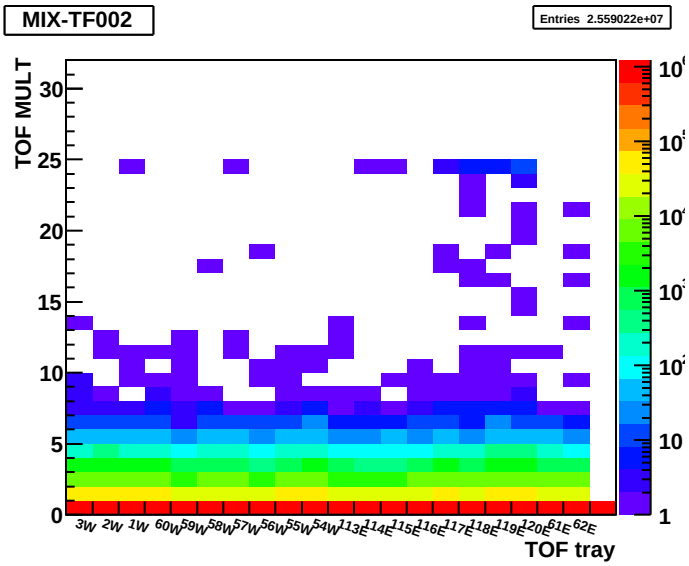

RAT board (ch4)
Entries 9748656
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

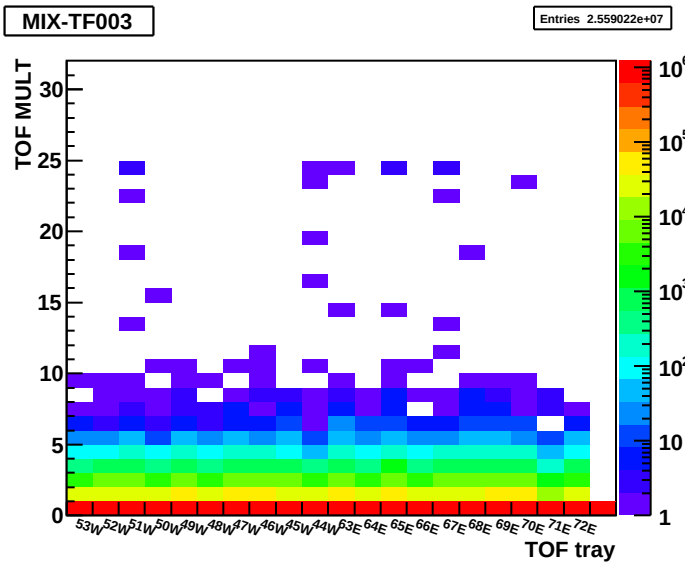

FP201 0-15 (ch5)
Entries 9748656
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

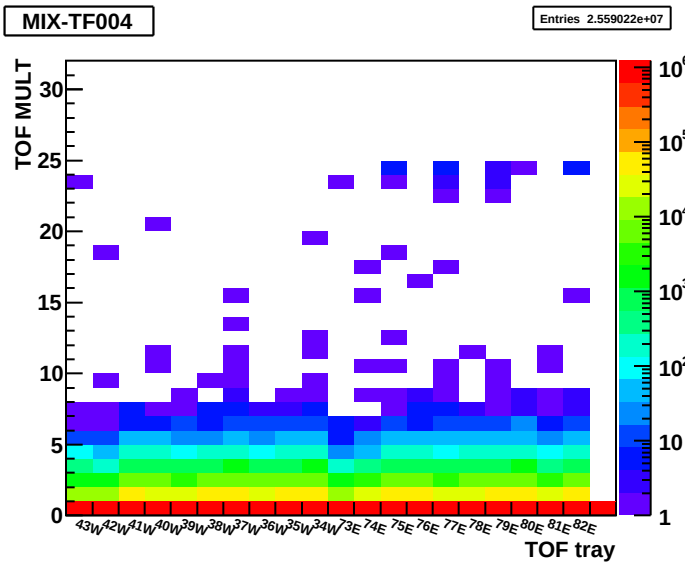

ST201 0-15 (ch6)
Entries 9748656
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

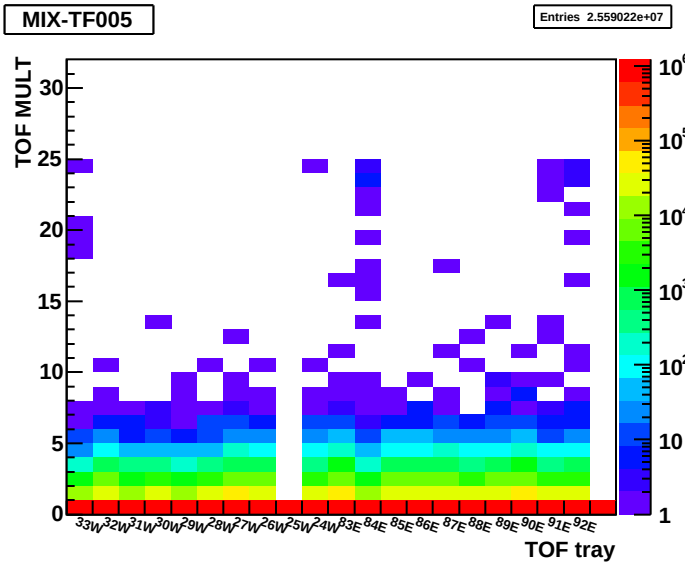

Unused (ch7)
Entries 9748656

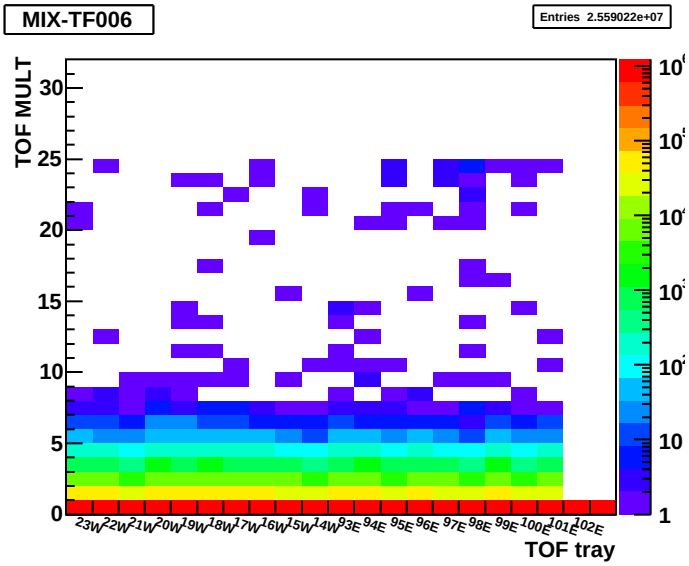

MIX-TF001
Entries 2.559022e+07
TOF MULT
TOF tray

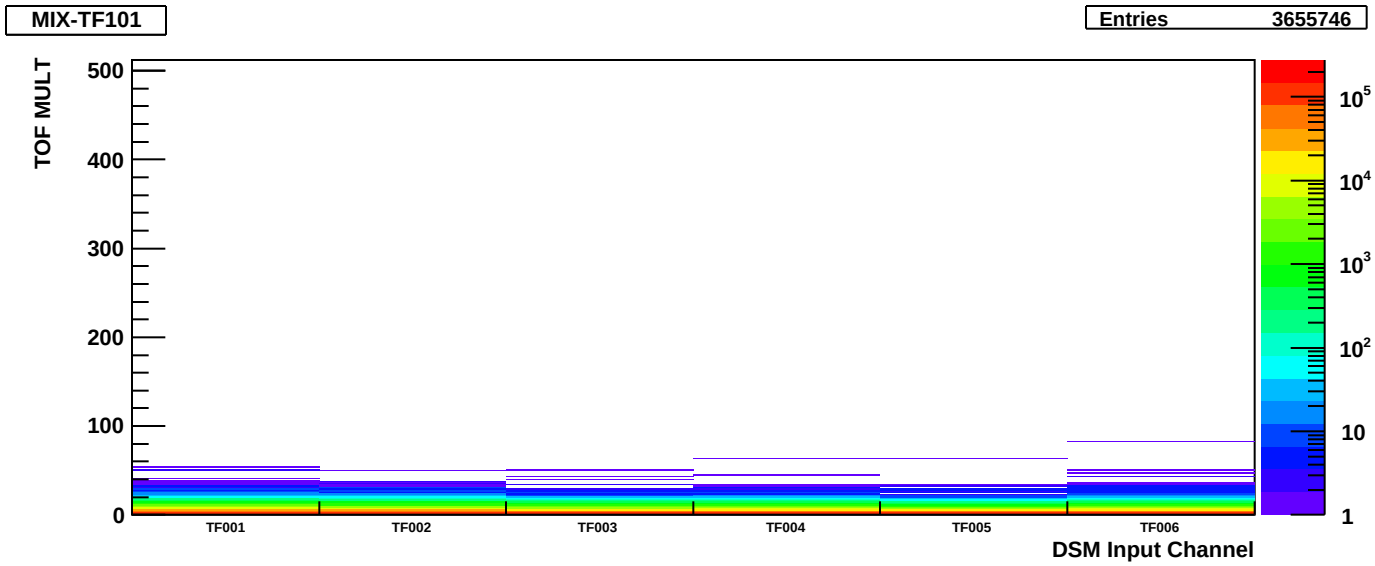

MIX-TF002
Entries 2.559022e+07
TOF MULT
TOF tray

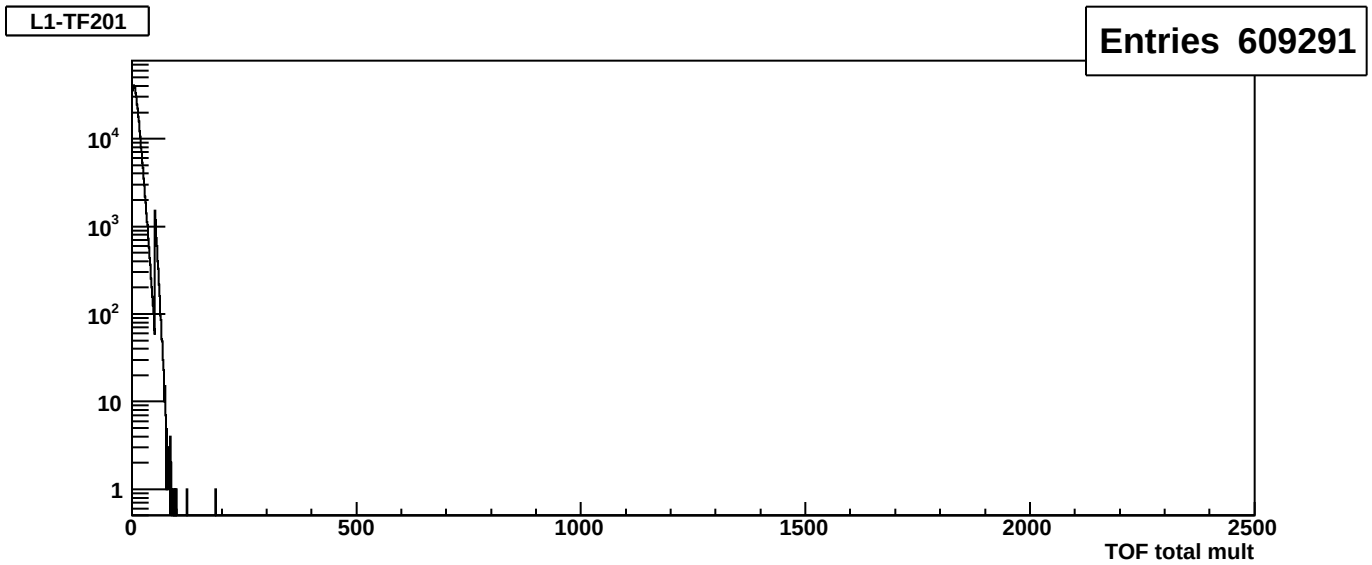

MIX-TF003
Entries 2.559022e+07
TOF MULT
TOF tray

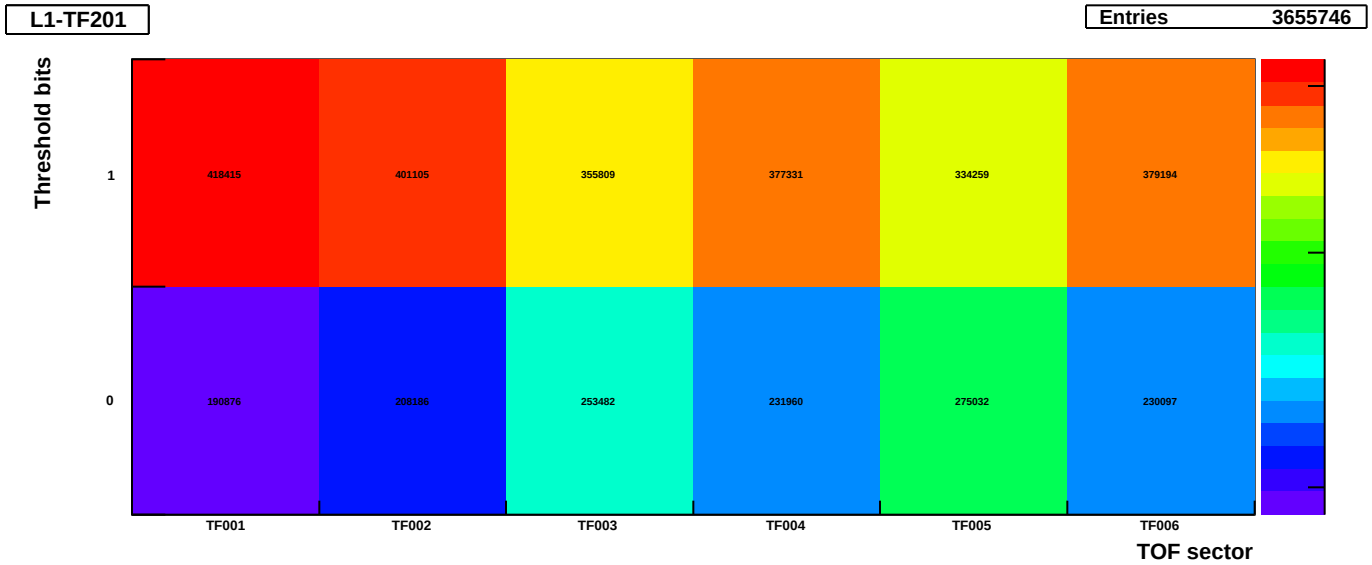

MIX-TF004
Entries 2.559022e+07
TOF MULT
TOF tray

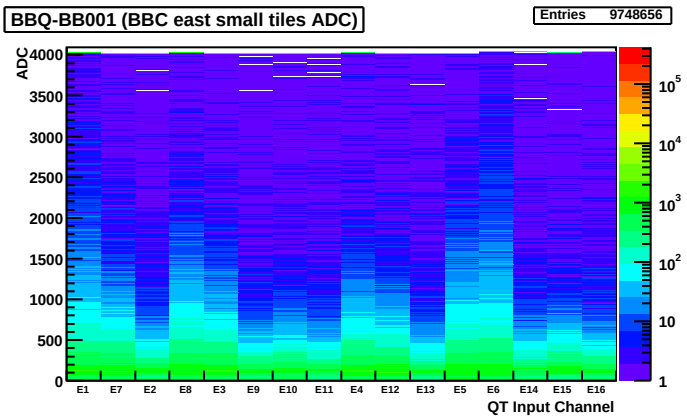

MIX-TF005
Entries 2.559022e+07
TOF MULT
TOF tray

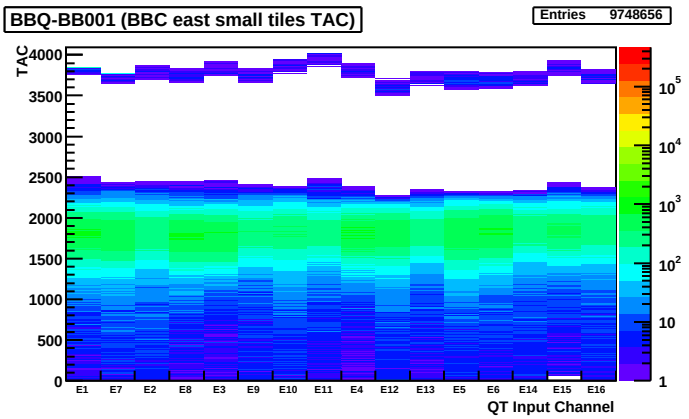

MIX-TF006
Entries 2.559022e+07
TOF MULT
TOF tray

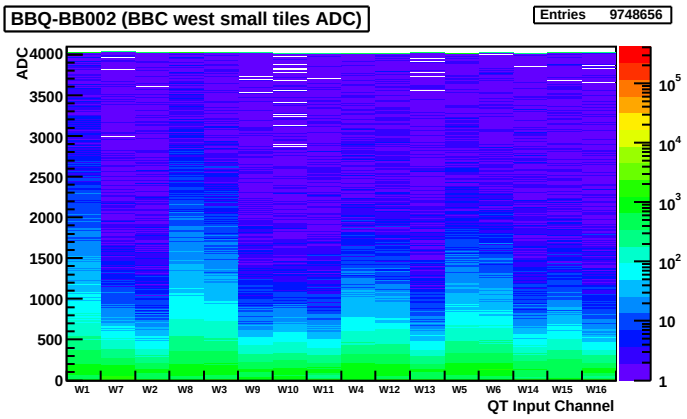

MIX-TF101
Entries 3655746
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

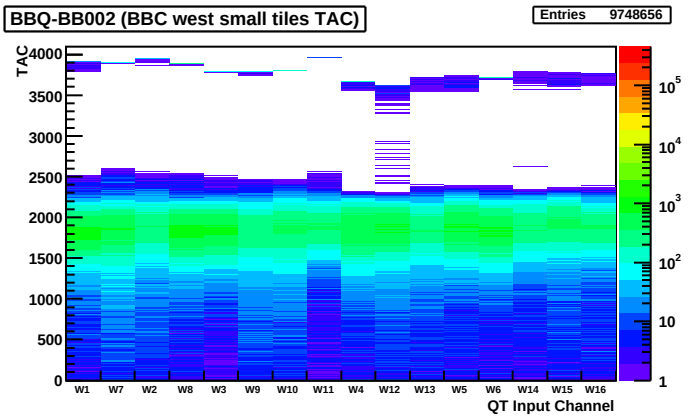

L1-TF201
Entries 609291
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

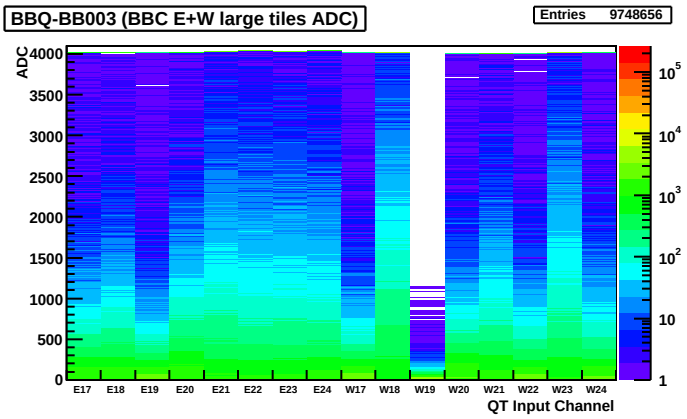

L1-TF201
Entries 3655746
Threshold bits
1
0
418415
401105
355809
377331
334259
379194
190876
208186
253482
231960
275032
230097
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

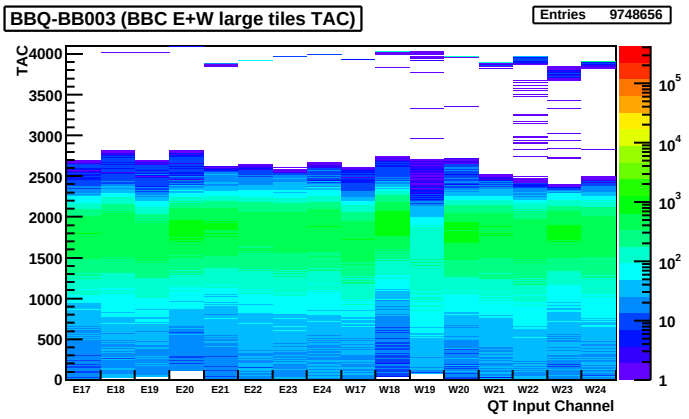

BBQ-BB001 (BBC east small tiles ADC)
Entries 9748656
ADC
QT Input Channel

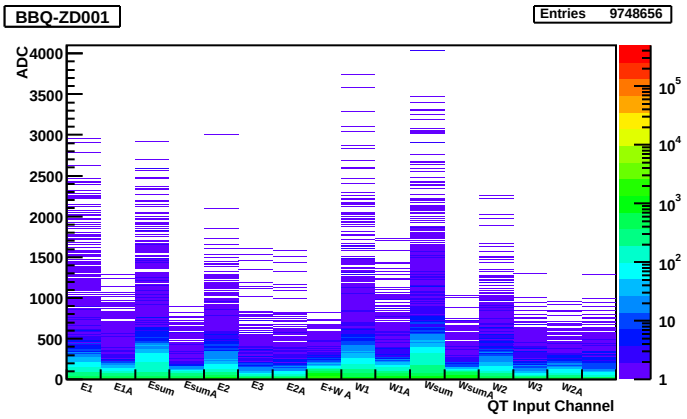

BBQ-BB001 (BBC east small tiles TAC)
Entries 9748656
TAC
QT Input Channel

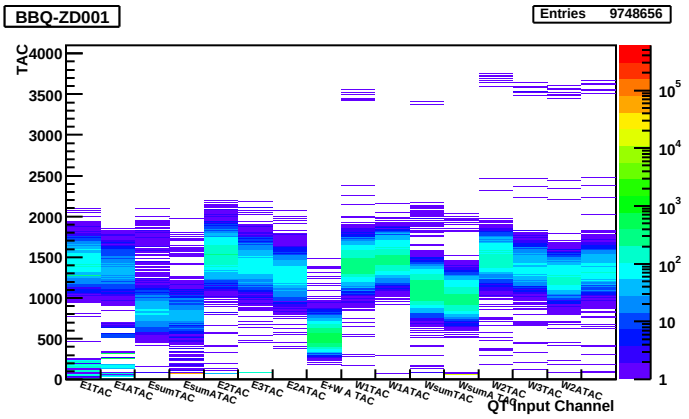

BBQ-BB002 (BBC west small tiles ADC)
Entries 9748656
ADC
QT Input Channel

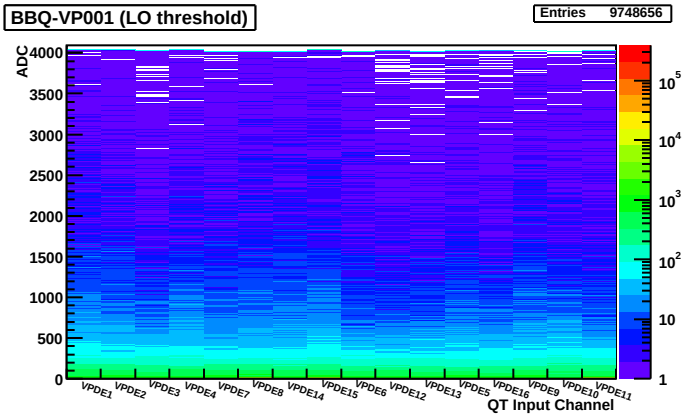

BBQ-BB002 (BBC west small tiles TAC)
Entries 9748656
TAC
QT Input Channel

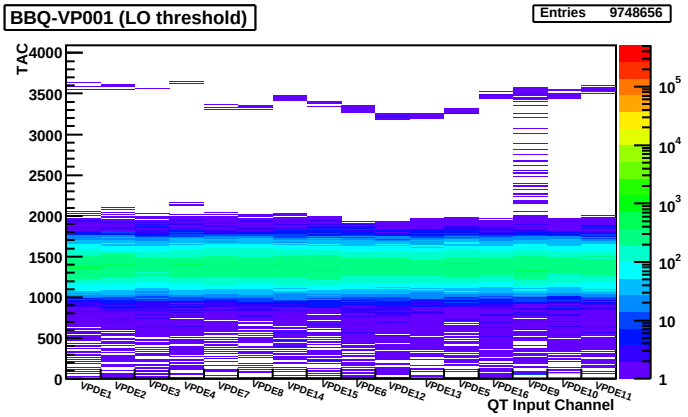

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9748656
ADC
QT Input Channel

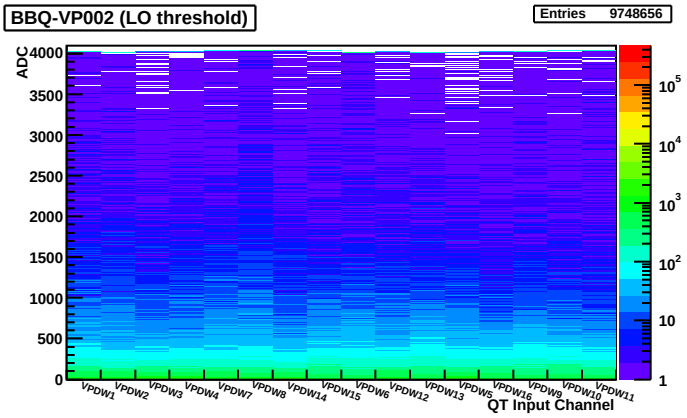

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9748656
TAC
QT Input Channel

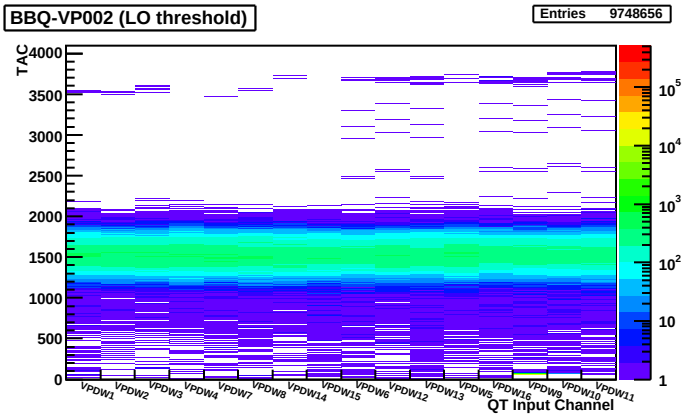

BBQ-ZD001
Entries 9748656
ADC
QT Input Channel

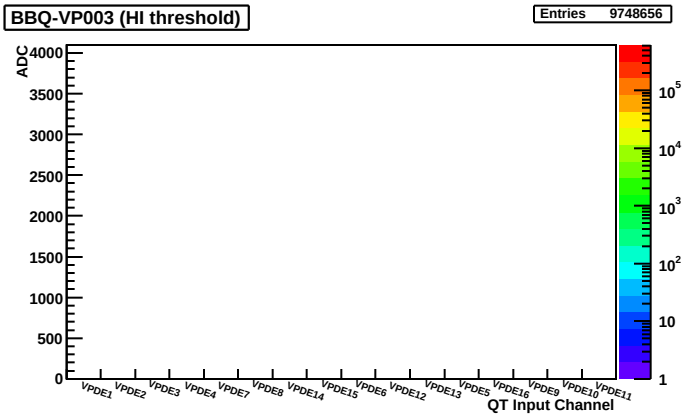

BBQ-ZD001
Entries 9748656
TAC
QT Input Channel

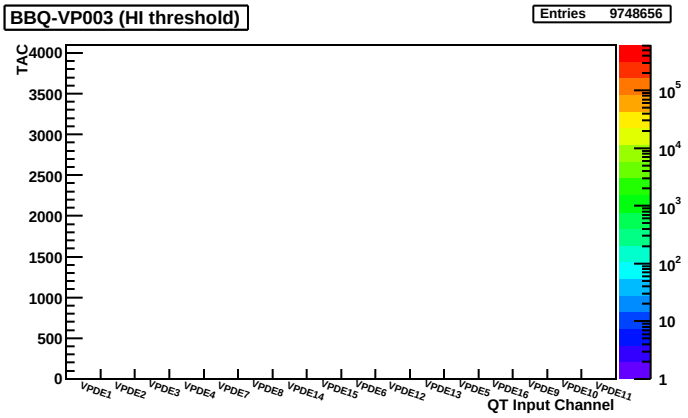

BBQ-VP001 (LO threshold)
Entries 9748656
ADC
QT Input Channel

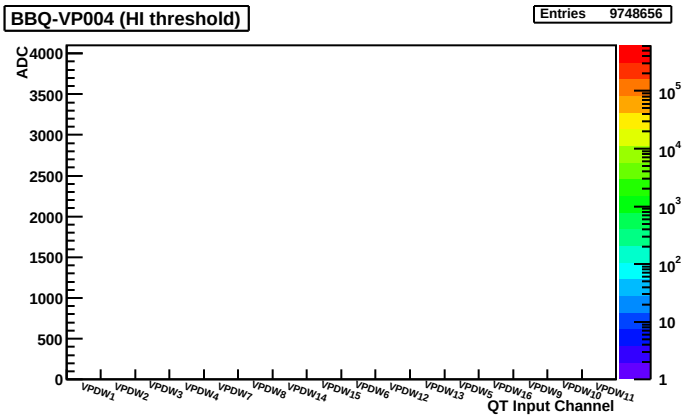

BBQ-VP001 (LO threshold)
Entries 9748656
TAC
QT Input Channel

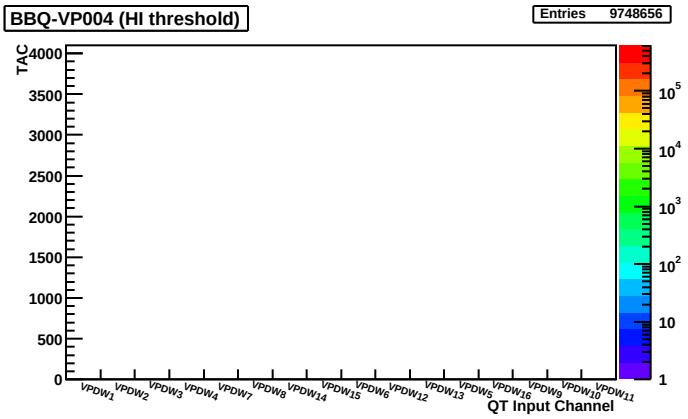

BBQ-VP002 (LO threshold)
Entries 9748656
ADC
QT Input Channel

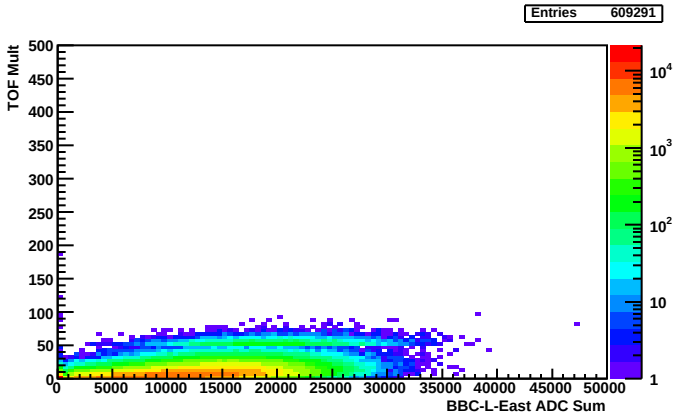

BBQ-VP002 (LO threshold)
Entries 9748656
TAC
QT Input Channel

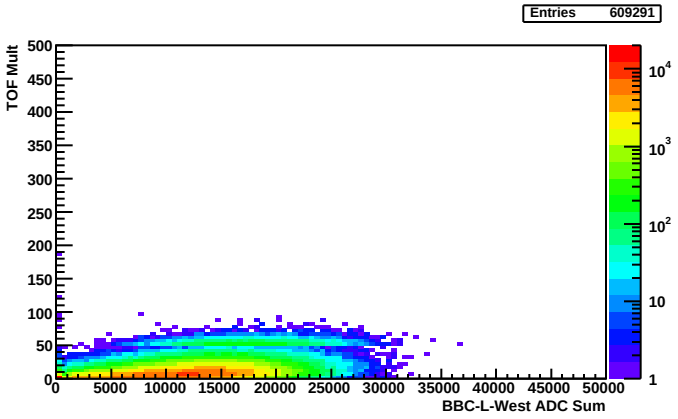

BBQ-VP003 (HI threshold)
Entries 9748656
ADC
QT Input Channel

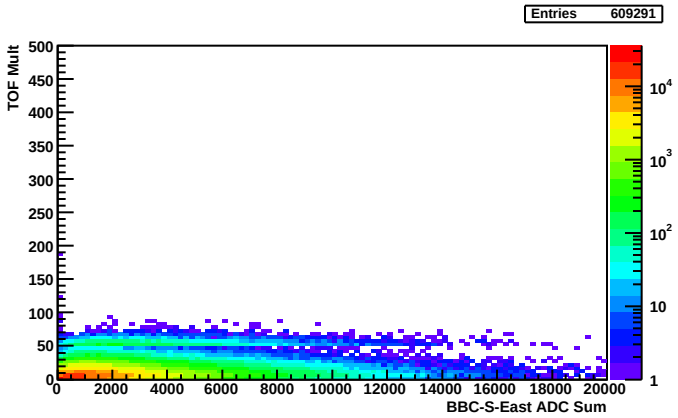

BBQ-VP003 (HI threshold)
Entries 9748656
TAC
QT Input Channel

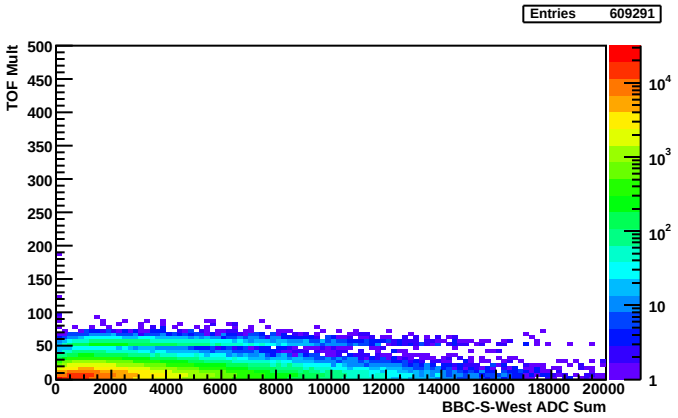

BBQ-VP004 (HI threshold)
Entries 9748656
ADC
QT Input Channel

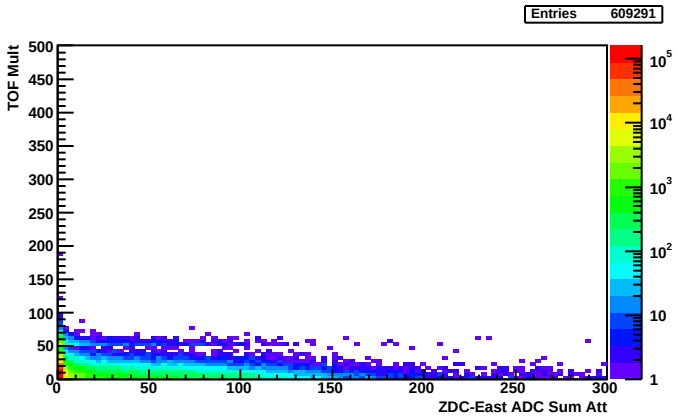

BBQ-VP004 (HI threshold)
Entries 9748656
TAC
QT Input Channel

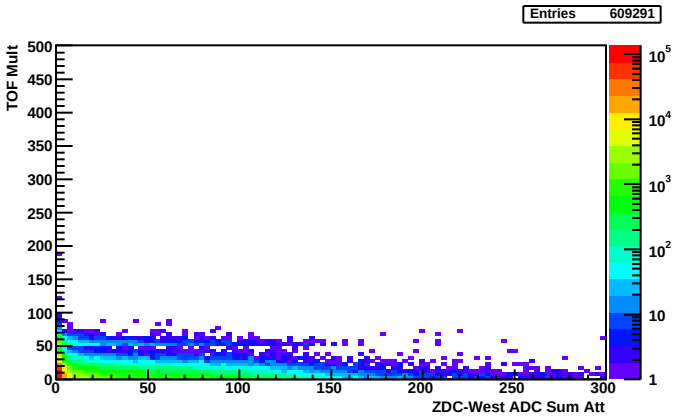

Entries 609291
TOF Mult
BBC-L-East ADC Sum

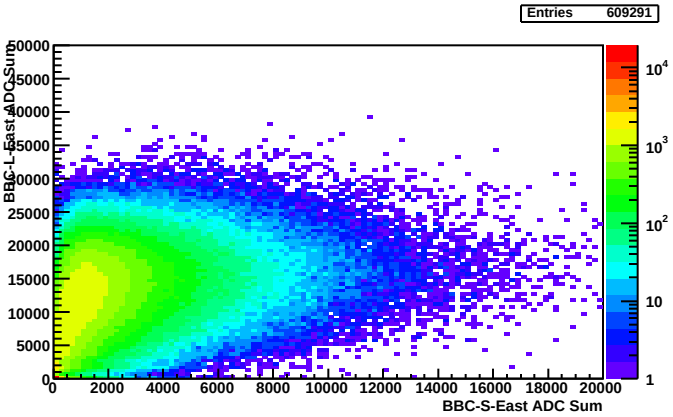

Entries 609291
TOF Mult
BBC-L-West ADC Sum

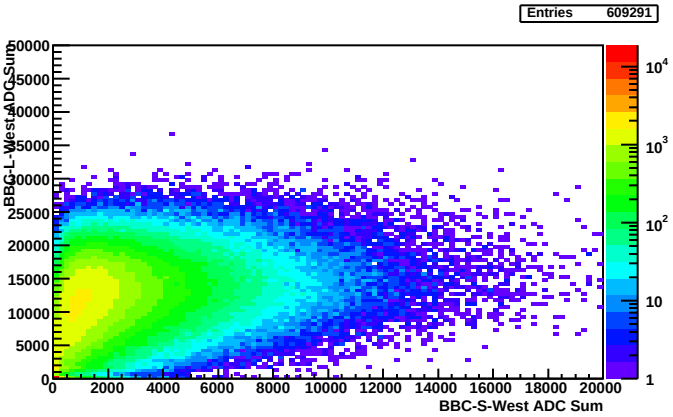

Entries 609291
TOF Mult
BBC-S-East ADC Sum

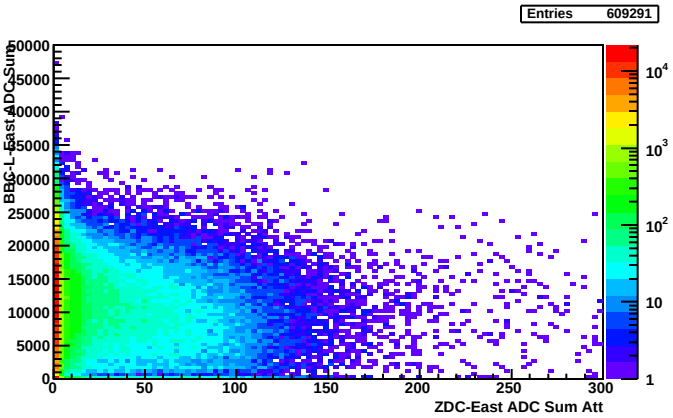

Entries 609291
TOF Mult
BBC-S-West ADC Sum

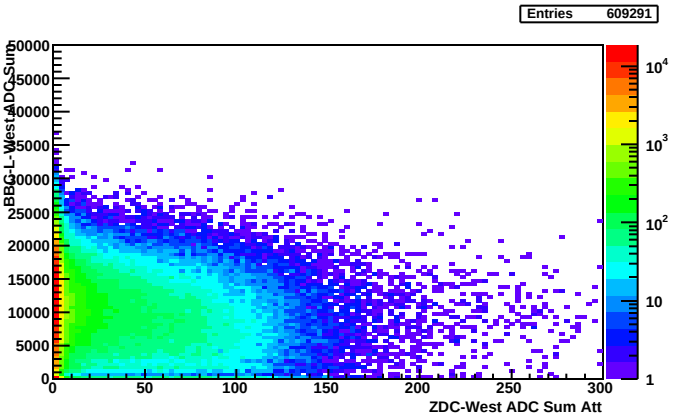

Entries 609291
TOF Mult
ZDC-East ADC Sum Att

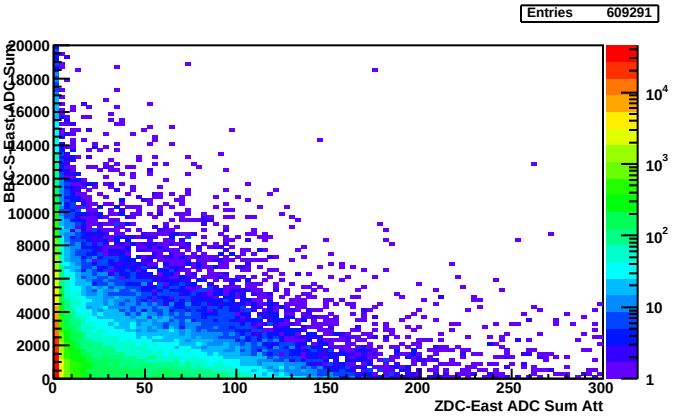

Entries 609291
TOF Mult
ZDC-West ADC Sum Att

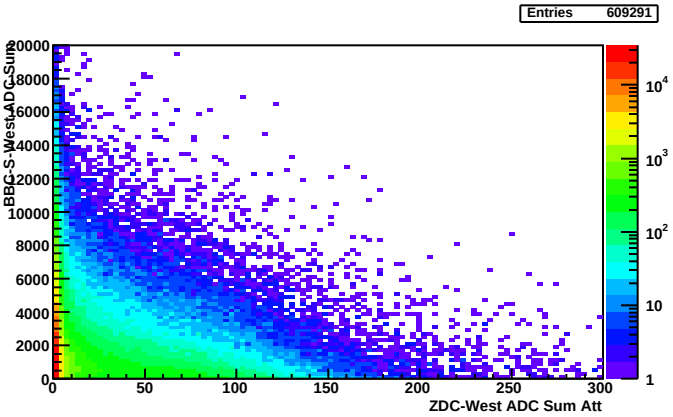

Entries 609291
BBC-L-East ADC Sum
BBC-S-East ADC Sum

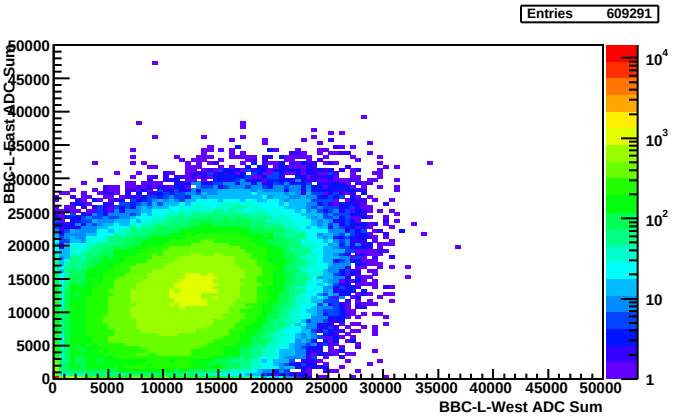

Entries 609291
BBC-L-West ADC Sum
BBC-S-West ADC Sum

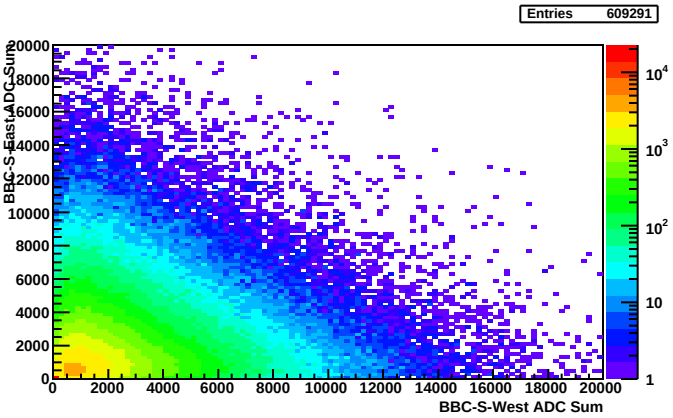

Entries 609291
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

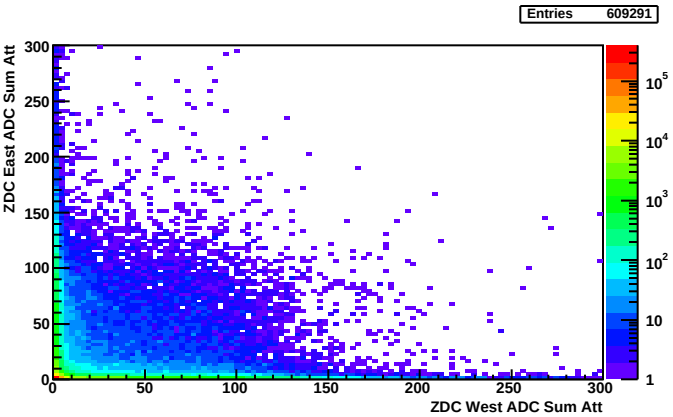

Entries 609291
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

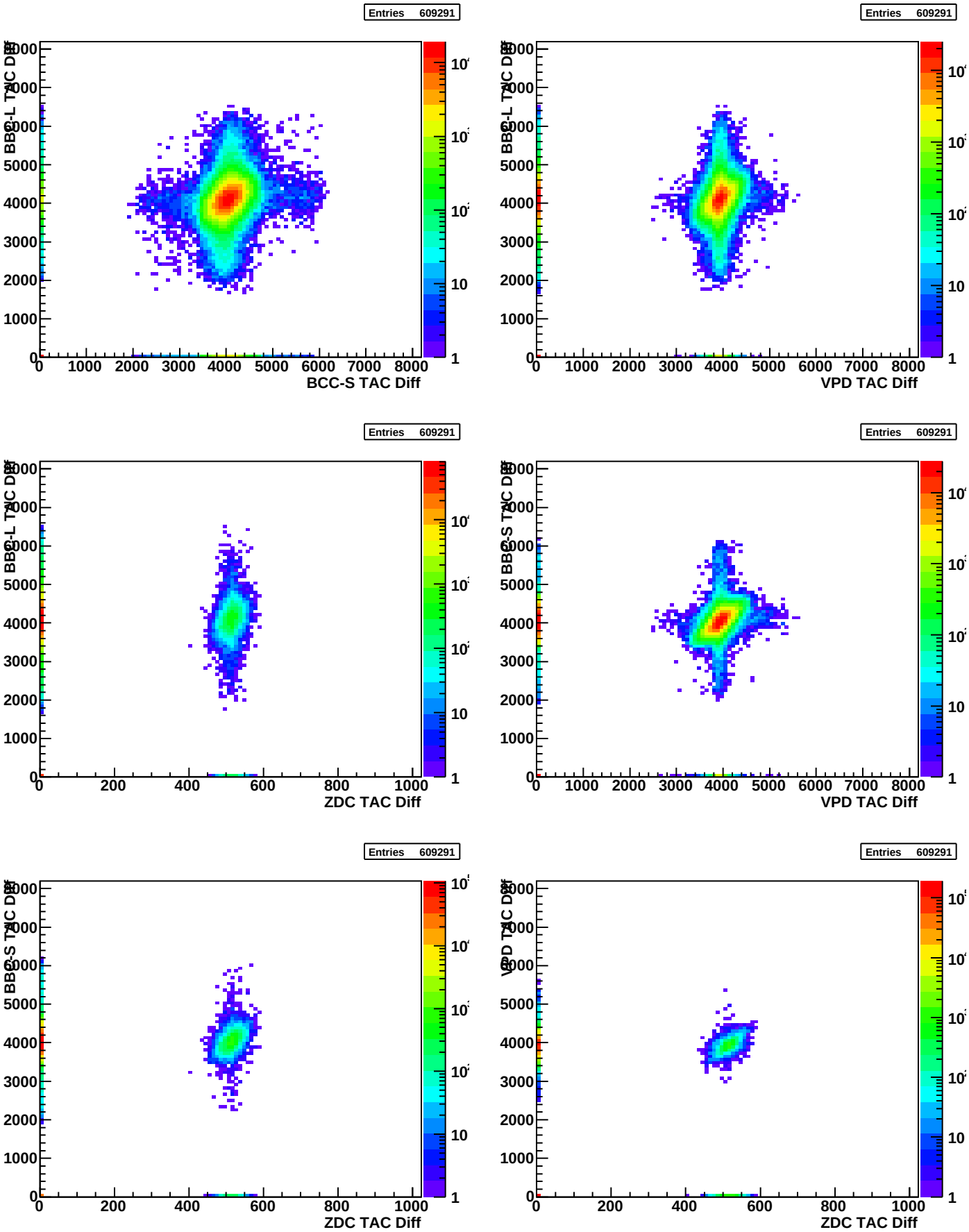

Entries 609291
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

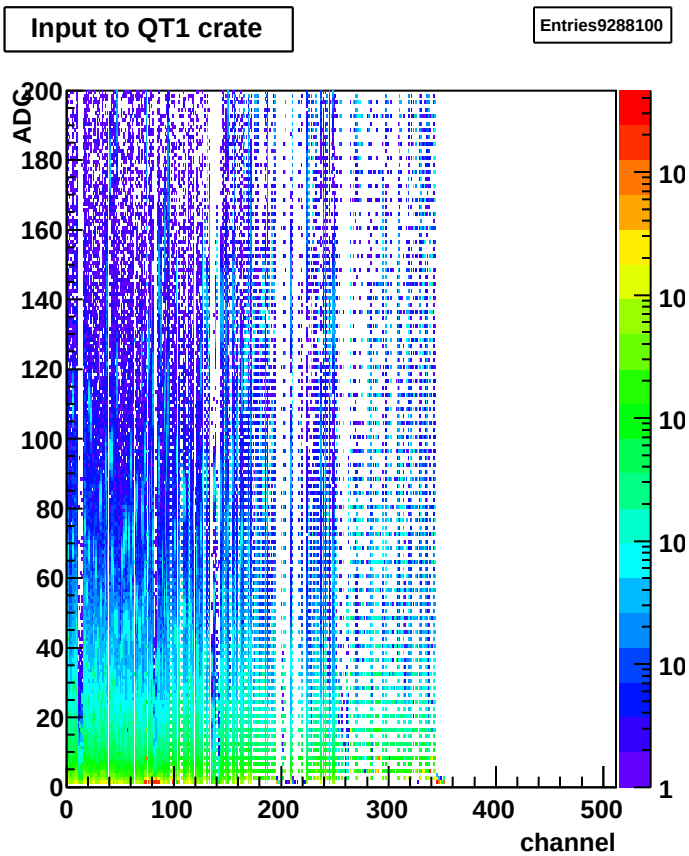

Entries 609291
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

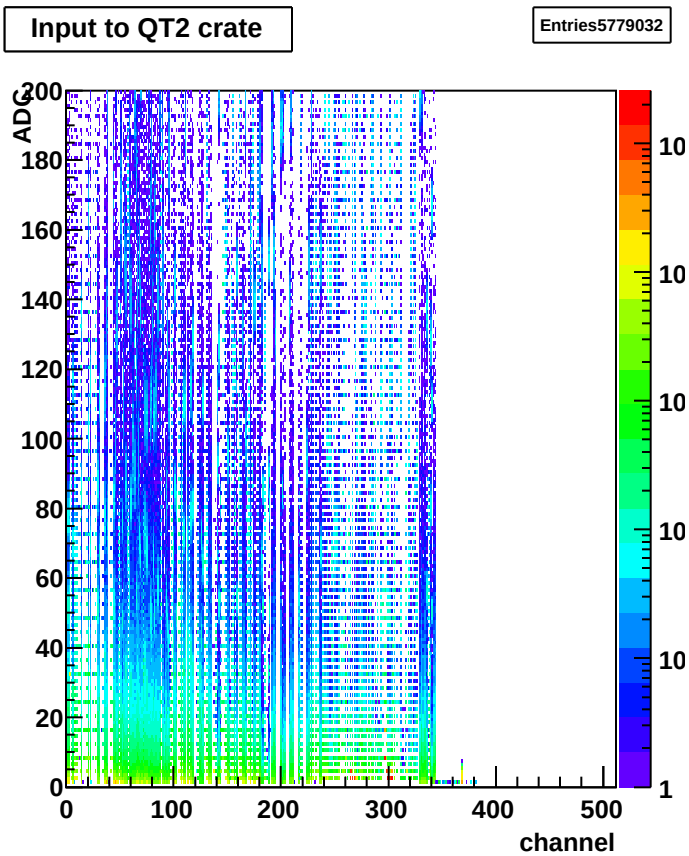

Entries 609291
BBC-L-East ADC Sum
BBC-L-West ADC Sum

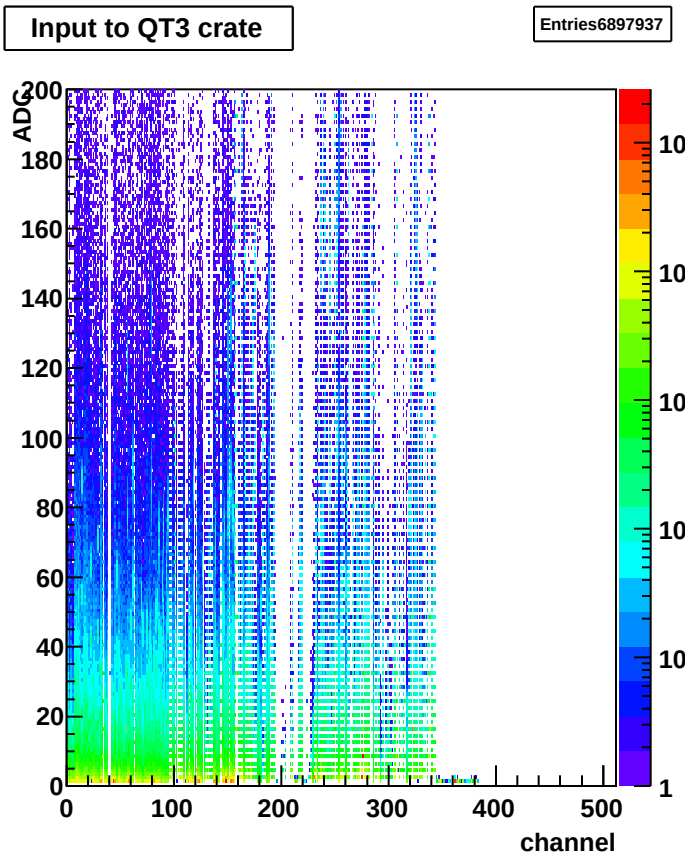

Entries 609291
BBC-S-East ADC Sum
BBC-S-West ADC Sum

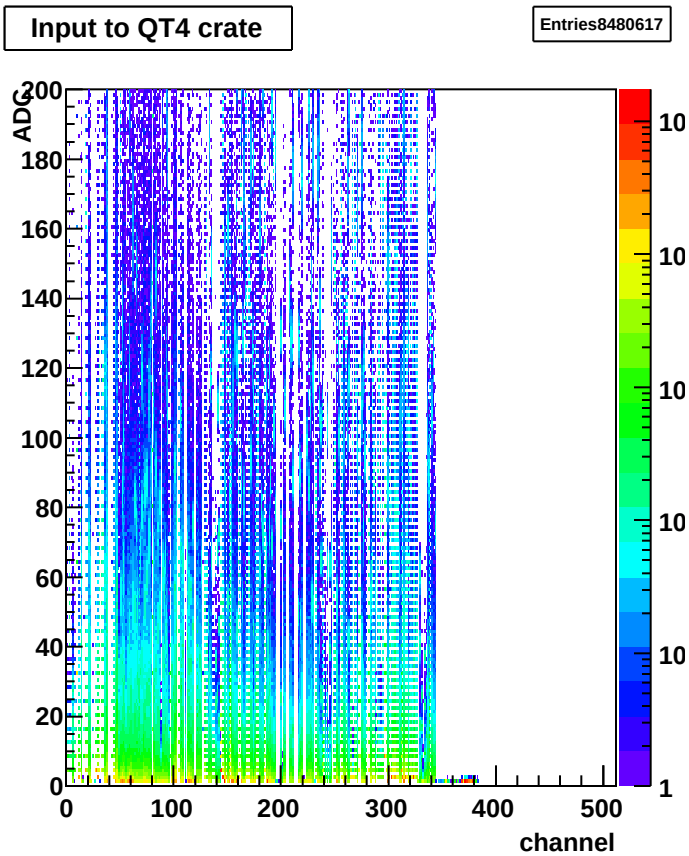

Entries 609291
ZDC East ADC Sum Att
ZDC West ADC Sum Att

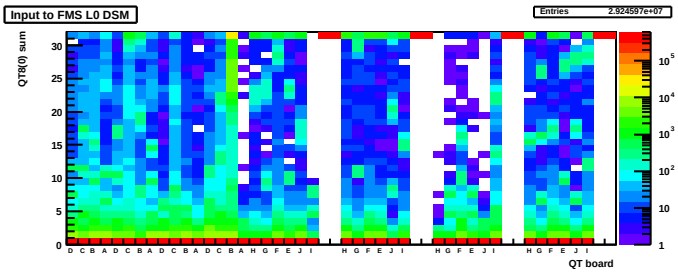

Entries 609291
BBC-L TAC Diff
BCC-S TAC Diff
Entries 609291
BBC-L TAC Diff
VPD TAC Diff
Entries 609291
BBC-L TAC Diff
ZDC TAC Diff
Entries 609291
BBC-S TAC Diff
VPD TAC Diff
Entries 609291
BBC-S TAC Diff
ZDC TAC Diff
Entries 609291
VPD TAC Diff
ZDC TAC Diff

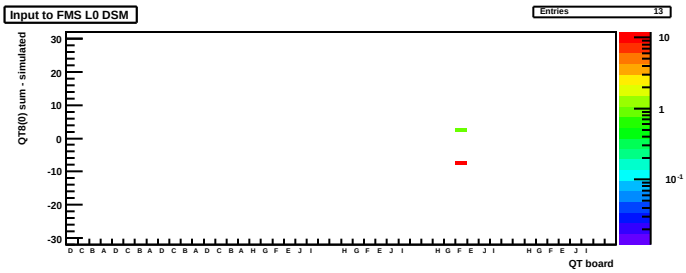

Input to QT1 crate
Entries9288100
ADC
channel

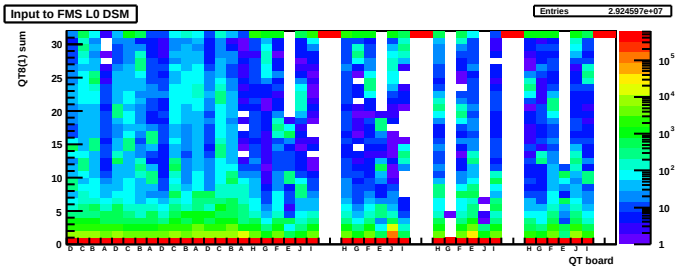

Input to QT2 crate
Entries5779032
ADC
channel

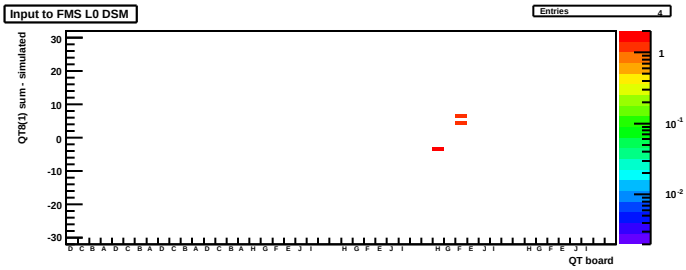

Input to QT3 crate
Entries6897937
ADC
channel

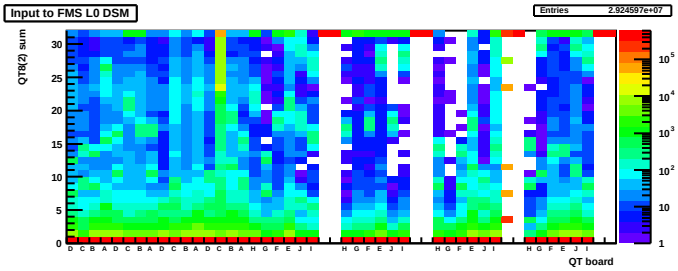

Input to QT4 crate
Entries8480617
ADC
channel

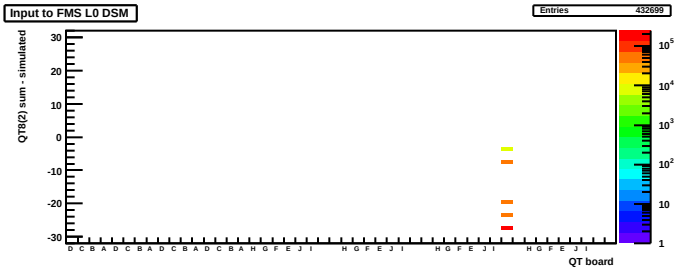

Input to FMS L0 DSM
Entries 2.924597e+07
QT8(0) sum
QT board

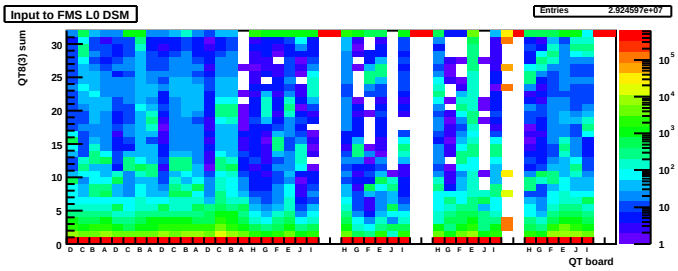

Input to FMS L0 DSM
Entries 13
QT8(0) sum - simulated
QT board

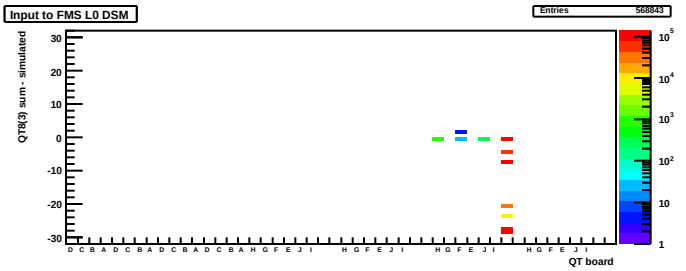

Input to FMS L0 DSM
Entries 2.924597e+07
QT8(1) sum
QT board

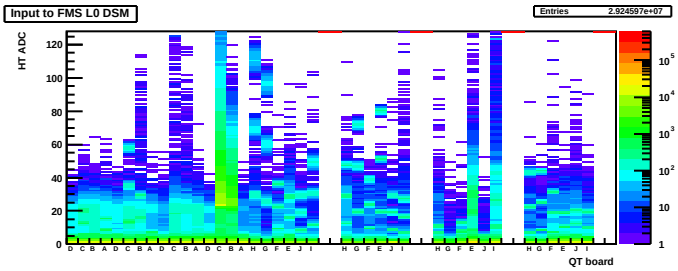

Input to FMS L0 DSM
Entries 4
QT8(1) sum - simulated
QT board

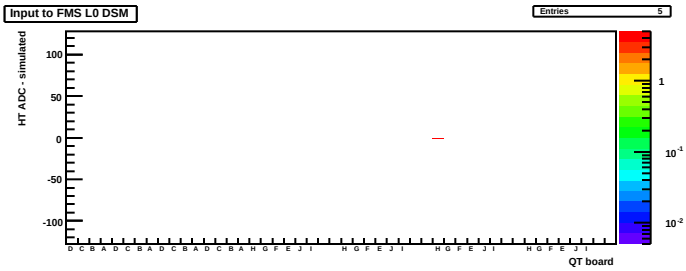

Input to FMS L0 DSM
Entries 2.924597e+07
QT8(2) sum
QT board

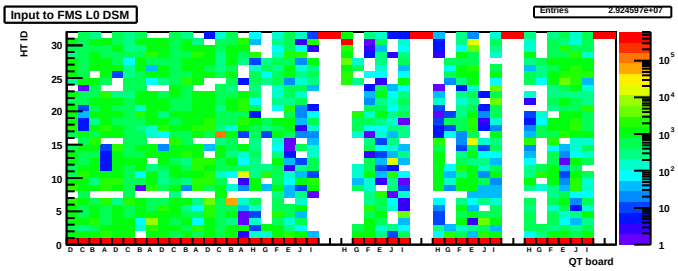

Input to FMS L0 DSM
Entries 432699
QT8(2) sum - simulated
QT board

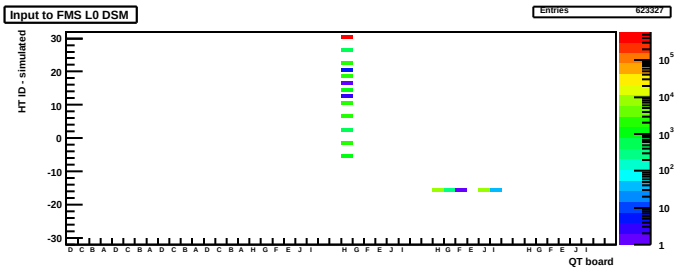

Input to FMS L0 DSM
Entries 2.924597e+07
QT8(3) sum
QT board

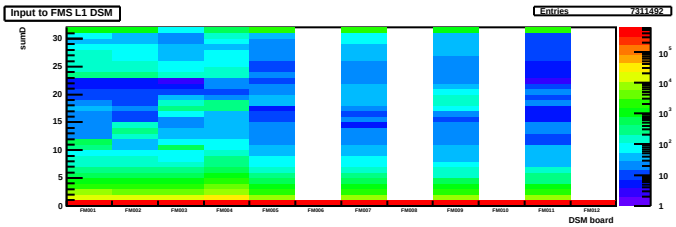

Input to FMS L0 DSM
Entries 568843
QT8(3) sum - simulated
QT board

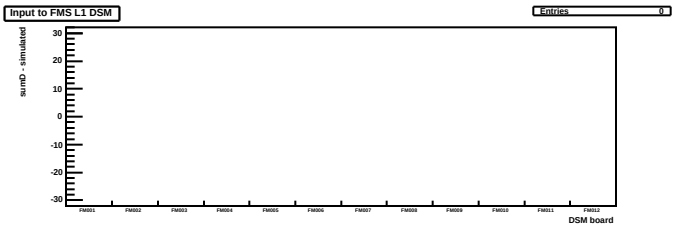

Input to FMS L0 DSM
Entries 2.924597e+07
HT ADC
QT board

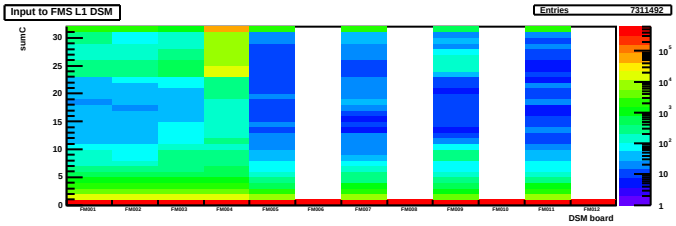

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

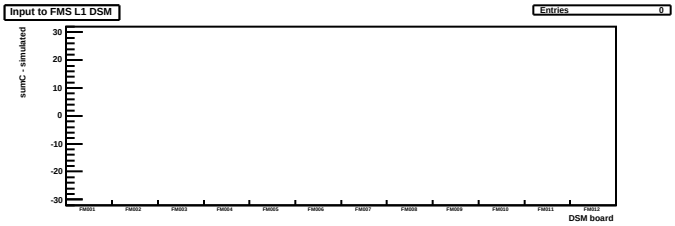

Input to FMS L0 DSM
Entries 2.924597e+07
HT ID
QT board

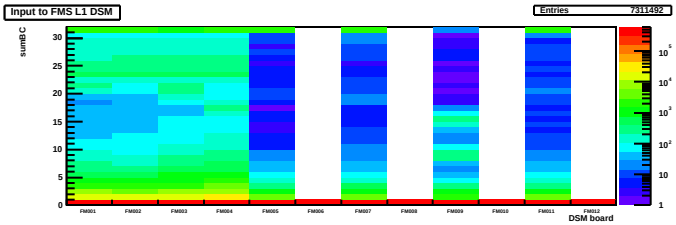

Input to FMS L0 DSM
Entries 623327
HT ID - simulated
QT board

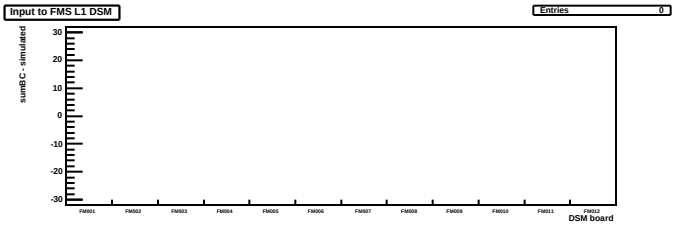

Input to FMS L1 DSM
Entries 7311492
sumD
DSM board

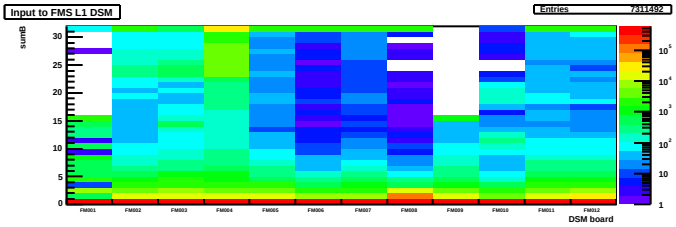

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

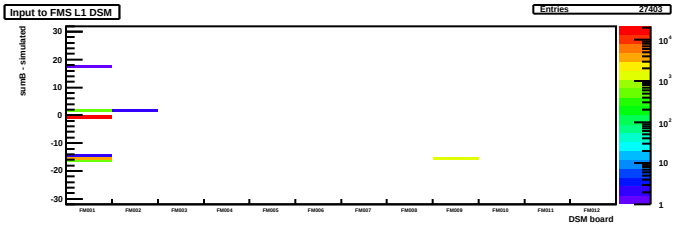

Input to FMS L1 DSM
Entries 7311492
sumC
DSM board

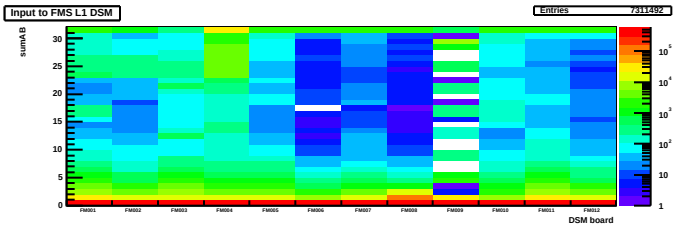

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

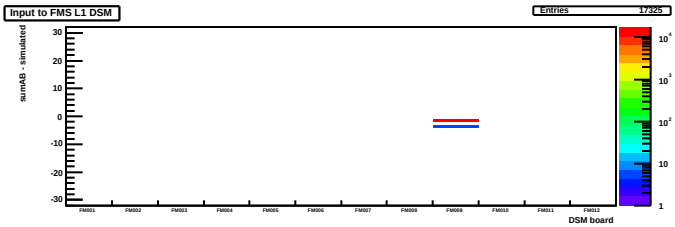

Input to FMS L1 DSM
Entries 7311492
sumBC
DSM board

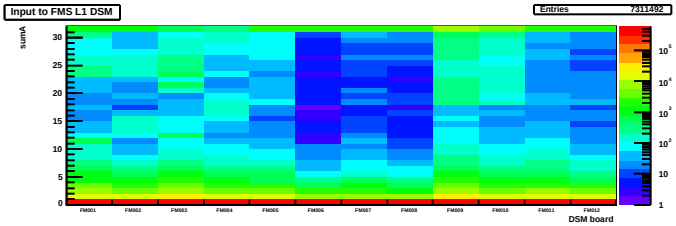

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

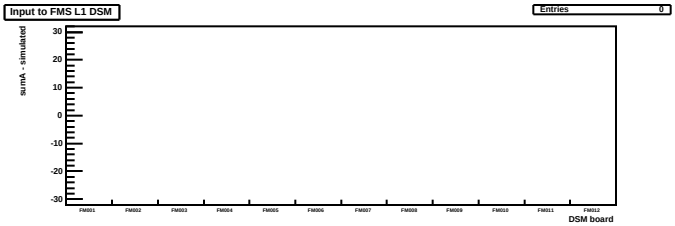

Input to FMS L1 DSM
Entries 7311492
sumB
DSM board

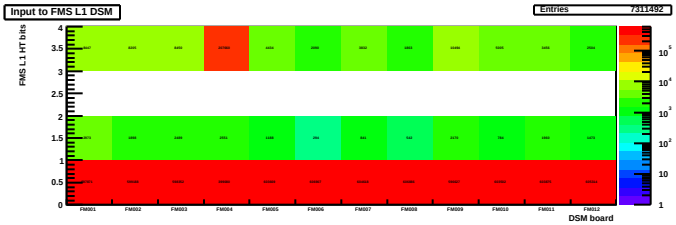

Input to FMS L1 DSM
Entries 27403
sumB - simulated
DSM board

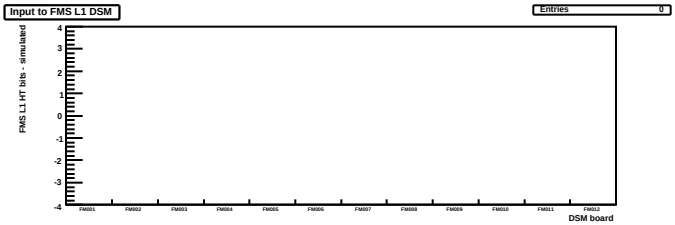

Input to FMS L1 DSM
Entries 7311492
sumAB
DSM board

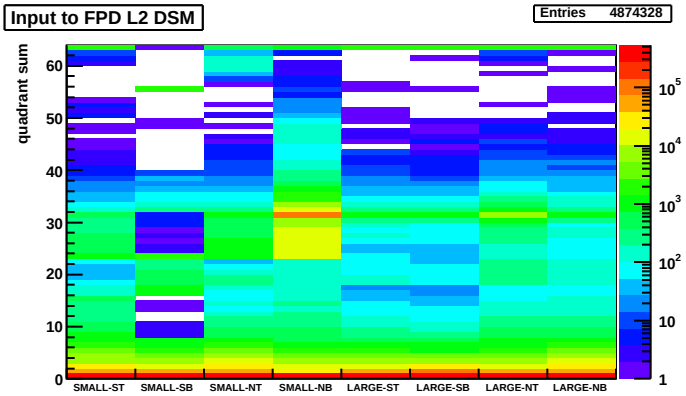

Input to FMS L1 DSM
Entries 17325
sumAB - simulated
DSM board

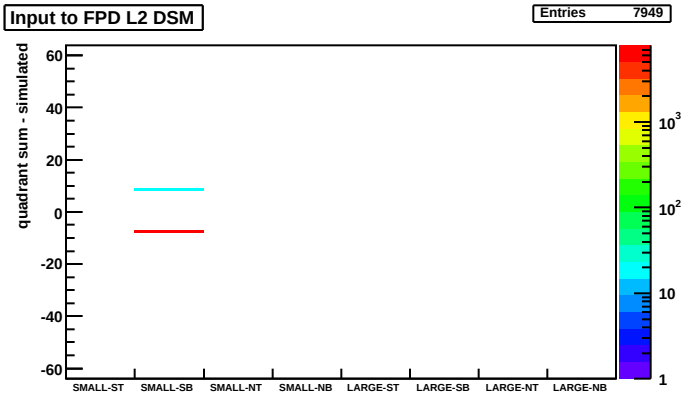

Input to FMS L1 DSM
Entries 7311492
sumA
DSM board

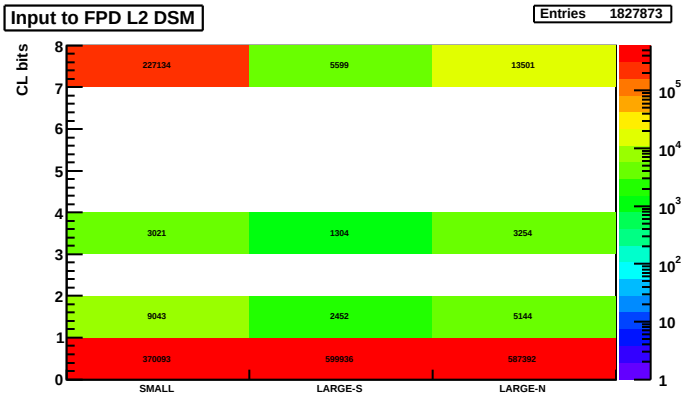

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

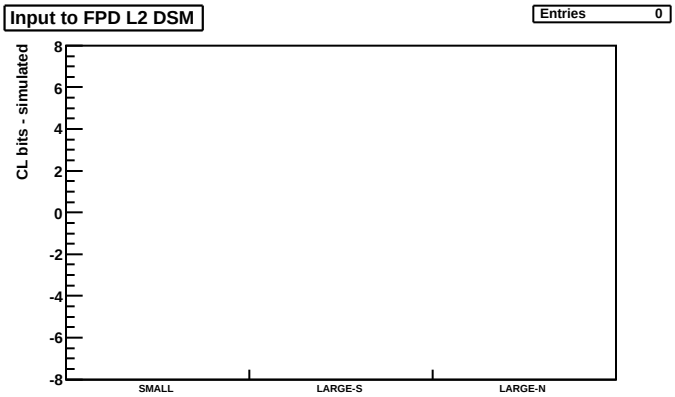

Input to FMS L1 DSM
Entries 7311492
FMS L1 HT bits
DSM board

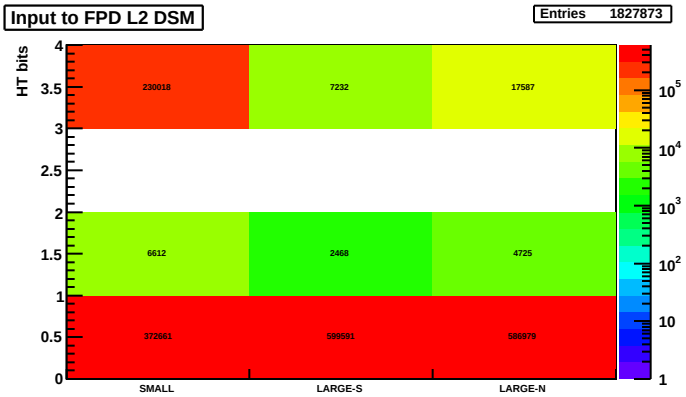

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

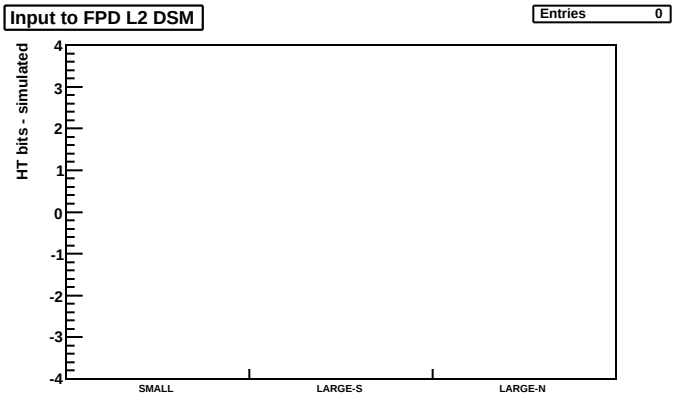

Input to FPD L2 DSM
Entries 4874328
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

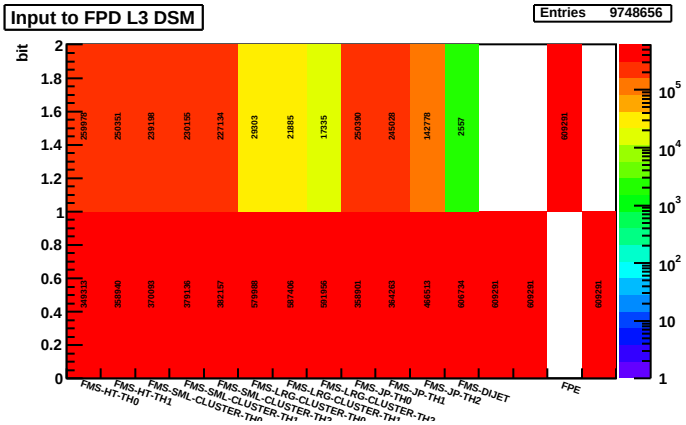

Input to FPD L2 DSM
Entries 7949
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

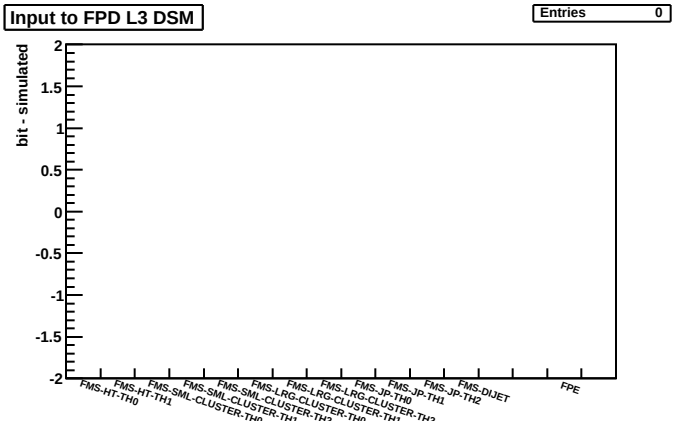

Input to FPD L2 DSM
Entries 1827873
CL bits
227134
5599
13501
3021
1304
3254
9043
2452
5144
370093
599936
587392
SMALL
LARGE-S
LARGE-N

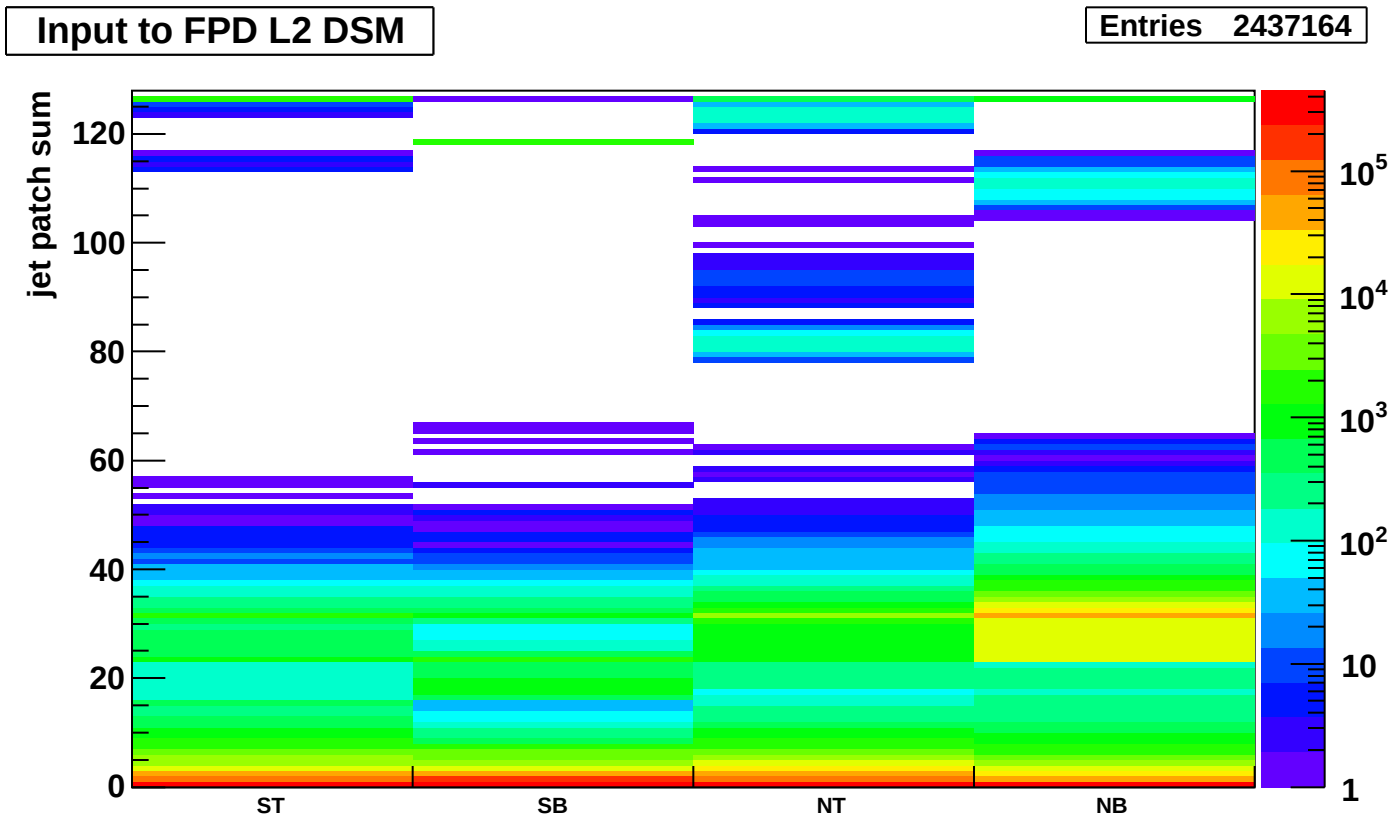

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

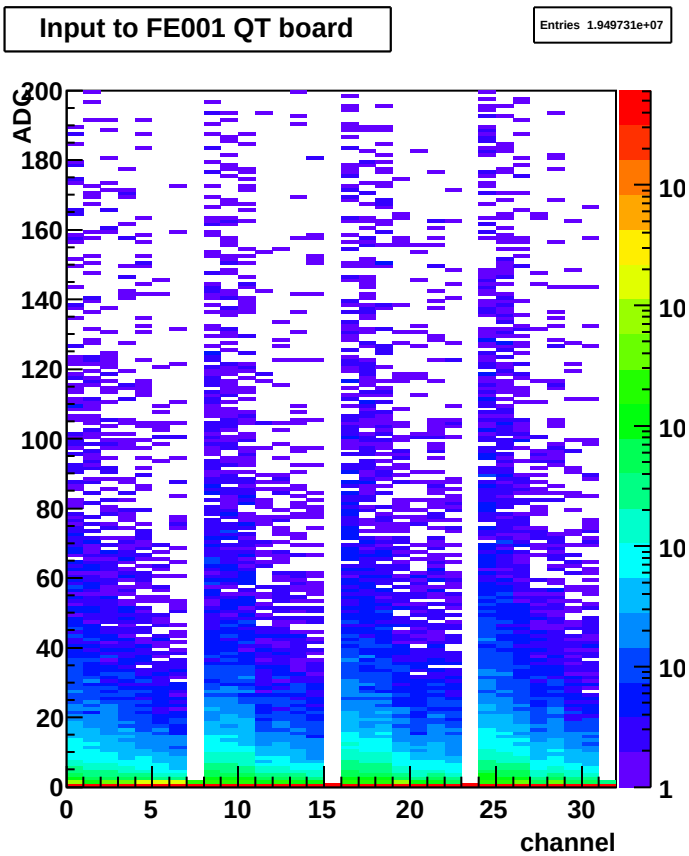

Input to FPD L2 DSM
Entries 1827873
HT bits
230018
7232
17587
6612
2468
4725
372661
599591
586979
SMALL
LARGE-S
LARGE-N

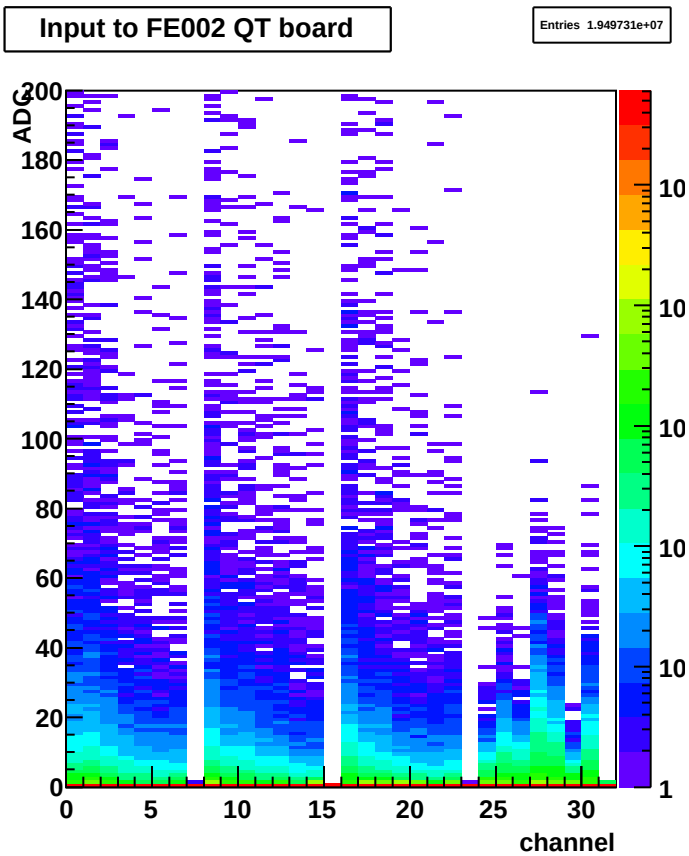

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

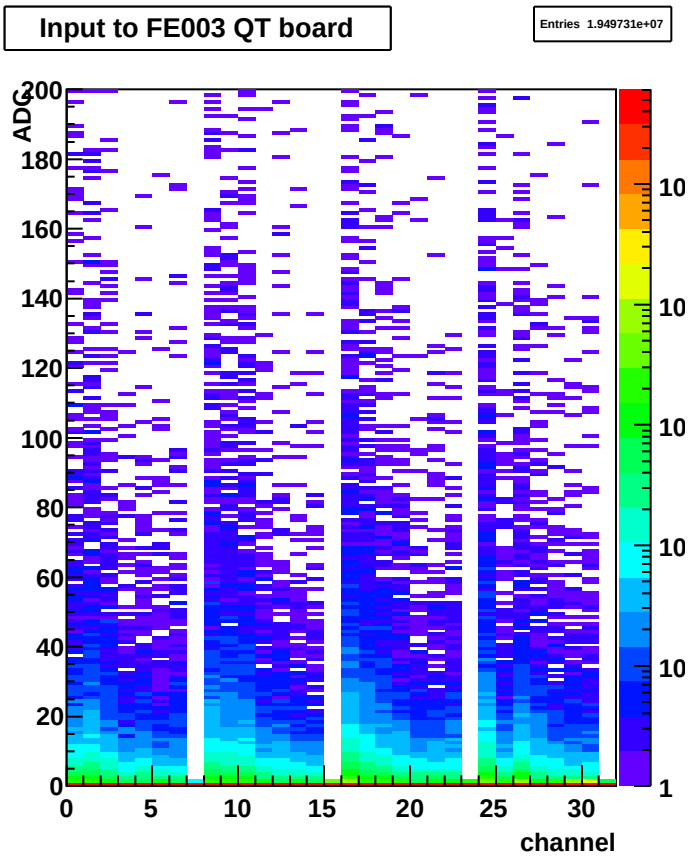

Input to FPD L3 DSM
Entries 9748656
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

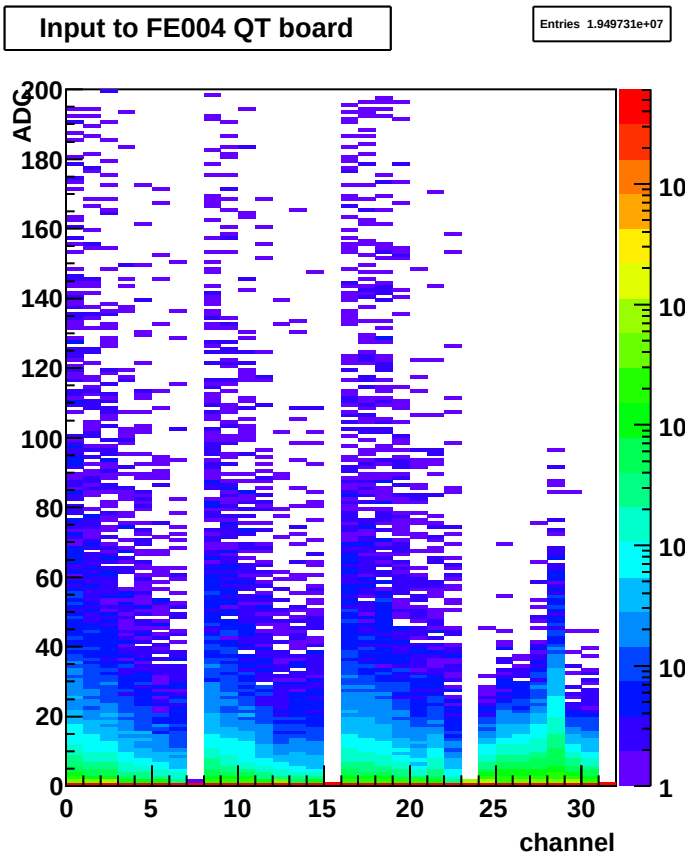

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

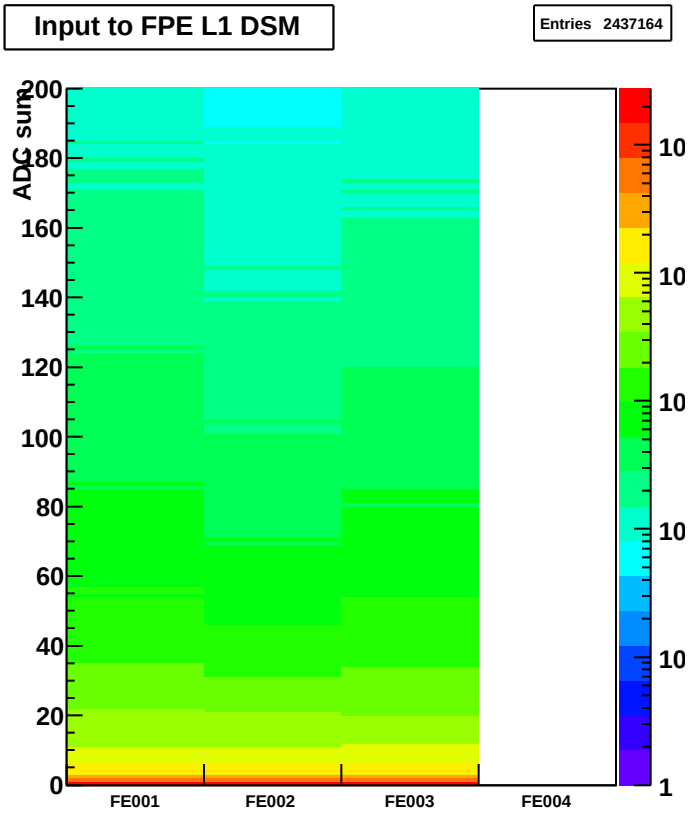

Input to FPD L2 DSM
Entries 2437164
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

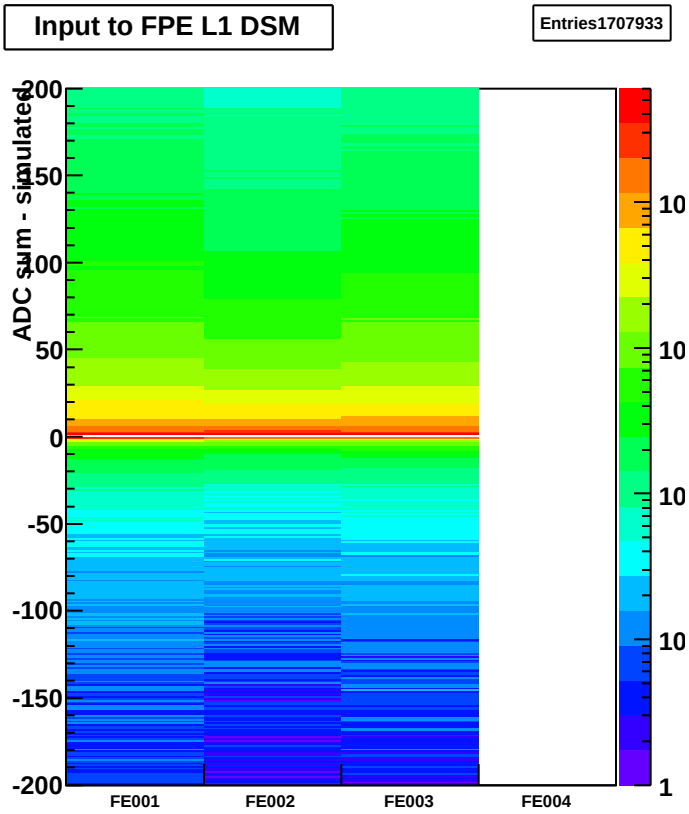

Input to FE001 QT board
Entries 1.949731e+07
ADC
channel

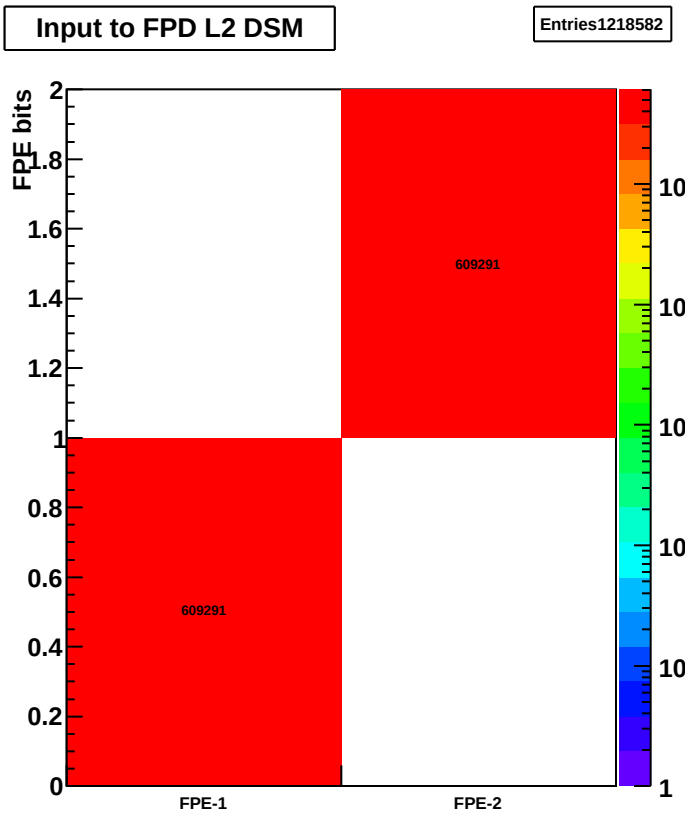

Input to FE002 QT board
Entries 1.949731e+07
ADC
channel

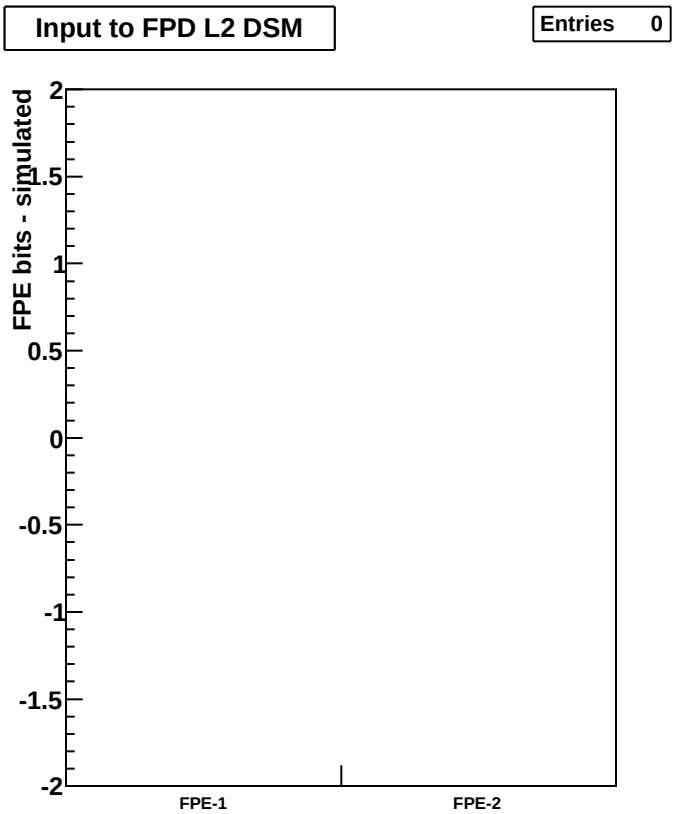

Input to FE003 QT board
Entries 1.949731e+07
ADC
channel


Input to FE004 QT board
Entries 1.949731e+07
ADC
channel


Input to FPE L1 DSM
Entries 2437164
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1707933
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1218582
FPE bits
609291
609291
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2