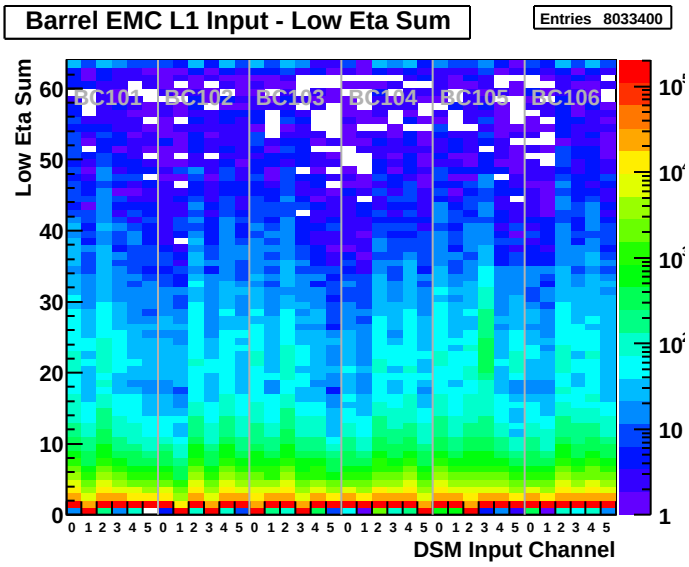
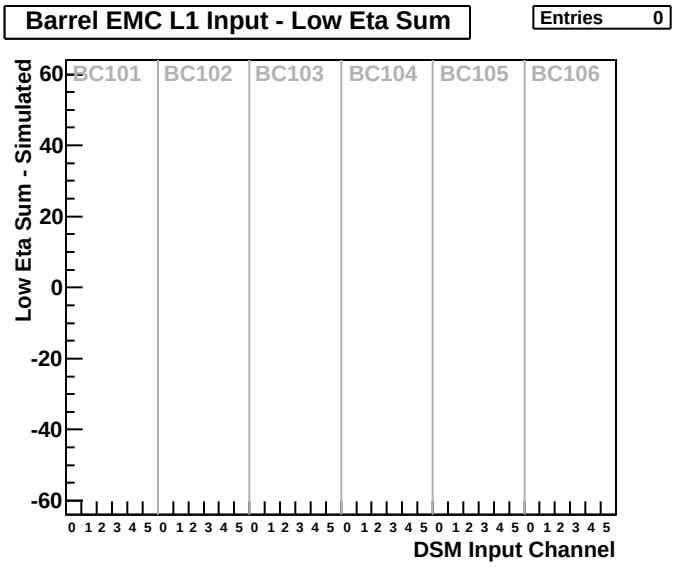
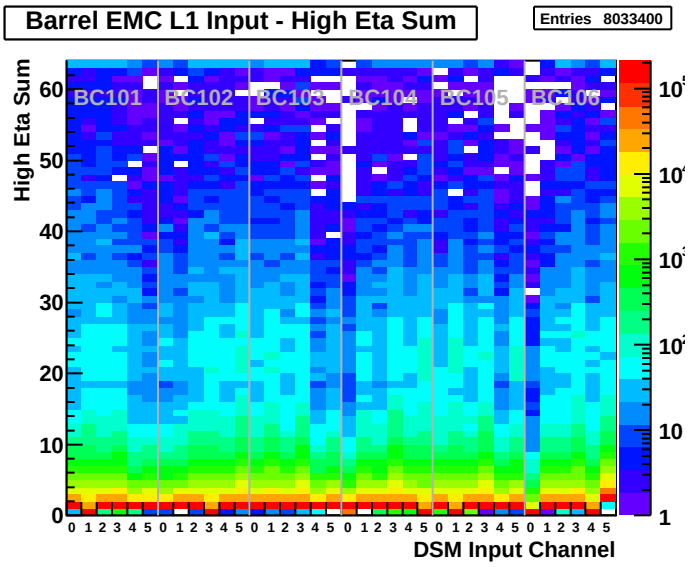
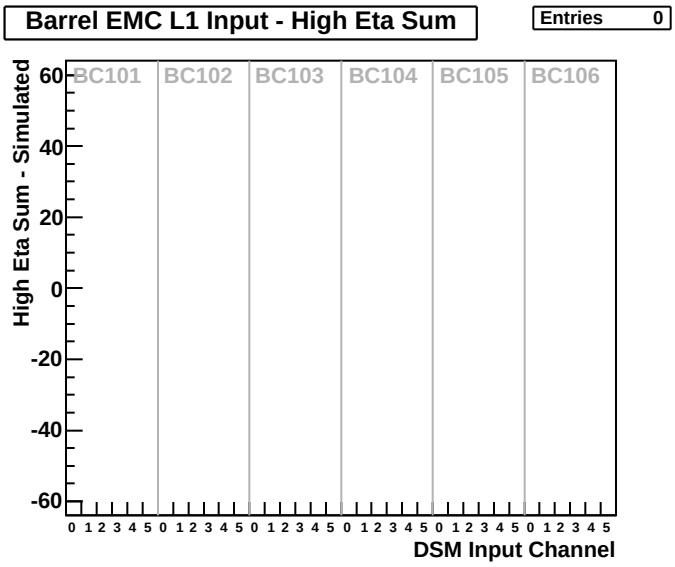
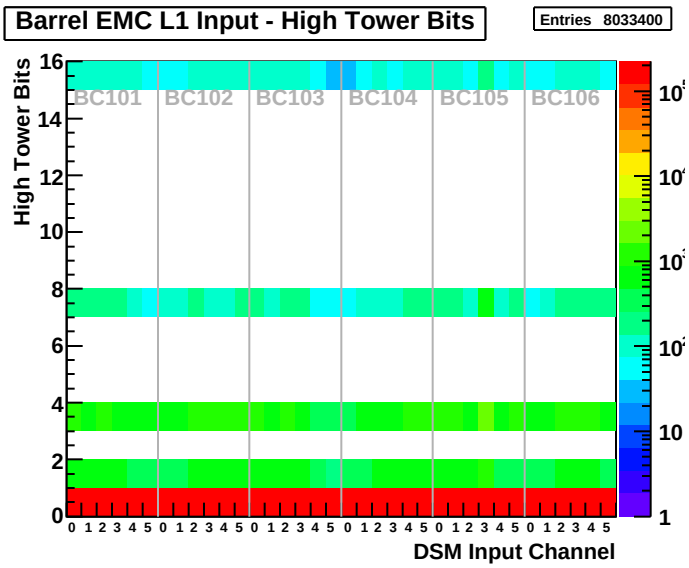
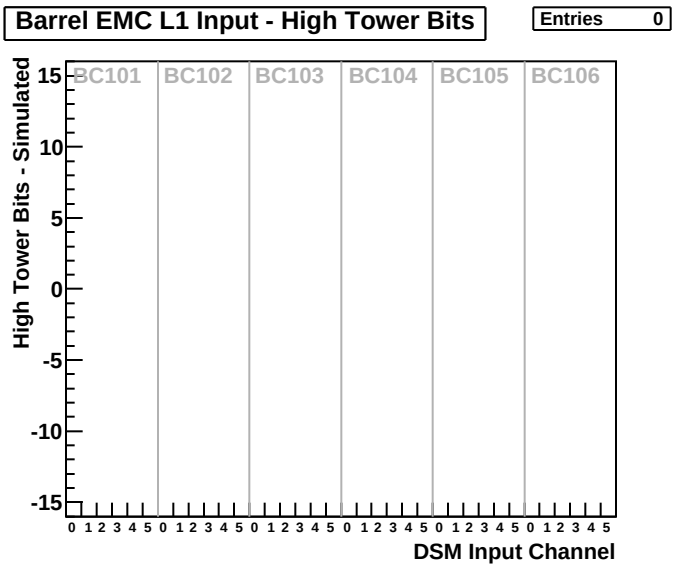

Barrel EMC L1 Input - Low Eta Sum
Entries 8033400
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

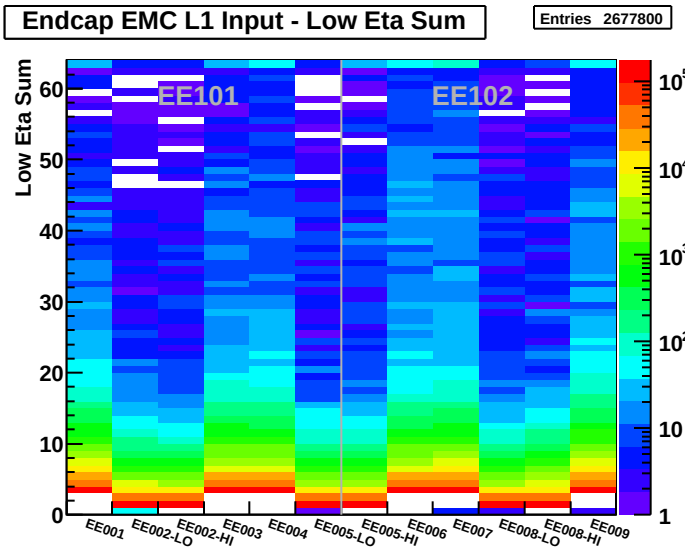

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

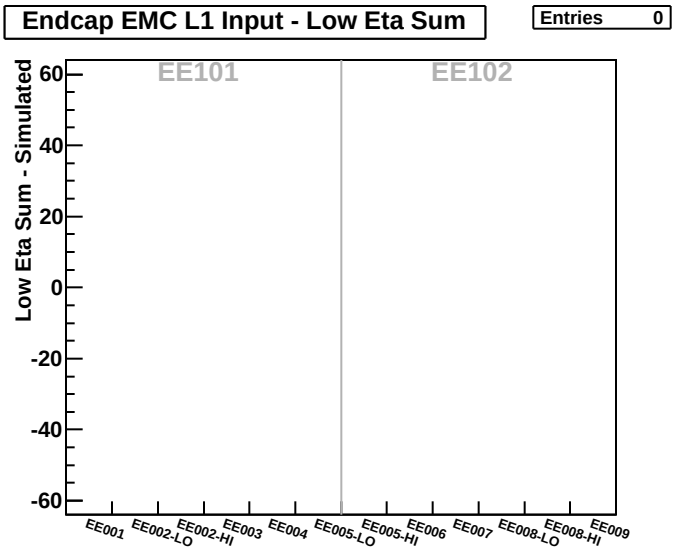

Barrel EMC L1 Input - High Eta Sum
Entries 8033400
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

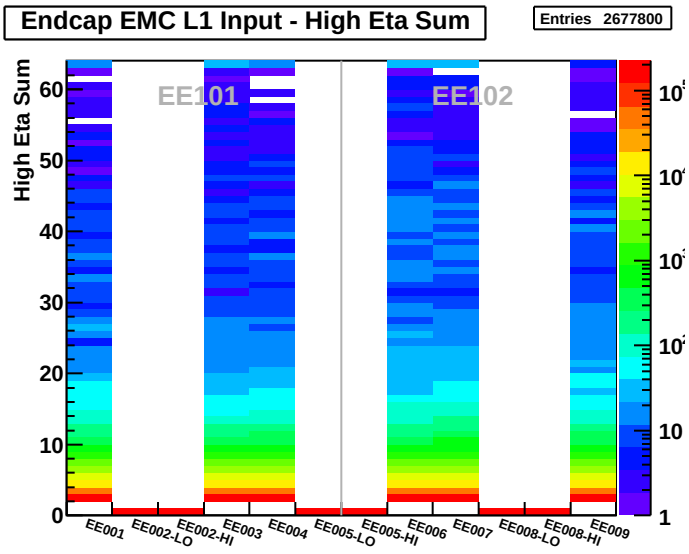

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

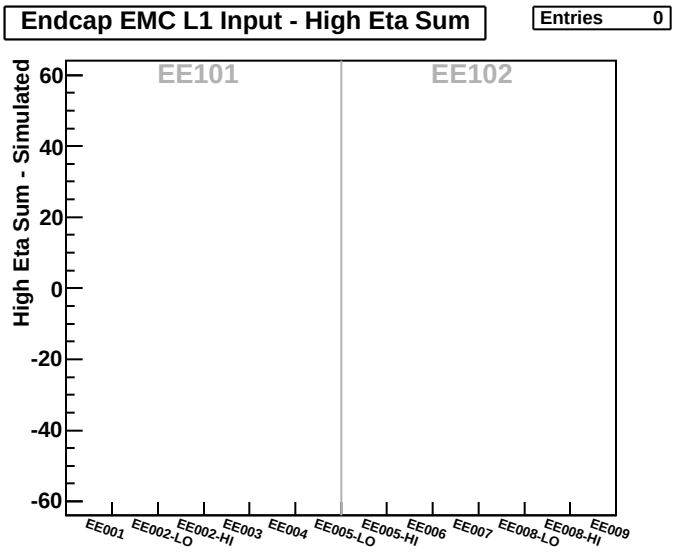

Barrel EMC L1 Input - High Tower Bits
Entries 8033400
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

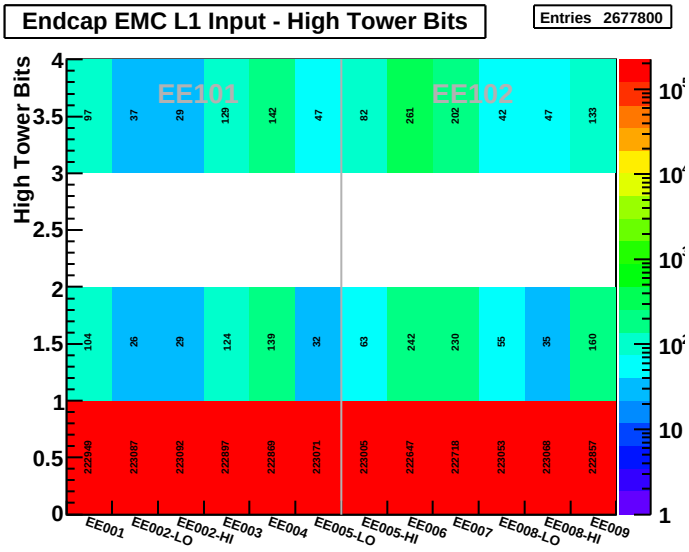

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

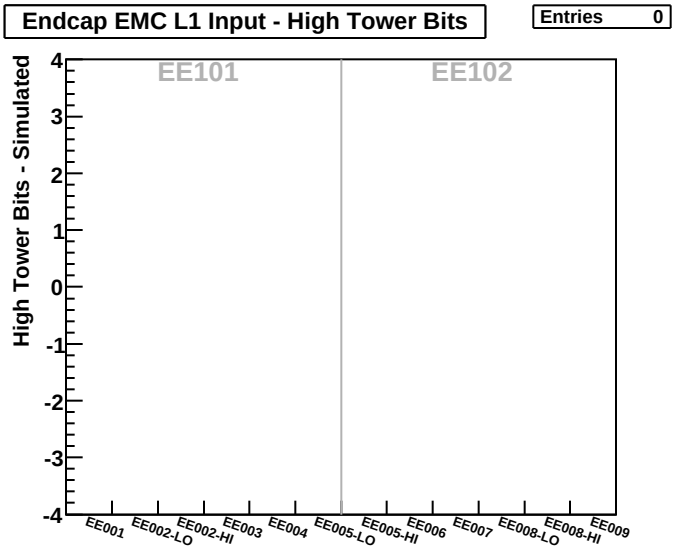

Endcap EMC L1 Input - Low Eta Sum
Entries 2677800
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

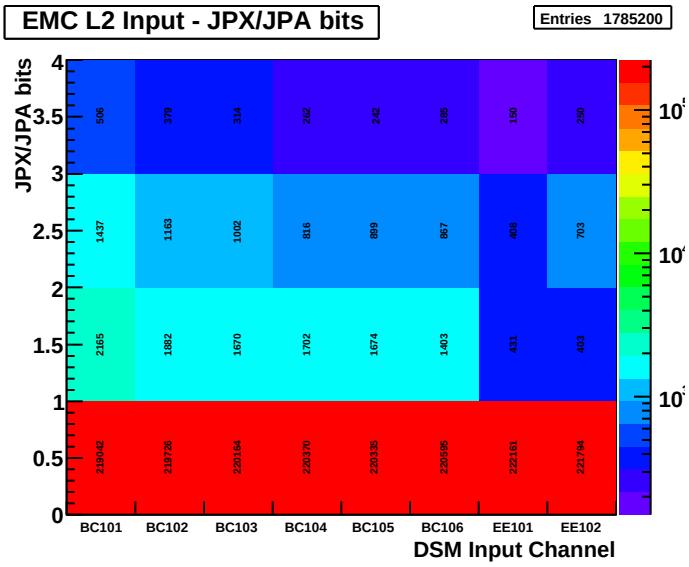

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

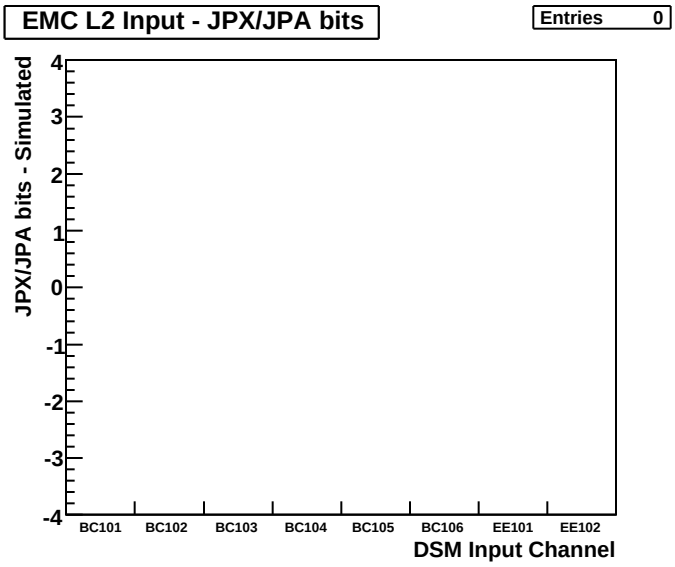

Endcap EMC L1 Input - High Eta Sum
Entries 2677800
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

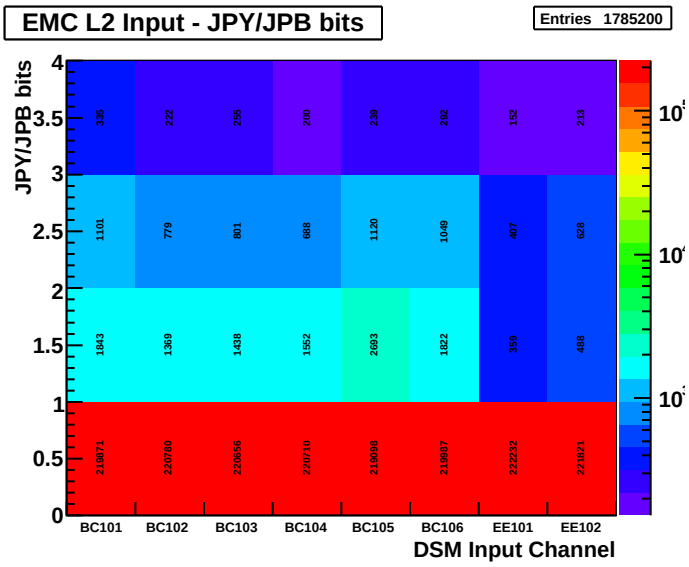

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

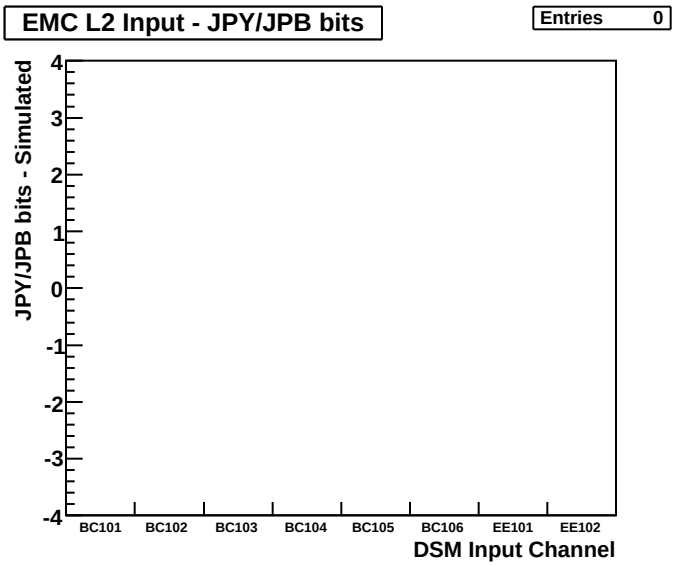

Endcap EMC L1 Input - High Tower Bits
Entries 2677800
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

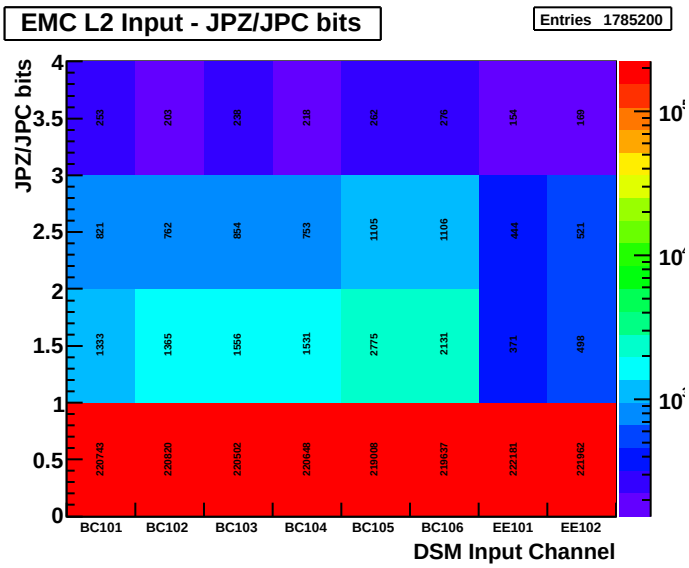

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

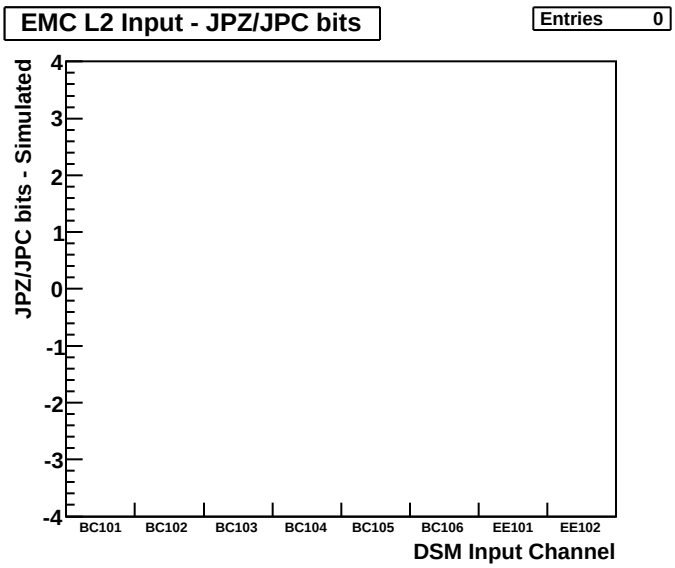

EMC L2 Input - JPX/JPA bits
Entries 1785200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
506 379 314 262 242 285 150 250
1437 1163 1002 816 899 867 408 703
2165 1882 1670 1702 1674 1403 431 403
219042 219726 220164 220370 220335 220595 222161 221704

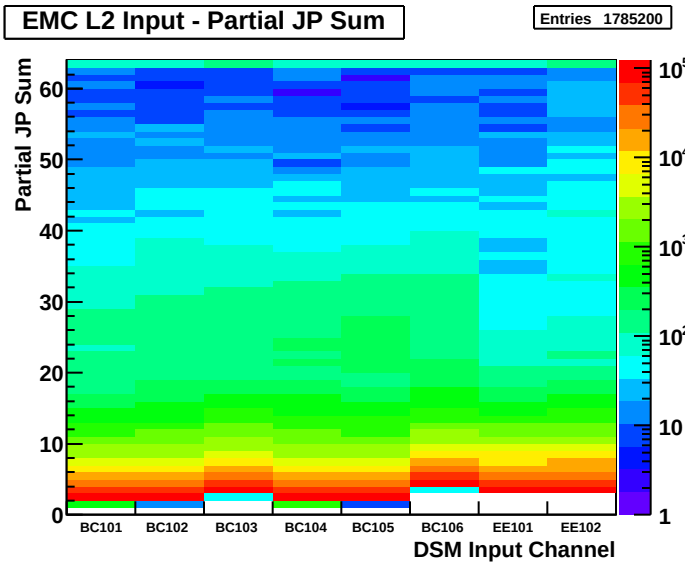

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

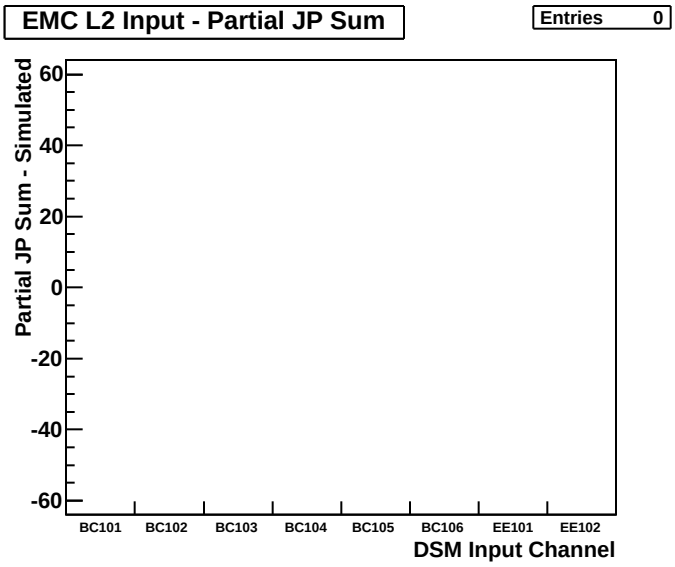

EMC L2 Input - JPY/JPB bits
Entries 1785200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
335 222 255 200 239 292 152 213
1101 779 801 688 1120 1049 407 628
1943 1369 1438 1552 2693 1822 359 488
218871 220780 220656 220710 219098 219987 222232 221821

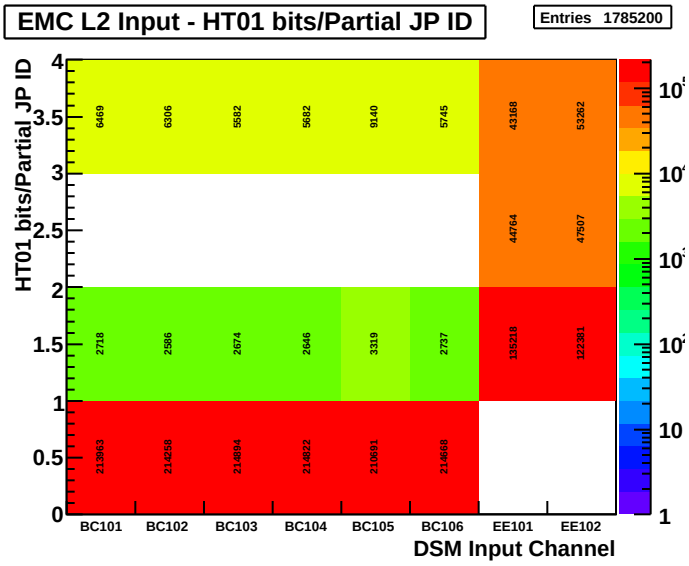

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

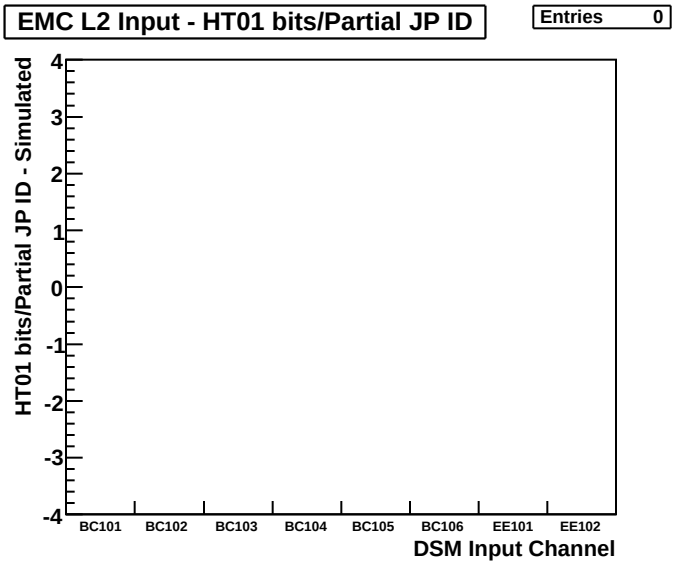

EMC L2 Input - JPZ/JPC bits
Entries 1785200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
259 203 238 218 262 276 154 169
821 762 854 753 1105 1106 444 521
1333 1365 1556 1531 2775 2131 371 498
220743 220820 220502 220648 219008 219537 222181 221962

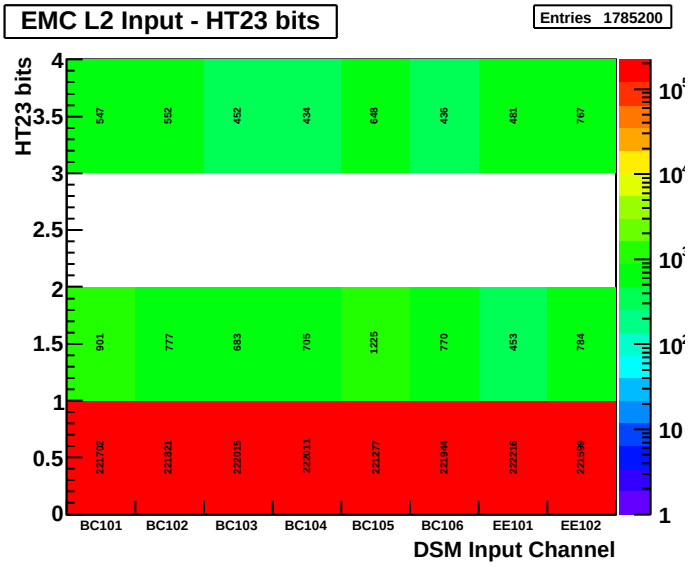

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

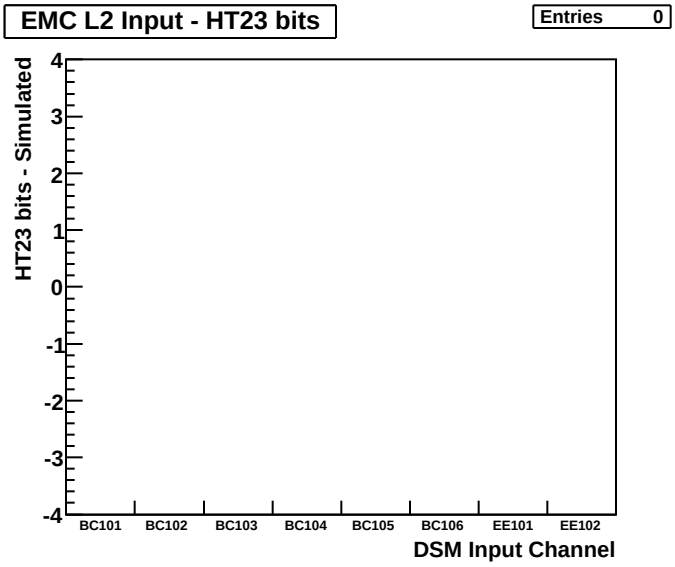

EMC L2 Input - Partial JP Sum
Entries 1785200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

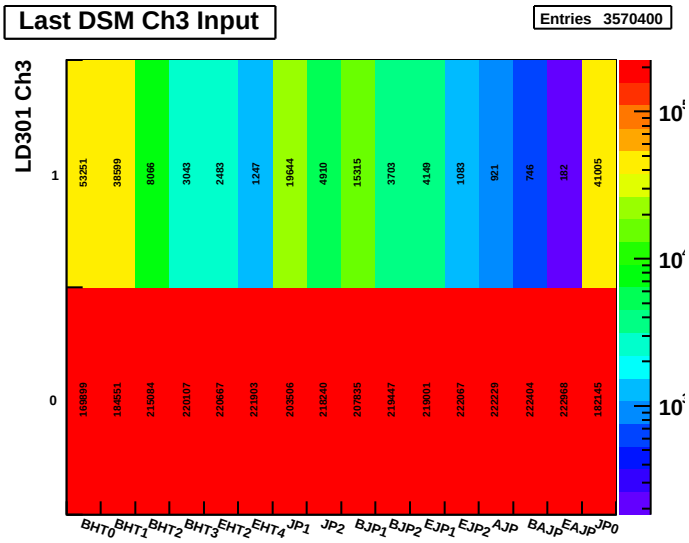

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

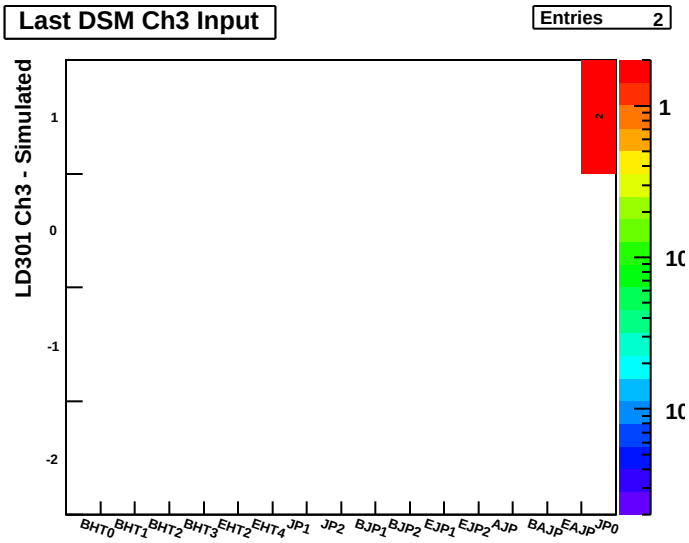

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1785200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

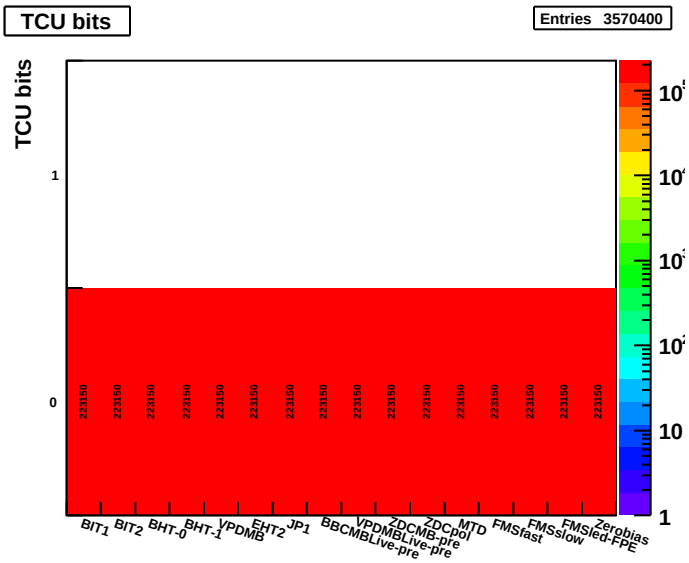

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

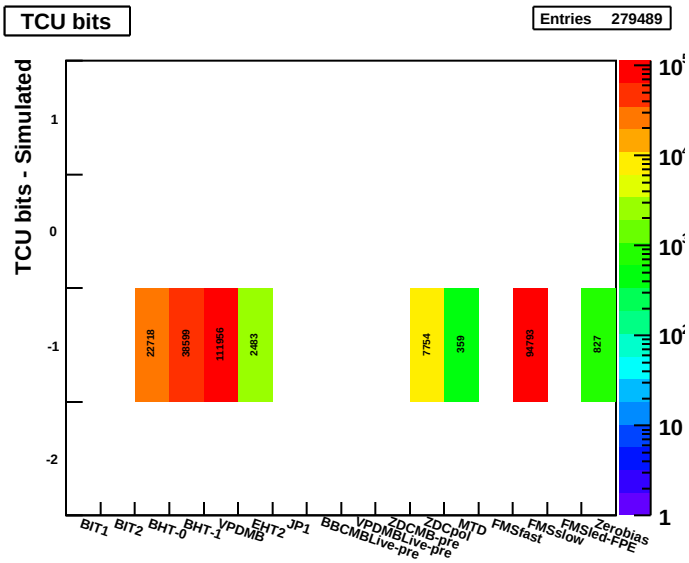

EMC L2 Input - HT23 bits
Entries 1785200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

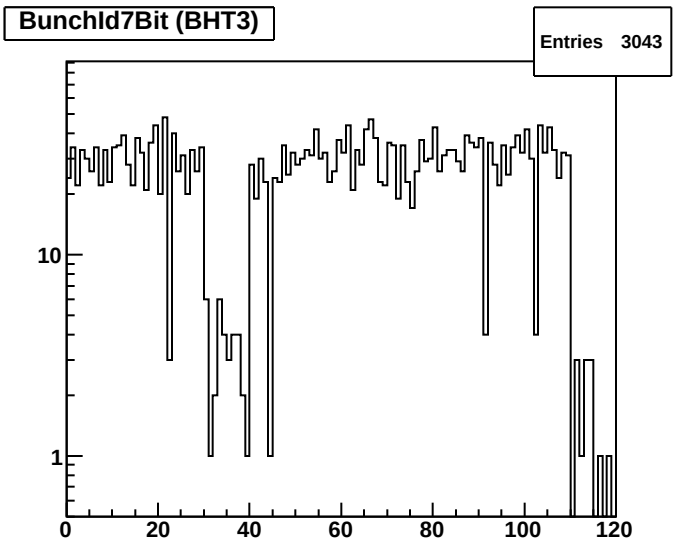

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

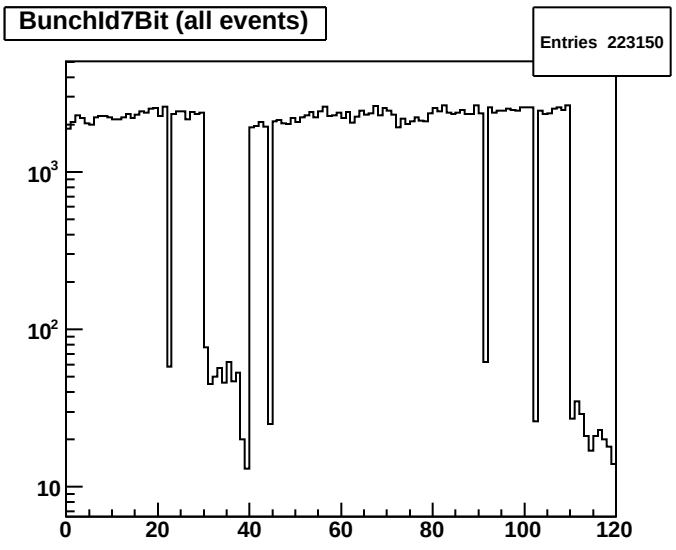

Last DSM Ch3 Input
Entries 3570400
LD301 Ch3
53251 38599 8066 3043 2483 1247 19644 4910 15315 3703 4149 1083 921 746 182 41005
168899 184551 215984 220107 220667 221903 203506 218240 207835 219447 219001 222067 222229 222404 222968 182145
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

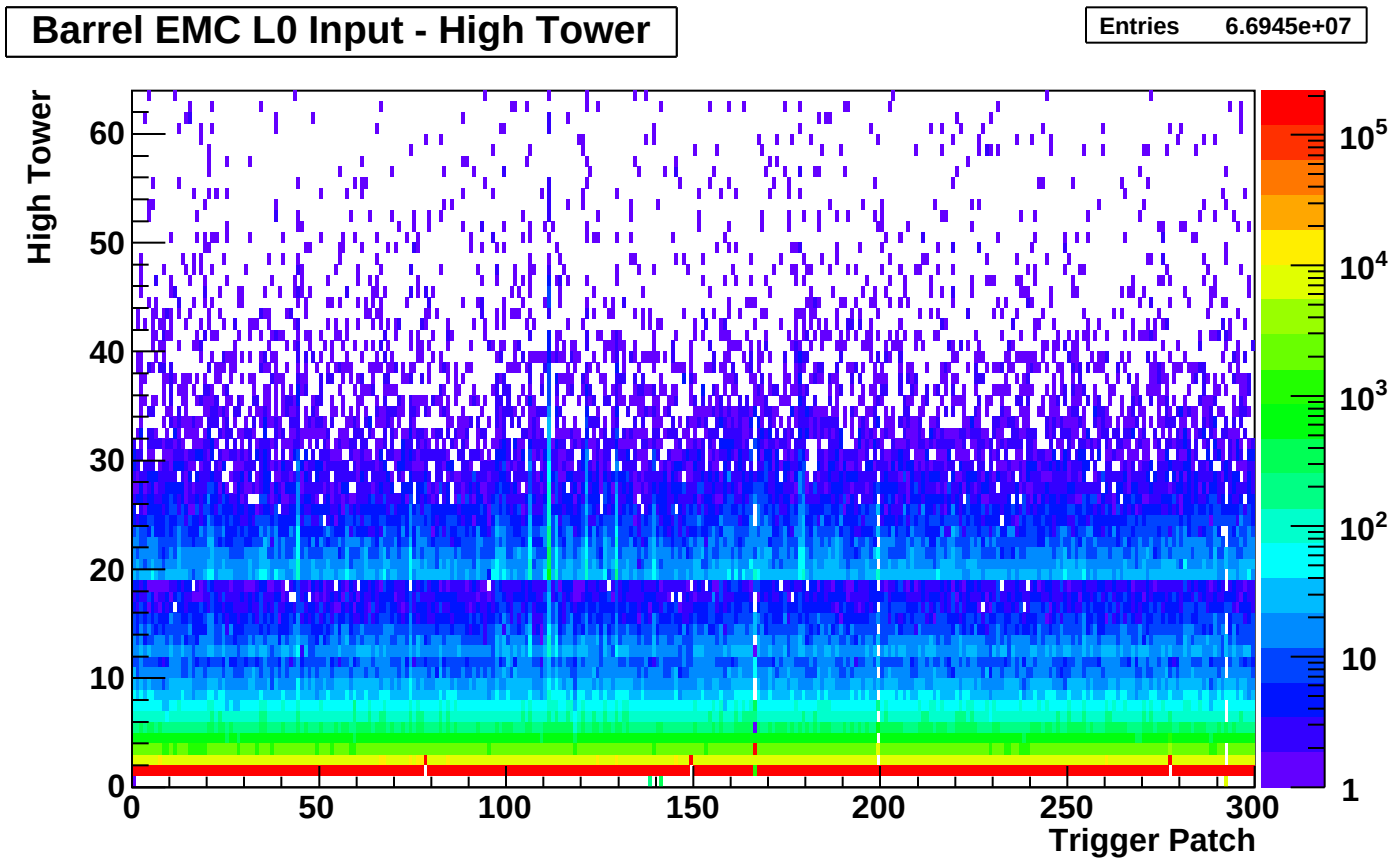

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

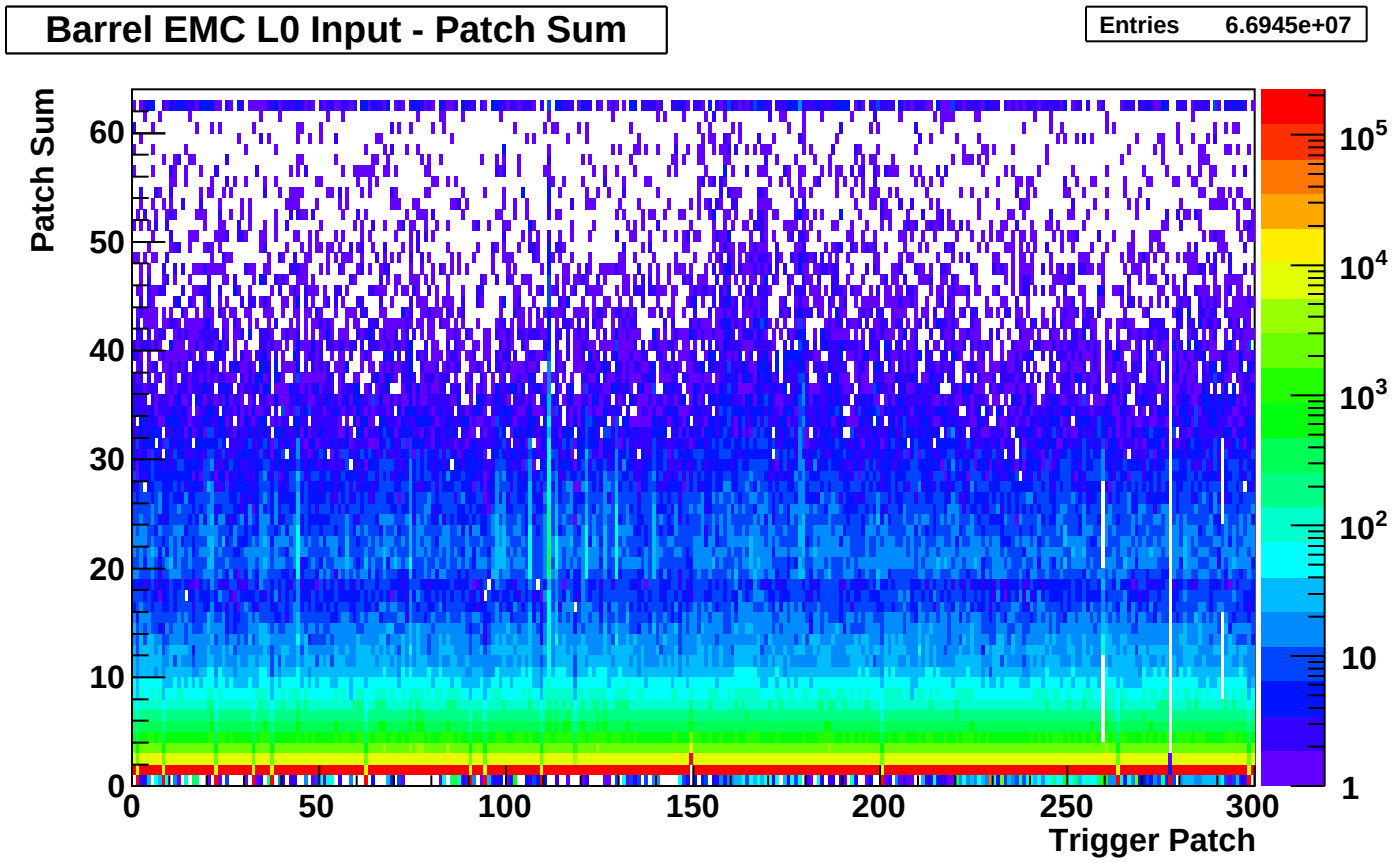

TCU bits
Entries 3570400
TCU bits
223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150 223150
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

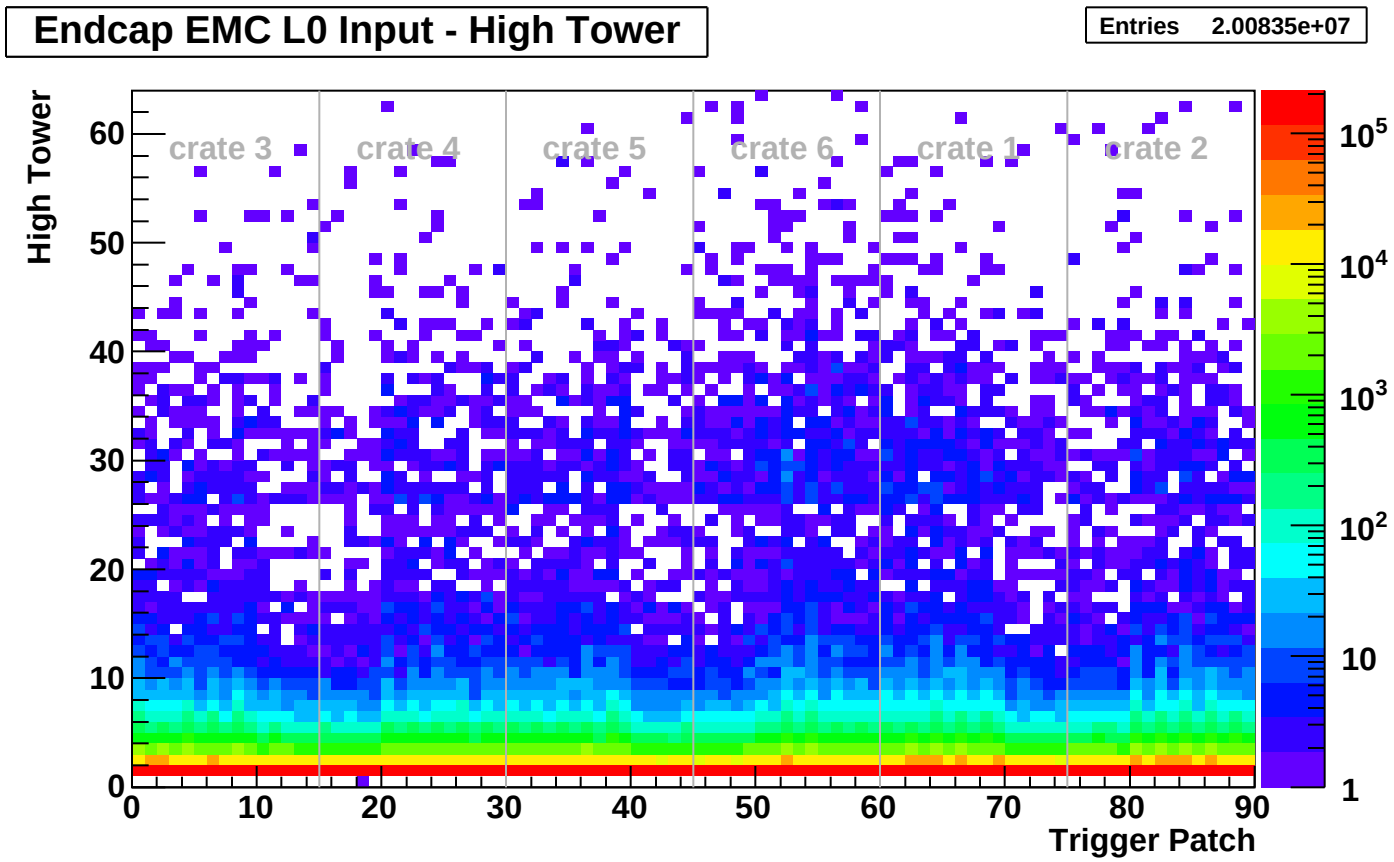

TCU bits
Entries 279489
TCU bits - Simulated
22718 38599 111956 2483 7754 359 94793 827
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

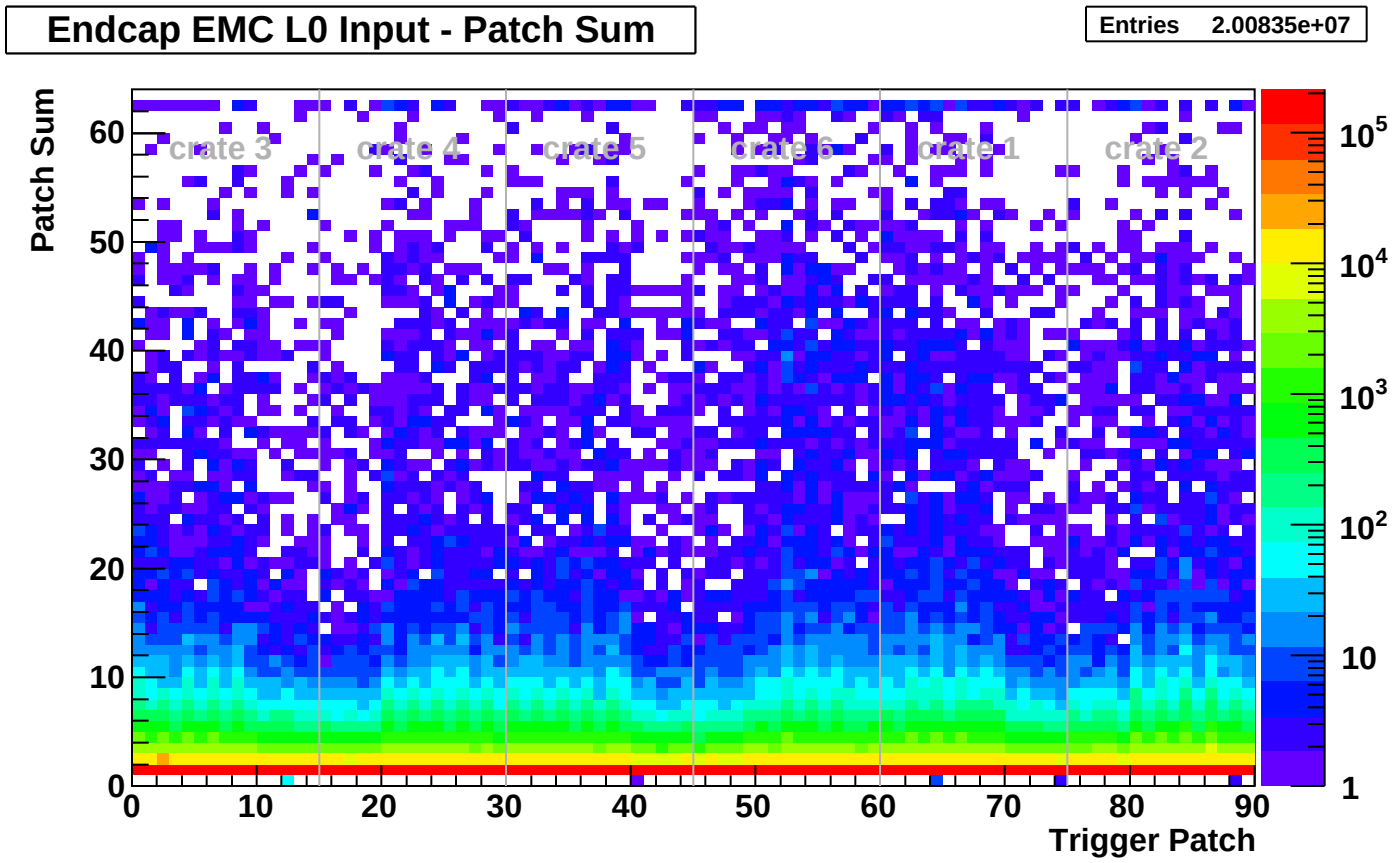

BunchId7Bit (BHT3)
Entries 3043

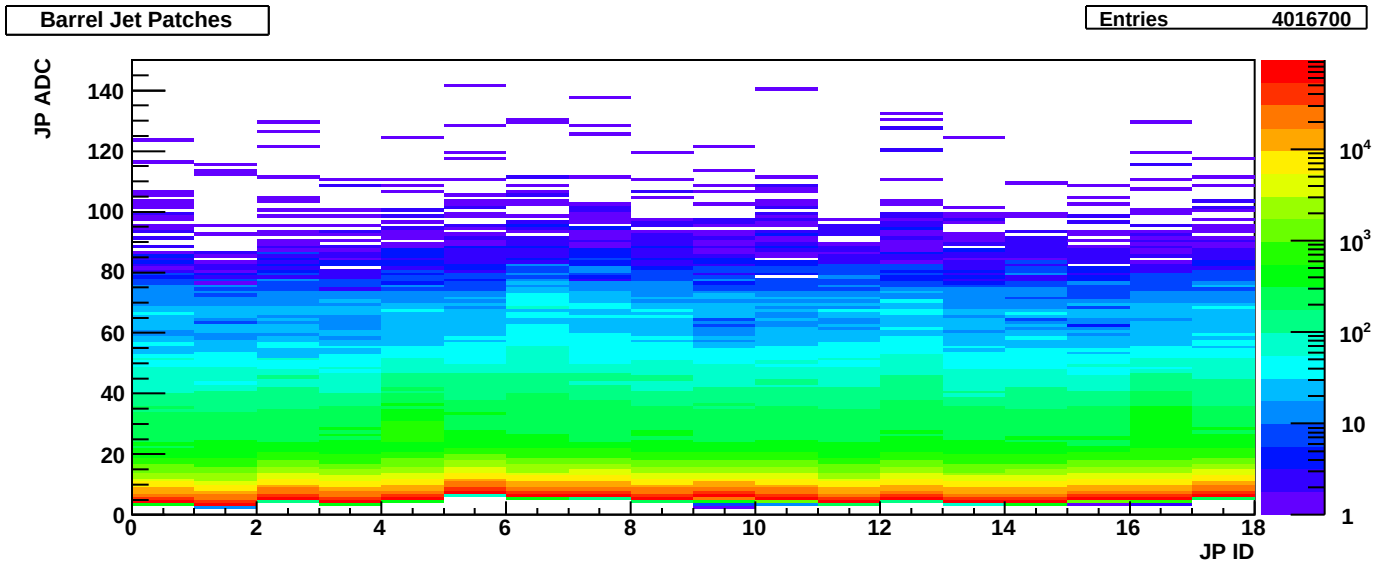

BunchId7Bit (all events)
Entries 223150

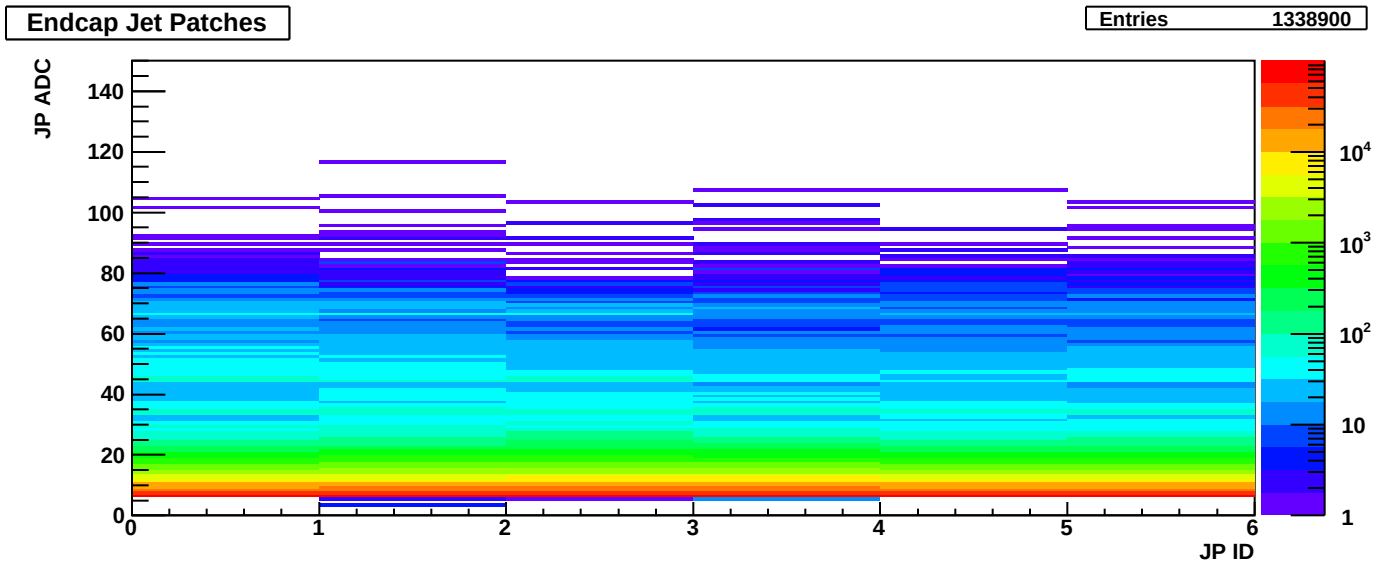

Barrel EMC L0 Input - High Tower
Entries 6.6945e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

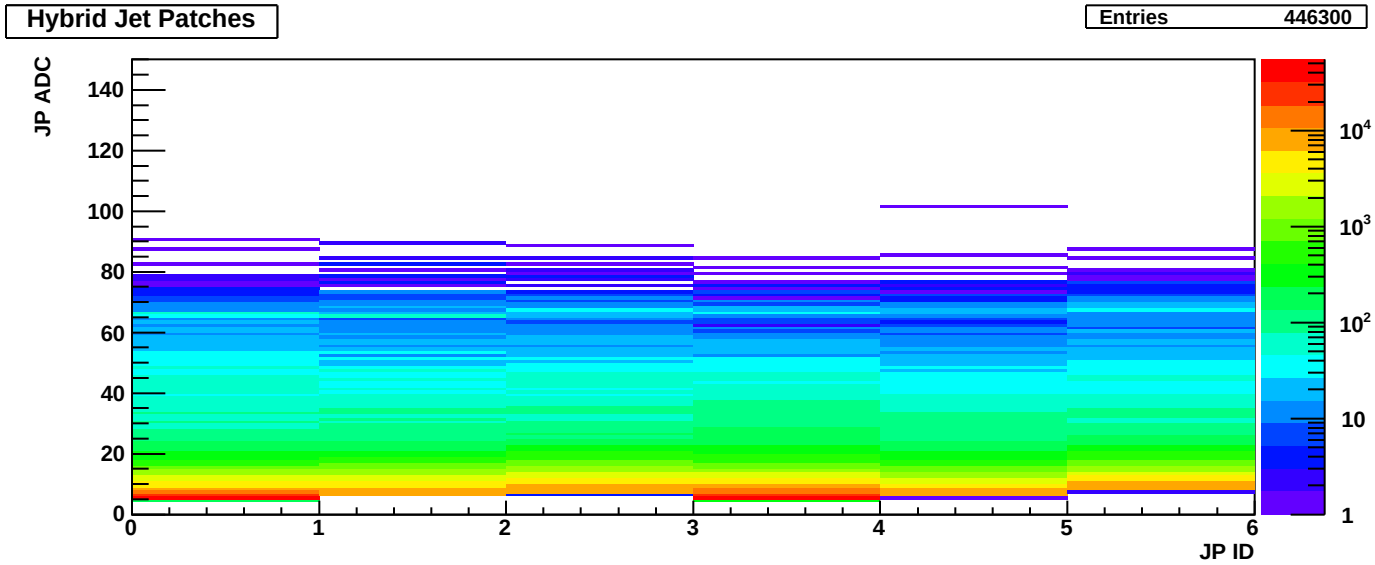

Barrel EMC L0 Input - Patch Sum
Entries 6.6945e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

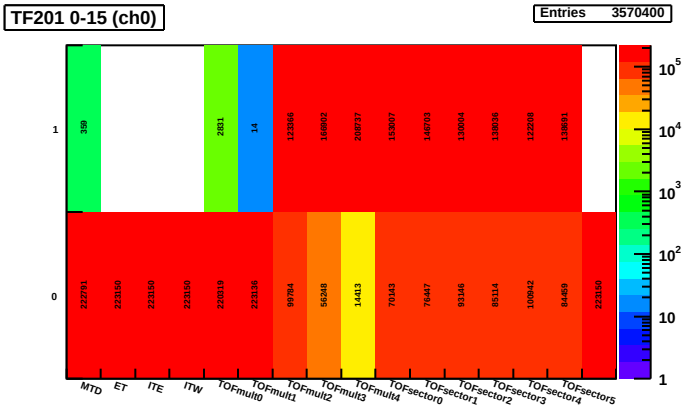

Endcap EMC L0 Input - High Tower
Entries 2.00835e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

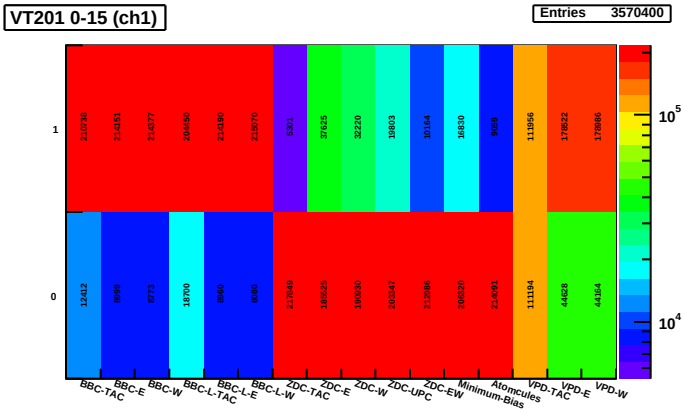

Endcap EMC L0 Input - Patch Sum
Entries 2.00835e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

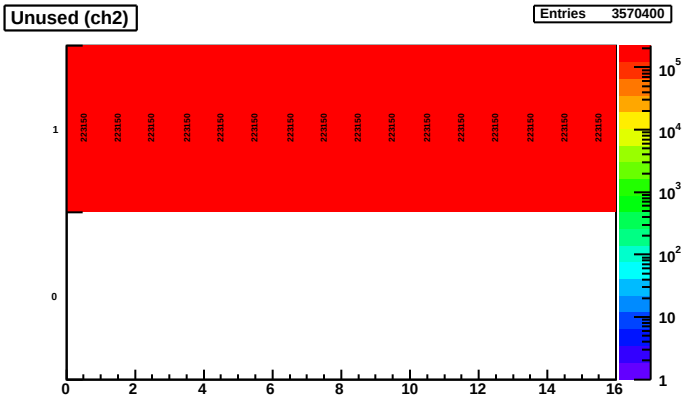

Barrel Jet Patches
Entries 4016700
JP ADC
JP ID

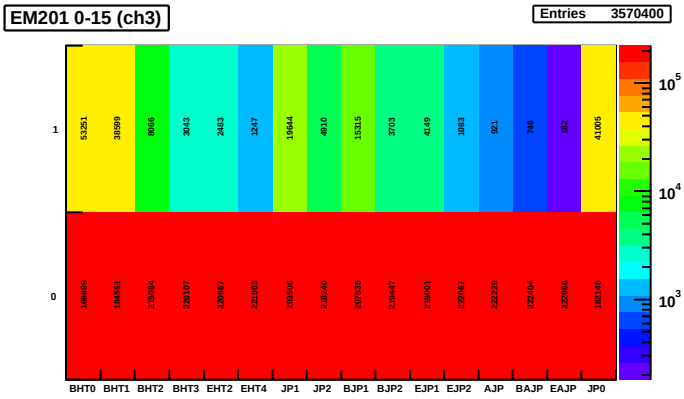

Endcap Jet Patches
Entries 1338900
JP ADC
JP ID

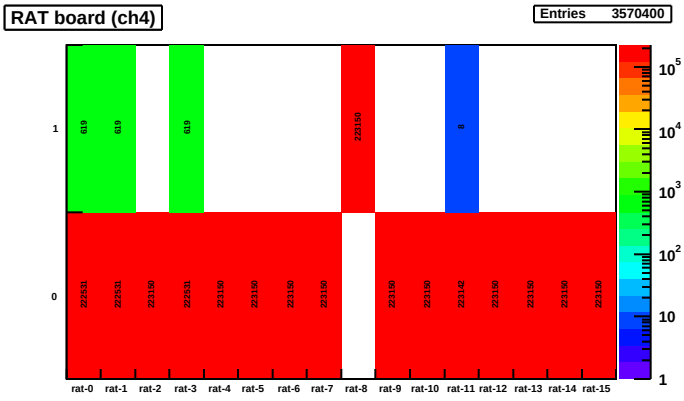

Hybrid Jet Patches
Entries 446300
JP ADC
JP ID

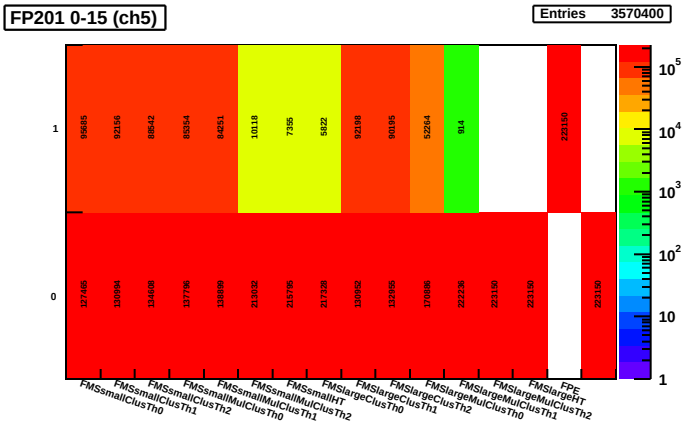

TF201 0-15 (ch0)
Entries 3570400
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

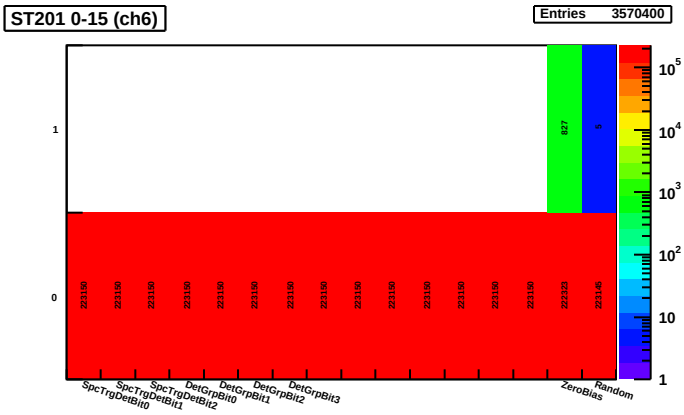

VT201 0-15 (ch1)
Entries 3570400
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

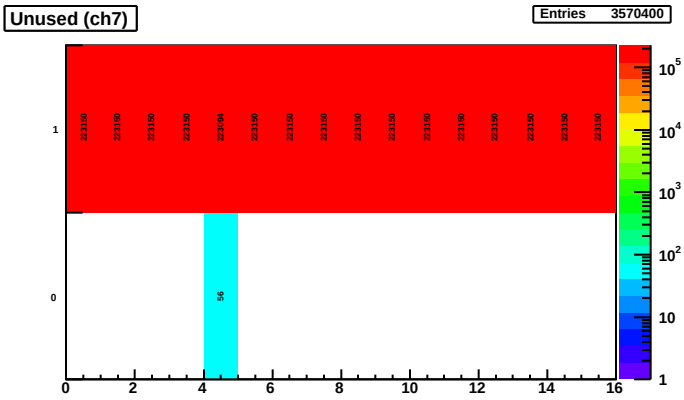

Unused (ch2)
Entries 3570400

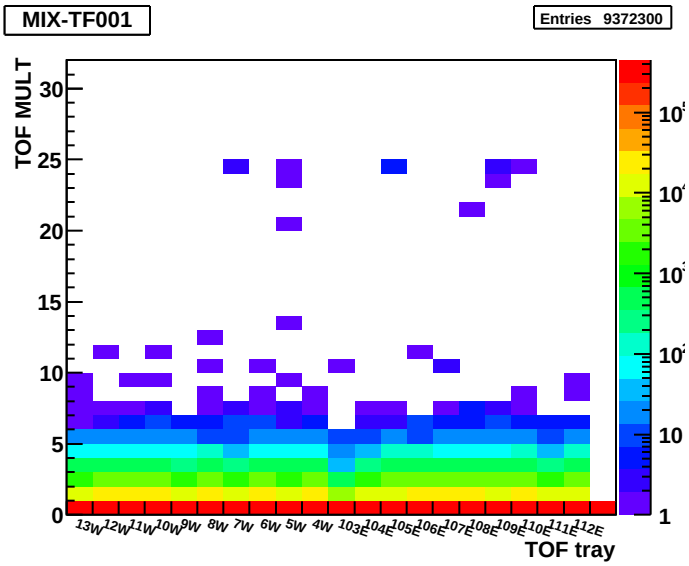

EM201 0-15 (ch3)
Entries 3570400
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

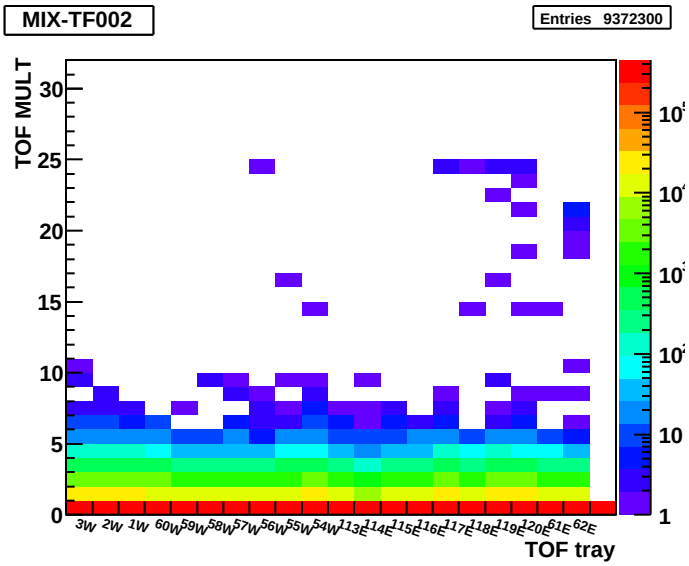

RAT board (ch4)
Entries 3570400
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

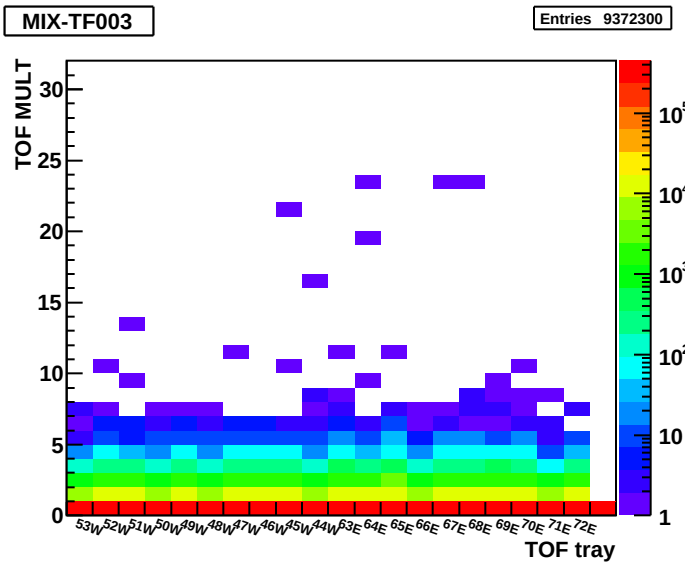

FP201 0-15 (ch5)
Entries 3570400
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

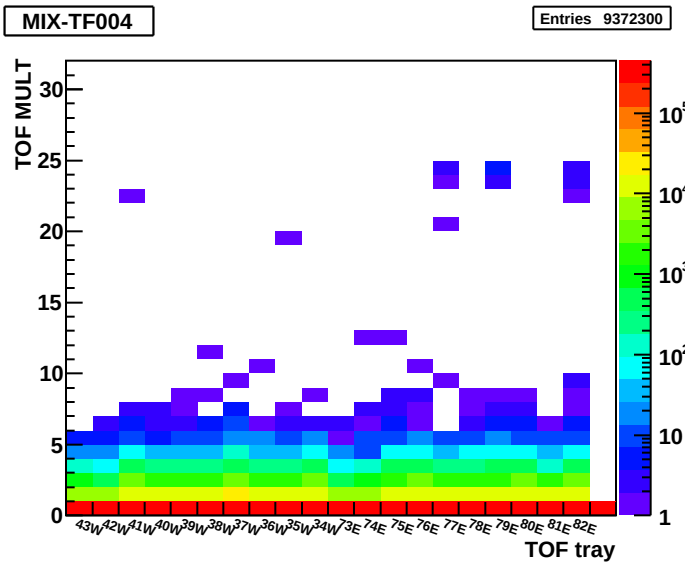

ST201 0-15 (ch6)
Entries 3570400
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

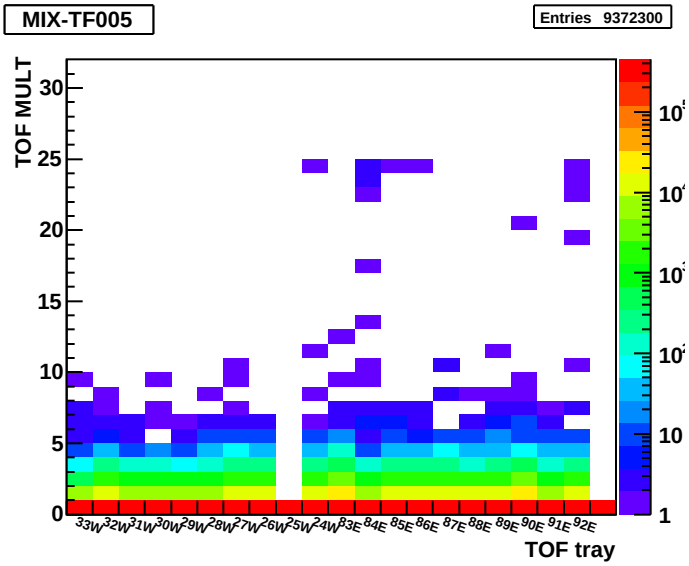

Unused (ch7)
Entries 3570400

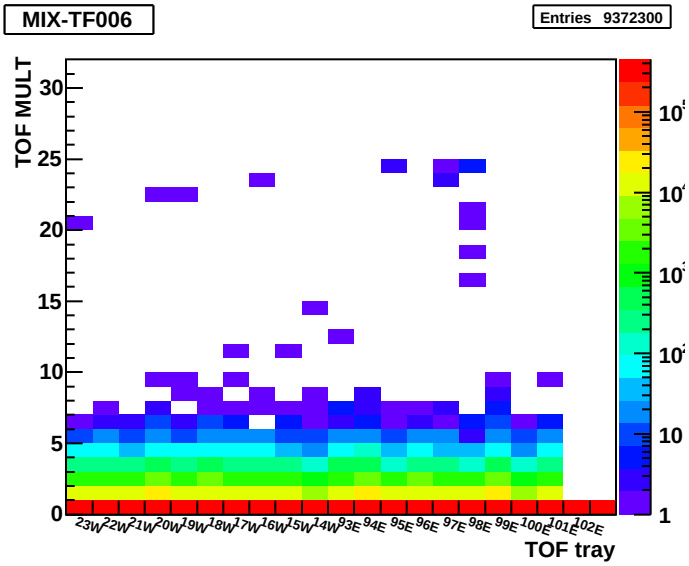

MIX-TF001
Entries 9372300
TOF MULT
TOF tray

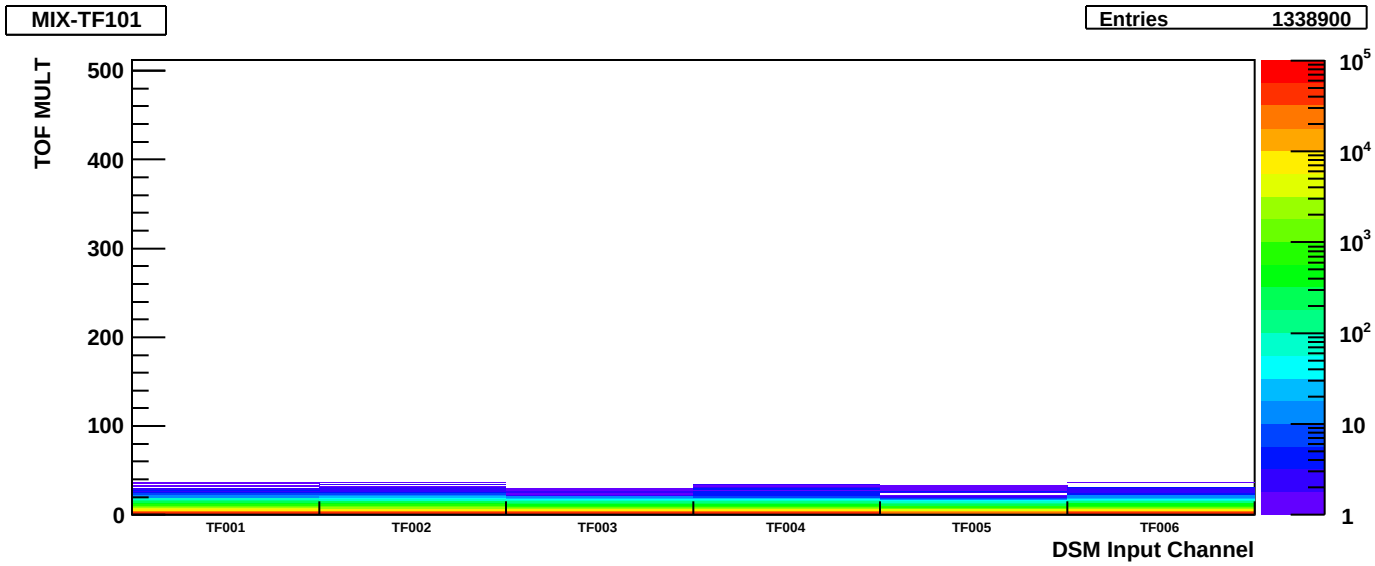

MIX-TF002
Entries 9372300
TOF MULT
TOF tray

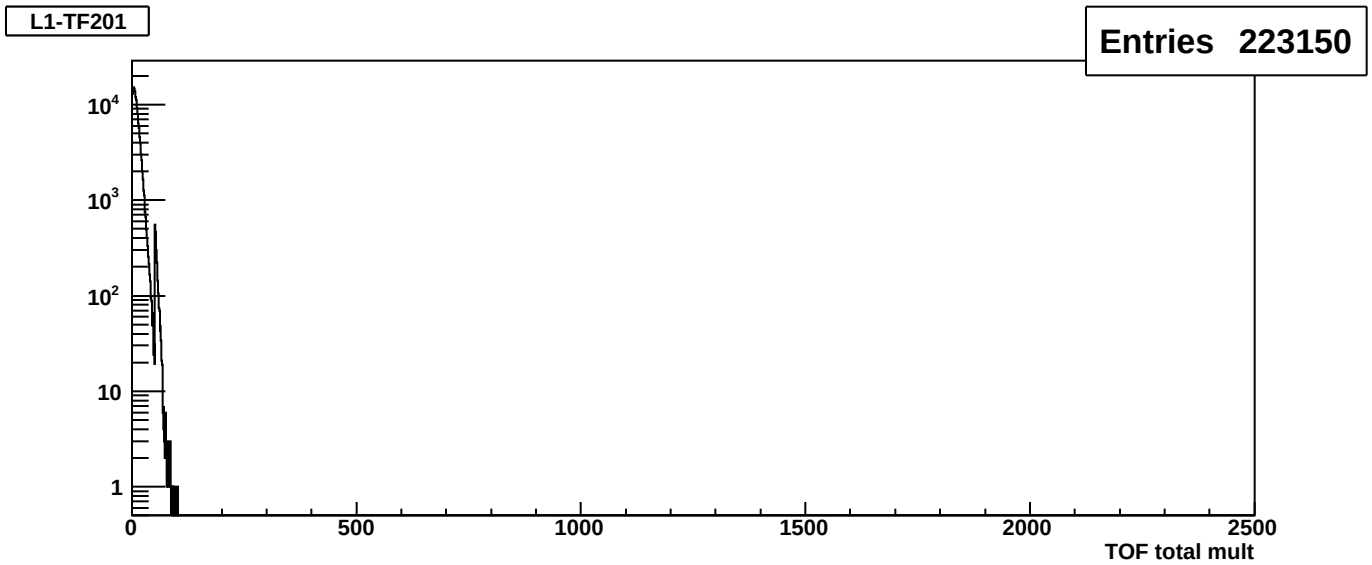

MIX-TF003
Entries 9372300
TOF MULT
TOF tray

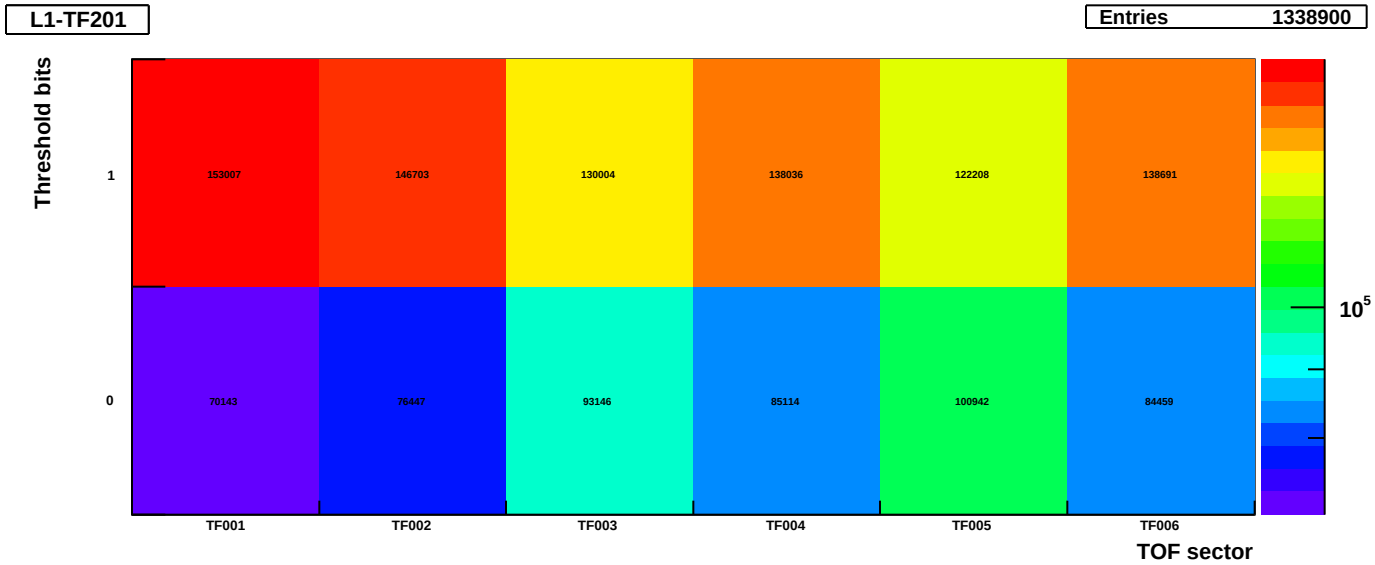

MIX-TF004
Entries 9372300
TOF MULT
TOF tray

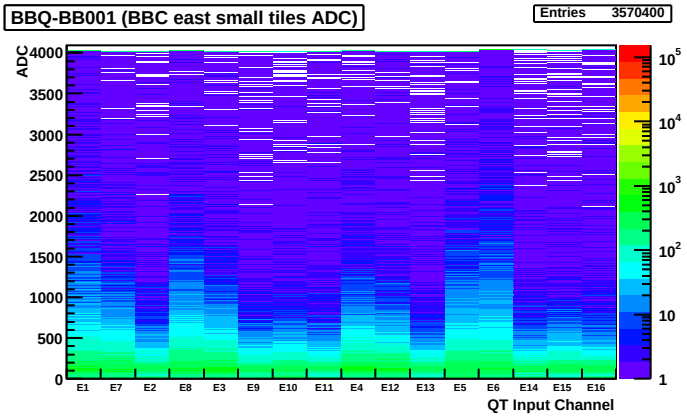

MIX-TF005
Entries 9372300
TOF MULT
TOF tray

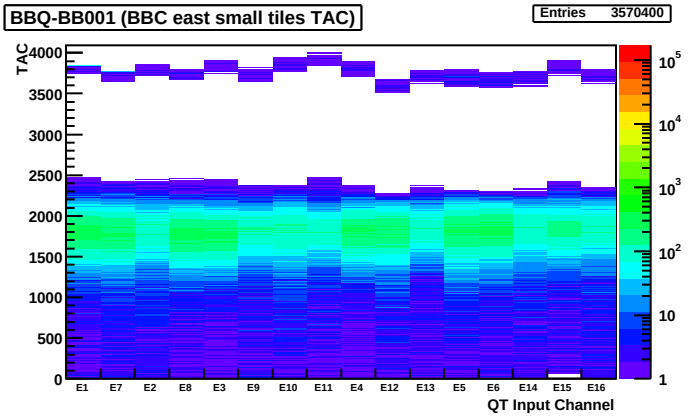

MIX-TF006
Entries 9372300
TOF MULT
TOF tray

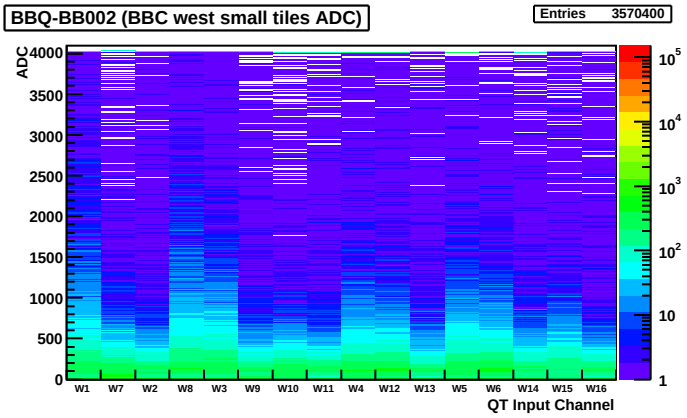

MIX-TF101
Entries 1338900
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

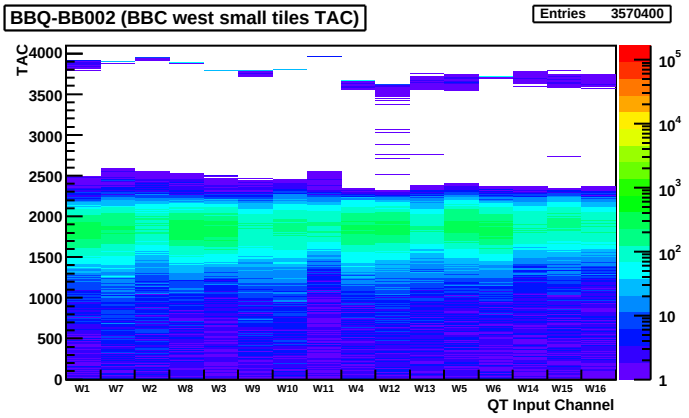

L1-TF201
Entries 223150
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

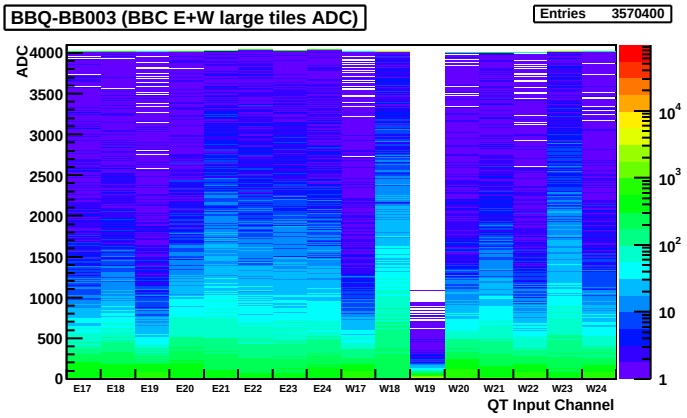

L1-TF201
Entries 1338900
Threshold bits
1
0
153007
146703
130004
138036
122208
138691
70143
76447
93146
85114
100942
84459
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

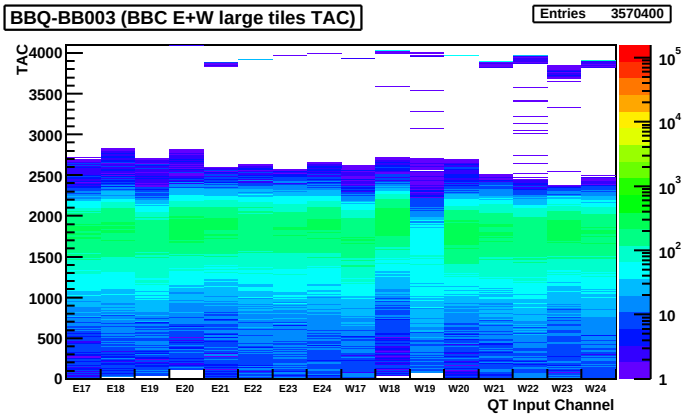

BBQ-BB001 (BBC east small tiles ADC)
Entries 3570400
ADC
QT Input Channel

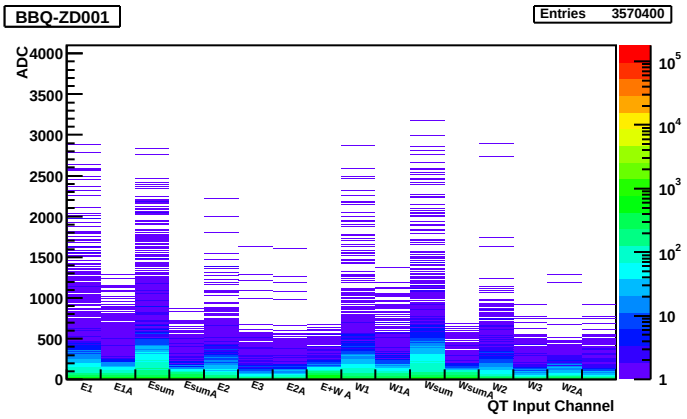

BBQ-BB001 (BBC east small tiles TAC)
Entries 3570400
TAC
QT Input Channel

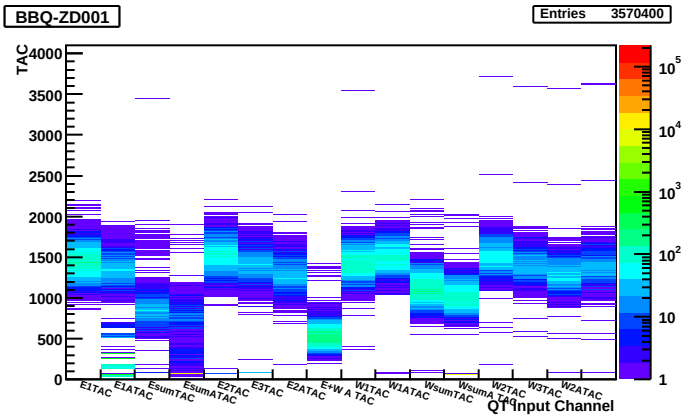

BBQ-BB002 (BBC west small tiles ADC)
Entries 3570400
ADC
QT Input Channel

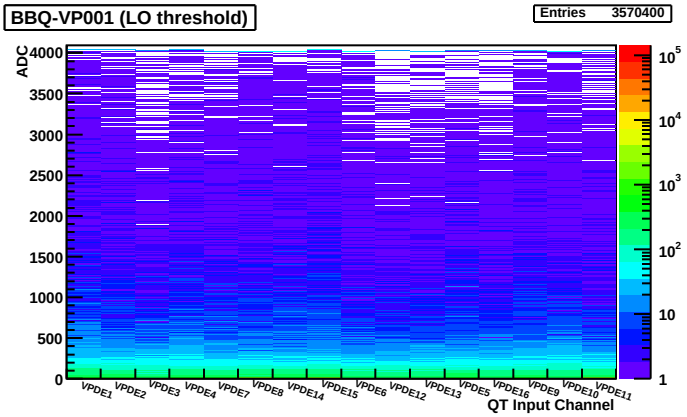

BBQ-BB002 (BBC west small tiles TAC)
Entries 3570400
TAC
QT Input Channel

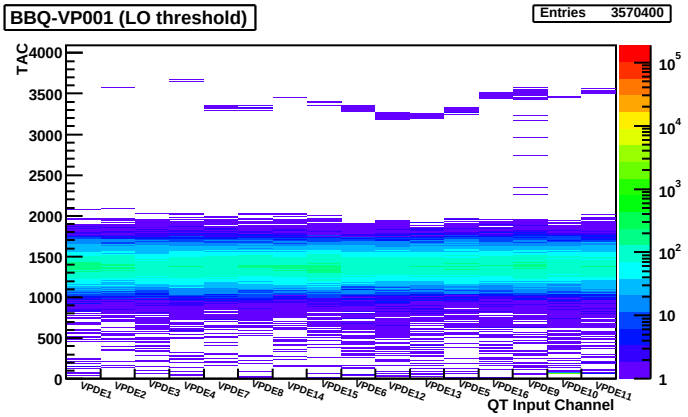

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3570400
ADC
QT Input Channel

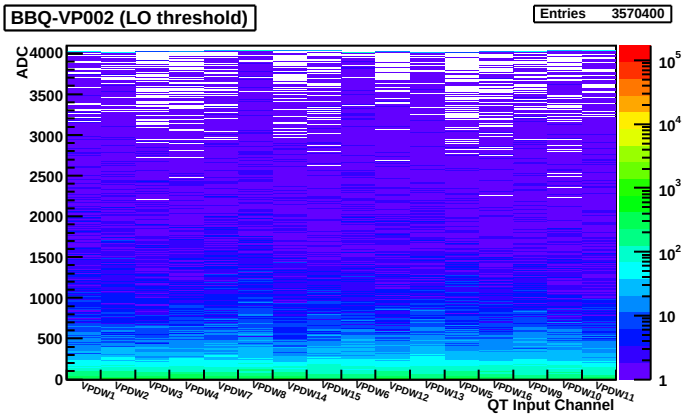

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3570400
TAC
QT Input Channel

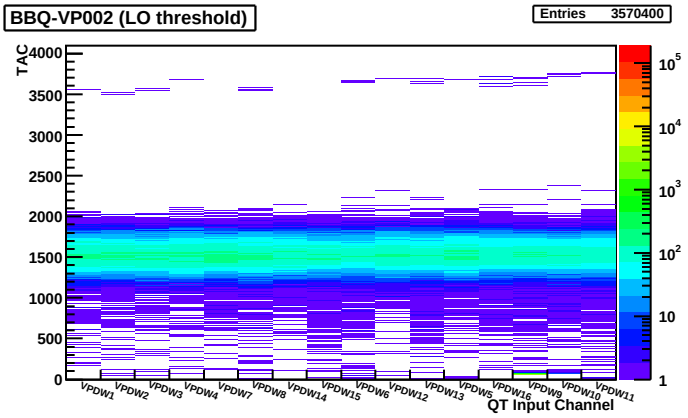

BBQ-ZD001
Entries 3570400
ADC
QT Input Channel

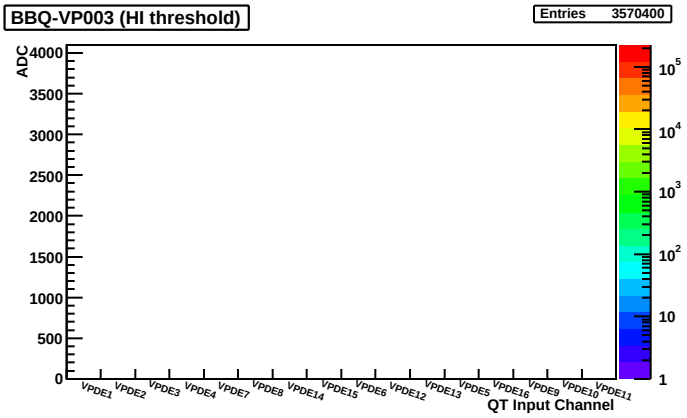

BBQ-ZD001
Entries 3570400
TAC
QT Input Channel

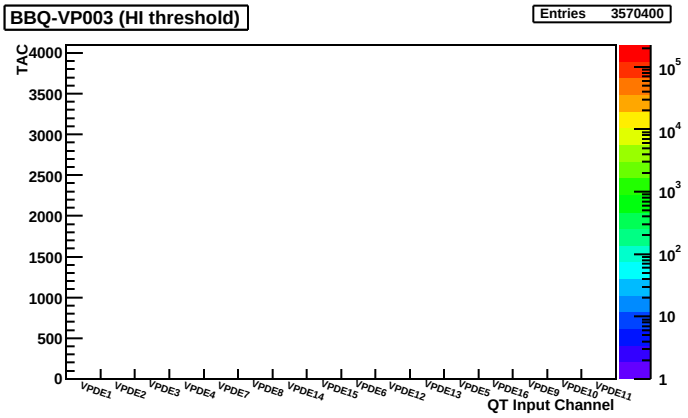

BBQ-VP001 (LO threshold)
Entries 3570400
ADC
QT Input Channel

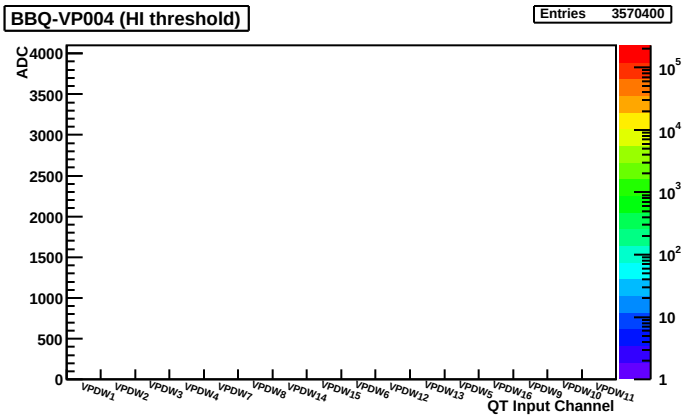

BBQ-VP001 (LO threshold)
Entries 3570400
TAC
QT Input Channel

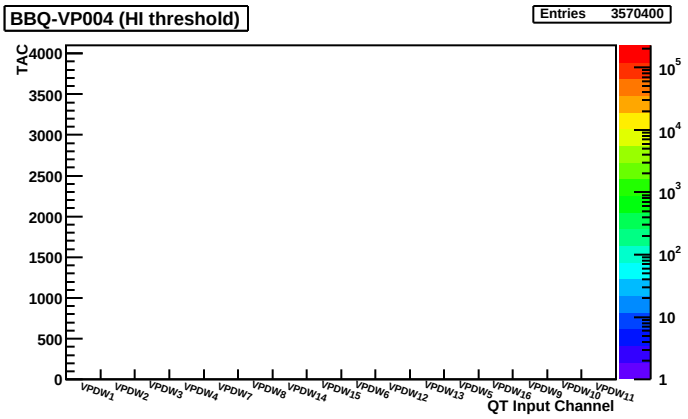

BBQ-VP002 (LO threshold)
Entries 3570400
ADC
QT Input Channel

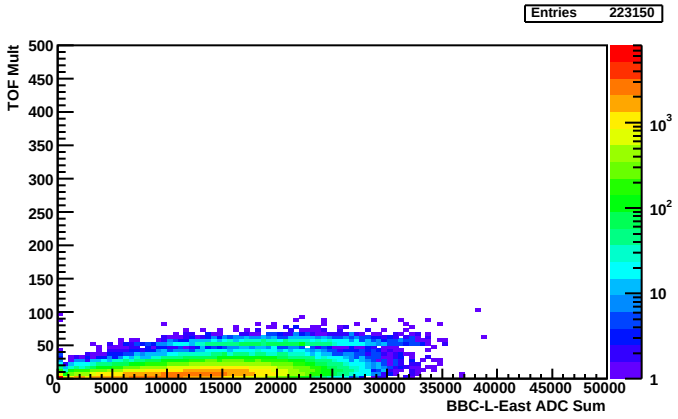

BBQ-VP002 (LO threshold)
Entries 3570400
TAC
QT Input Channel

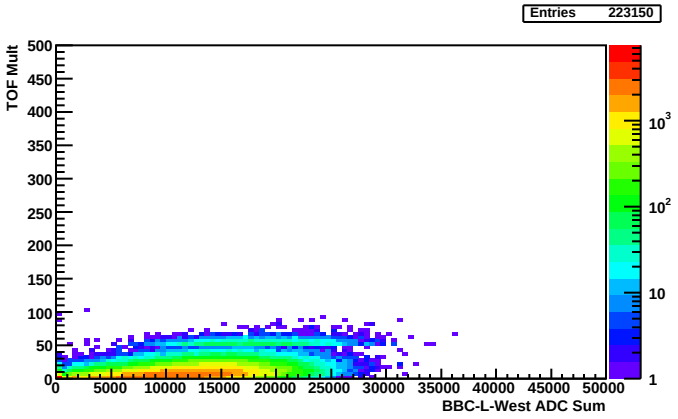

BBQ-VP003 (HI threshold)
Entries 3570400
ADC
QT Input Channel

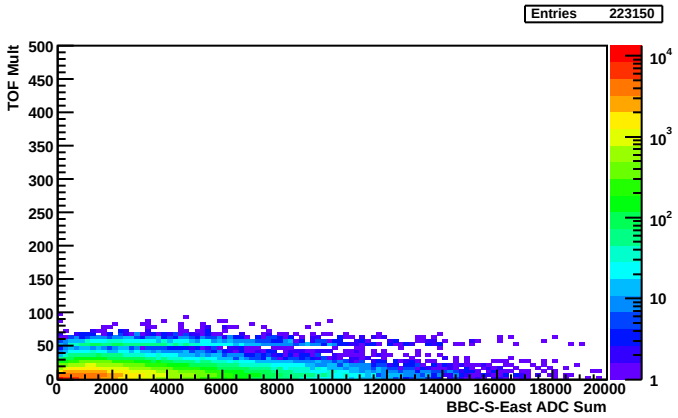

BBQ-VP003 (HI threshold)
Entries 3570400
TAC
QT Input Channel

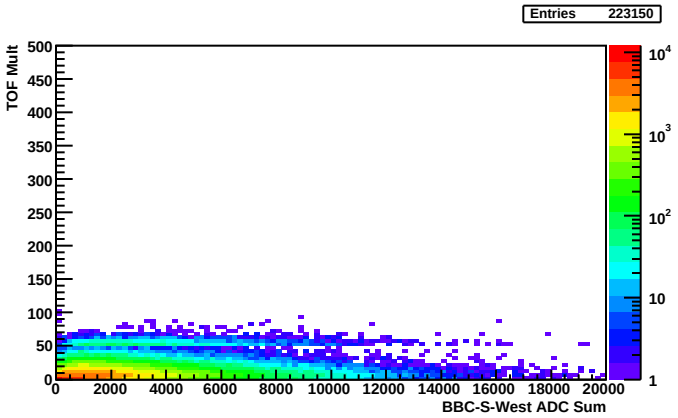

BBQ-VP004 (HI threshold)
Entries 3570400
ADC
QT Input Channel

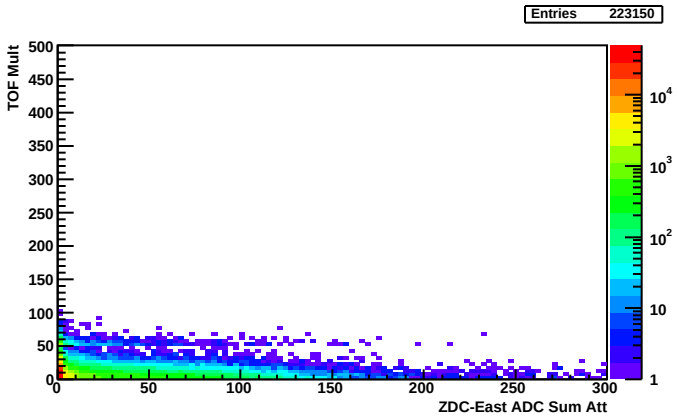

BBQ-VP004 (HI threshold)
Entries 3570400
TAC
QT Input Channel

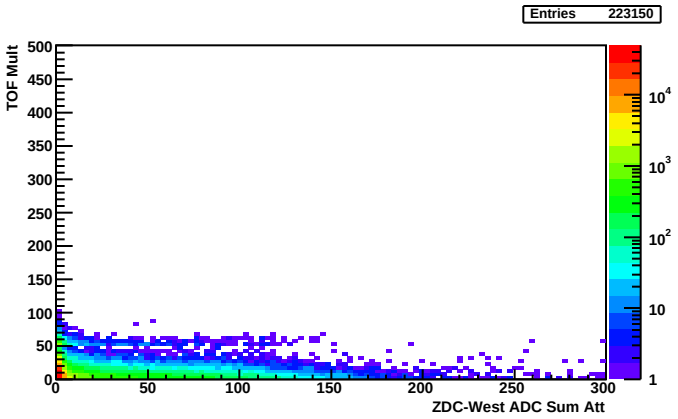

Entries 223150
TOF Mult
BBC-L-East ADC Sum

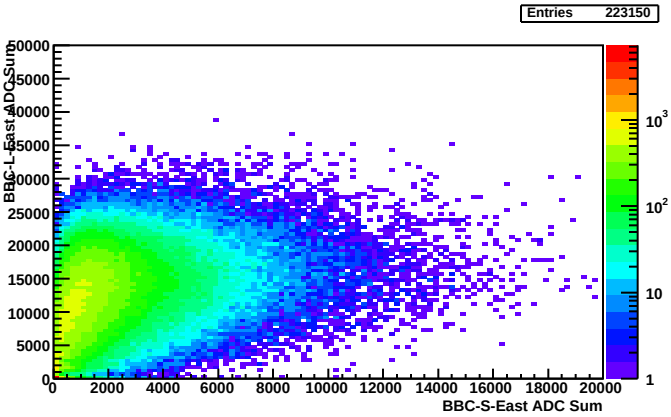

Entries 223150
TOF Mult
BBC-L-West ADC Sum

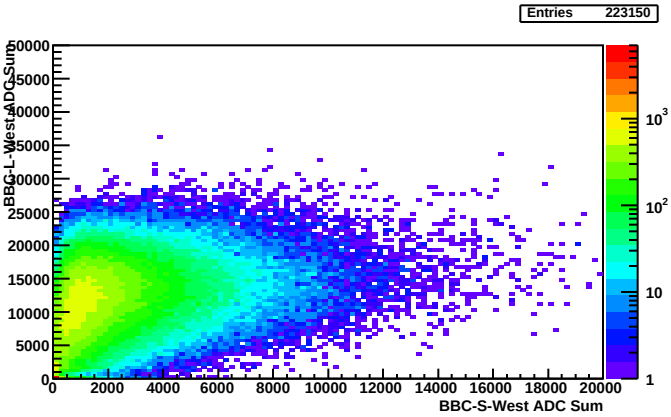

Entries 223150
TOF Mult
BBC-S-East ADC Sum

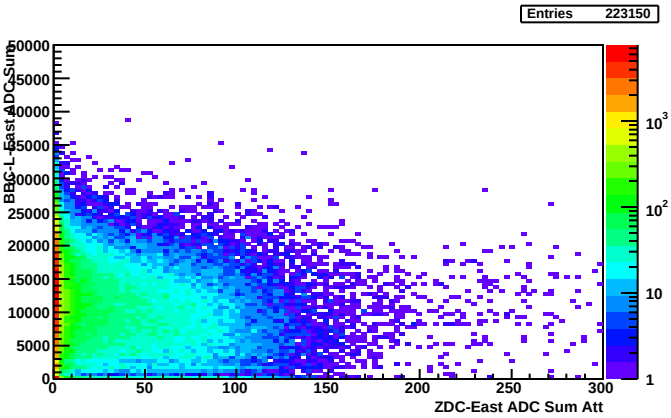

Entries 223150
TOF Mult
BBC-S-West ADC Sum

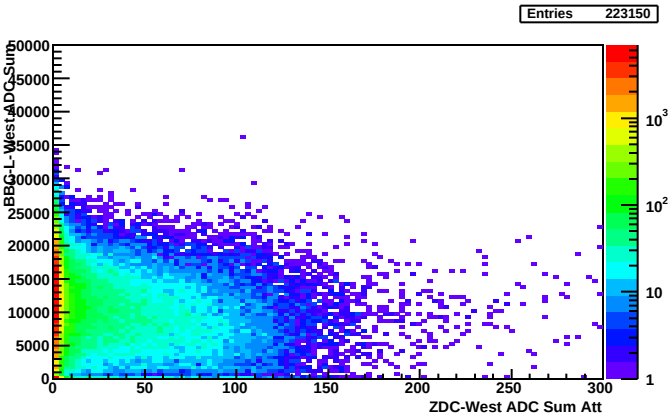

Entries 223150
TOF Mult
ZDC-East ADC Sum Att

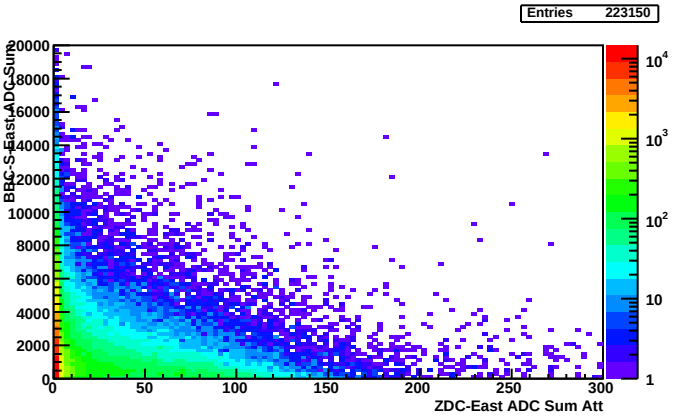

Entries 223150
TOF Mult
ZDC-West ADC Sum Att

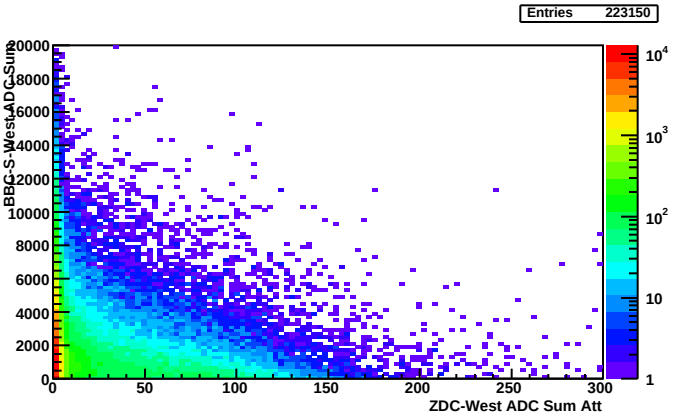

Entries 223150
BBC-L-East ADC Sum
BBC-S-East ADC Sum

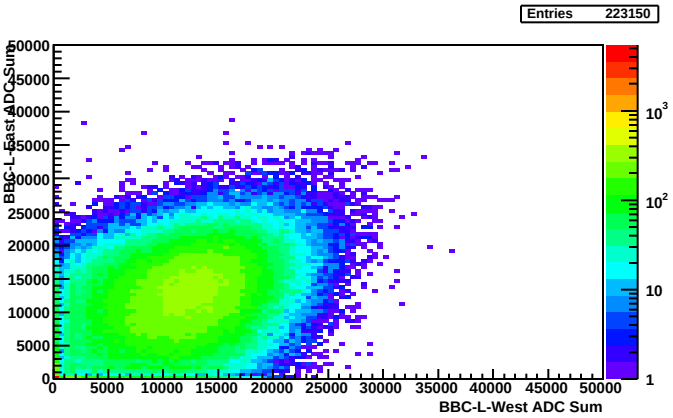

Entries 223150
BBC-L-West ADC Sum
BBC-S-West ADC Sum

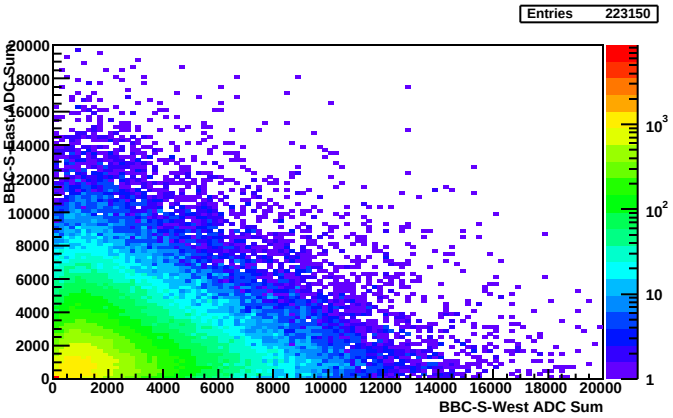

Entries 223150
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

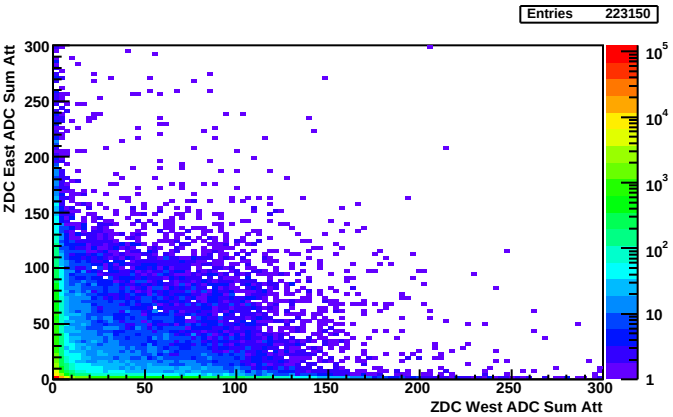

Entries 223150
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

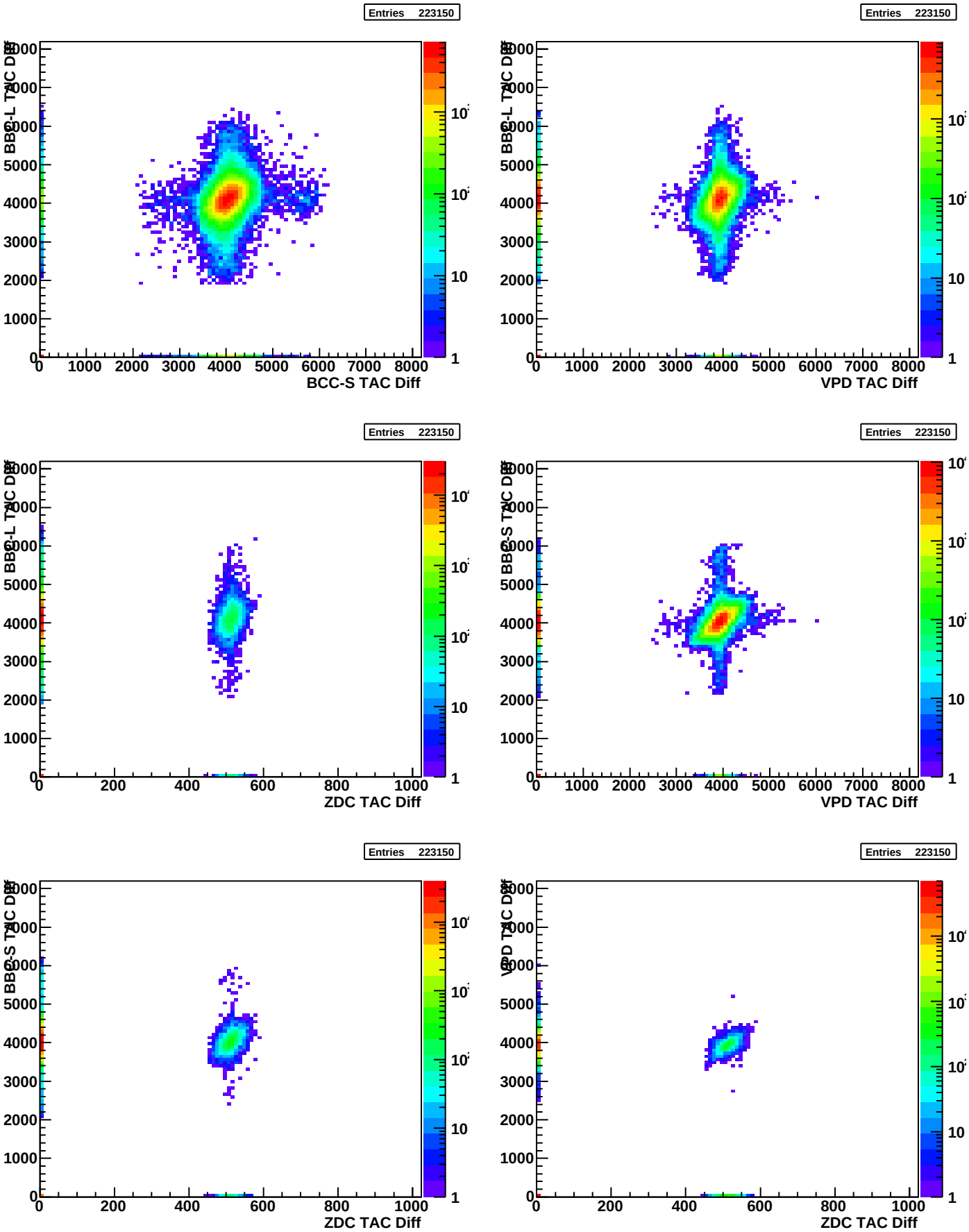

Entries 223150
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

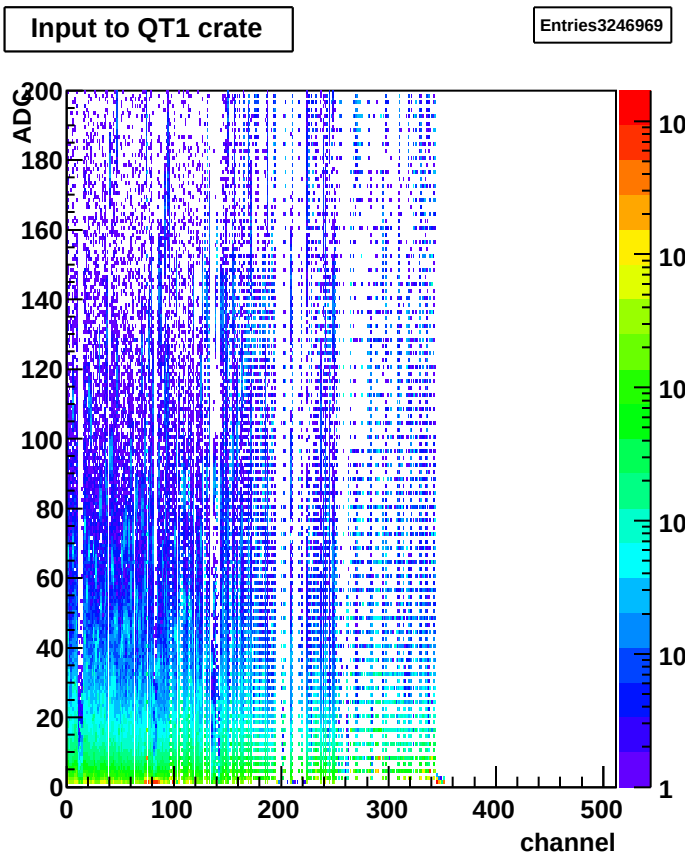

Entries 223150
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

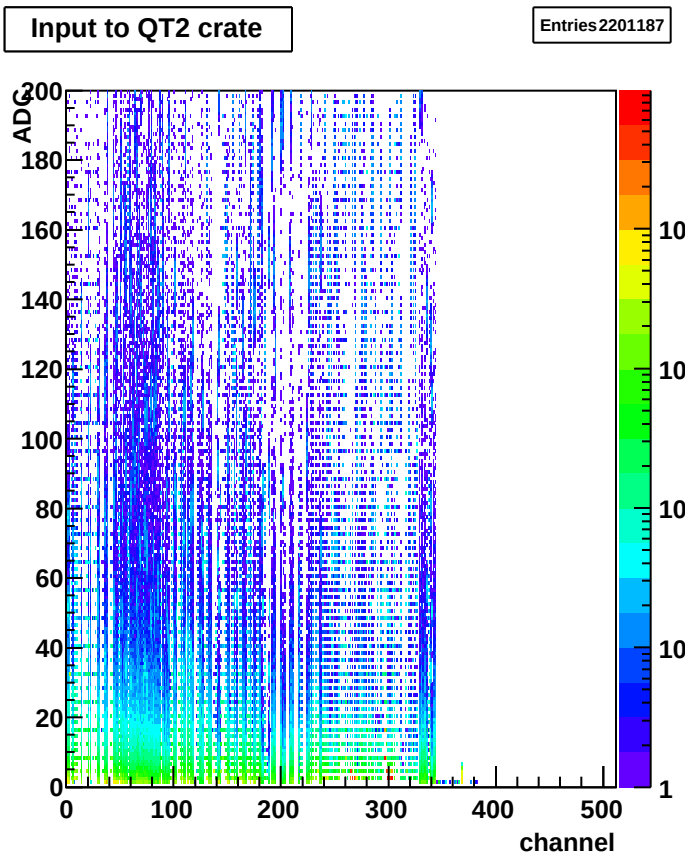

Entries 223150
BBC-L-East ADC Sum
BBC-L-West ADC Sum

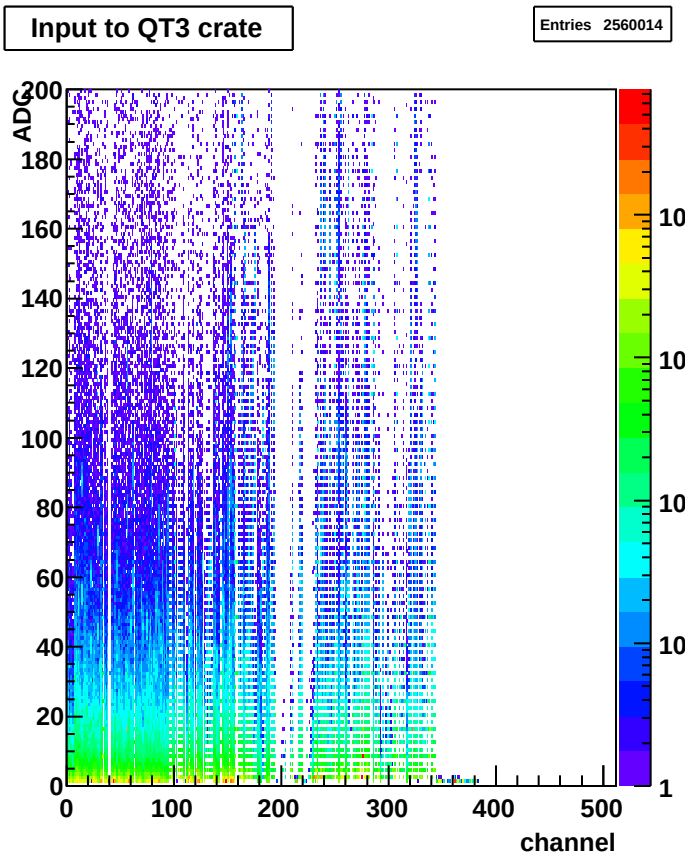

Entries 223150
BBC-S-East ADC Sum
BBC-S-West ADC Sum

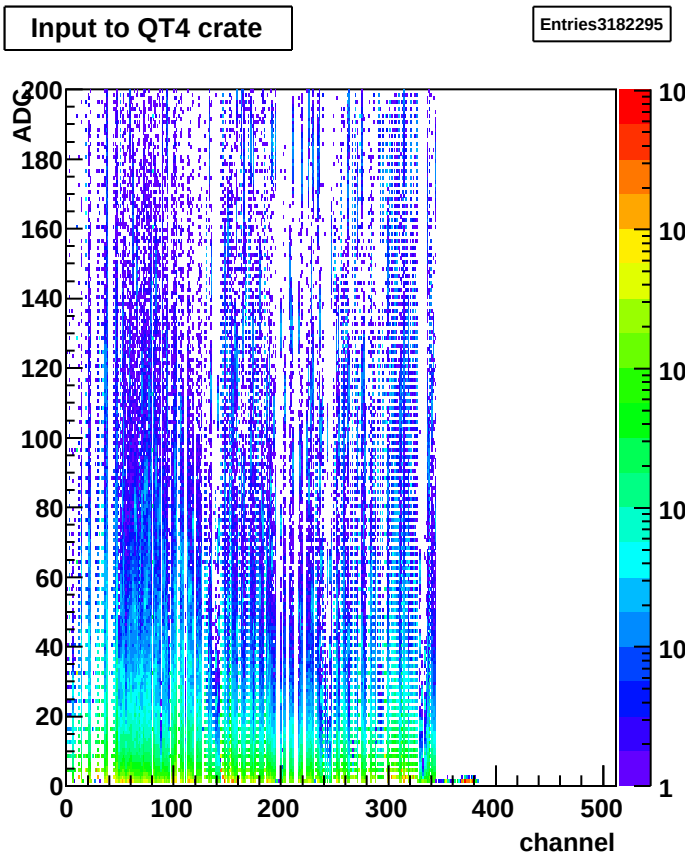

Entries 223150
ZDC East ADC Sum Att
ZDC West ADC Sum Att

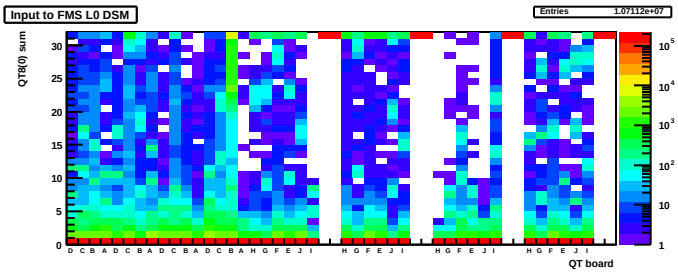

Entries 223150
BBC-L TAC Diff
BCC-S TAC Diff
Entries 223150
BBC-L TAC Diff
VPD TAC Diff
Entries 223150
BBC-L TAC Diff
ZDC TAC Diff
Entries 223150
BBC-S TAC Diff
VPD TAC Diff
Entries 223150
BBC-S TAC Diff
ZDC TAC Diff
Entries 223150
VPD TAC Diff
ZDC TAC Diff

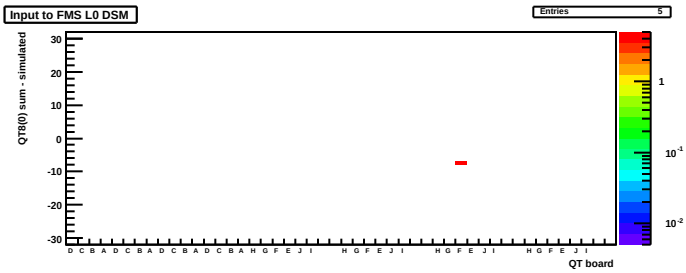

Input to QT1 crate
Entries3246969
ADC
channel

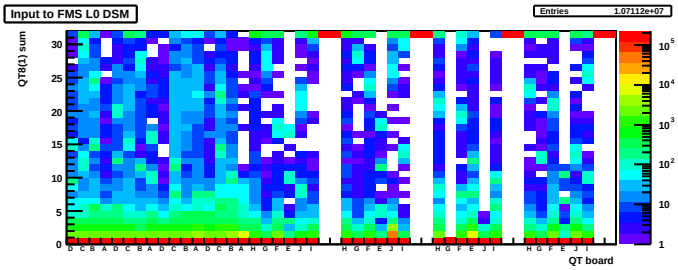

Input to QT2 crate
Entries2201187
ADC
channel

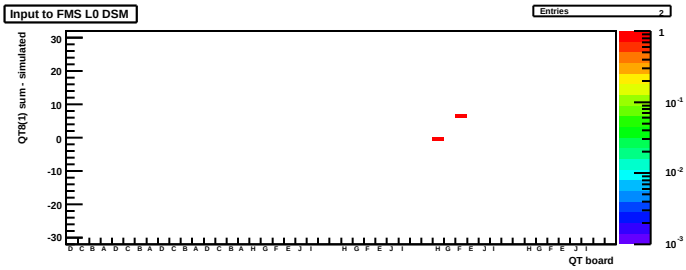

Input to QT3 crate
Entries 2560014
ADC
channel

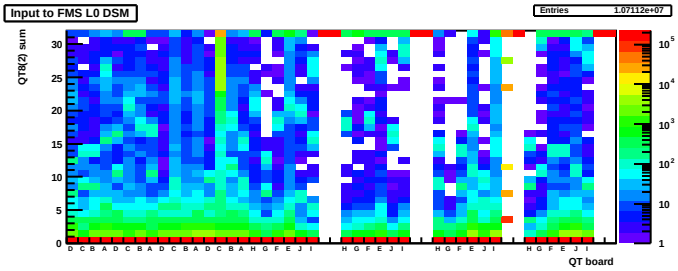

Input to QT4 crate
Entries3182295
ADC
channel

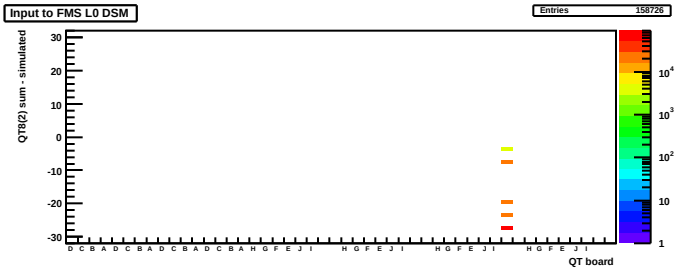

Input to FMS L0 DSM
Entries 1.07112e+07
QT8(0) sum
QT board

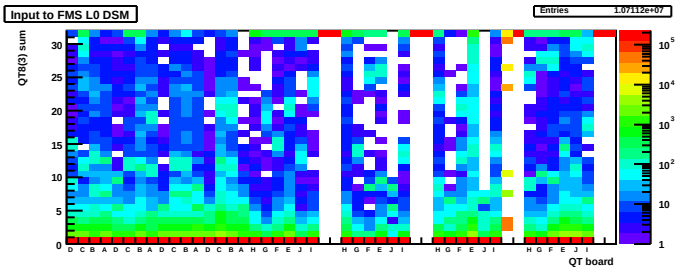

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

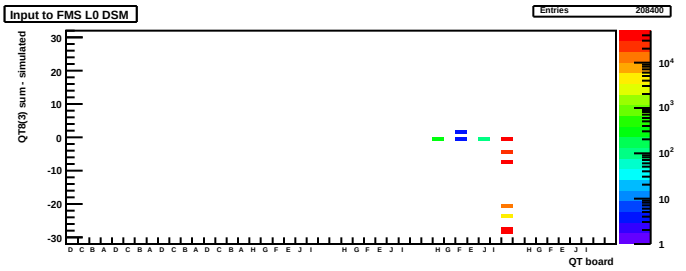

Input to FMS L0 DSM
Entries 1.07112e+07
QT8(1) sum
QT board

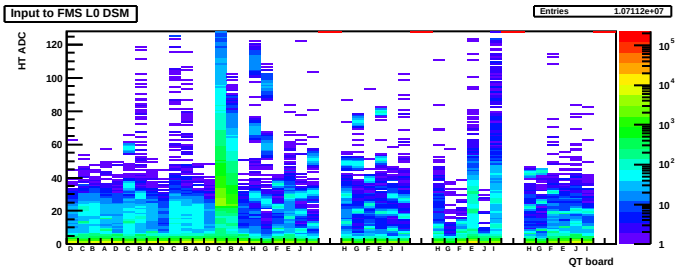

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

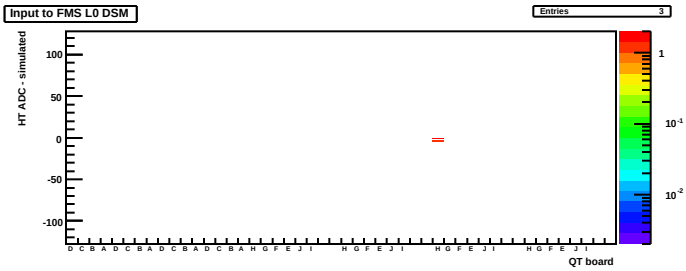

Input to FMS L0 DSM
Entries 1.07112e+07
QT8(2) sum
QT board

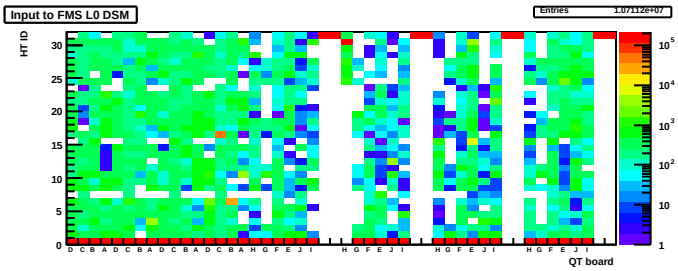

Input to FMS L0 DSM
Entries 158726
QT8(2) sum - simulated
QT board

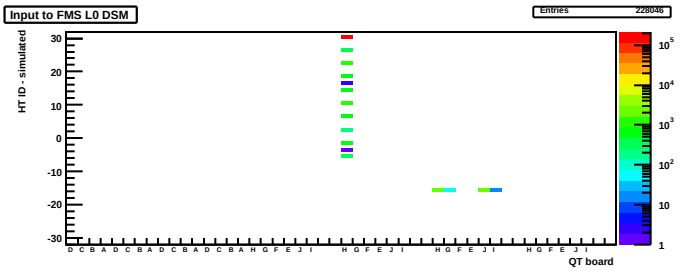

Input to FMS L0 DSM
Entries 1.07112e+07
QT8(3) sum
QT board

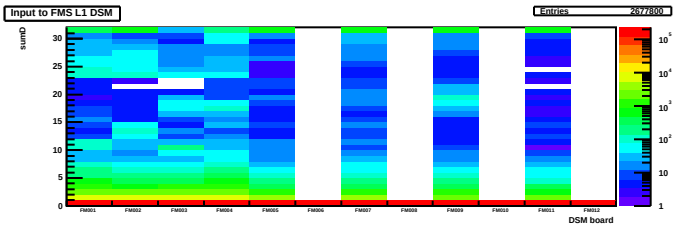

Input to FMS L0 DSM
Entries 208400
QT8(3) sum - simulated
QT board

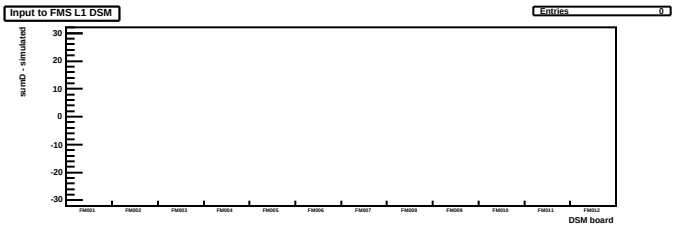

Input to FMS L0 DSM
Entries 1.07112e+07
HT ADC
QT board

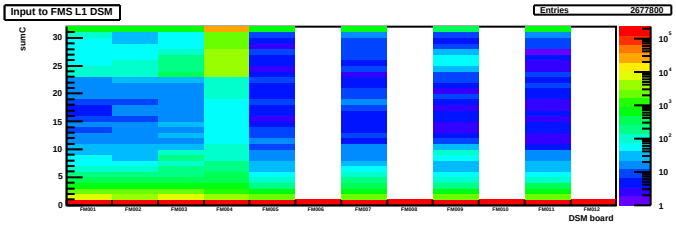

Input to FMS L0 DSM
Entries 3
HT ADC - simulated
QT board

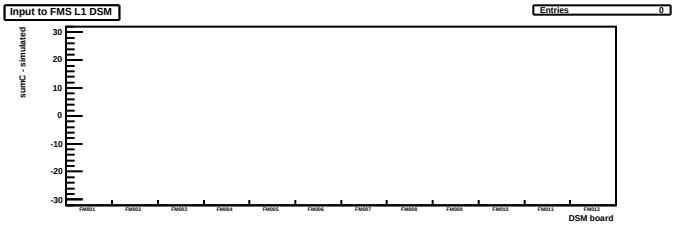

Input to FMS L0 DSM
Entries 1.07112e+07
HT ID
QT board

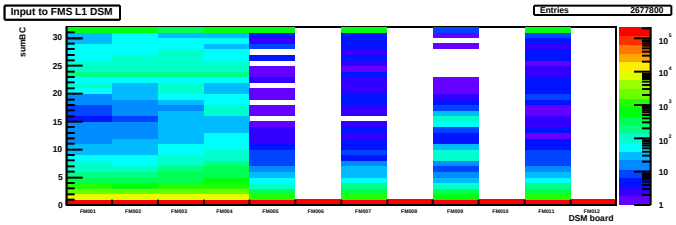

Input to FMS L0 DSM
Entries 228046
HT ID - simulated
QT board

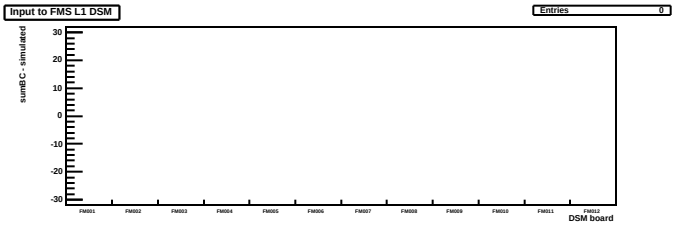

Input to FMS L1 DSM
Entries 2677800
sumD
DSM board

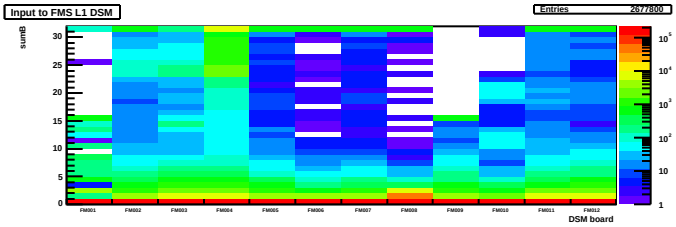

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

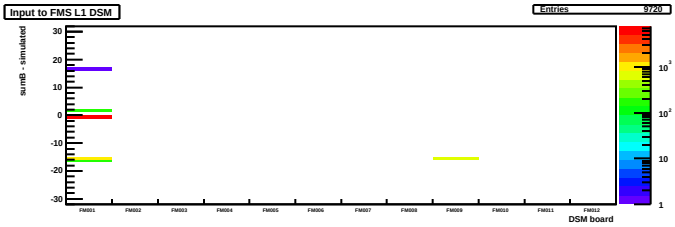

Input to FMS L1 DSM
Entries 2677800
sumC
DSM board

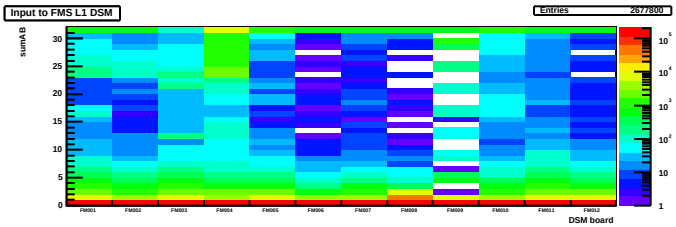

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

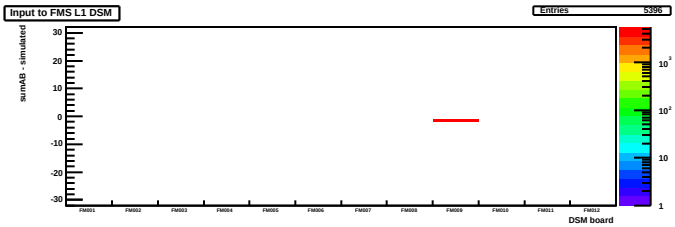

Input to FMS L1 DSM
Entries 2677800
sumBC
DSM board

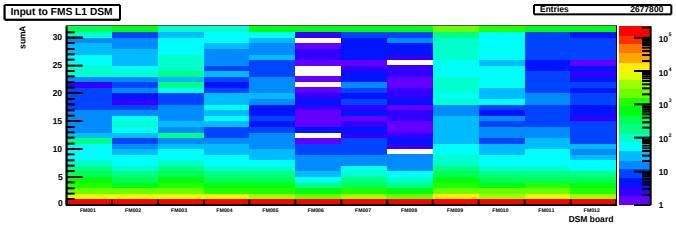

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

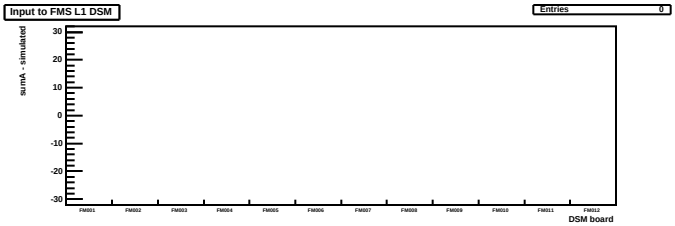

Input to FMS L1 DSM
Entries 2677800
sumB
DSM board

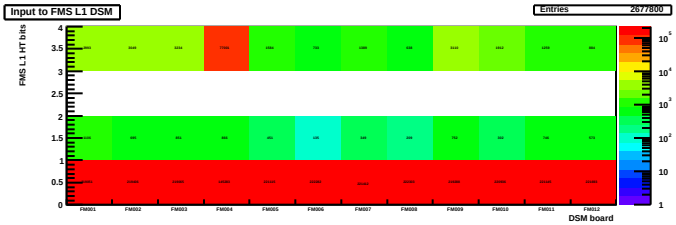

Input to FMS L1 DSM
Entries 9720
sumB - simulated
DSM board

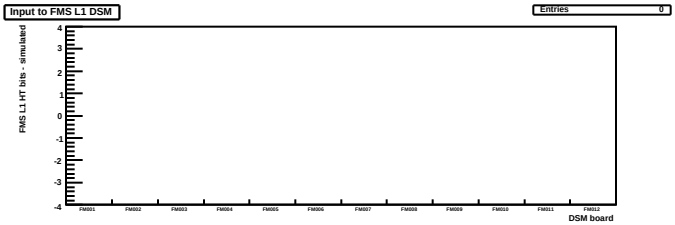

Input to FMS L1 DSM
Entries 2677800
sumAB
DSM board

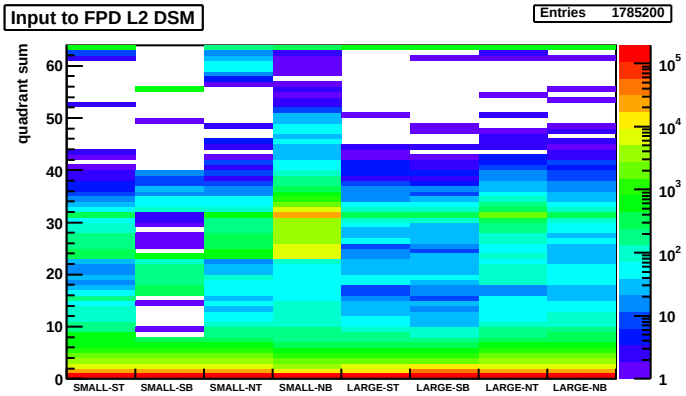

Input to FMS L1 DSM
Entries 5396
sumAB - simulated
DSM board

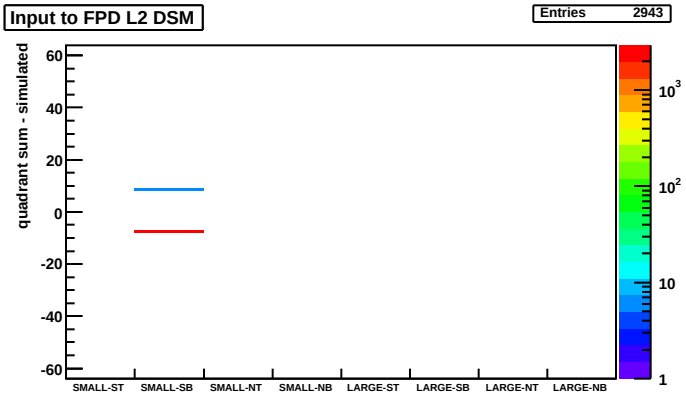

Input to FMS L1 DSM
Entries 2677800
sumA
DSM board

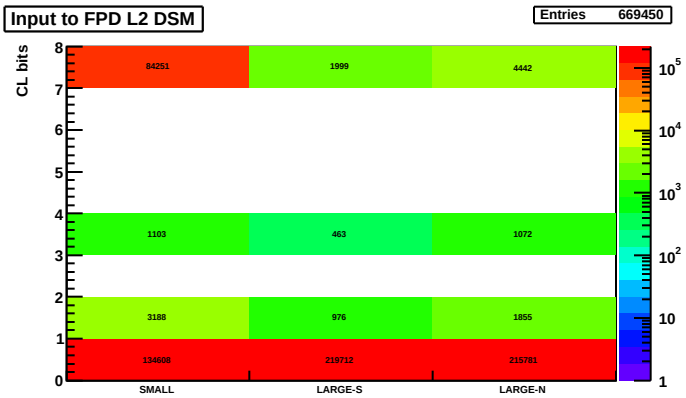

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

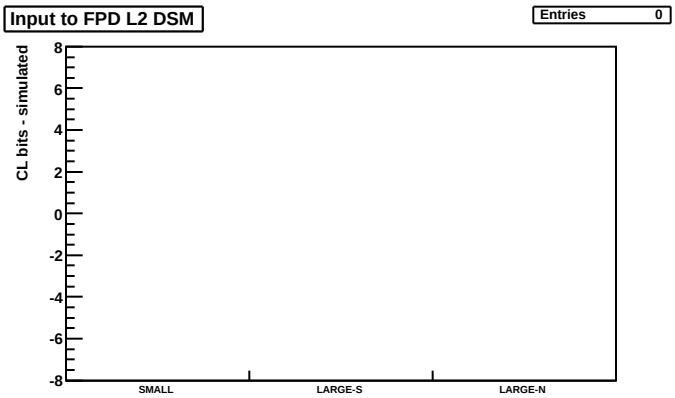

Input to FMS L1 DSM
Entries 2677800
FMS L1 HT bits
DSM board

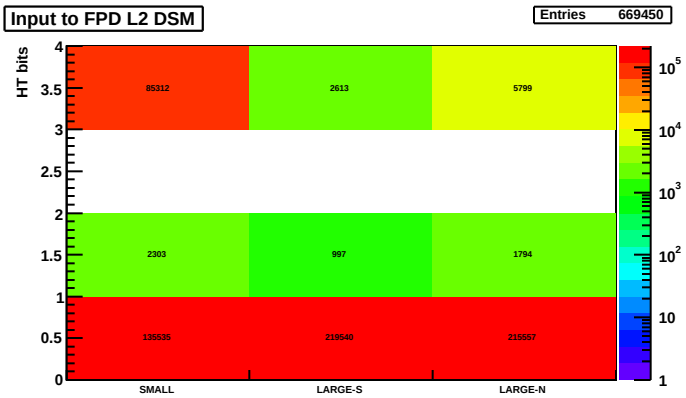

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

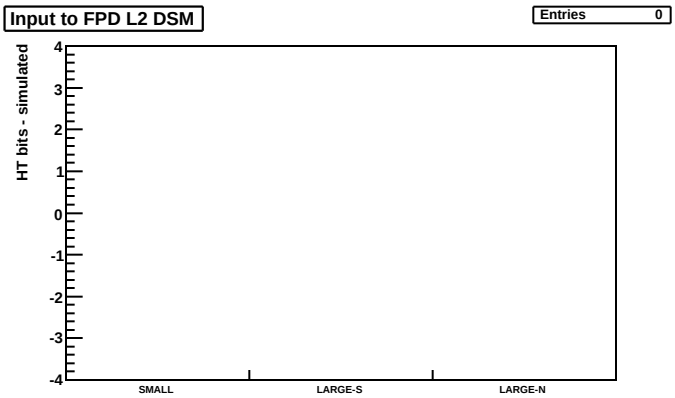

Input to FPD L2 DSM
Entries 1785200
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

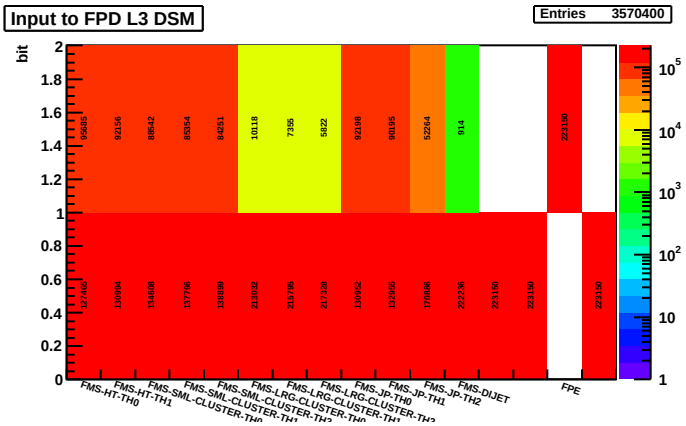

Input to FPD L2 DSM
Entries 2943
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

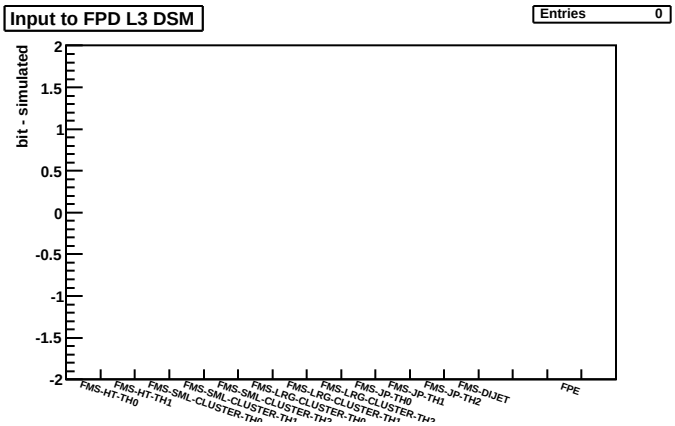

Input to FPD L2 DSM
Entries 669450
CL bits
84251 1999 4442
1103 463 1072
3188 976 1855
134608 219712 215781
SMALL LARGE-S LARGE-N

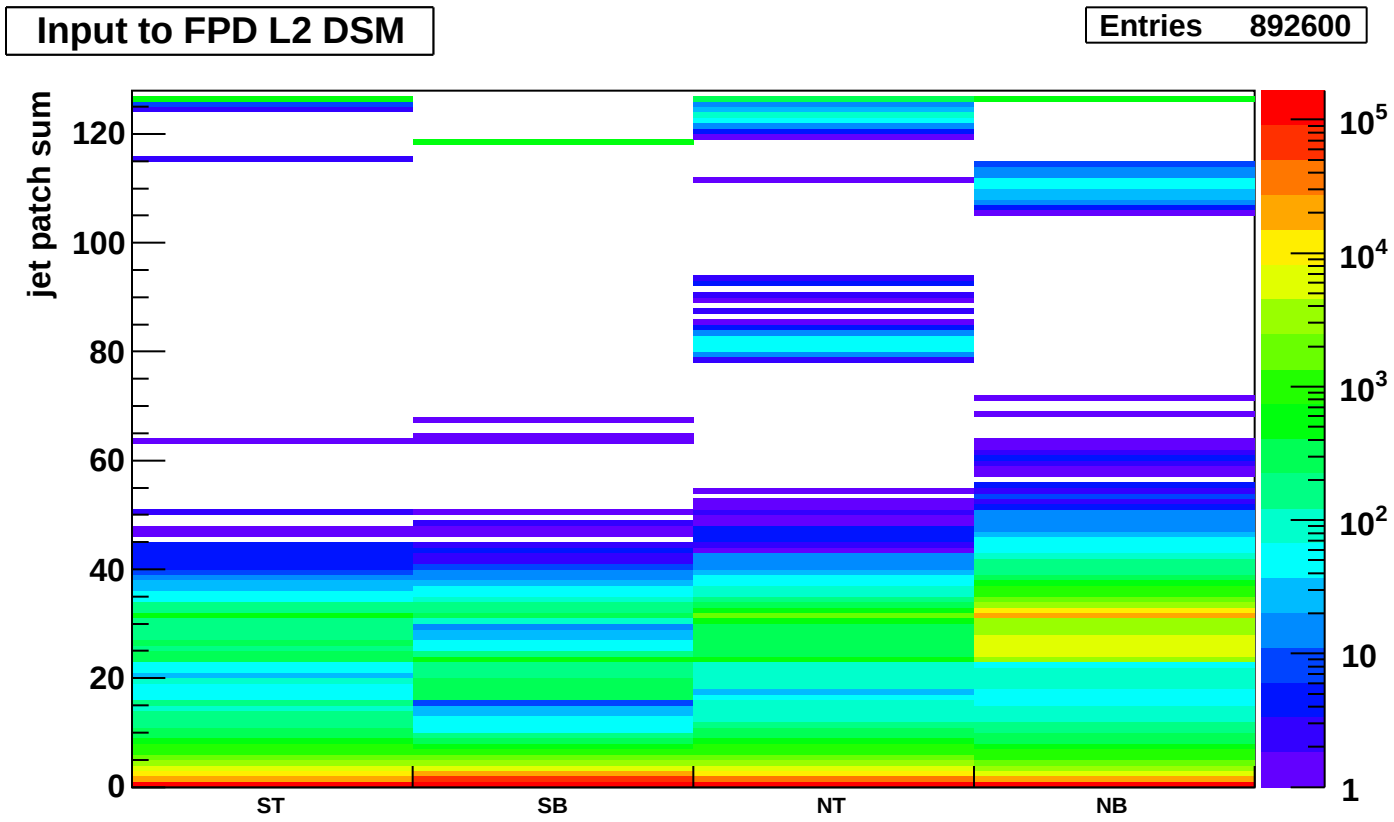

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

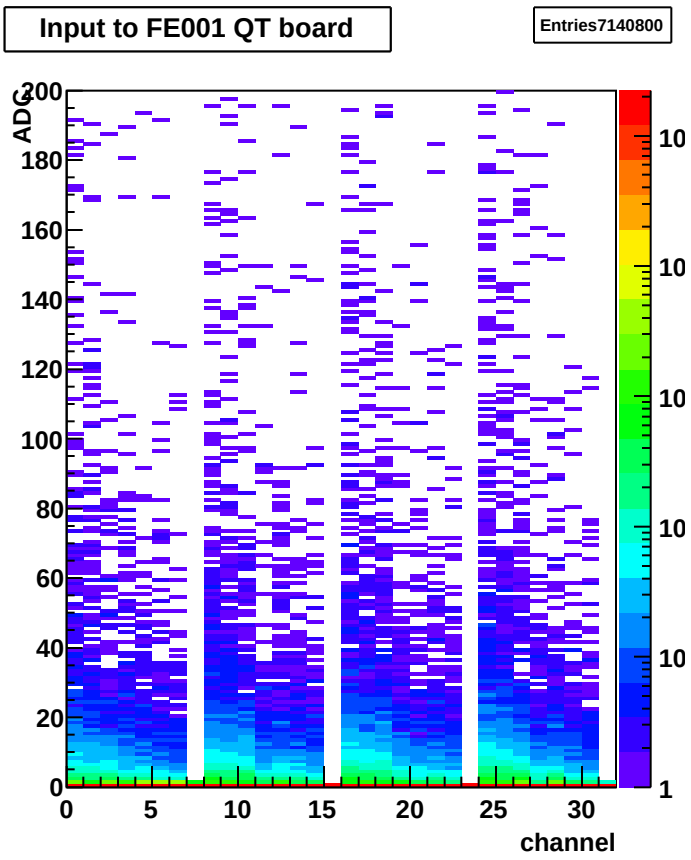

Input to FPD L2 DSM
Entries 669450
HT bits
85312 2613 5799
2303 997 1794
135535 219540 215557
SMALL LARGE-S LARGE-N

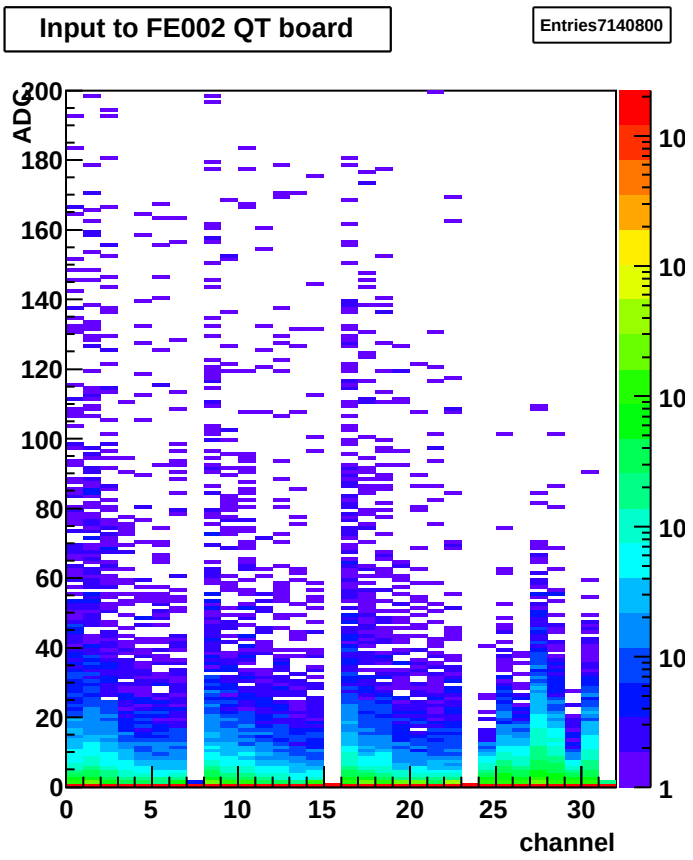

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

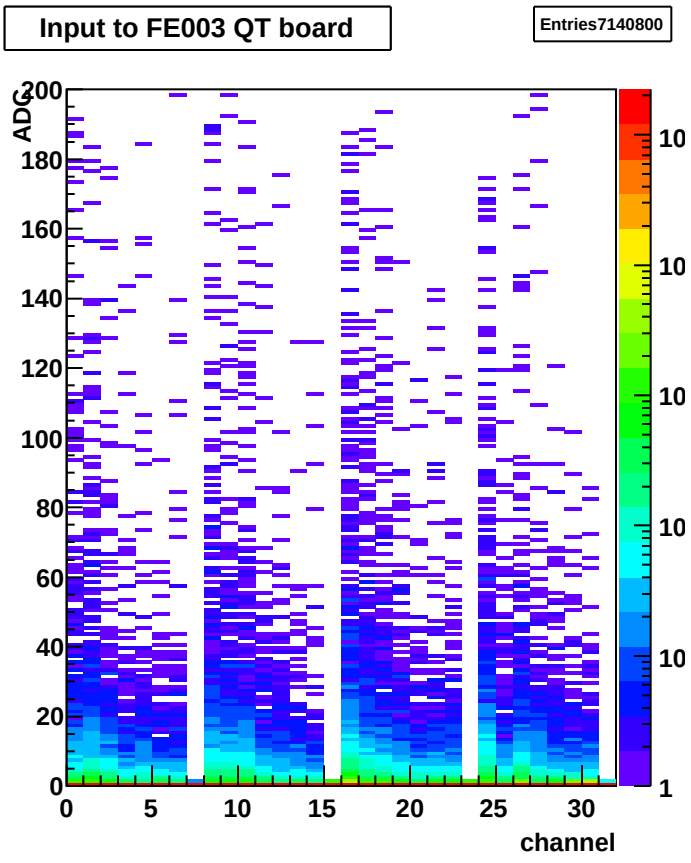

Input to FPD L3 DSM
Entries 3570400
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

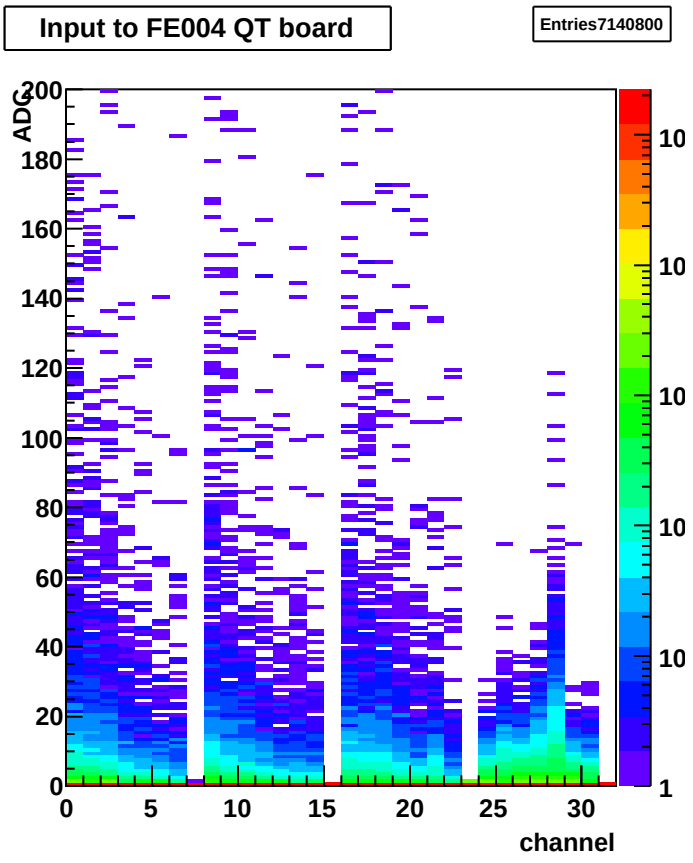

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

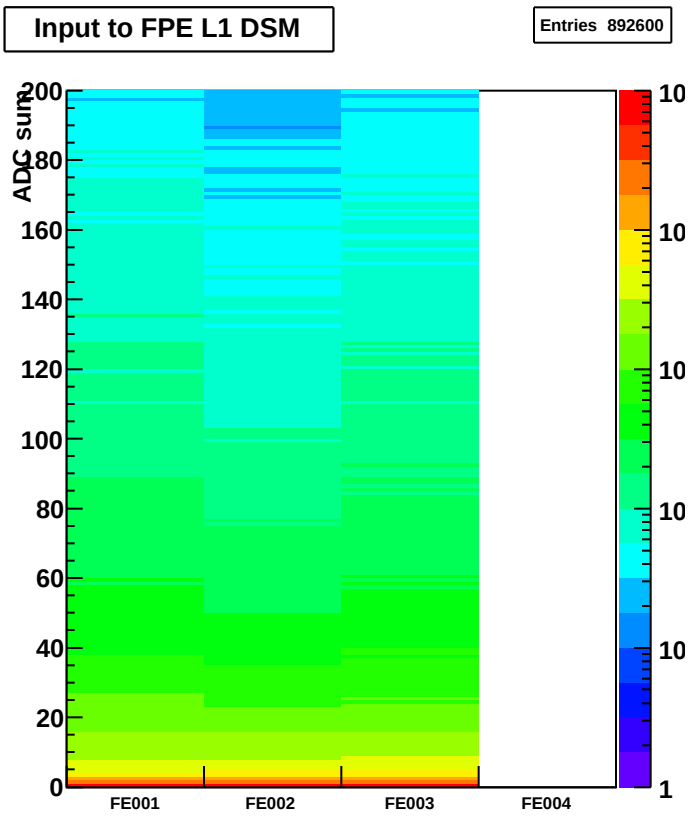

Input to FPD L2 DSM
Entries 892600
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

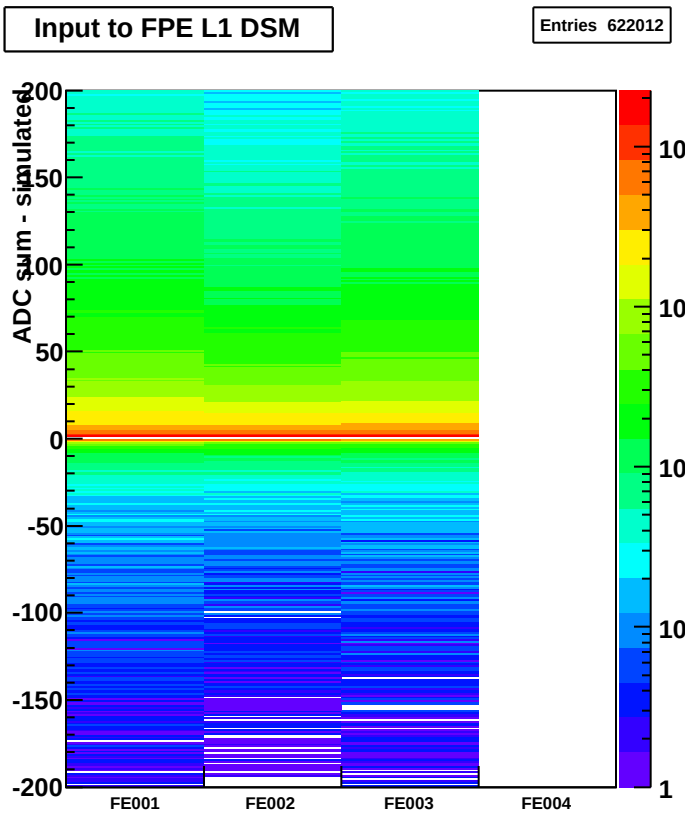

Input to FE001 QT board
Entries7140800
ADC
channel

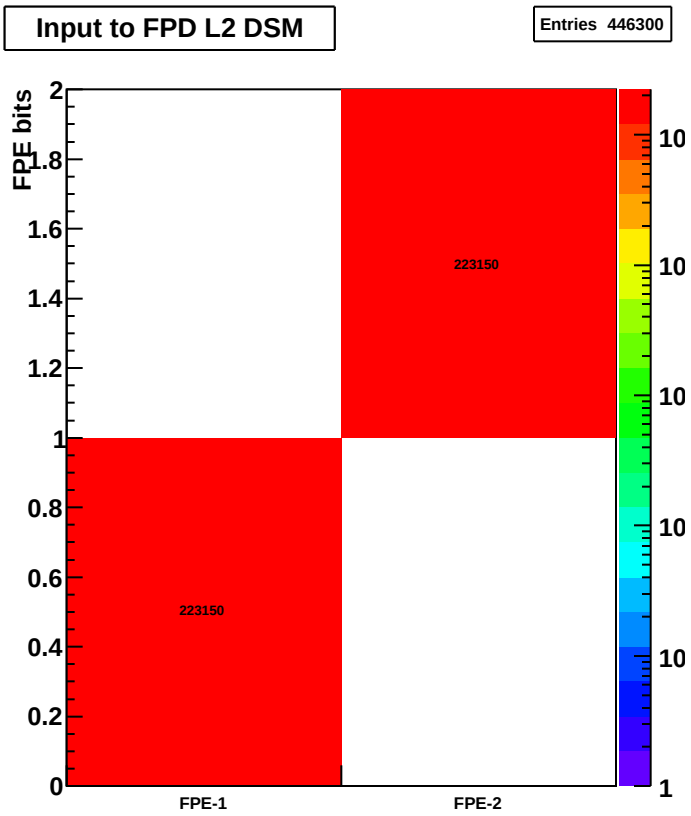

Input to FE002 QT board
Entries7140800
ADC
channel

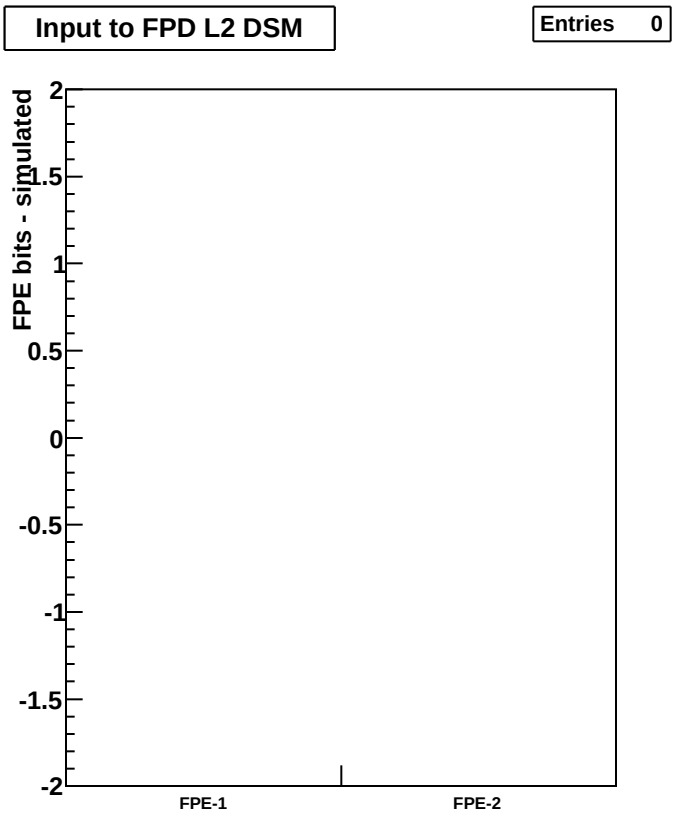

Input to FE003 QT board
Entries7140800
ADC
channel


Input to FE004 QT board
Entries7140800
ADC
channel


Input to FPE L1 DSM
Entries 892600
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 622012
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 446300
FPE bits
223150
223150
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2