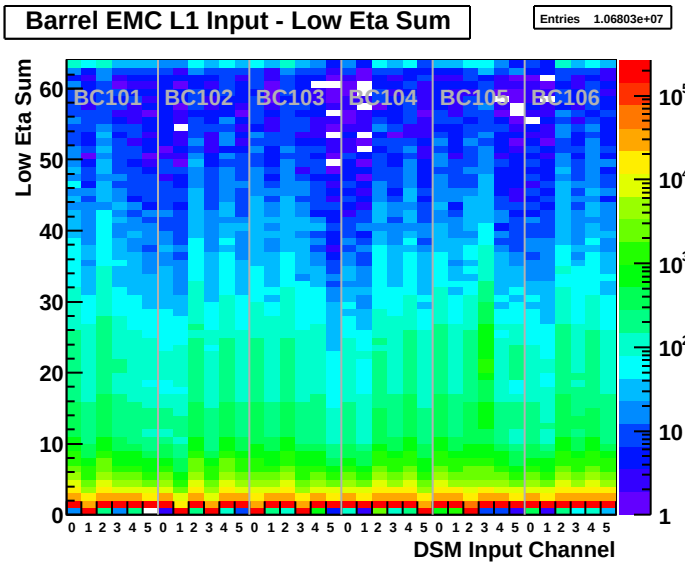
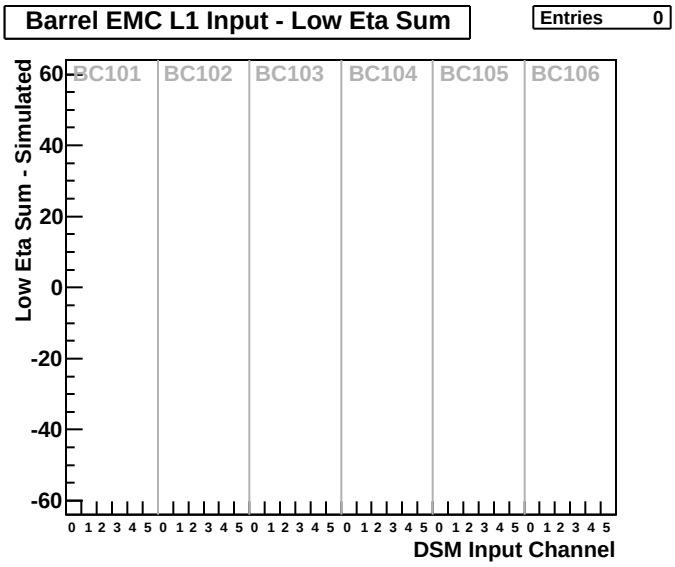
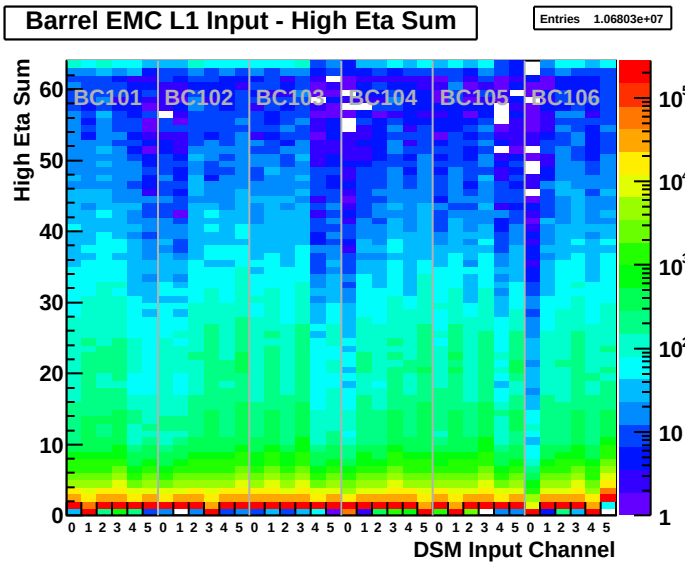
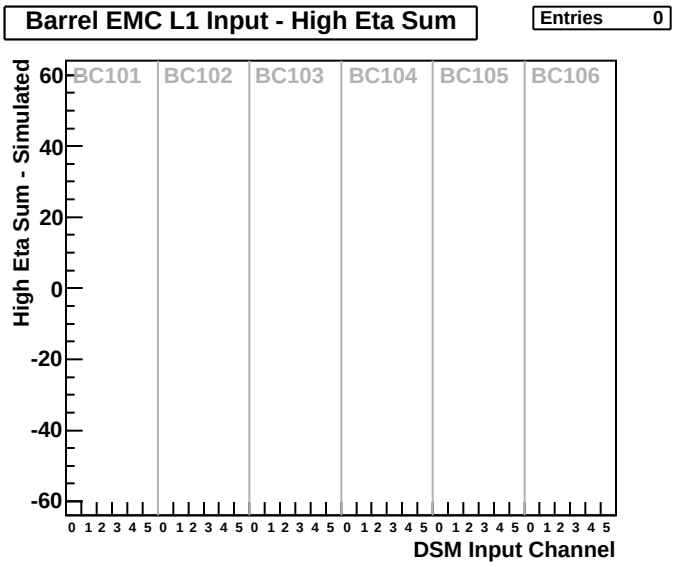
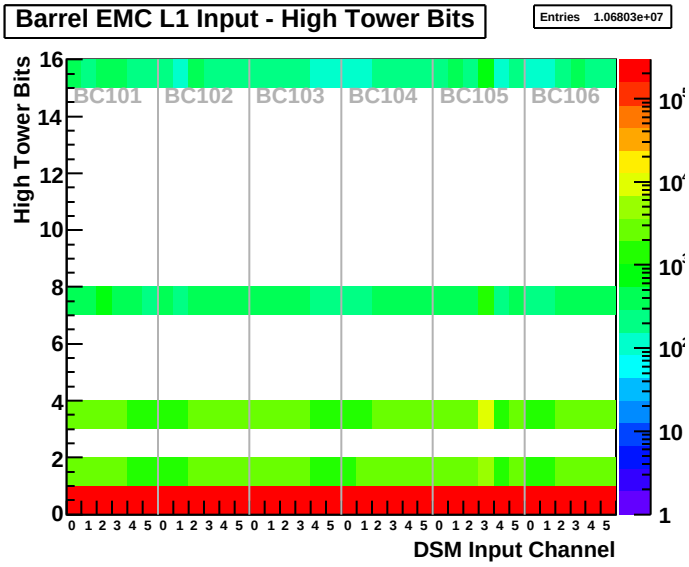
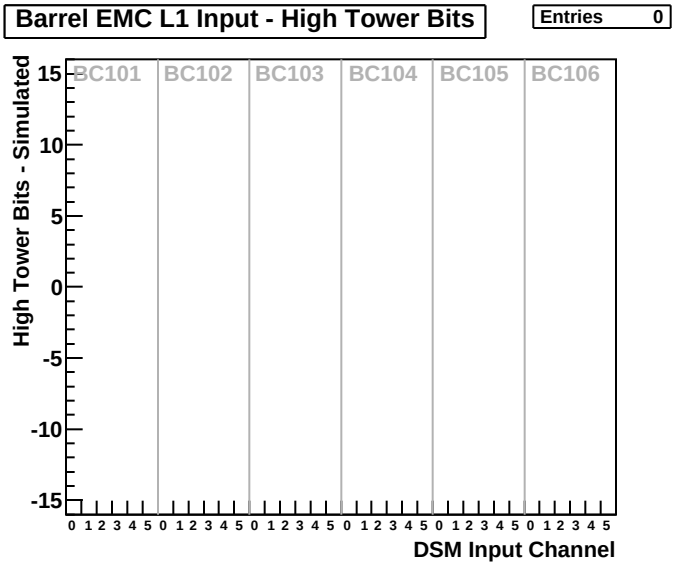

Barrel EMC L1 Input - Low Eta Sum
Entries 1.06803e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

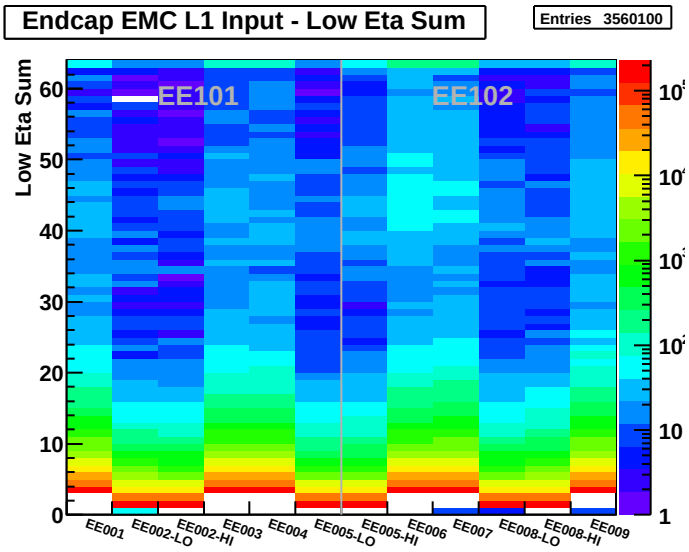

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

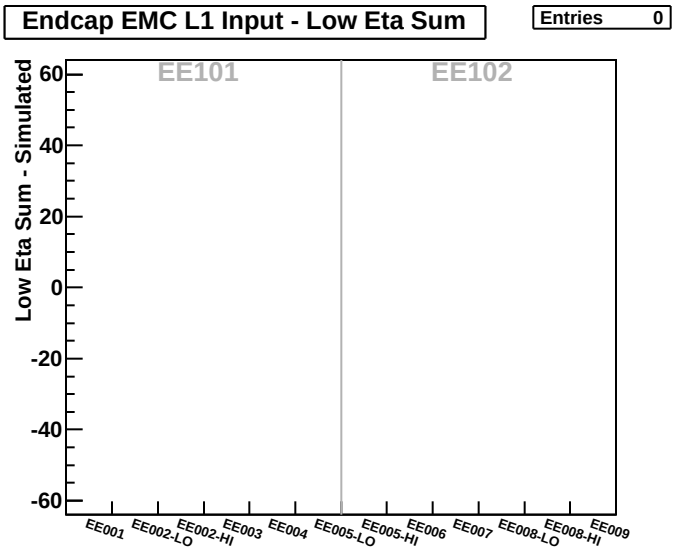

Barrel EMC L1 Input - High Eta Sum
Entries 1.06803e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

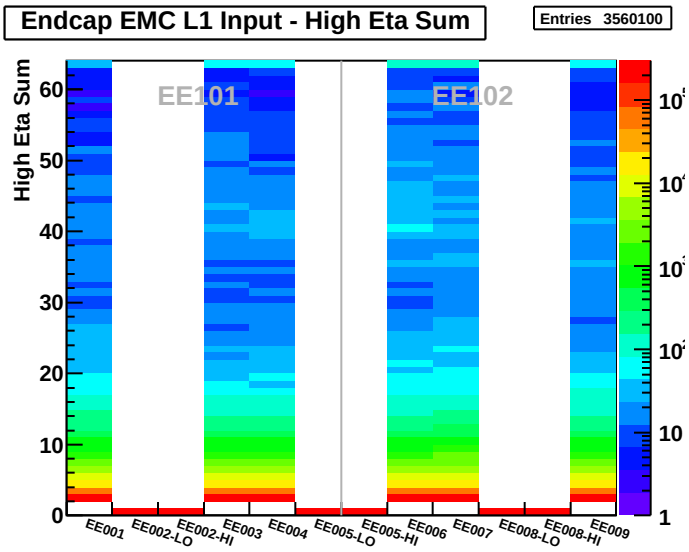

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

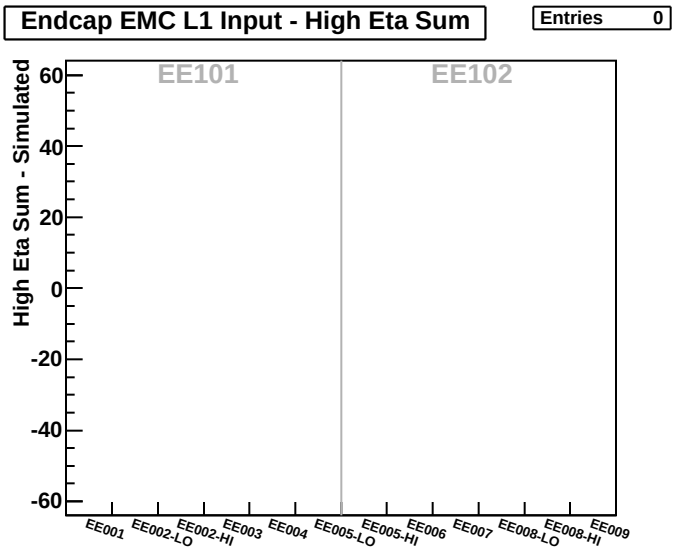

Barrel EMC L1 Input - High Tower Bits
Entries 1.06803e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

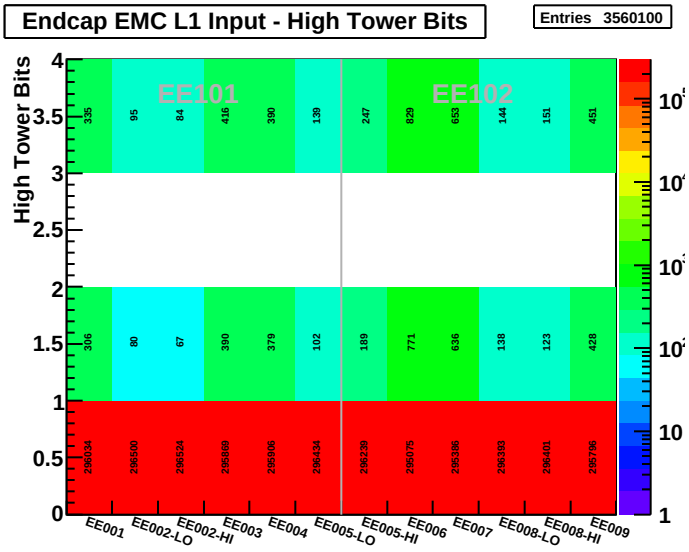

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

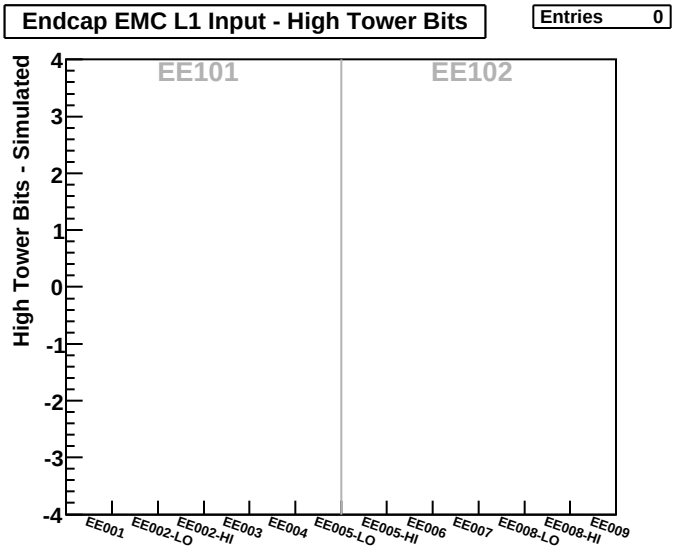

Endcap EMC L1 Input - Low Eta Sum
Entries 3560100
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

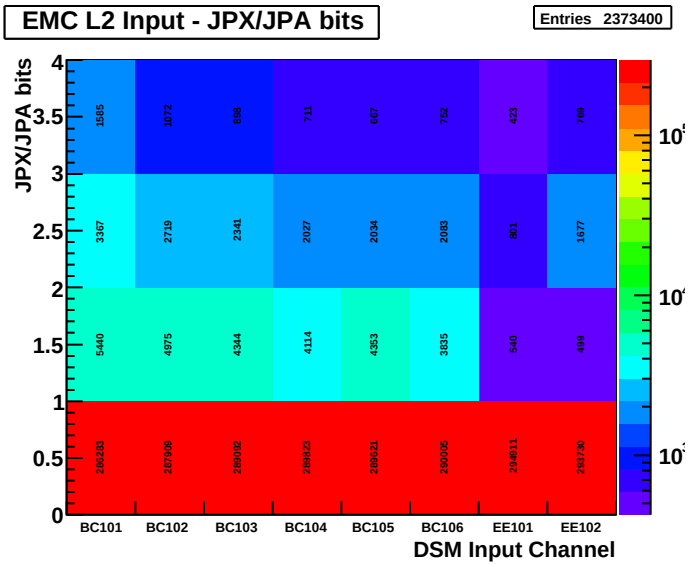

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

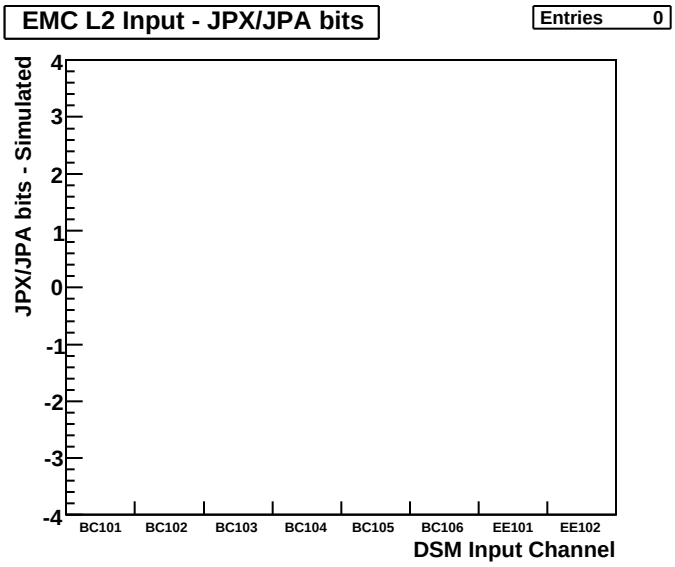

Endcap EMC L1 Input - High Eta Sum
Entries 3560100
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

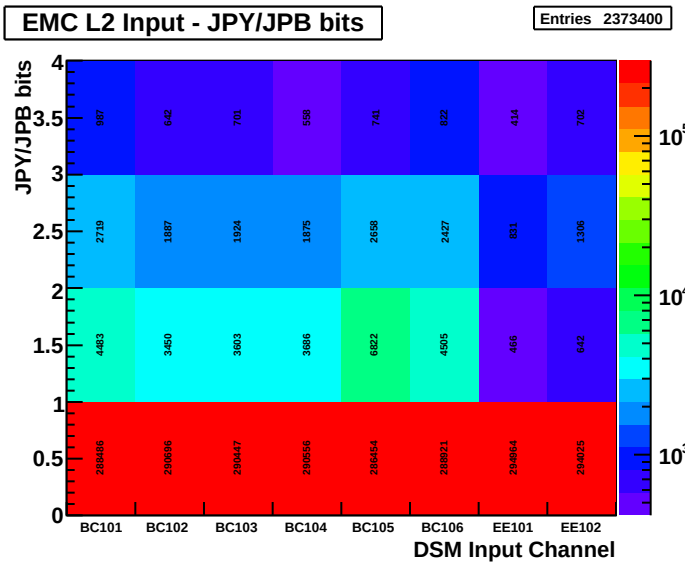

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

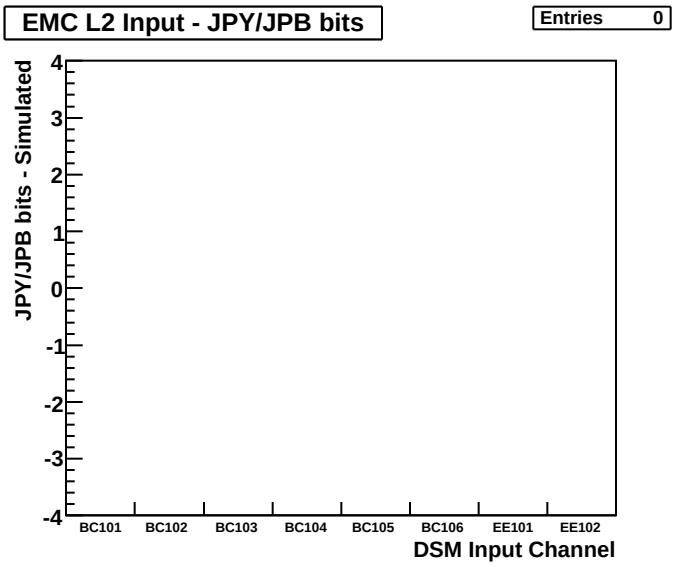

Endcap EMC L1 Input - High Tower Bits
Entries 3560100
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

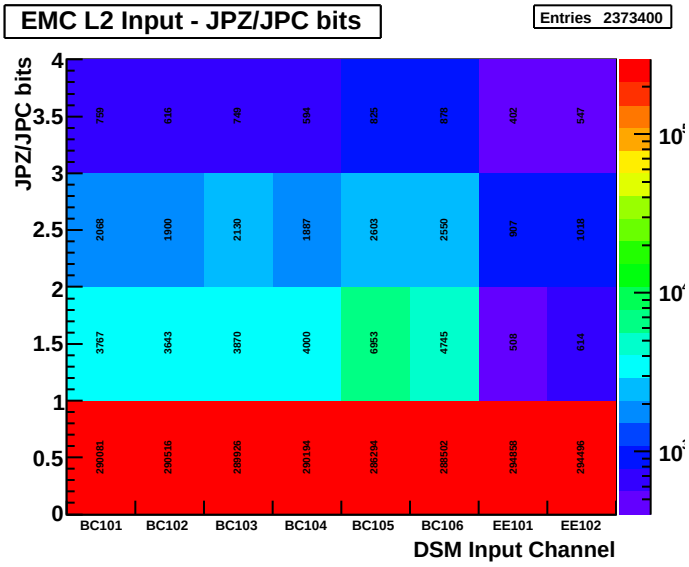

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

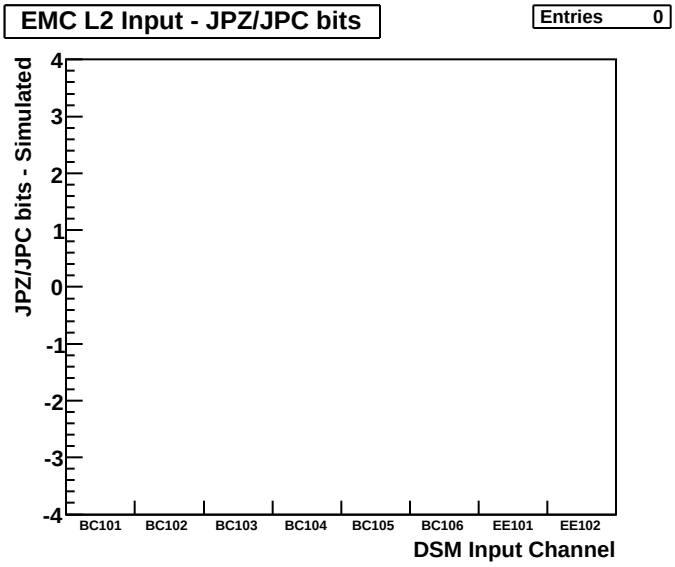

EMC L2 Input - JPX/JPA bits
Entries 2373400
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1585 1072 898 711 667 752 423 769
3367 2719 2341 2027 2034 2083 801 1677
5440 4975 4344 4114 4353 3835 540 499
286283 287909 289032 289823 289621 290005 294911 293730

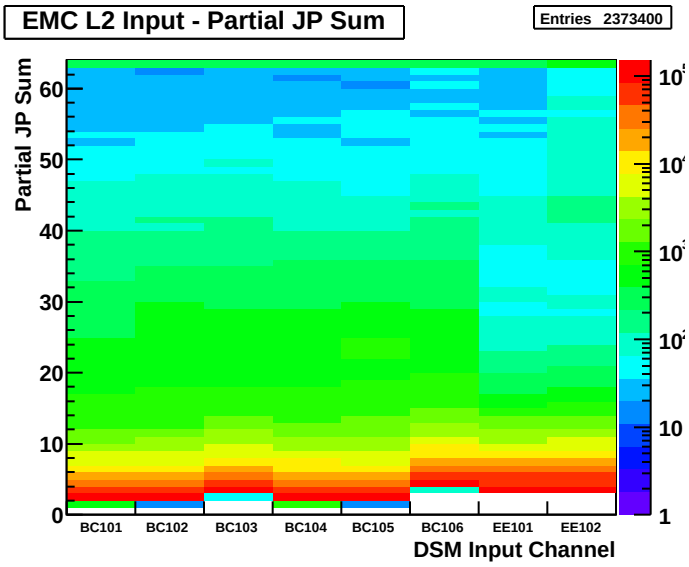

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

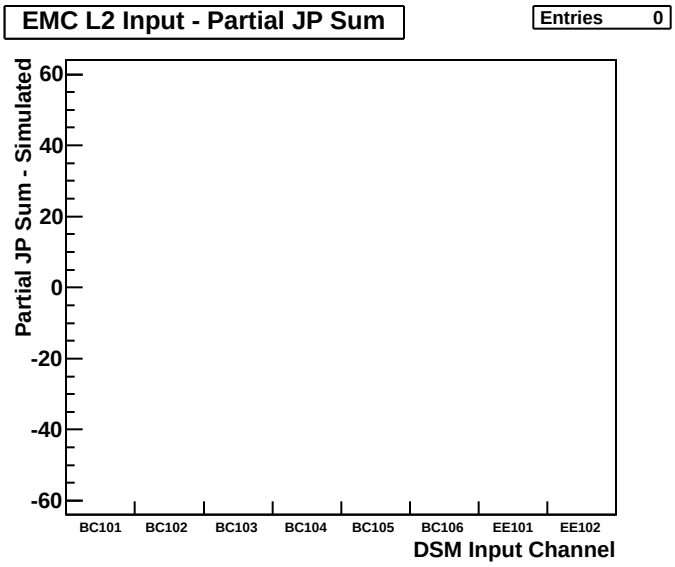

EMC L2 Input - JPY/JPB bits
Entries 2373400
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
987 642 701 558 741 822 414 702
2719 1887 1924 1875 2658 2427 831 1306
4483 3450 3603 3686 6022 4505 466 642
288486 290696 290447 290556 289454 288921 294864 294025

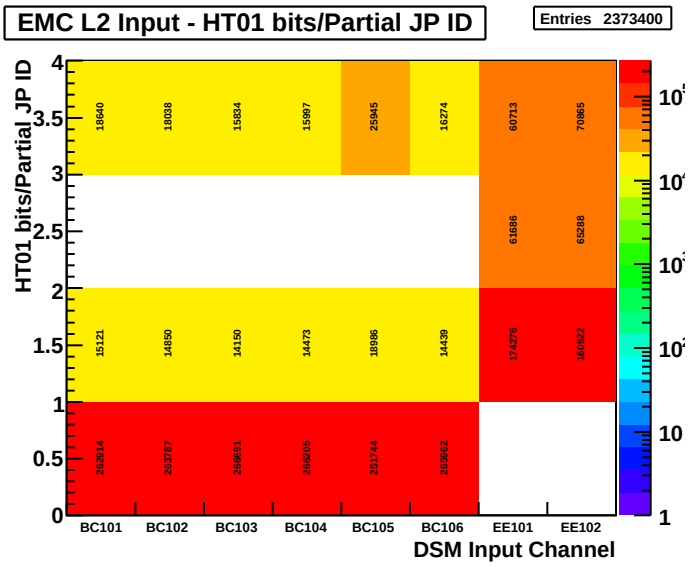

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

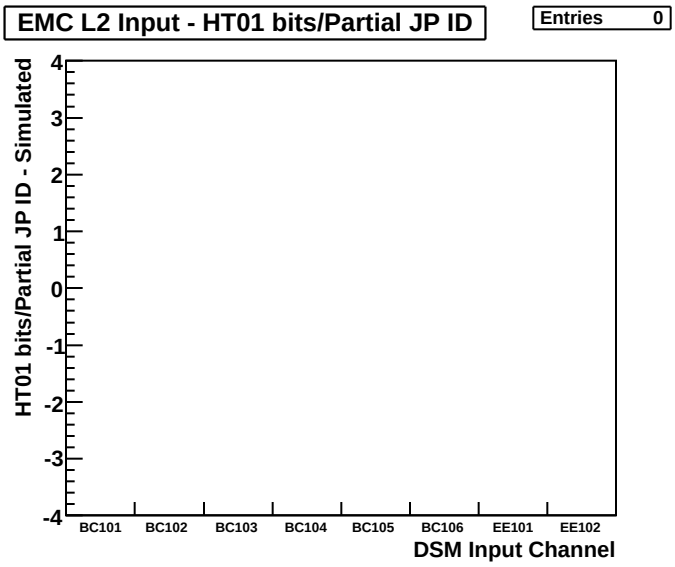

EMC L2 Input - JPZ/JPC bits
Entries 2373400
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
759 616 749 594 825 878 402 547
2068 1900 2130 1887 2603 2590 907 1018
3767 3643 3870 4000 6953 4745 508 614
290081 290516 289926 290104 286294 288502 294958 294496

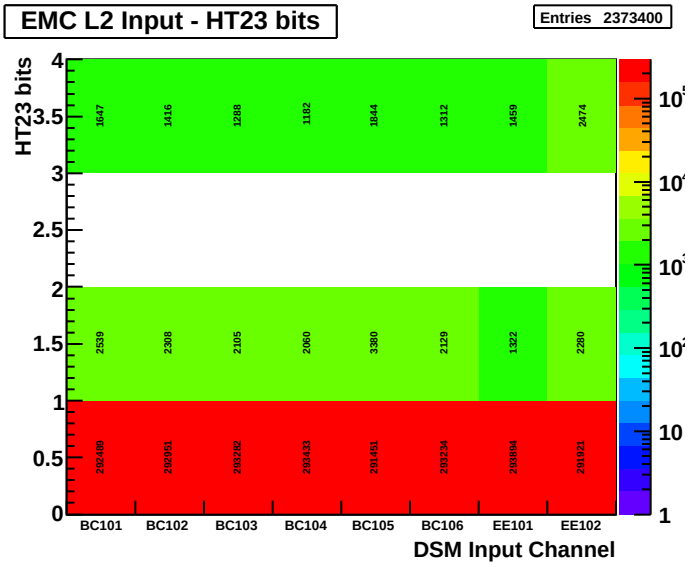

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

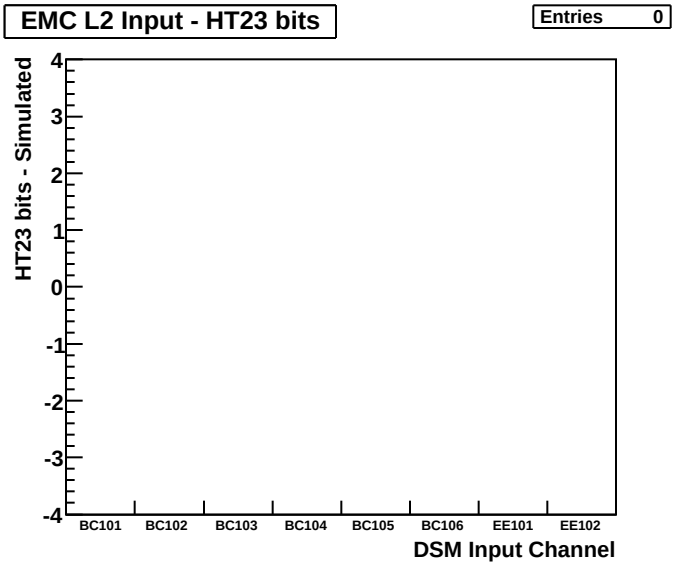

EMC L2 Input - Partial JP Sum
Entries 2373400
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

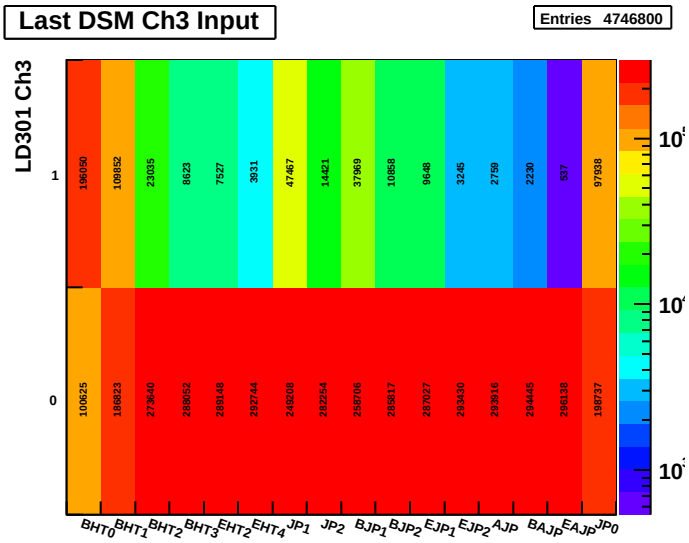

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

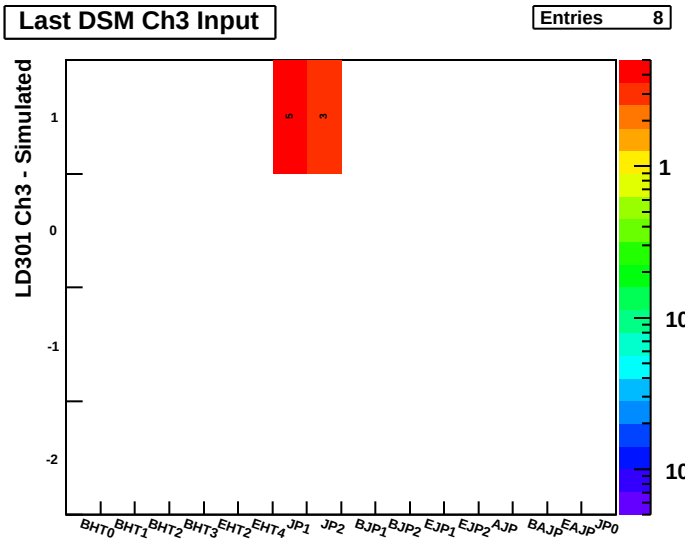

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2373400
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

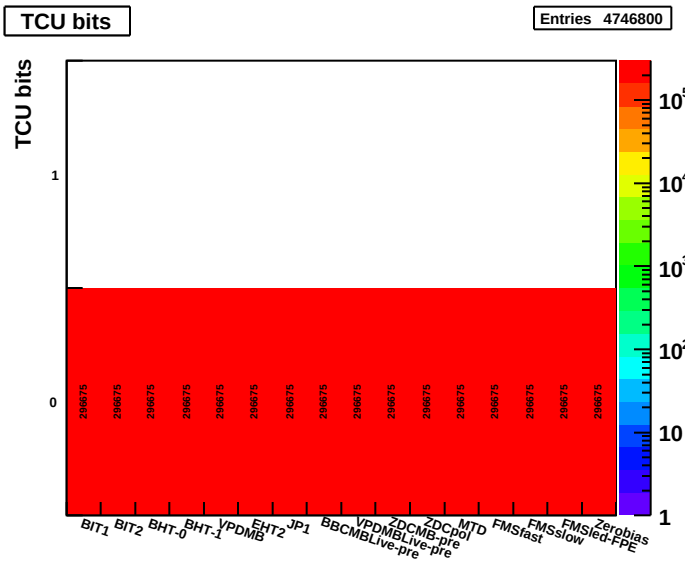

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

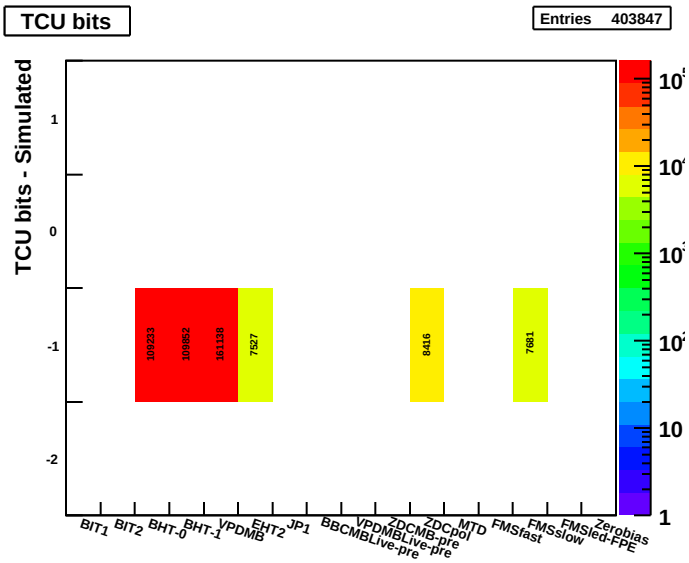

EMC L2 Input - HT23 bits
Entries 2373400
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

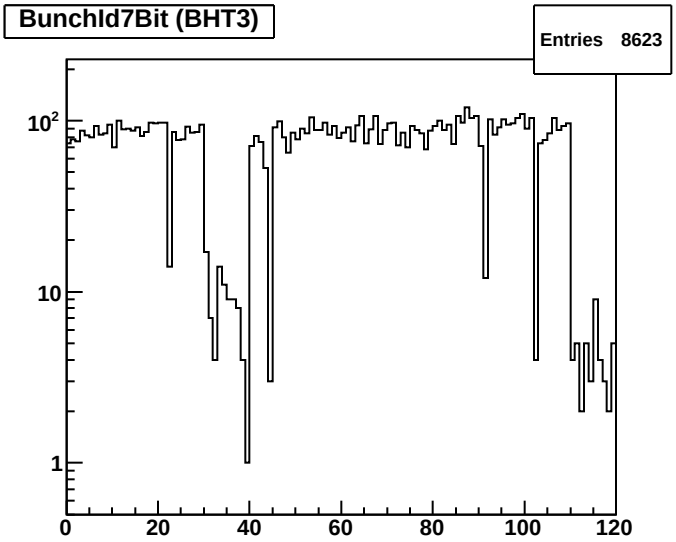

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

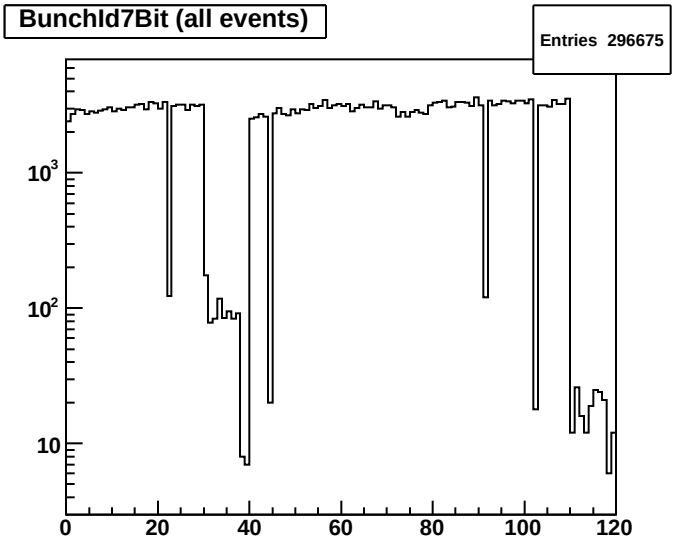

Last DSM Ch3 Input
Entries 4746800
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

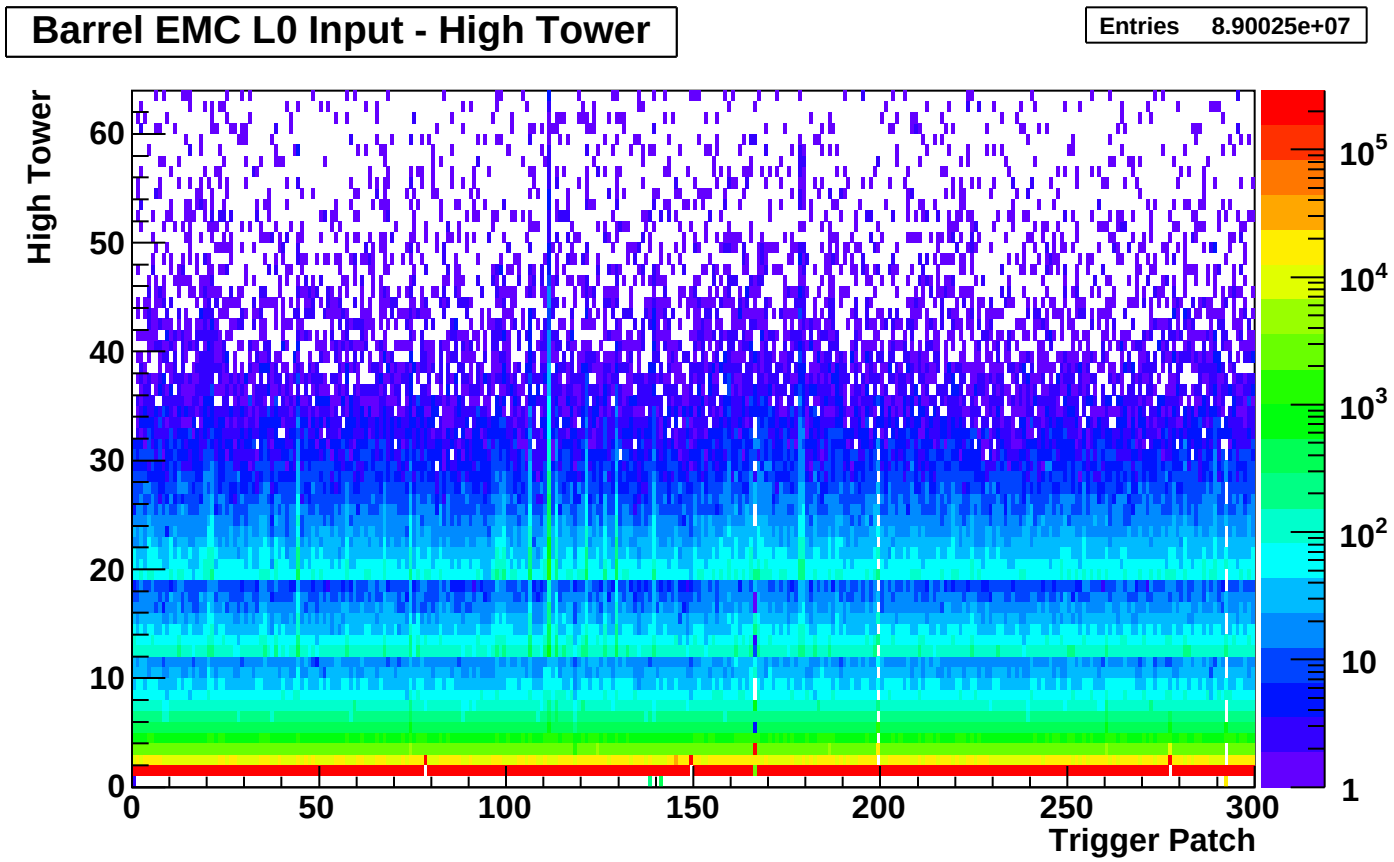

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

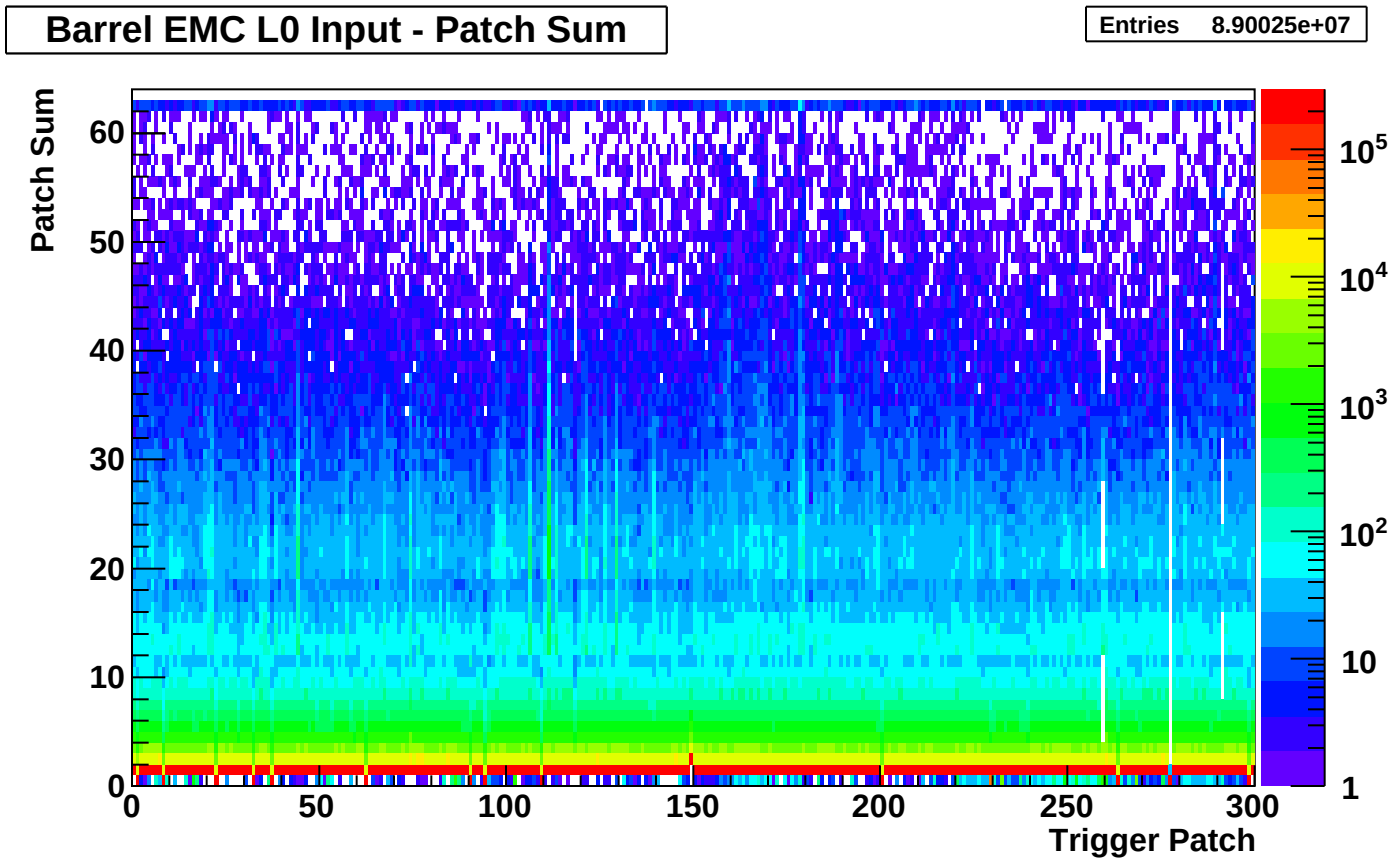

TCU bits
Entries 4746800
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

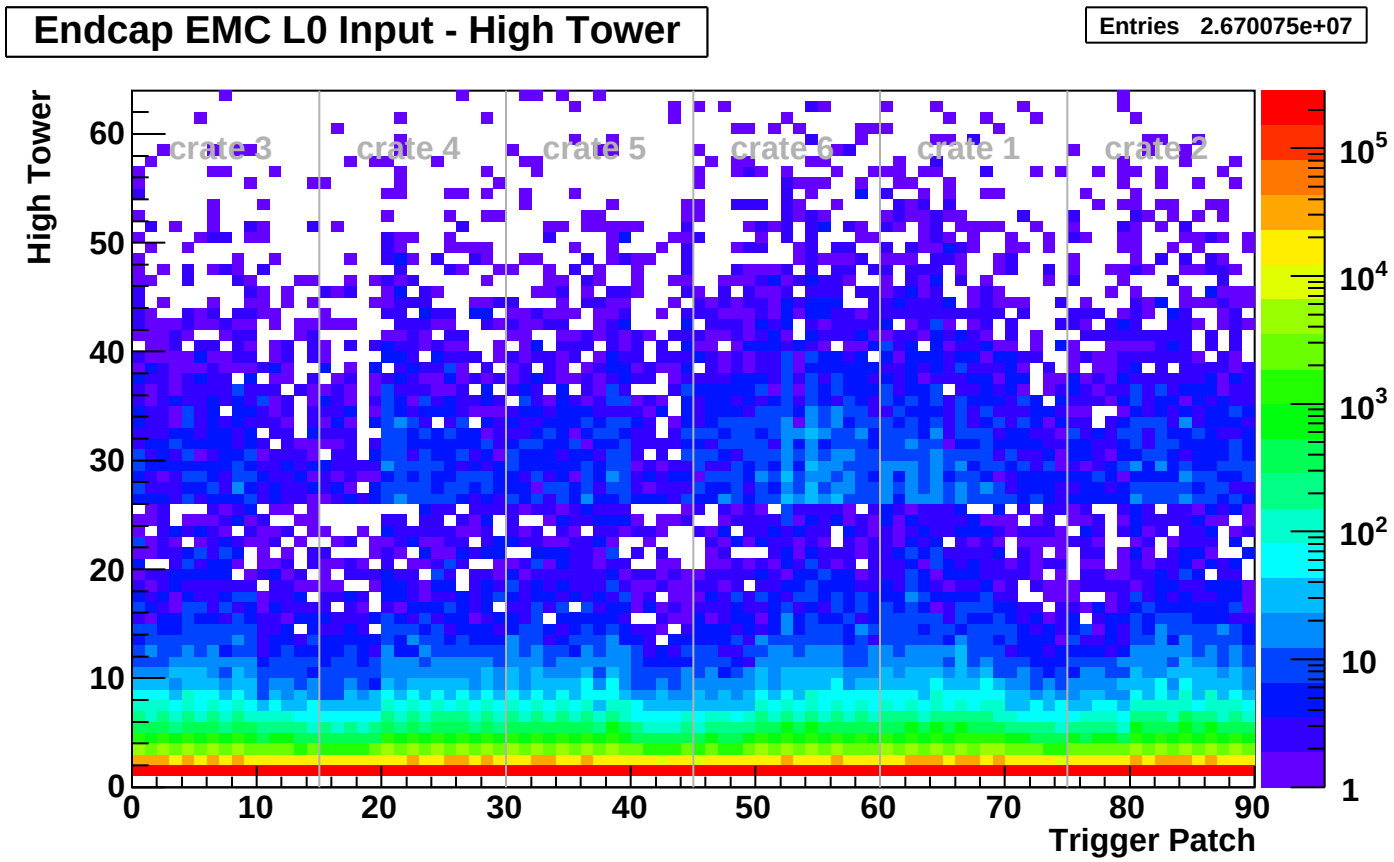

TCU bits
Entries 403847
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

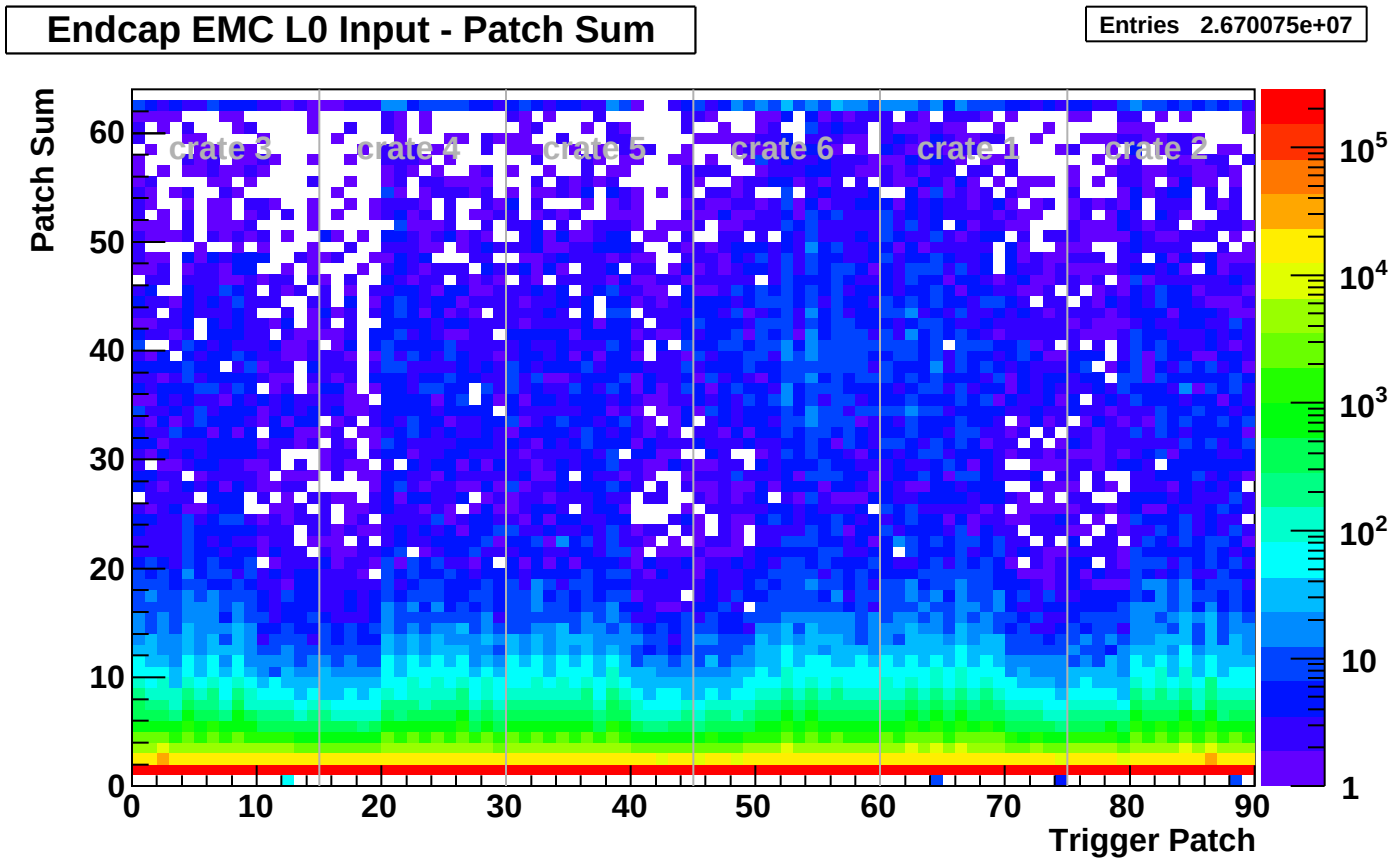

BunchId7Bit (BHT3)
Entries 8623

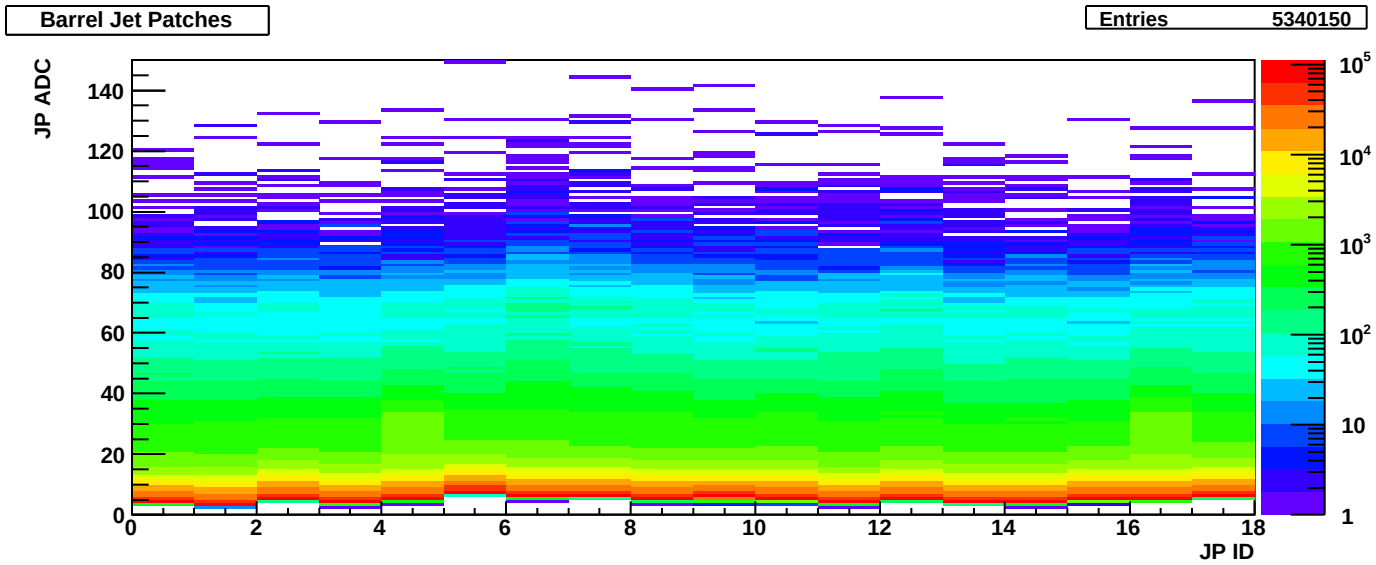

BunchId7Bit (all events)
Entries 296675

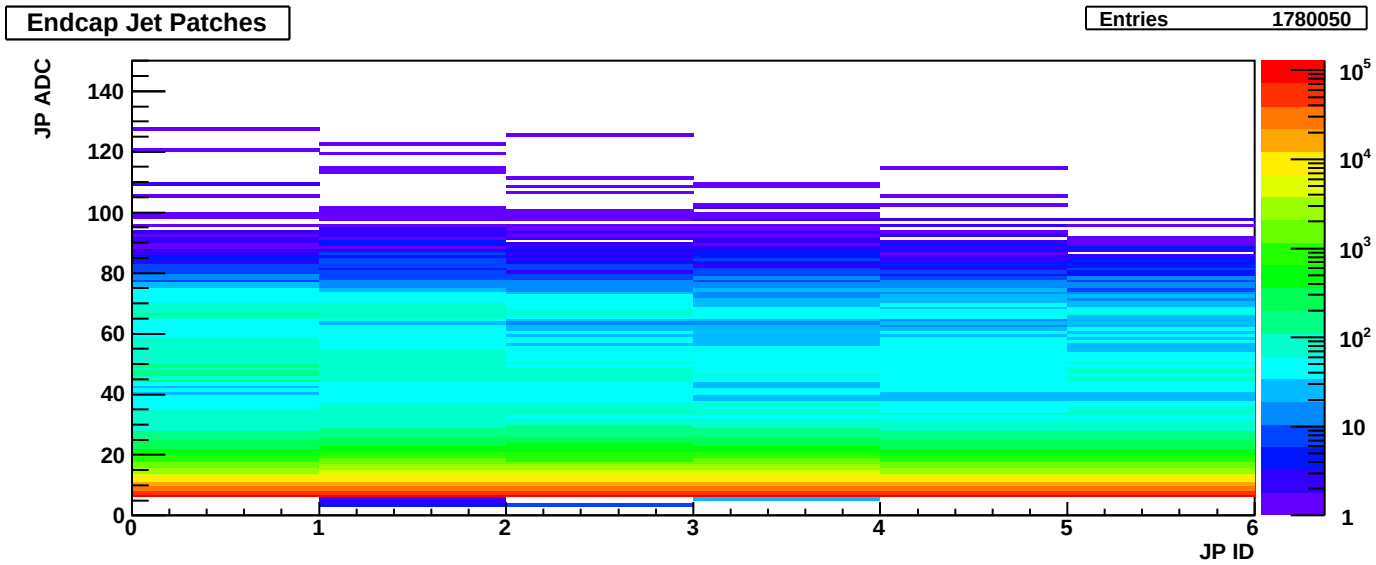

Barrel EMC L0 Input - High Tower
Entries 8.90025e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

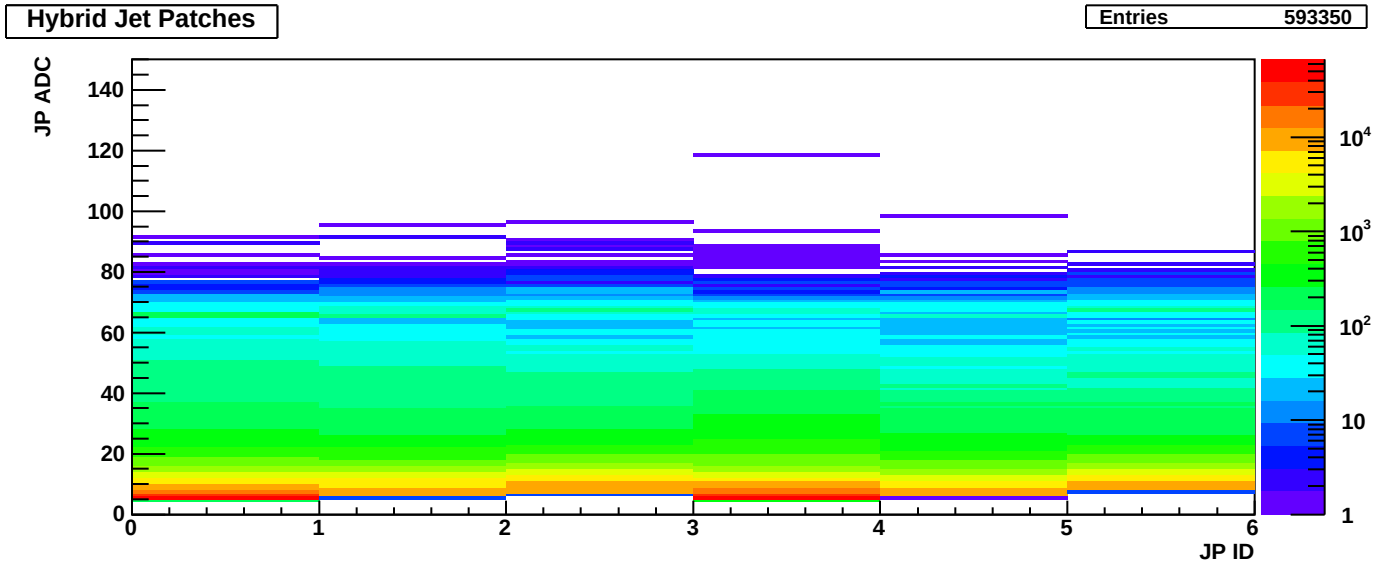

Barrel EMC L0 Input - Patch Sum
Entries 8.90025e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

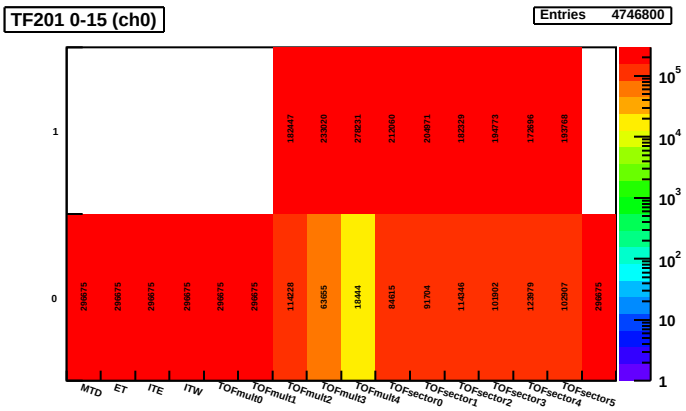

Endcap EMC L0 Input - High Tower
Entries 2.670075e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

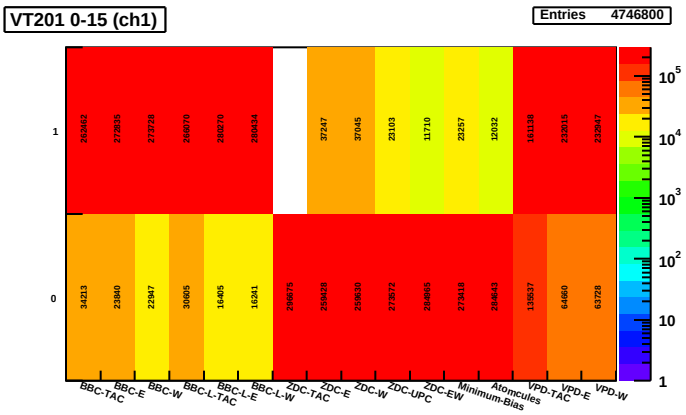

Endcap EMC L0 Input - Patch Sum
Entries 2.670075e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

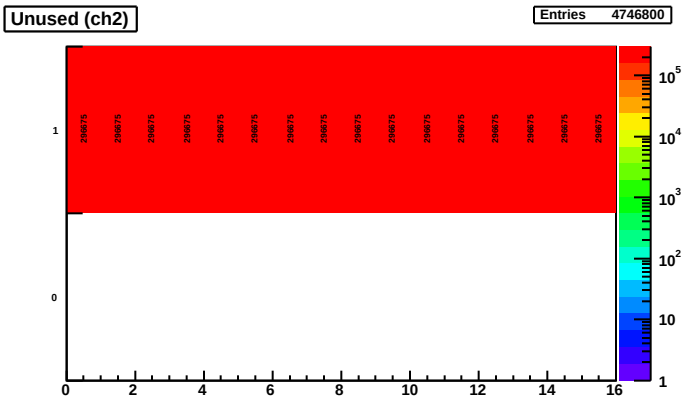

Barrel Jet Patches
Entries 5340150
JP ADC
JP ID

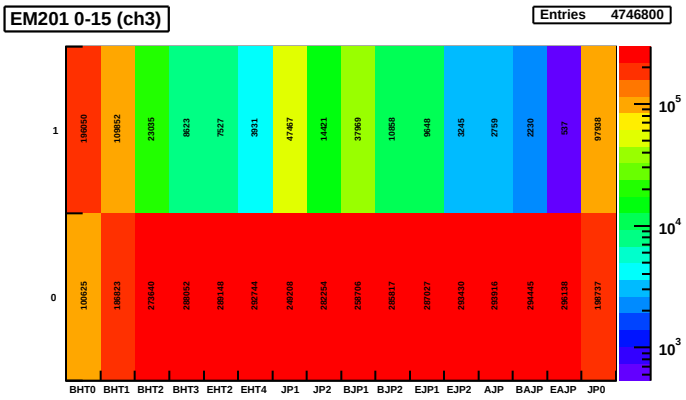

Endcap Jet Patches
Entries 1780050
JP ADC
JP ID

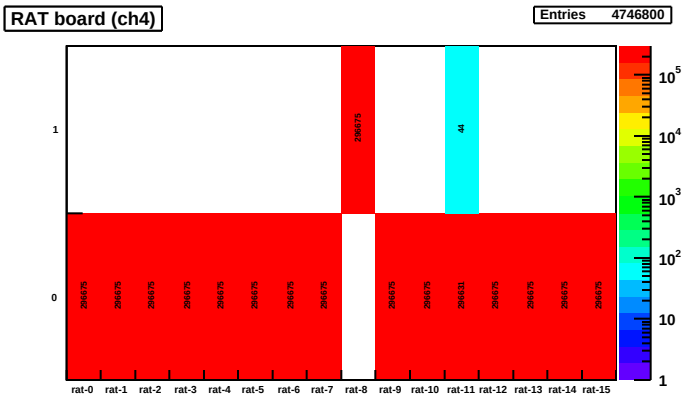

Hybrid Jet Patches
Entries 593350
JP ADC
JP ID

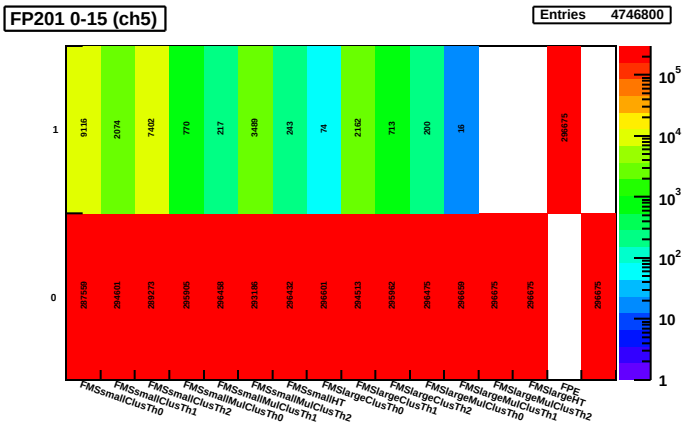

TF201 0-15 (ch0)
Entries 4746800
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

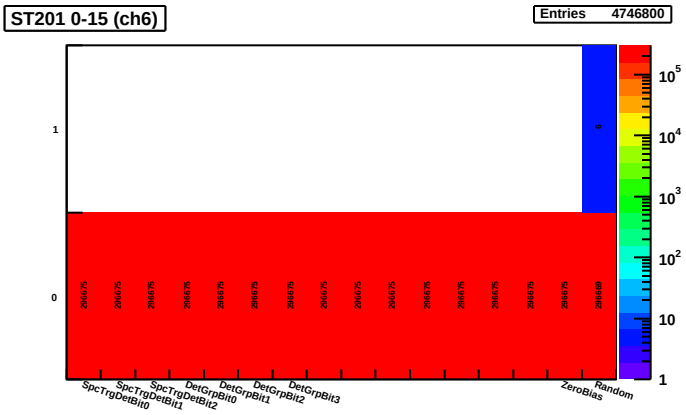

VT201 0-15 (ch1)
Entries 4746800
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

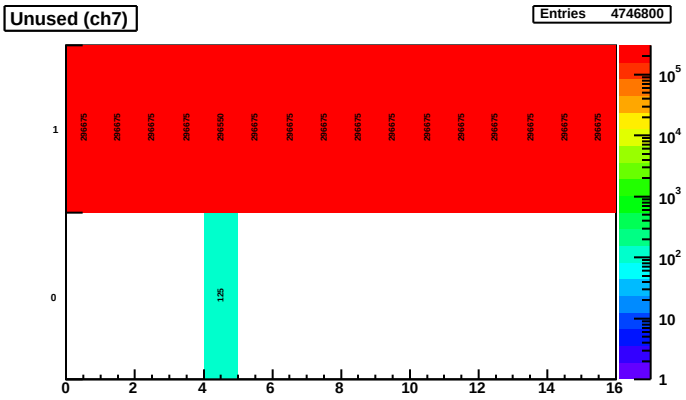

Unused (ch2)
Entries 4746800

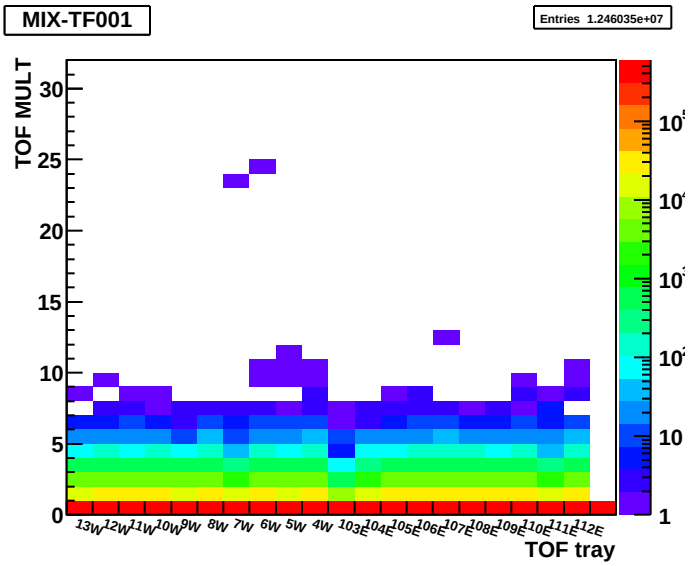

EM201 0-15 (ch3)
Entries 4746800
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

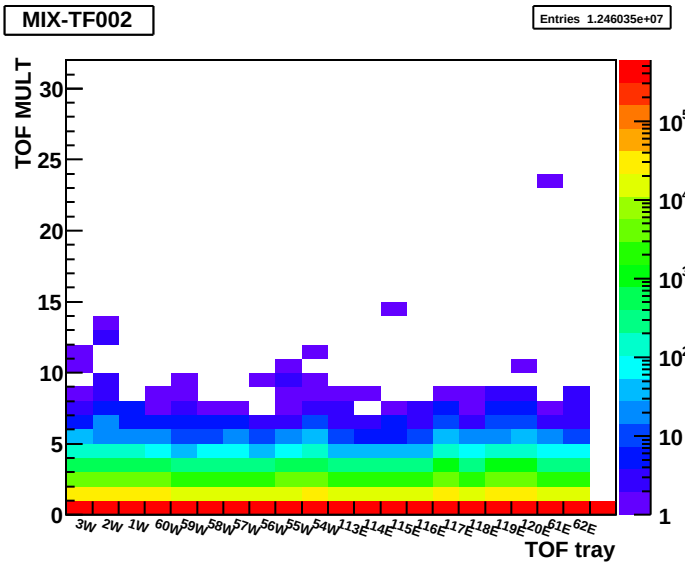

RAT board (ch4)
Entries 4746800
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

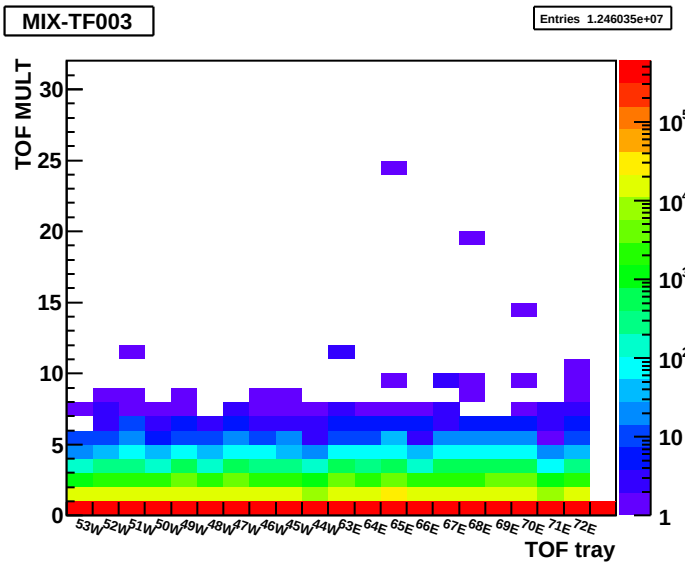

FP201 0-15 (ch5)
Entries 4746800
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

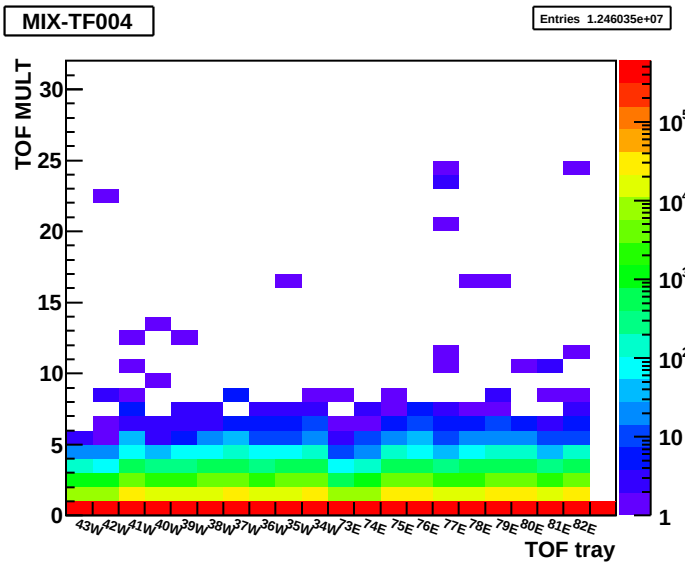

ST201 0-15 (ch6)
Entries 4746800
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

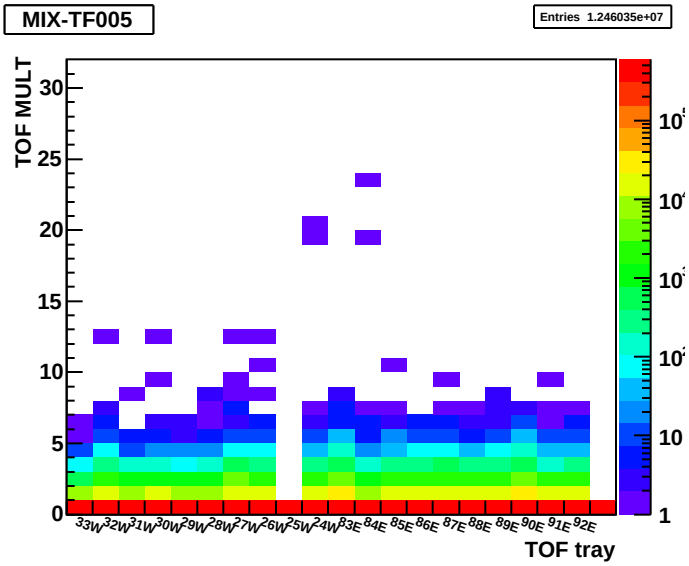

Unused (ch7)
Entries 4746800

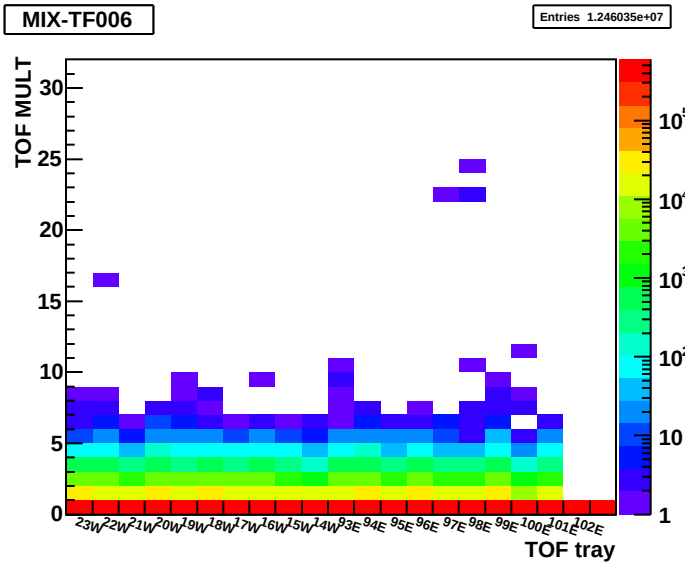

MIX-TF001
Entries 1.246035e+07
TOF MULT
TOF tray

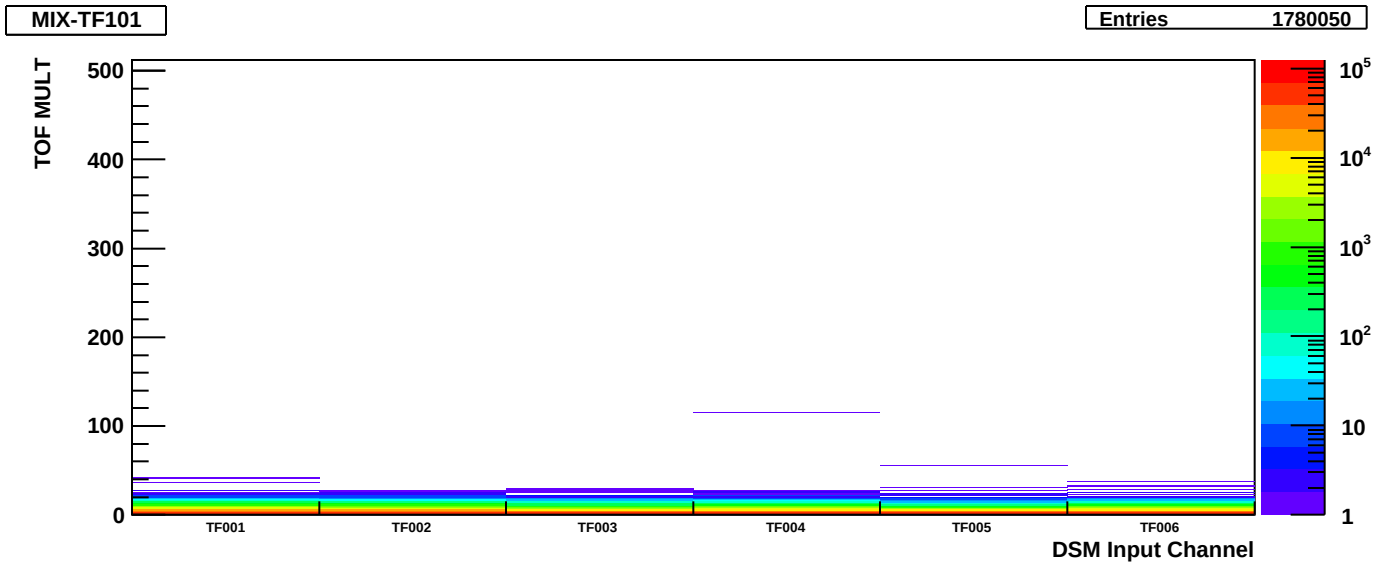

MIX-TF002
Entries 1.246035e+07
TOF MULT
TOF tray

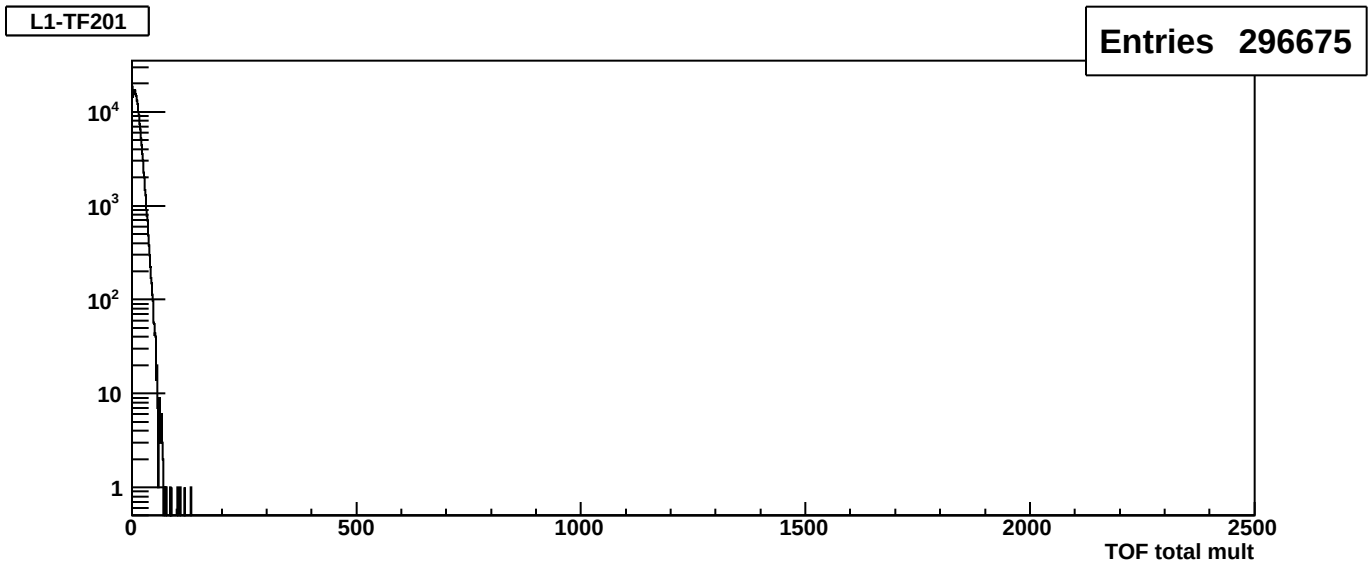

MIX-TF003
Entries 1.246035e+07
TOF MULT
TOF tray

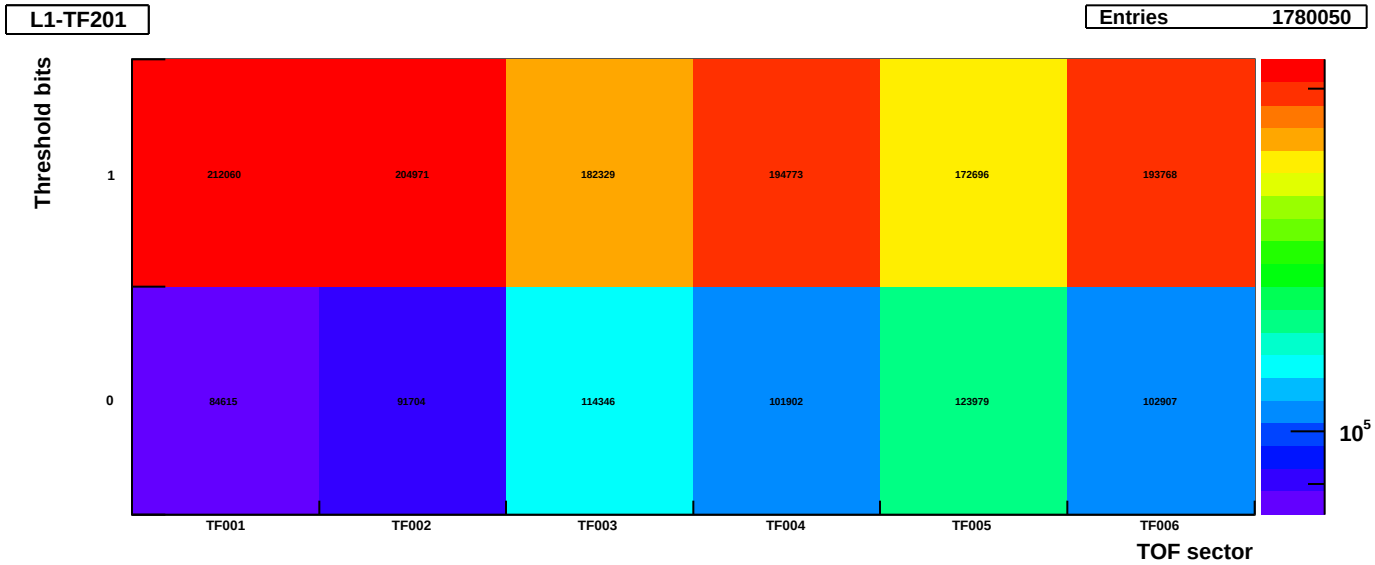

MIX-TF004
Entries 1.246035e+07
TOF MULT
TOF tray

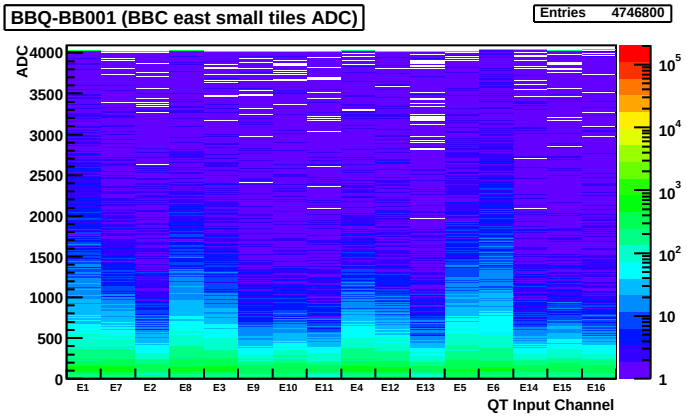

MIX-TF005
Entries 1.246035e+07
TOF MULT
TOF tray

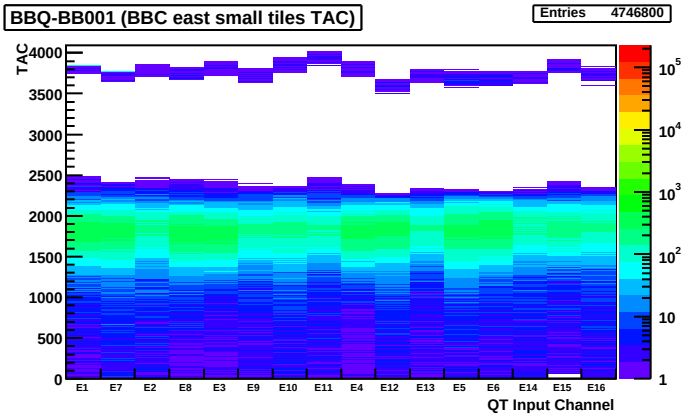

MIX-TF006
Entries 1.246035e+07
TOF MULT
TOF tray

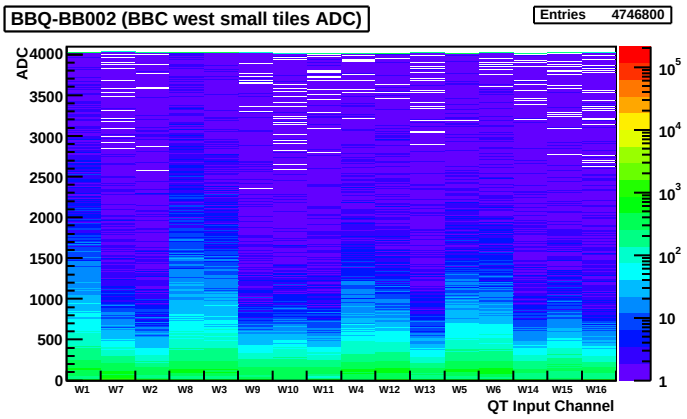

MIX-TF101
Entries 1780050
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

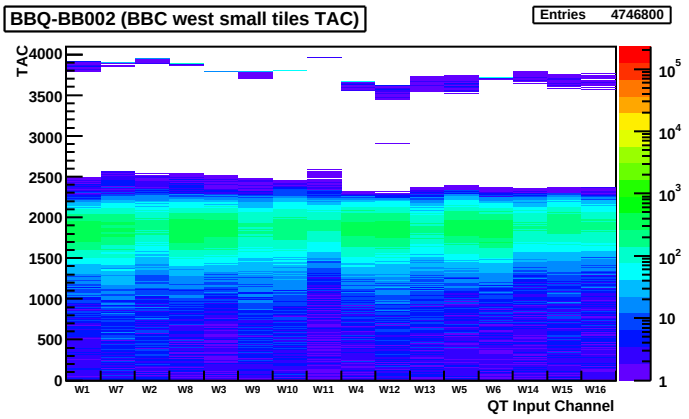

L1-TF201
Entries 296675
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

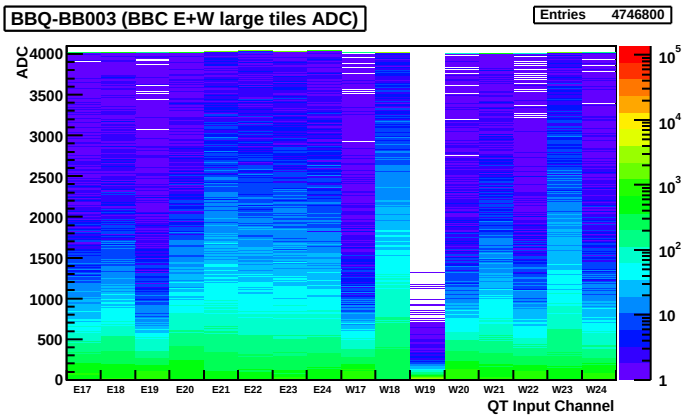

L1-TF201
Entries 1780050
Threshold bits
1
0
212060
204971
182329
194773
172696
193768
84615
91704
114346
101902
123979
102907
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

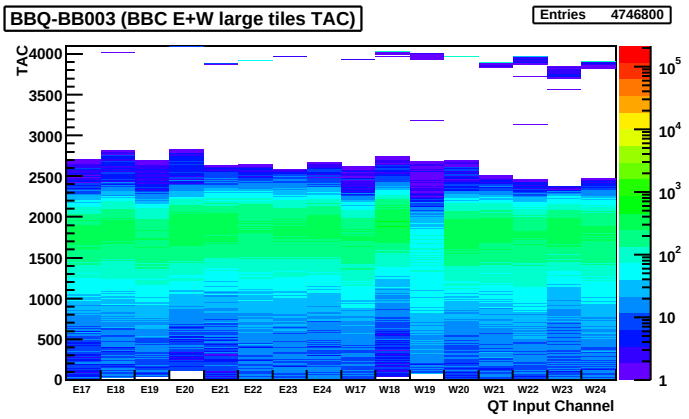

BBQ-BB001 (BBC east small tiles ADC)
Entries 4746800
ADC
QT Input Channel

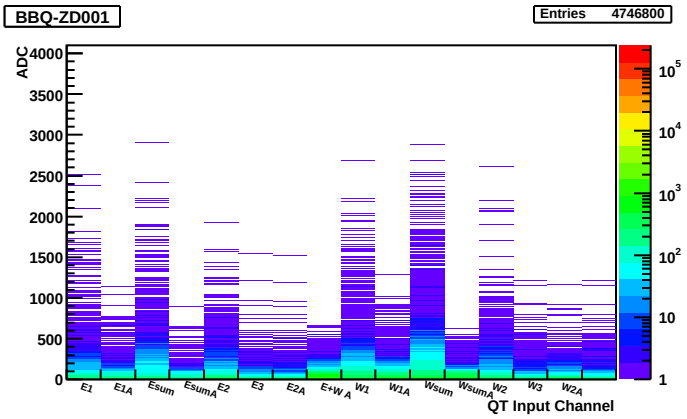

BBQ-BB001 (BBC east small tiles TAC)
Entries 4746800
TAC
QT Input Channel

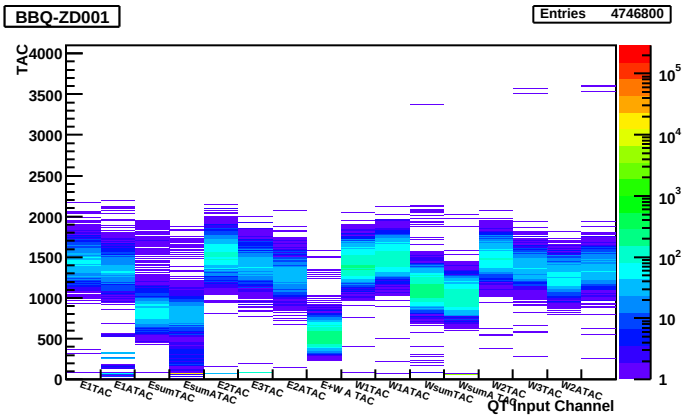

BBQ-BB002 (BBC west small tiles ADC)
Entries 4746800
ADC
QT Input Channel

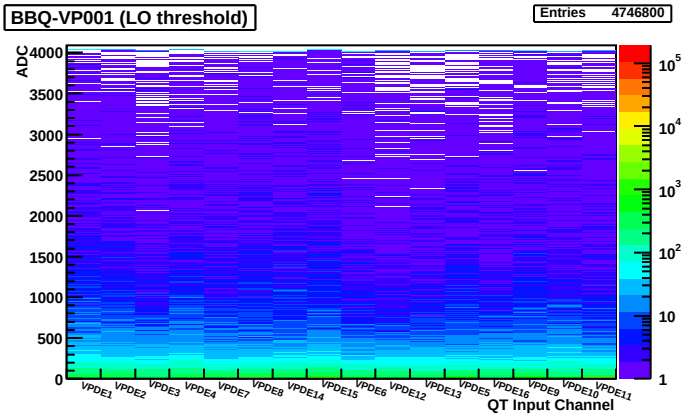

BBQ-BB002 (BBC west small tiles TAC)
Entries 4746800
TAC
QT Input Channel

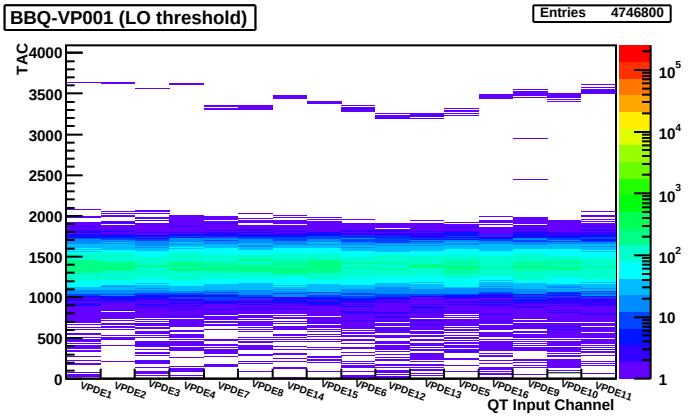

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4746800
ADC
QT Input Channel

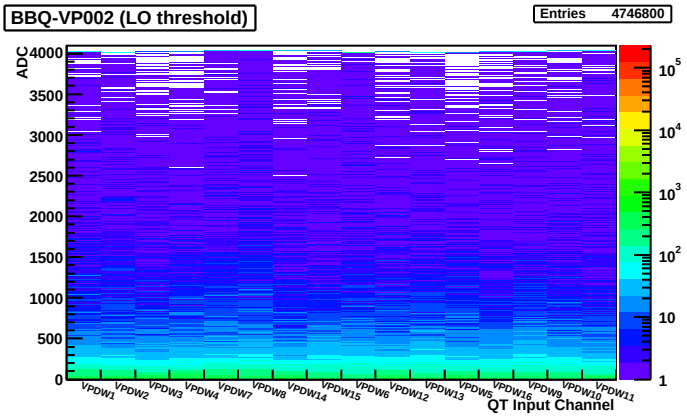

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4746800
TAC
QT Input Channel

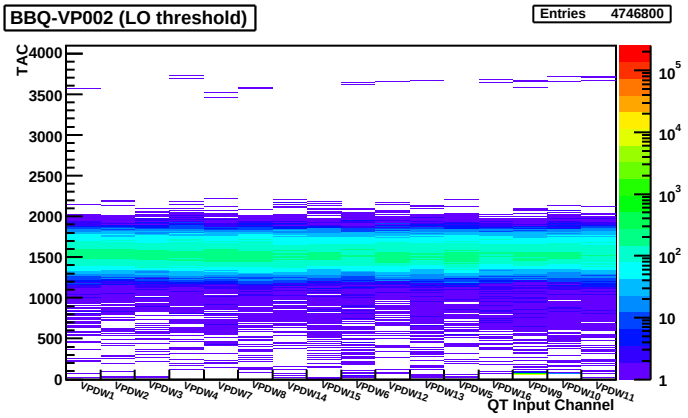

BBQ-ZD001
Entries 4746800
ADC
QT Input Channel

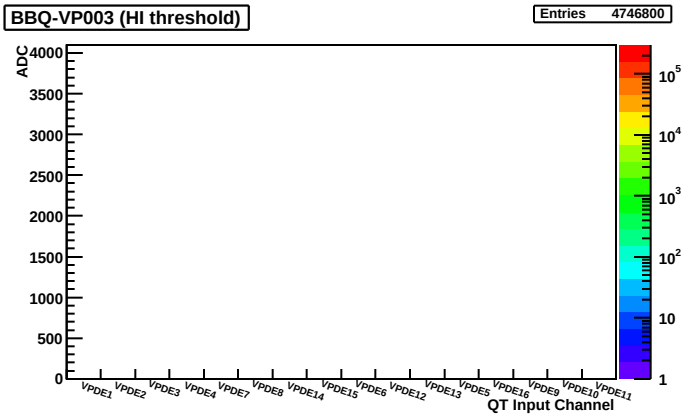

BBQ-ZD001
Entries 4746800
TAC
QT Input Channel

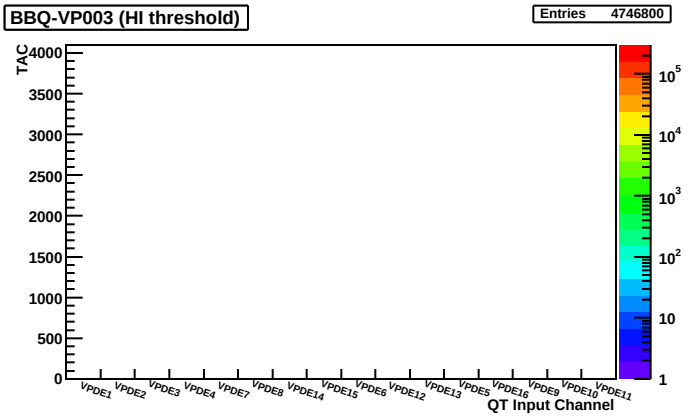

BBQ-VP001 (LO threshold)
Entries 4746800
ADC
QT Input Channel

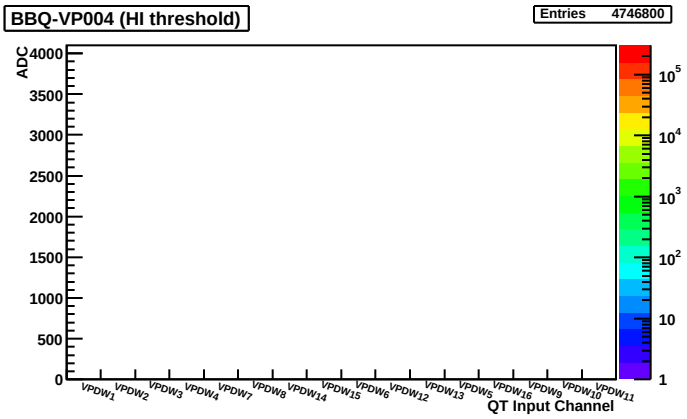

BBQ-VP001 (LO threshold)
Entries 4746800
TAC
QT Input Channel

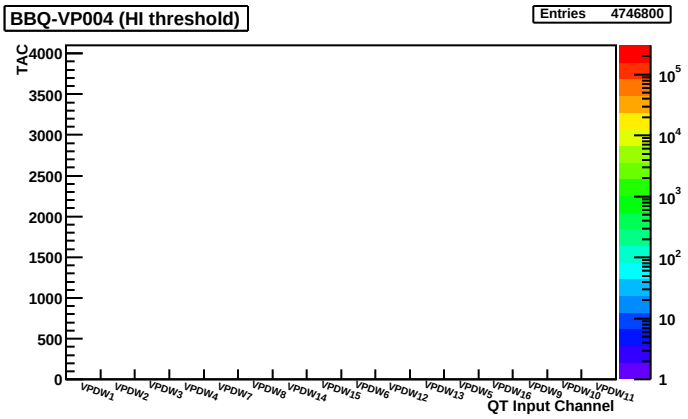

BBQ-VP002 (LO threshold)
Entries 4746800
ADC
QT Input Channel

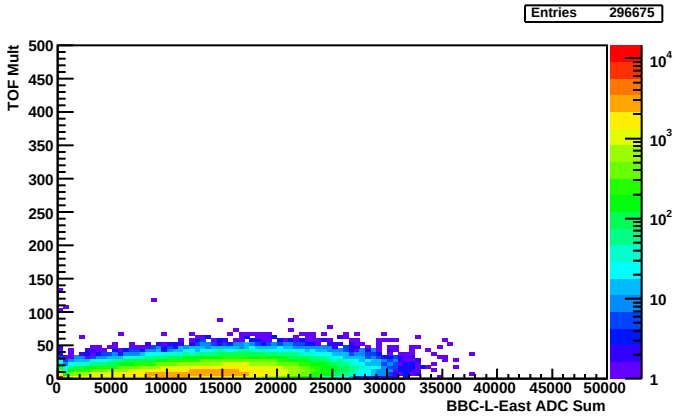

BBQ-VP002 (LO threshold)
Entries 4746800
TAC
QT Input Channel

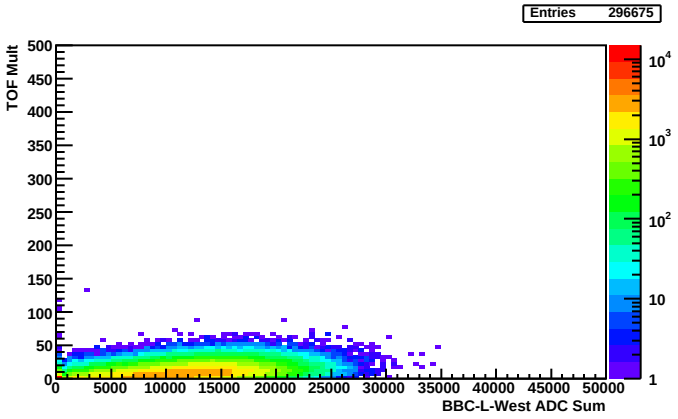

BBQ-VP003 (HI threshold)
Entries 4746800
ADC
QT Input Channel

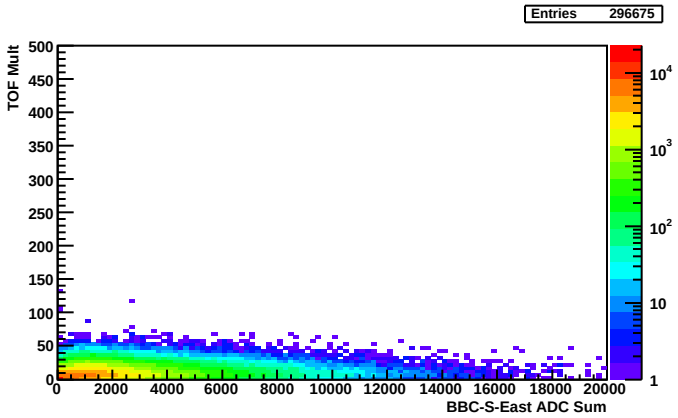

BBQ-VP003 (HI threshold)
Entries 4746800
TAC
QT Input Channel

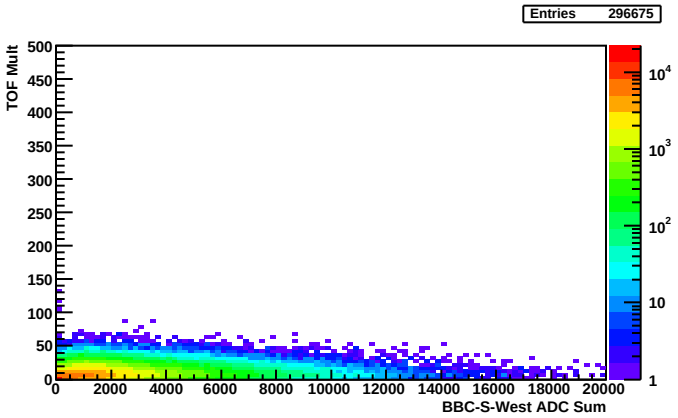

BBQ-VP004 (HI threshold)
Entries 4746800
ADC
QT Input Channel

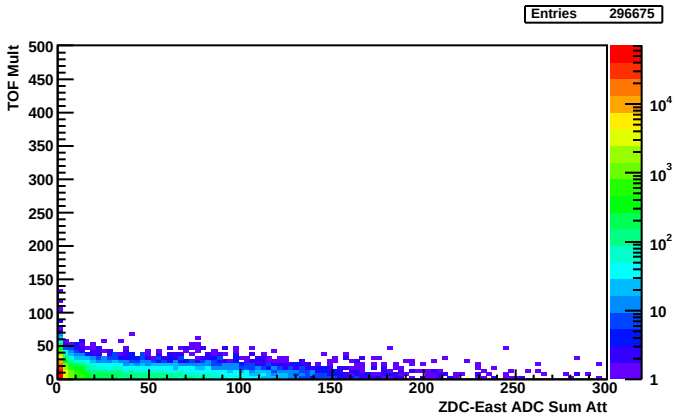

BBQ-VP004 (HI threshold)
Entries 4746800
TAC
QT Input Channel

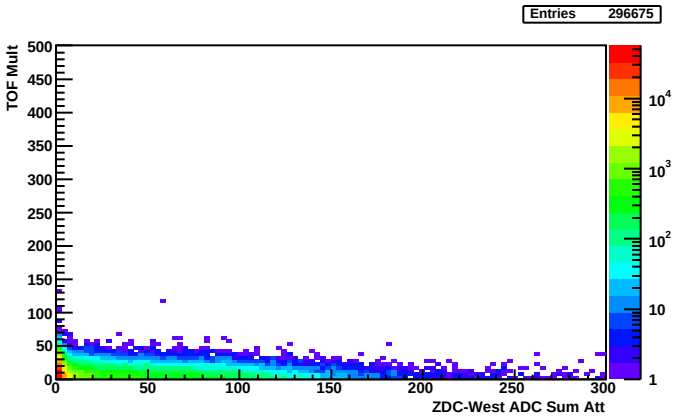

Entries 296675
TOF Mult
BBC-L-East ADC Sum

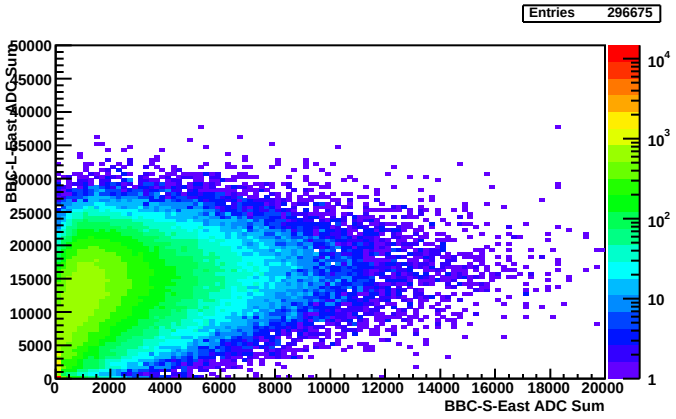

Entries 296675
TOF Mult
BBC-L-West ADC Sum

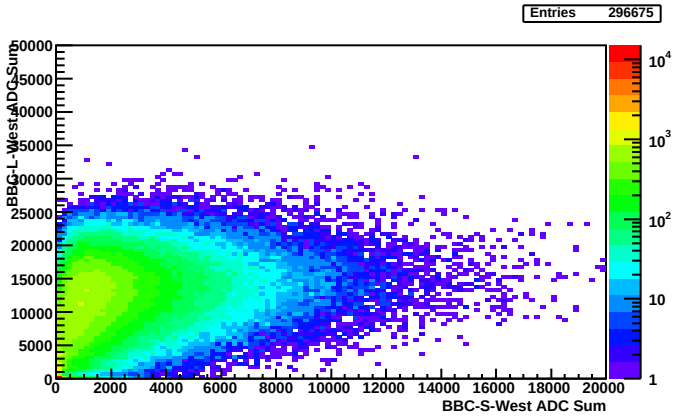

Entries 296675
TOF Mult
BBC-S-East ADC Sum

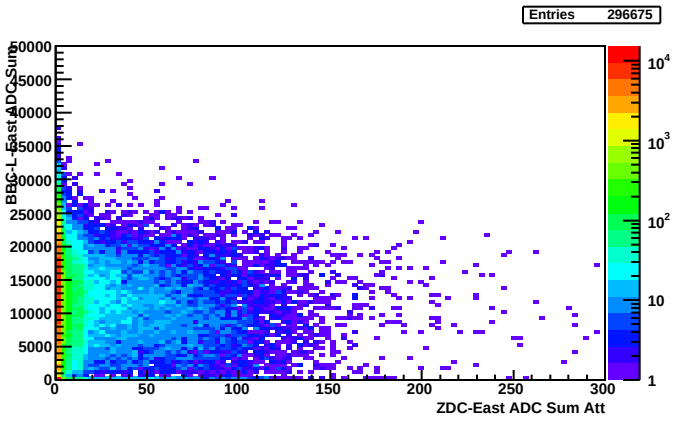

Entries 296675
TOF Mult
BBC-S-West ADC Sum

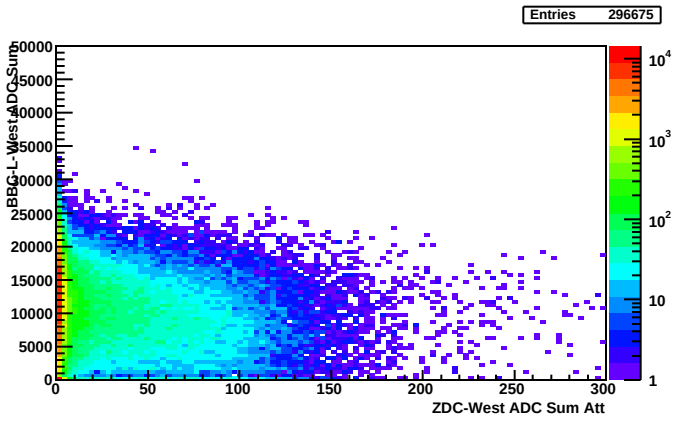

Entries 296675
TOF Mult
ZDC-East ADC Sum Att

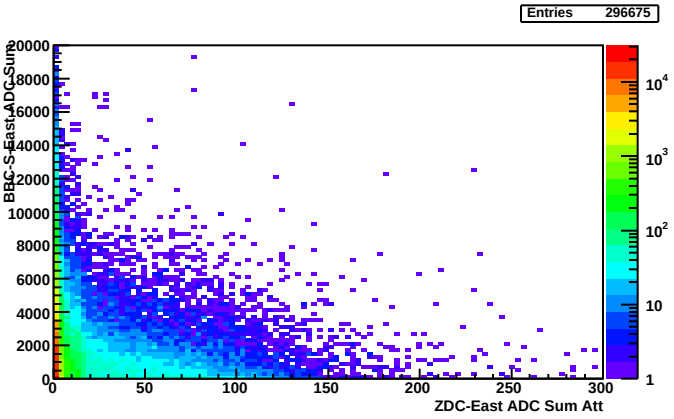

Entries 296675
TOF Mult
ZDC-West ADC Sum Att

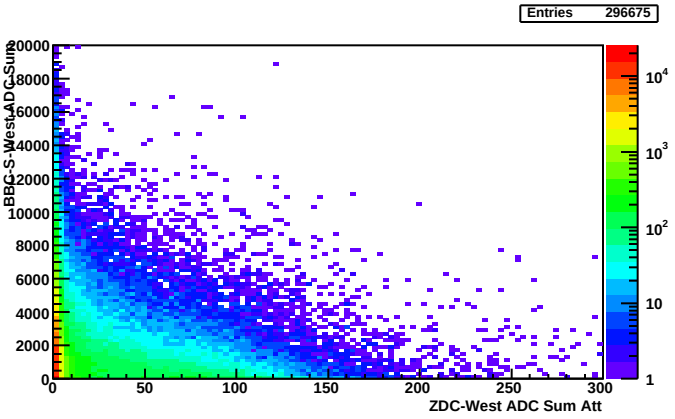

Entries 296675
BBC-L-East ADC Sum
BBC-S-East ADC Sum

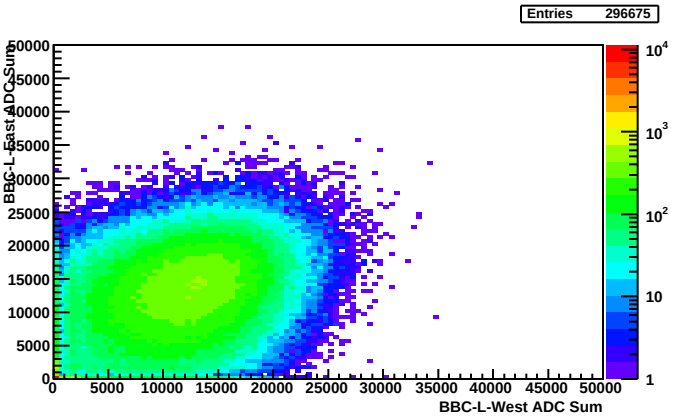

Entries 296675
BBC-L-West ADC Sum
BBC-S-West ADC Sum

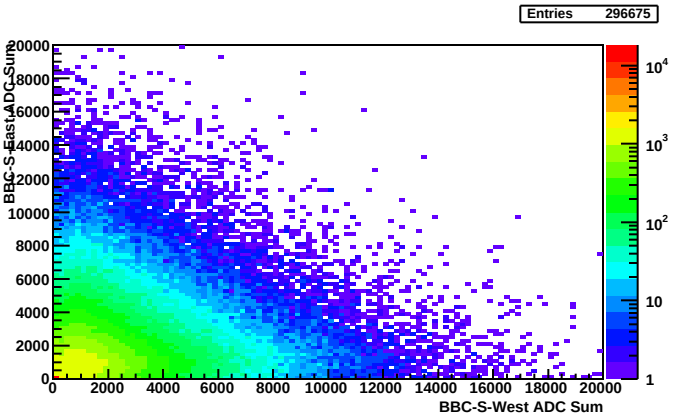

Entries 296675
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

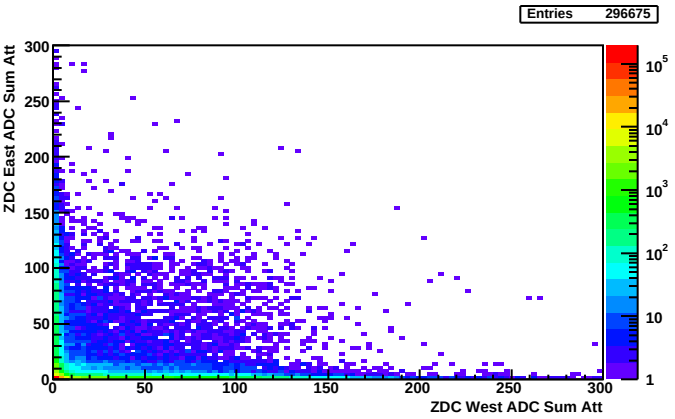

Entries 296675
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

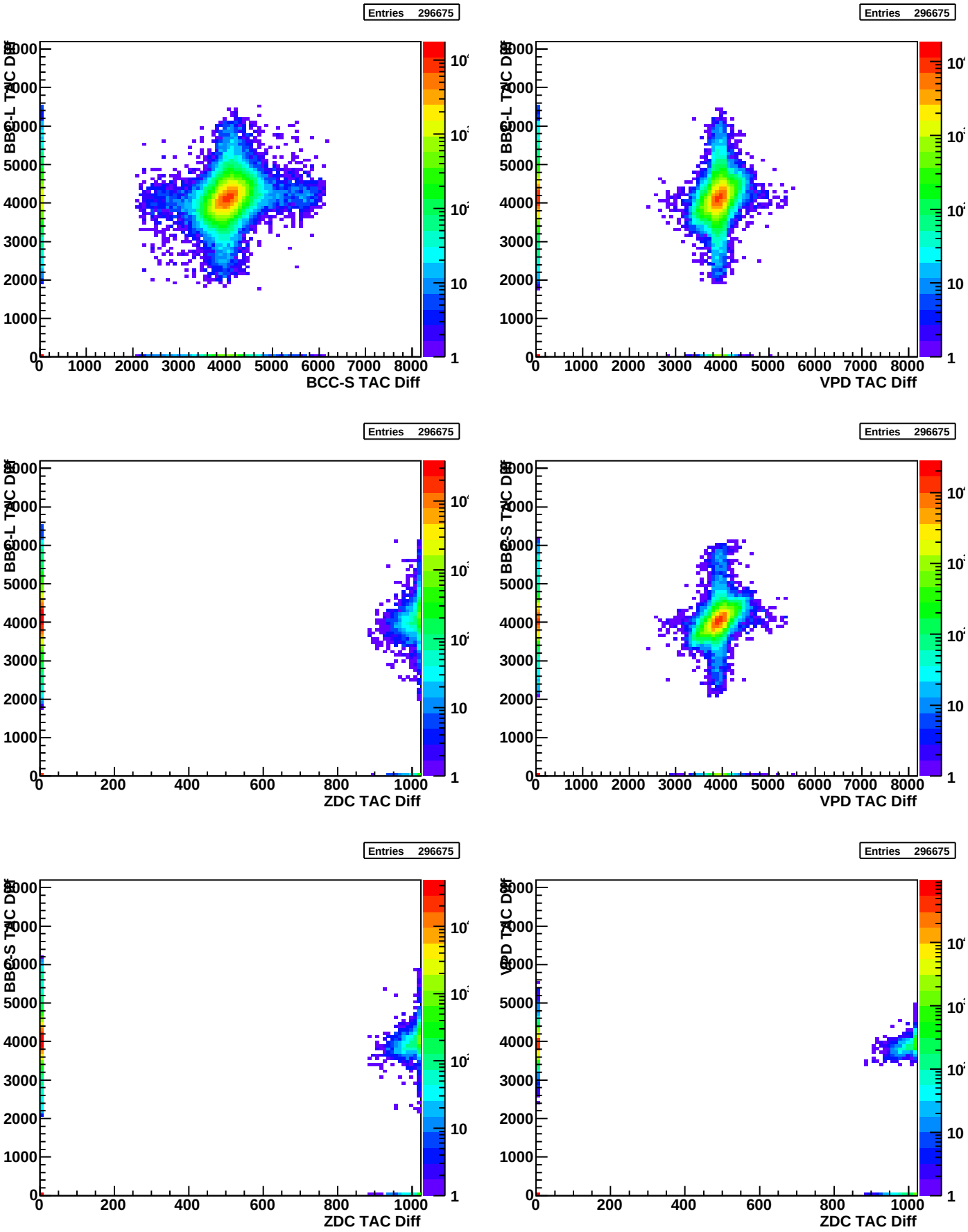

Entries 296675
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

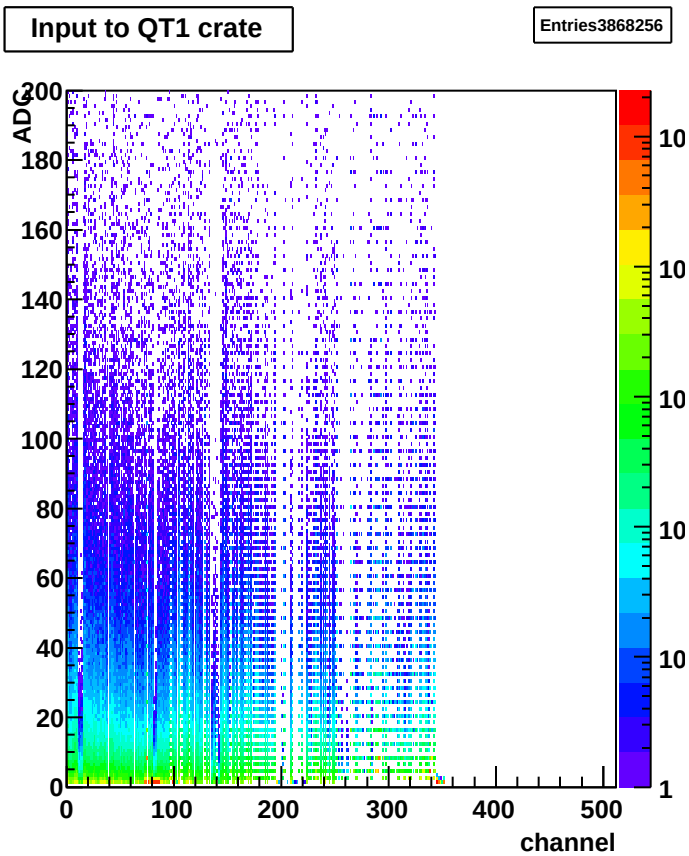

Entries 296675
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

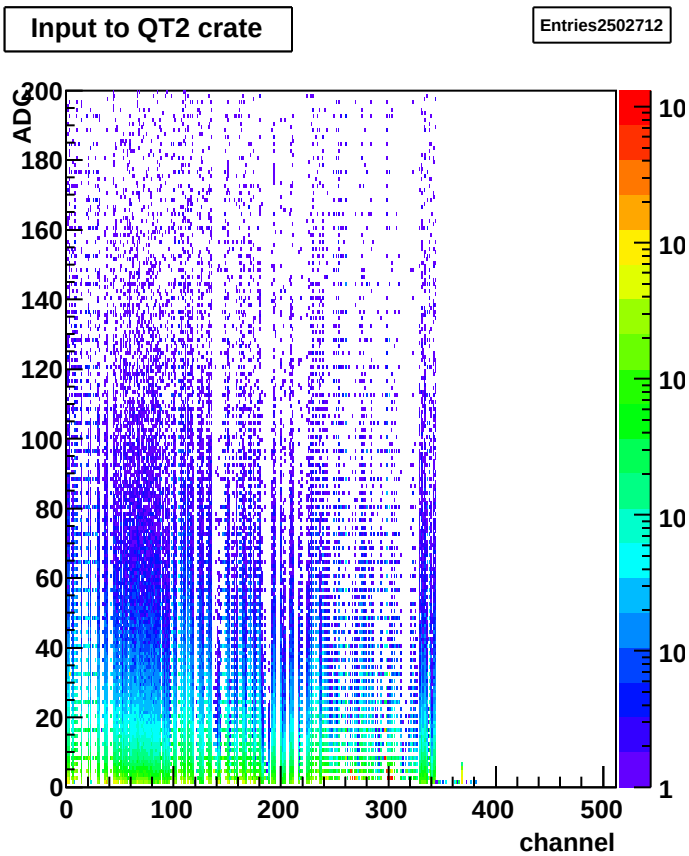

Entries 296675
BBC-L-East ADC Sum
BBC-L-West ADC Sum

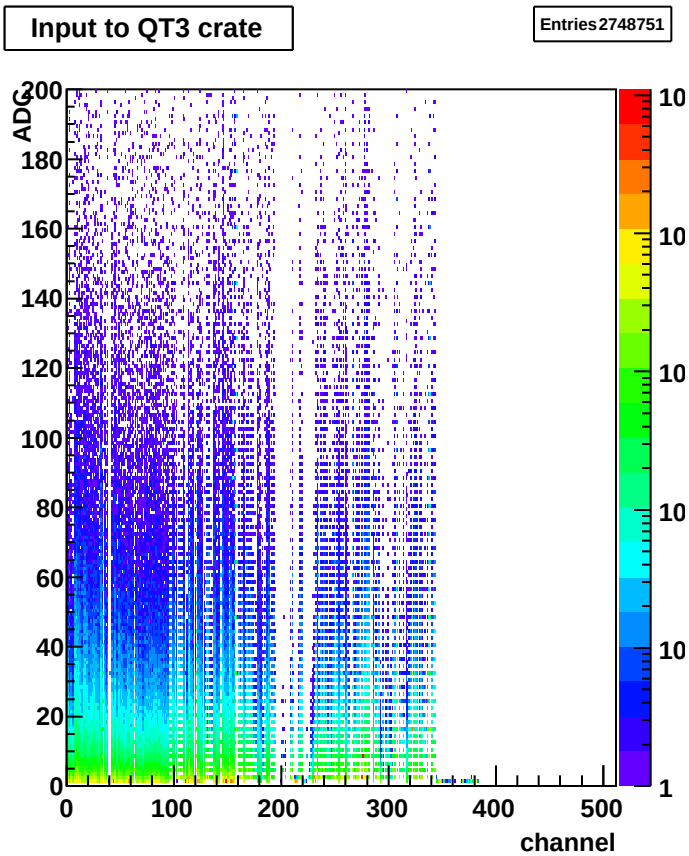

Entries 296675
BBC-S-East ADC Sum
BBC-S-West ADC Sum

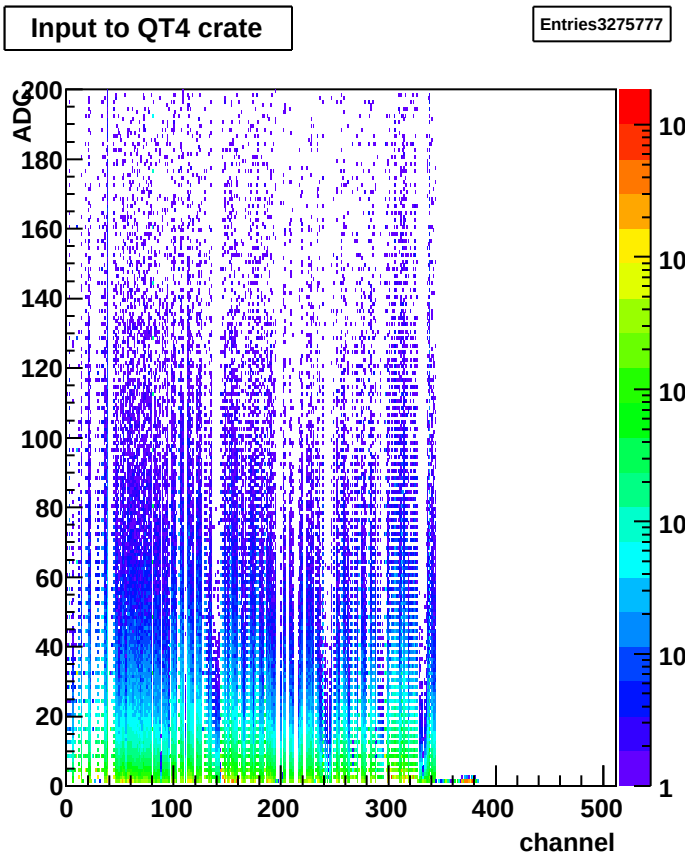

Entries 296675
ZDC East ADC Sum Att
ZDC West ADC Sum Att

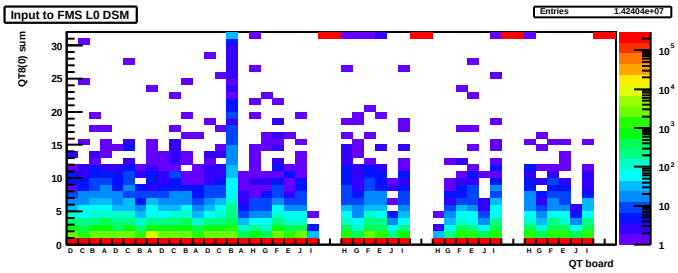

Entries 296675
BBC-L TAC Diff
BCC-S TAC Diff
Entries 296675
BBC-L TAC Diff
VPD TAC Diff
Entries 296675
BBC-L TAC Diff
ZDC TAC Diff
Entries 296675
BBC-S TAC Diff
VPD TAC Diff
Entries 296675
BBC-S TAC Diff
ZDC TAC Diff
Entries 296675
VPD TAC Diff
ZDC TAC Diff

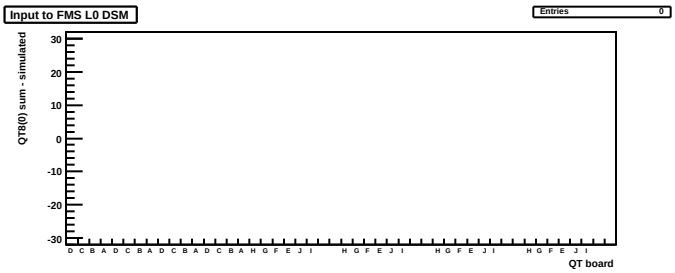

Input to QT1 crate
Entries3868256
ADC
channel

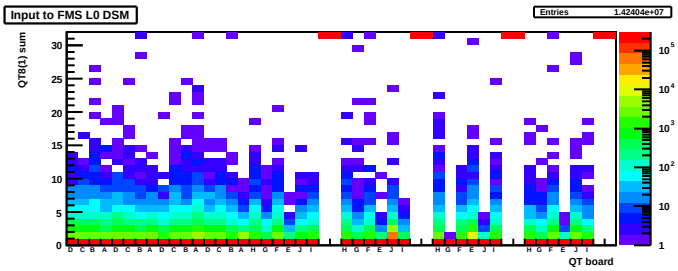

Input to QT2 crate
Entries2502712
ADC
channel

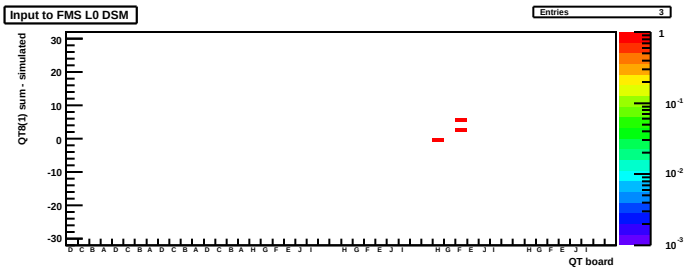

Input to QT3 crate
Entries2748751
ADC
channel

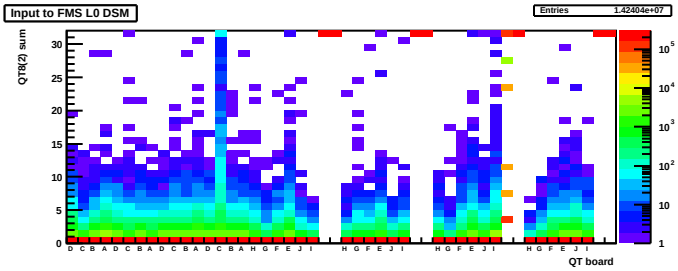

Input to QT4 crate
Entries3275777
ADC
channel

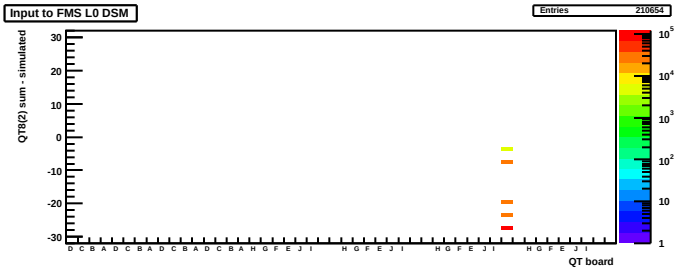

Input to FMS L0 DSM
Entries 1.42404e+07
QT8(0) sum
QT board

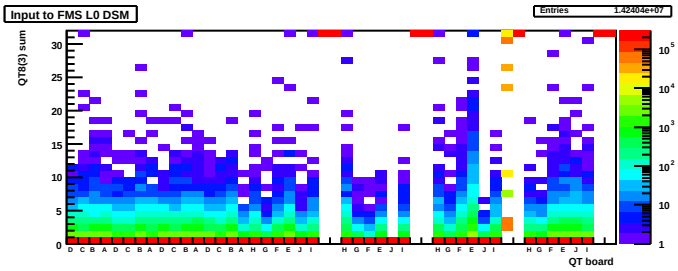

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

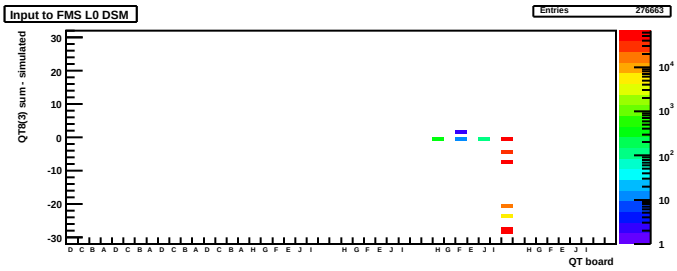

Input to FMS L0 DSM
Entries 1.42404e+07
QT8(1) sum
QT board

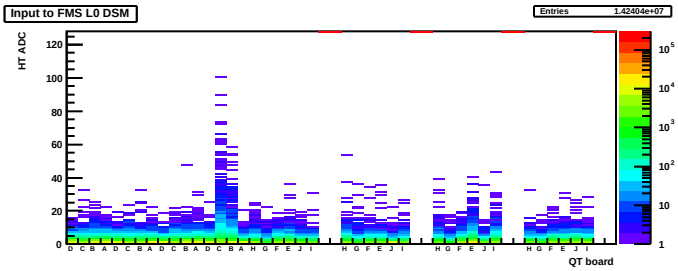

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

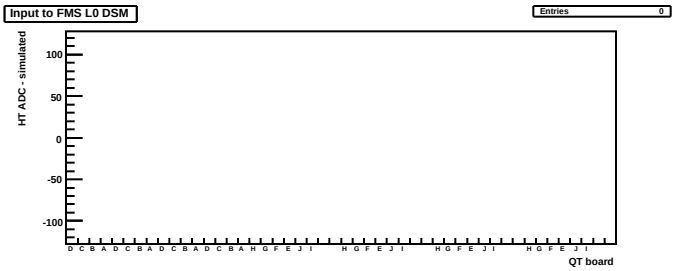

Input to FMS L0 DSM
Entries 1.42404e+07
QT8(2) sum
QT board

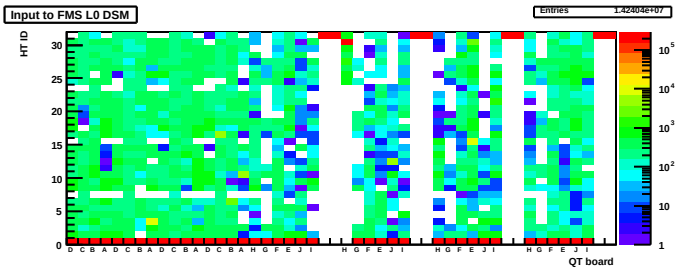

Input to FMS L0 DSM
Entries 210654
QT8(2) sum - simulated
QT board

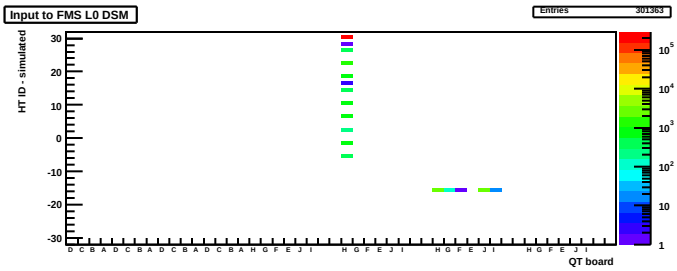

Input to FMS L0 DSM
Entries 1.42404e+07
QT8(3) sum
QT board

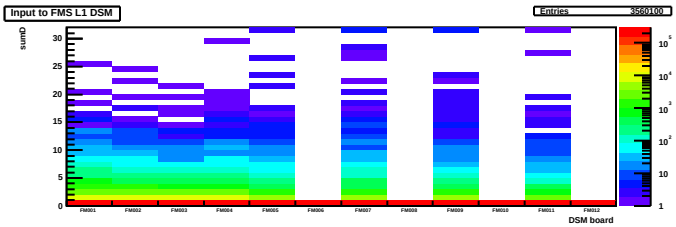

Input to FMS L0 DSM
Entries 276663
QT8(3) sum - simulated
QT board

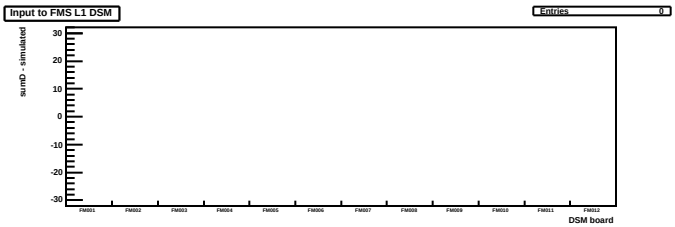

Input to FMS L0 DSM
Entries 1.42404e+07
HT ADC
QT board

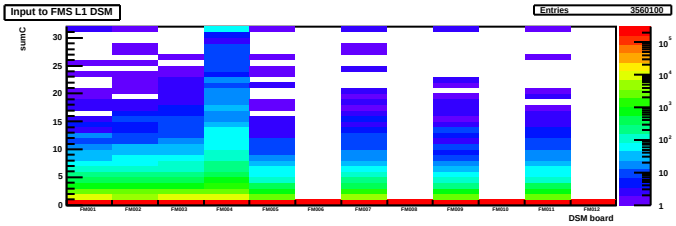

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

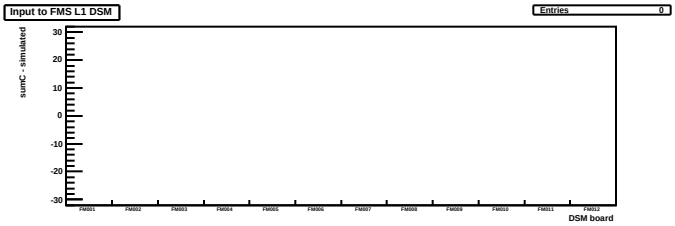

Input to FMS L0 DSM
Entries 1.42404e+07
HT ID
QT board

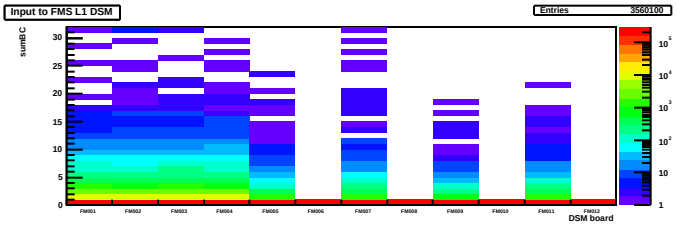

Input to FMS L0 DSM
Entries 301363
HT ID - simulated
QT board

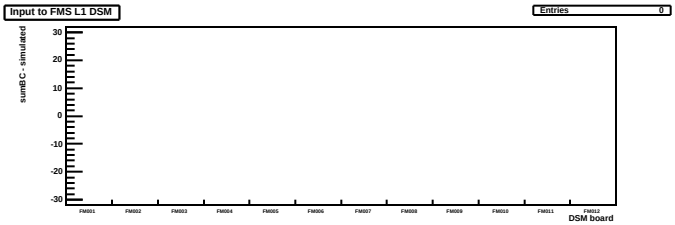

Input to FMS L1 DSM
Entries 3560100
sumD
DSM board

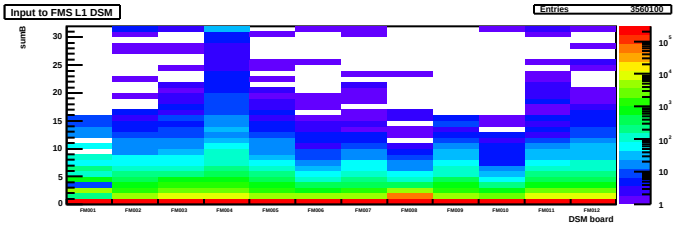

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

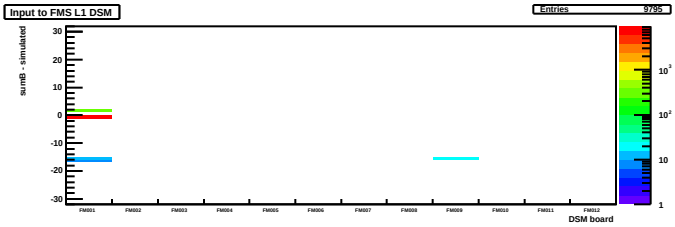

Input to FMS L1 DSM
Entries 3560100
sumC
DSM board

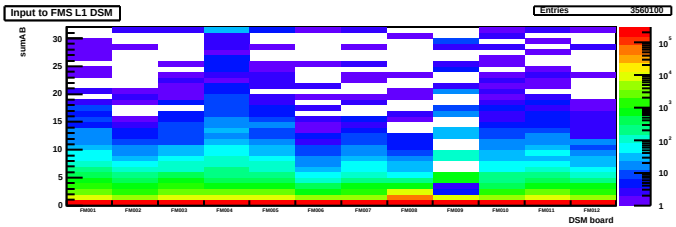

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

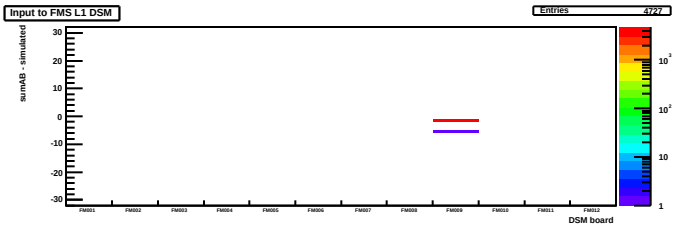

Input to FMS L1 DSM
Entries 3560100
sumBC
DSM board

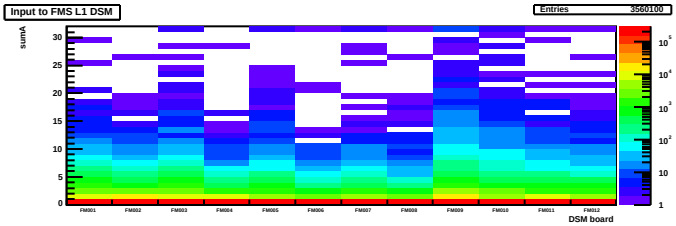

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

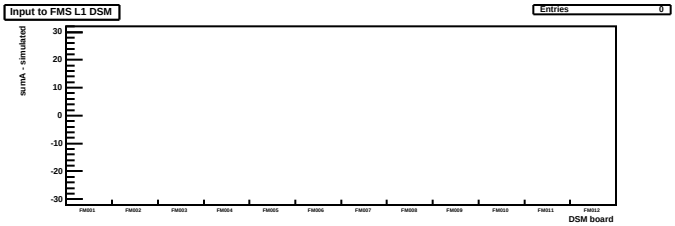

Input to FMS L1 DSM
Entries 3560100
sumB
DSM board

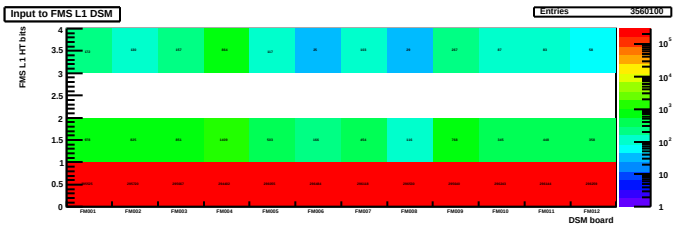

Input to FMS L1 DSM
Entries 9795
sumB - simulated
DSM board

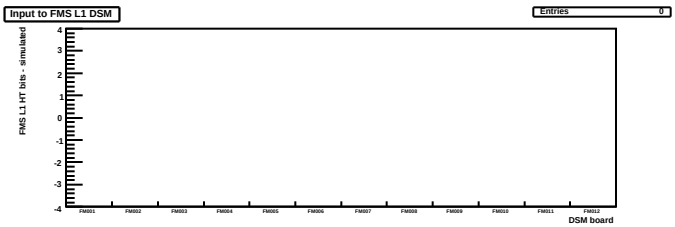

Input to FMS L1 DSM
Entries 3560100
sumAB
DSM board

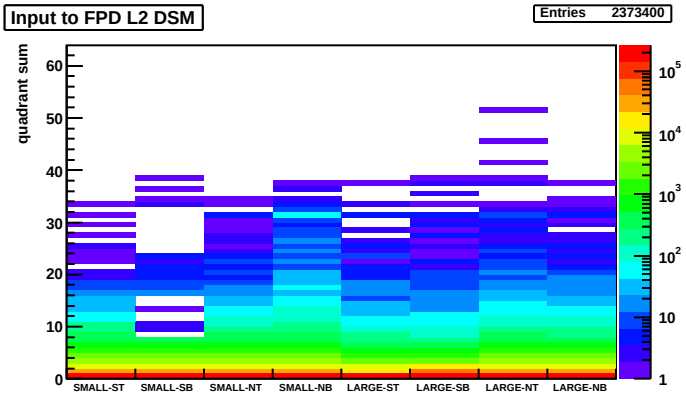

Input to FMS L1 DSM
Entries 4727
sumAB - simulated
DSM board

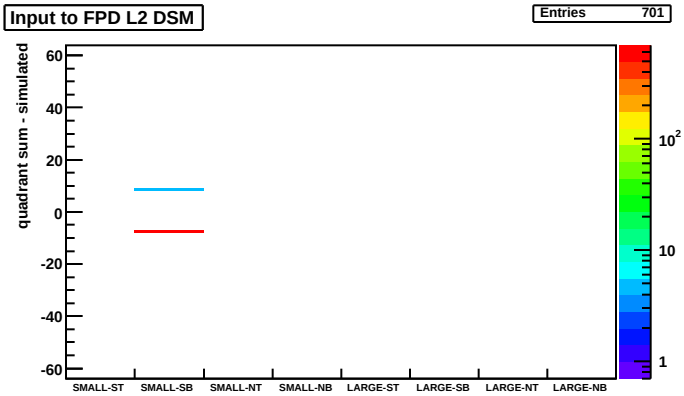

Input to FMS L1 DSM
Entries 3560100
sumA
DSM board

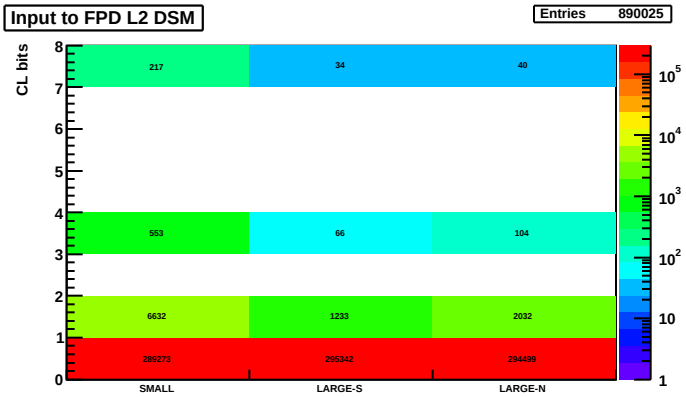

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

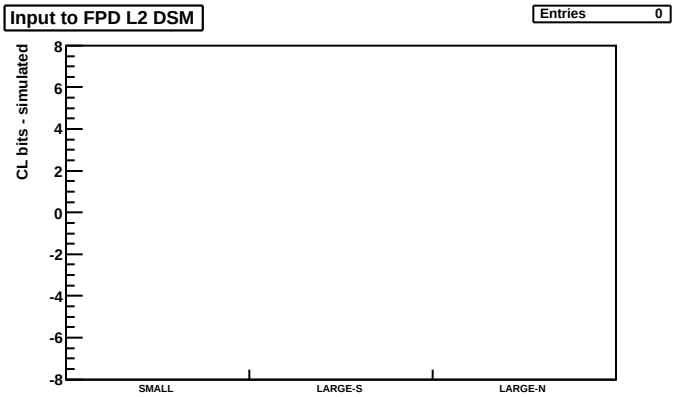

Input to FMS L1 DSM
Entries 3560100
FMS L1 HT bits
DSM board

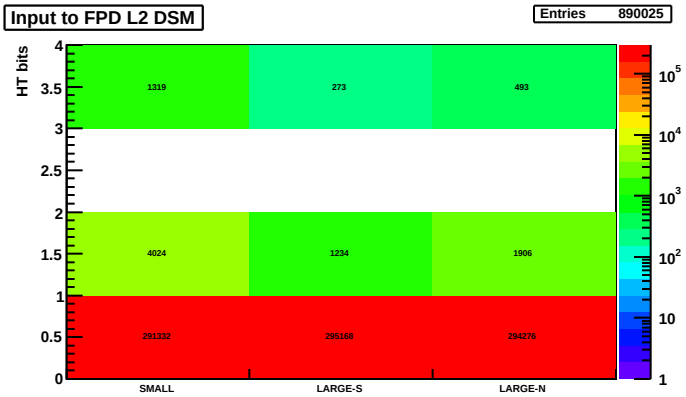

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

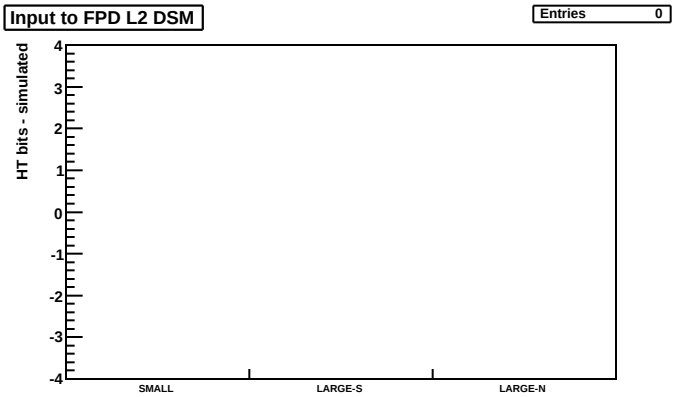

Input to FPD L2 DSM
Entries 2373400
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

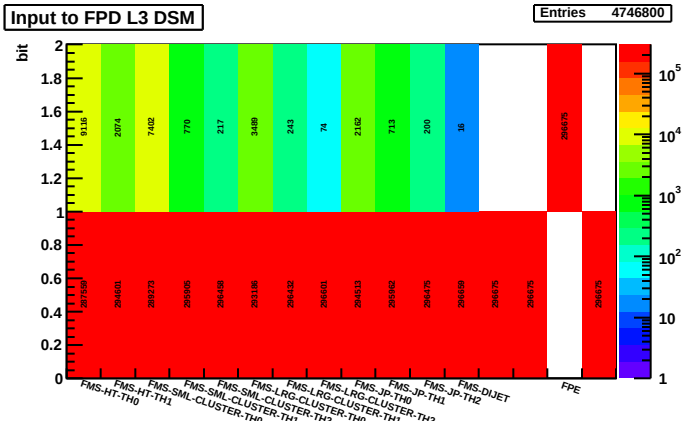

Input to FPD L2 DSM
Entries 701
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

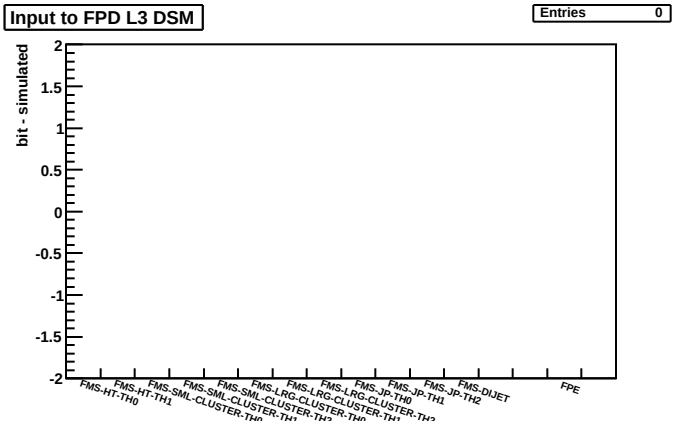

Input to FPD L2 DSM
Entries 890025
CL bits
217
34
40
553
66
104
6632
1233
2032
289273
295342
294499
SMALL
LARGE-S
LARGE-N

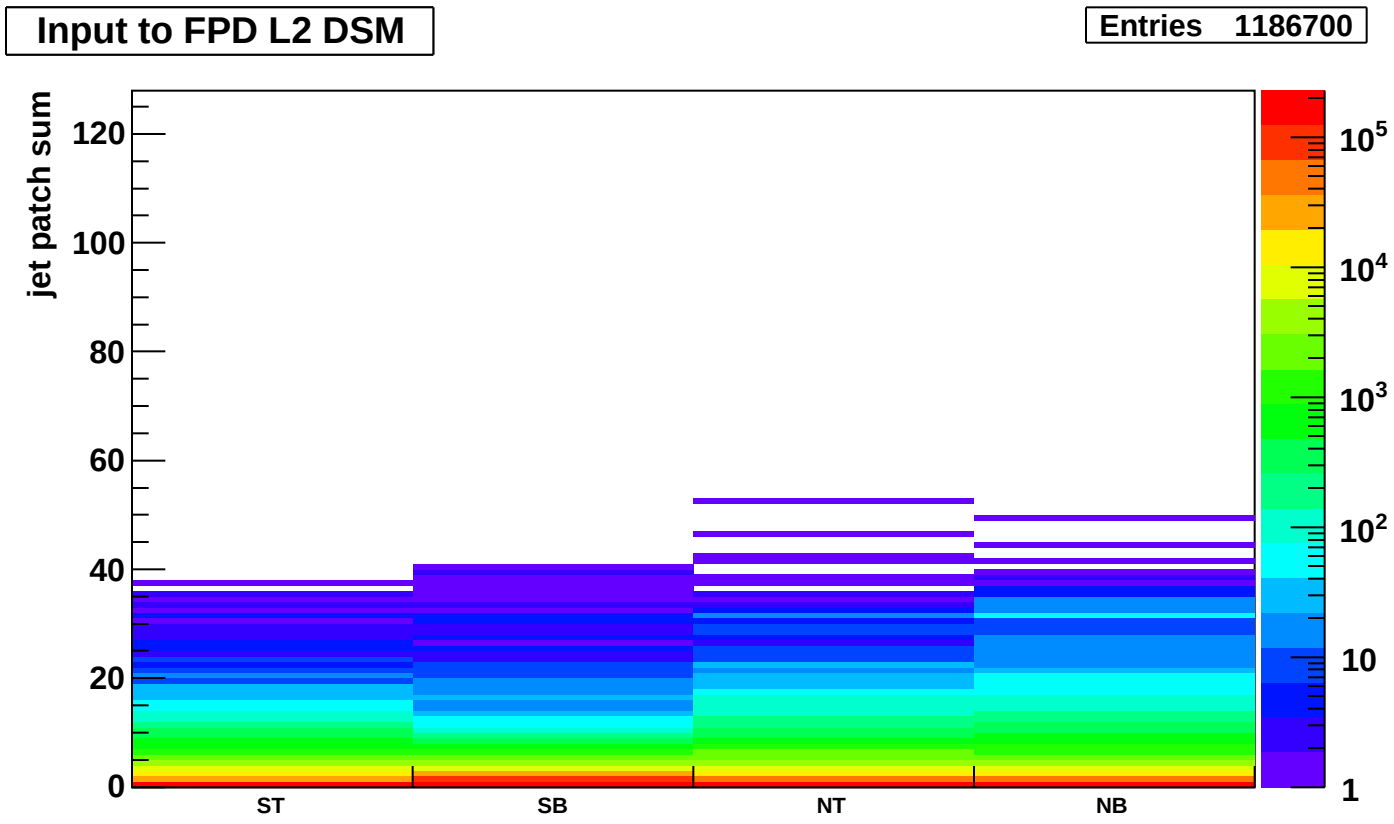

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

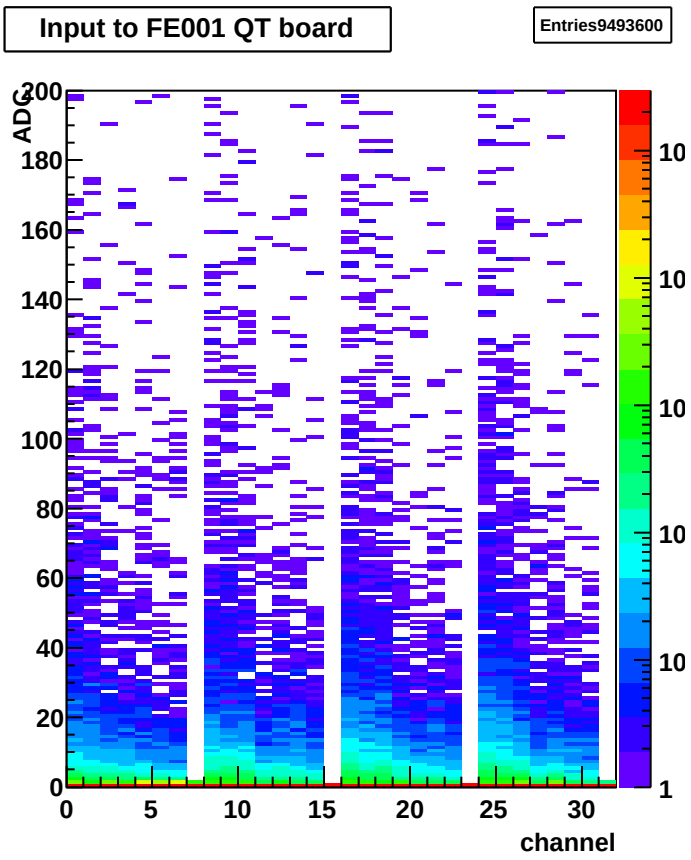

Input to FPD L2 DSM
Entries 890025
HT bits
1319
273
493
4024
1234
1906
291332
295168
294276
SMALL
LARGE-S
LARGE-N

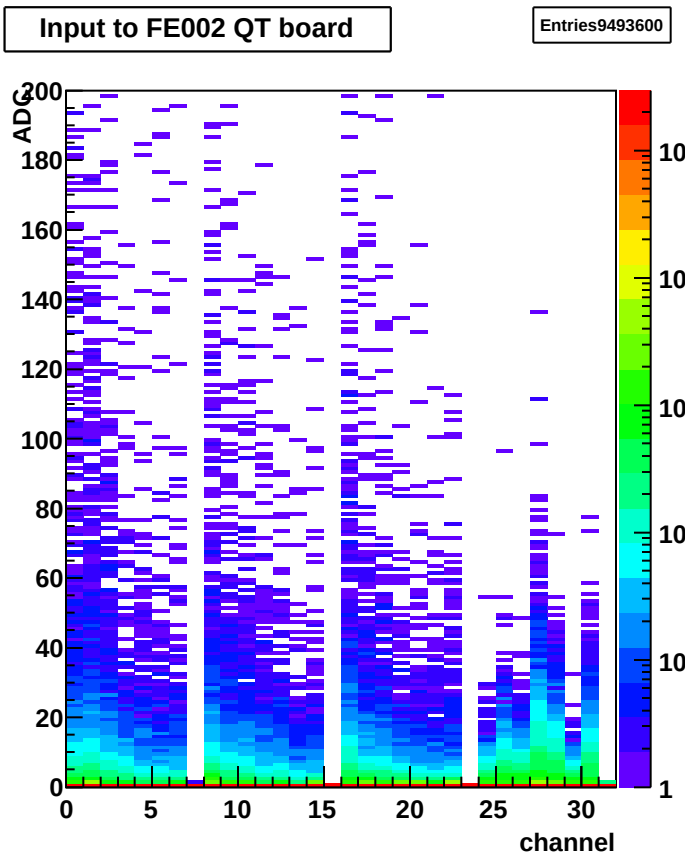

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

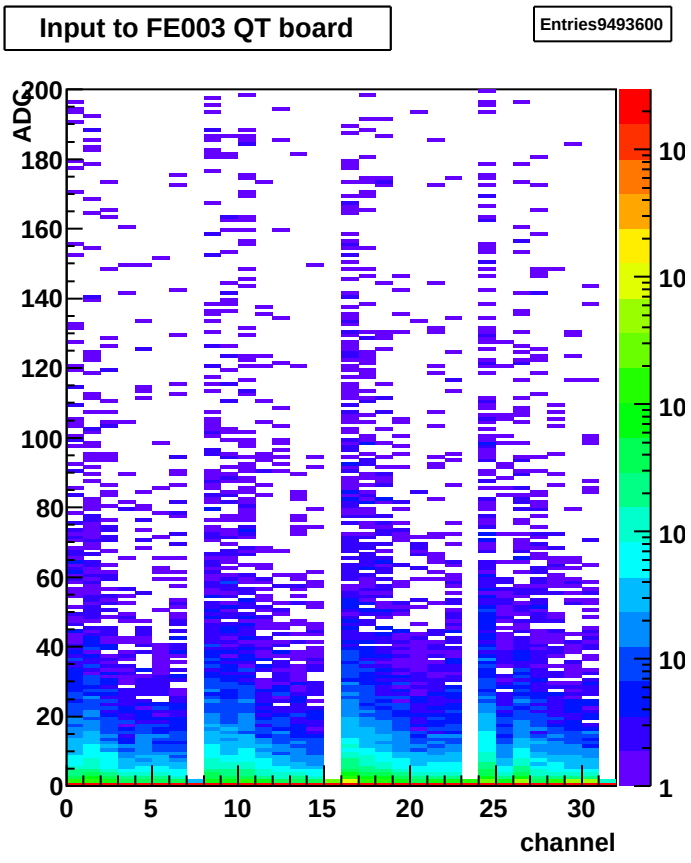

Input to FPD L3 DSM
Entries 4746800
bit
9116
2074
7402
770
217
3489
243
74
2162
713
200
16
296675
297539
294601
289273
295905
296498
293186
296432
296601
294513
295962
296475
296659
296675
296675
296675
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

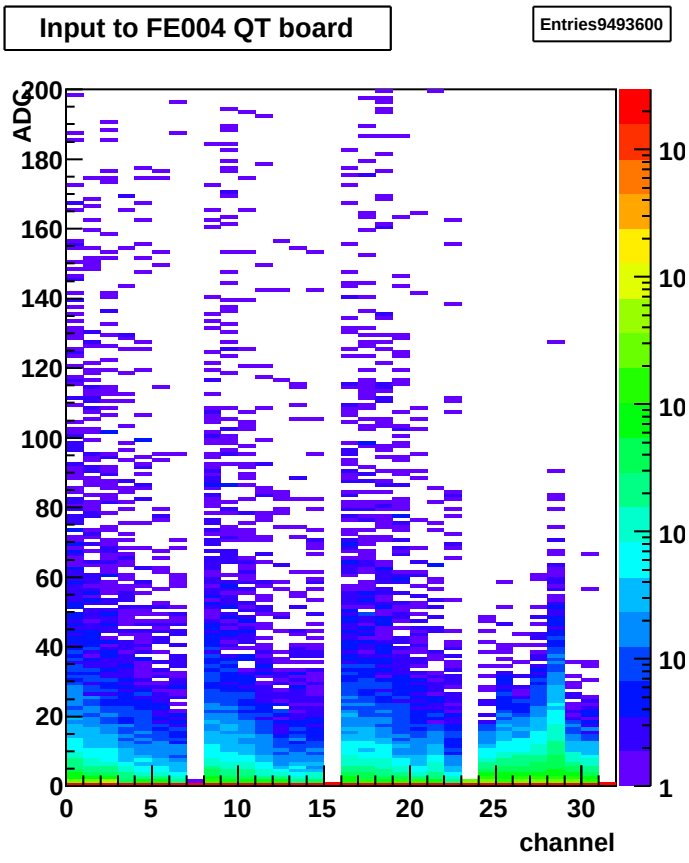

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

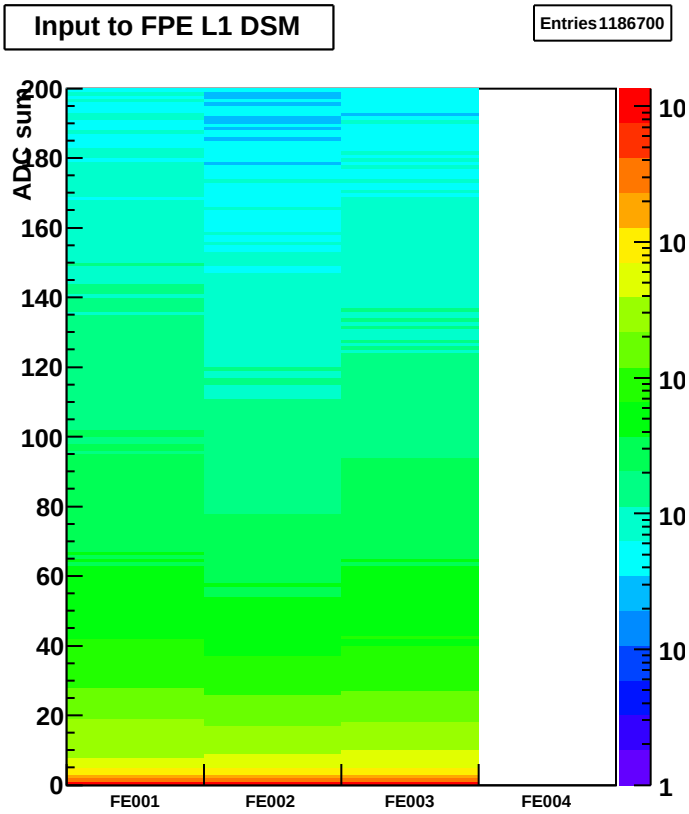

Input to FPD L2 DSM
Entries 1186700
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

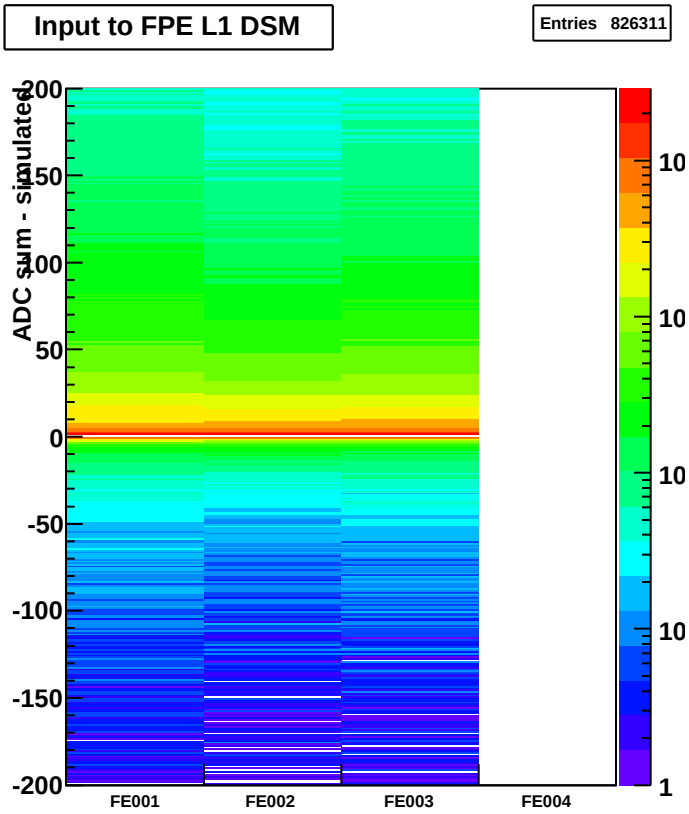

Input to FE001 QT board
Entries9493600
ADC
channel

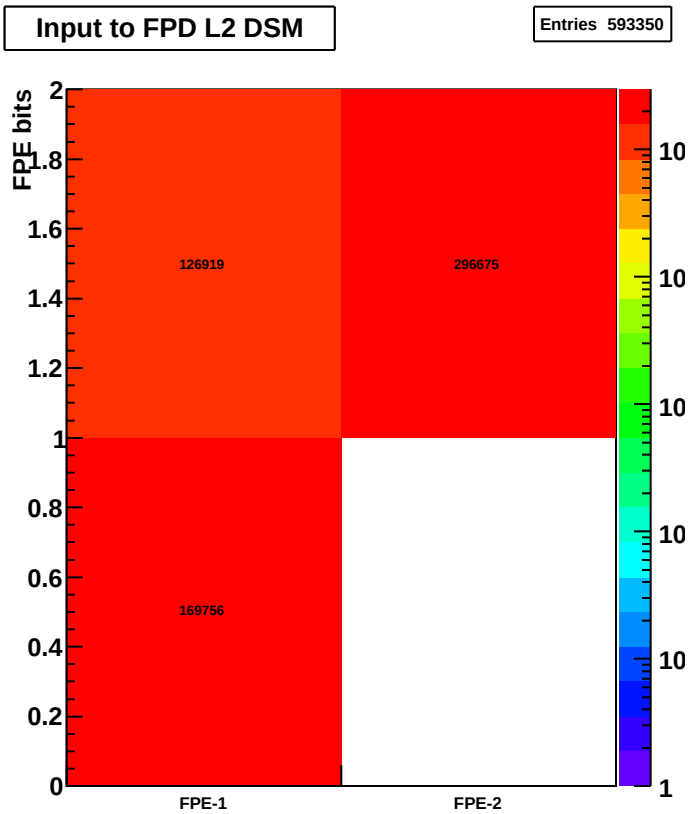

Input to FE002 QT board
Entries9493600
ADC
channel

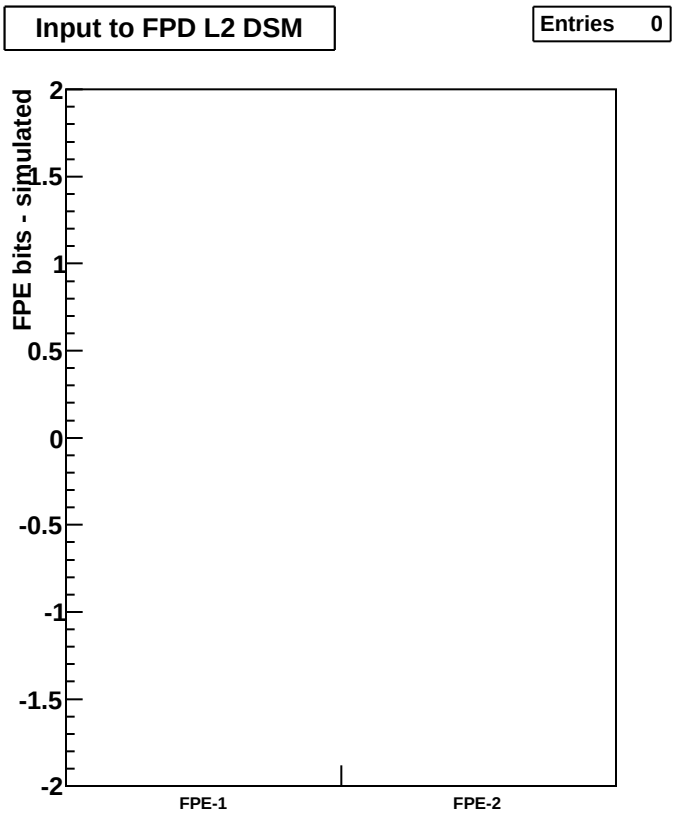

Input to FE003 QT board
Entries9493600
ADC
channel


Input to FE004 QT board
Entries9493600
ADC
channel


Input to FPE L1 DSM
Entries 1186700
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 826311
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 593350
FPE bits
126919
296675
169756
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2