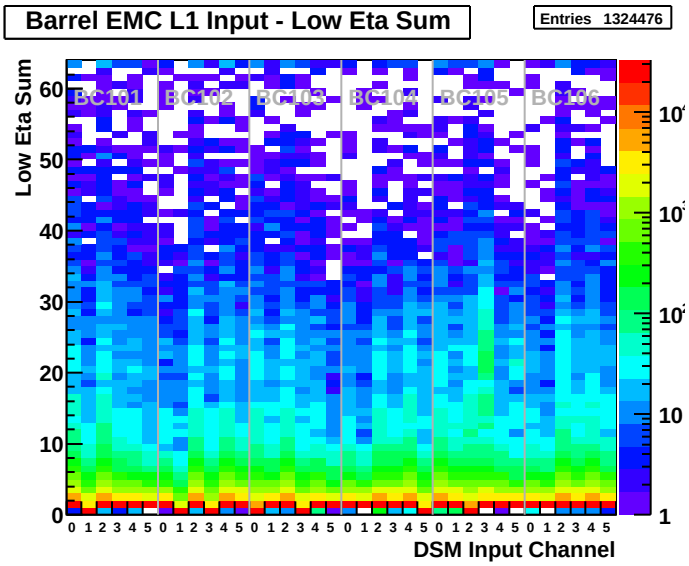
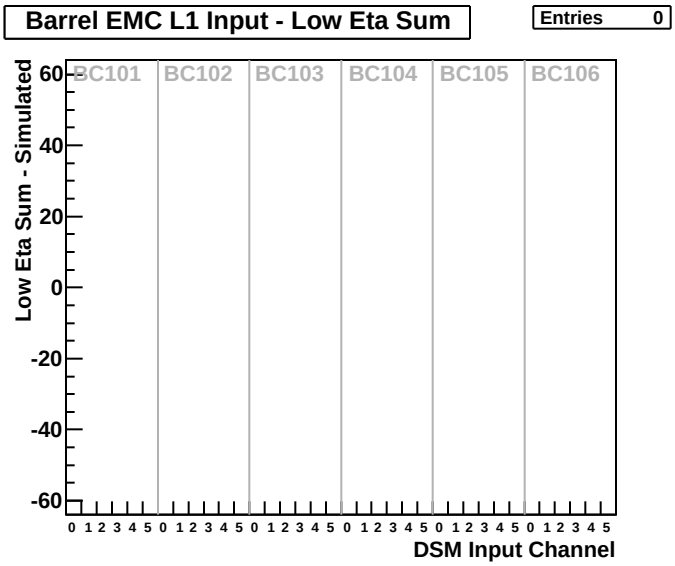
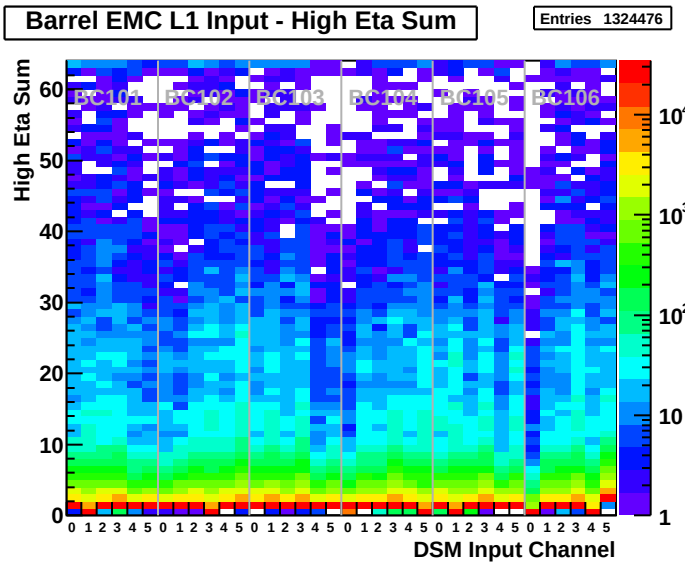
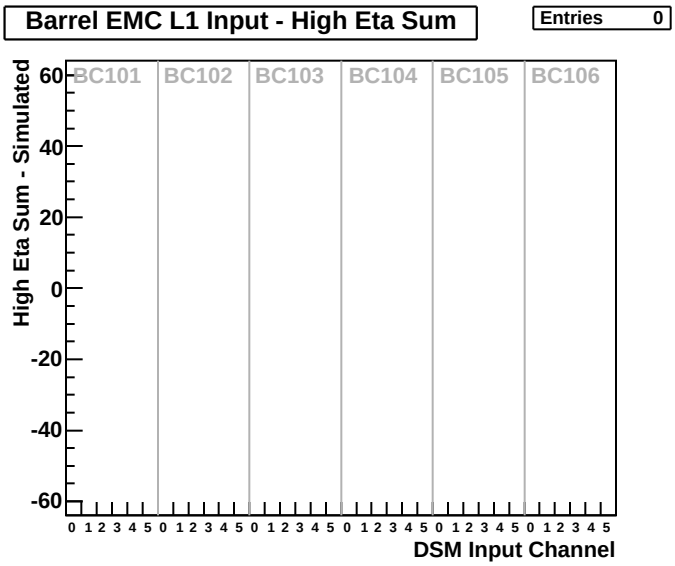
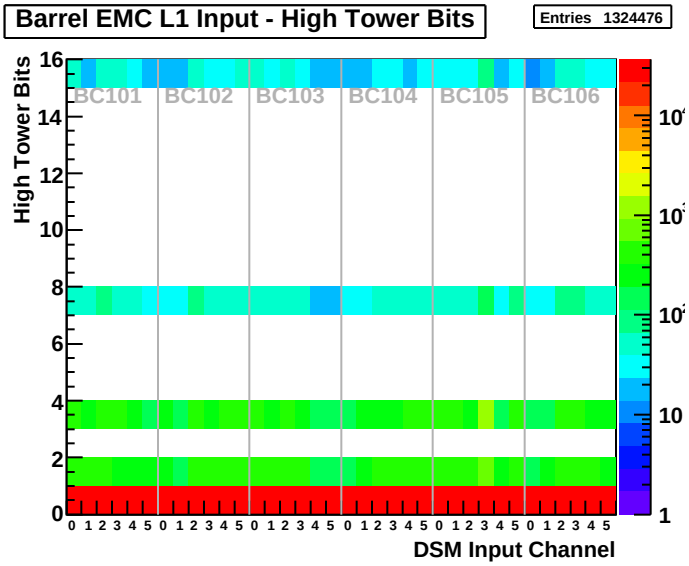
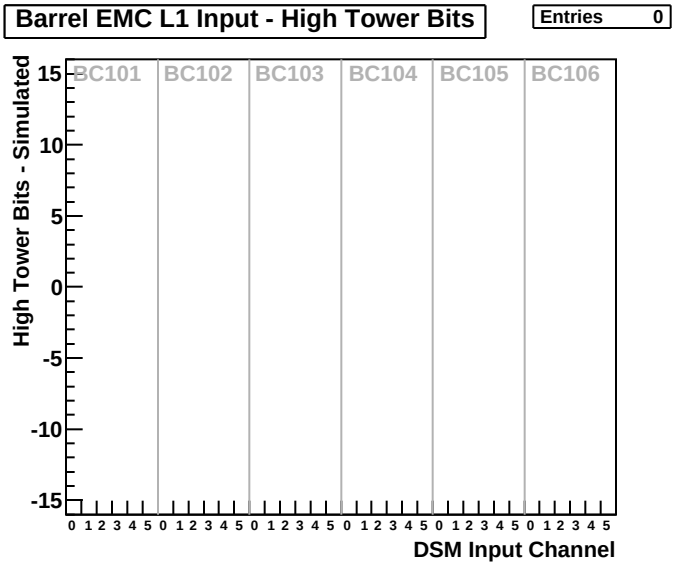

Barrel EMC L1 Input - Low Eta Sum
Entries 1324476
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

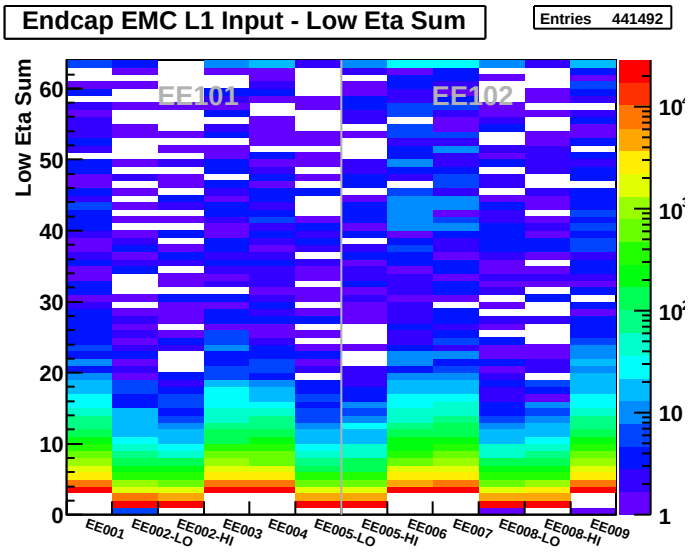

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

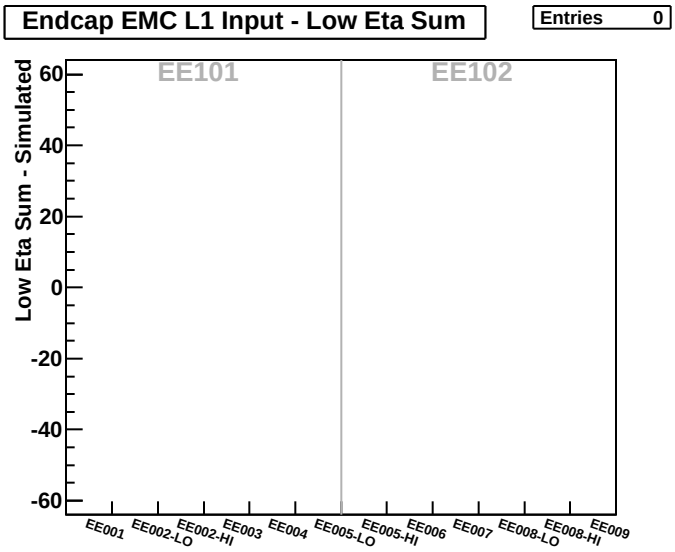

Barrel EMC L1 Input - High Eta Sum
Entries 1324476
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

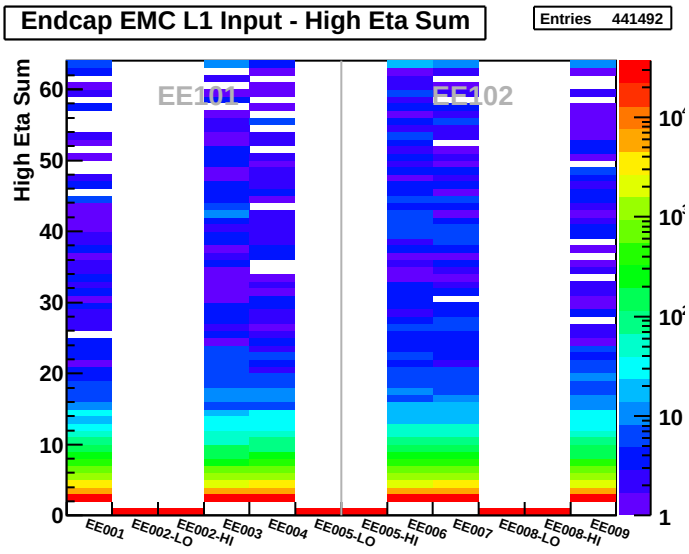

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

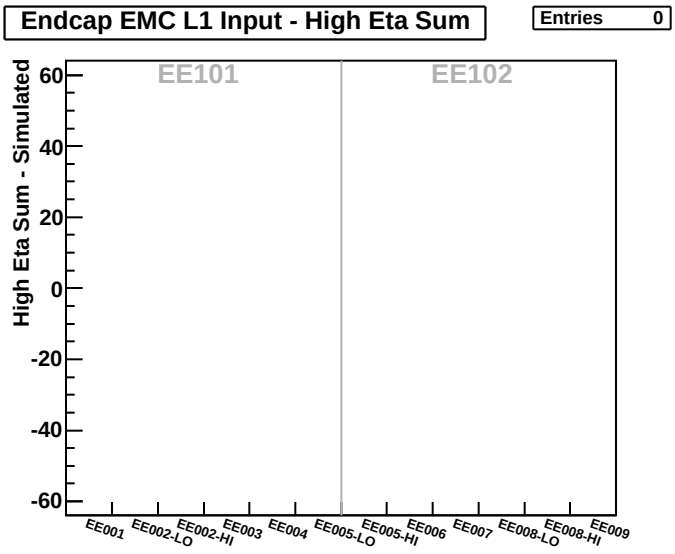

Barrel EMC L1 Input - High Tower Bits
Entries 1324476
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

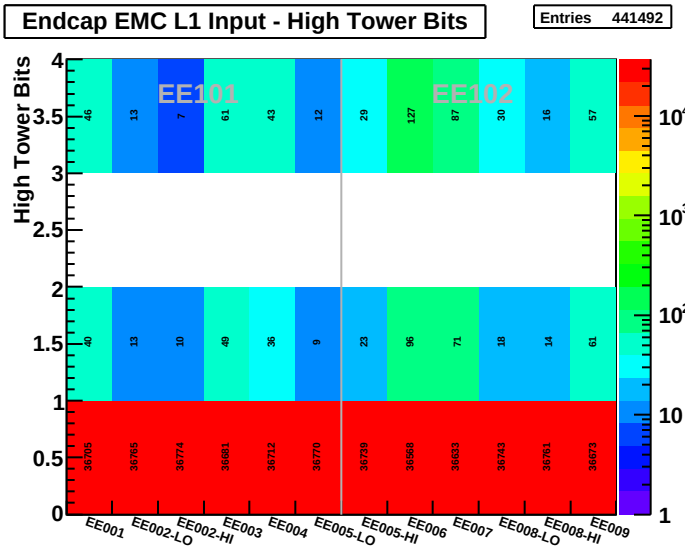

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

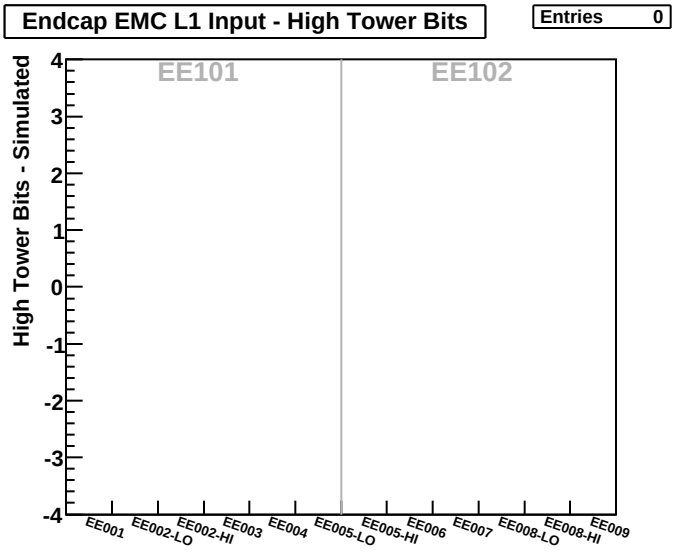

Endcap EMC L1 Input - Low Eta Sum
Entries 441492
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

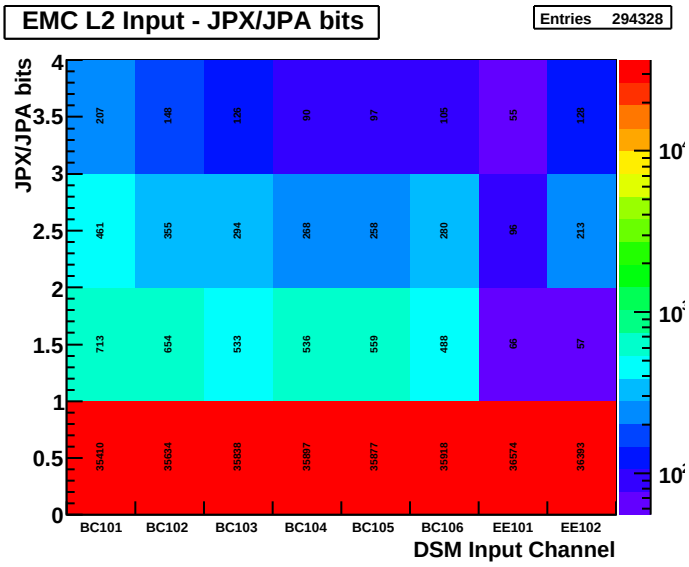

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

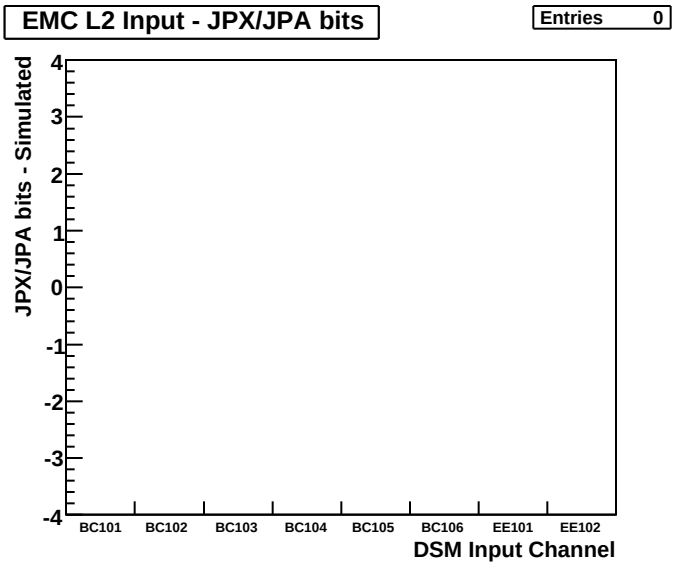

Endcap EMC L1 Input - High Eta Sum
Entries 441492
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

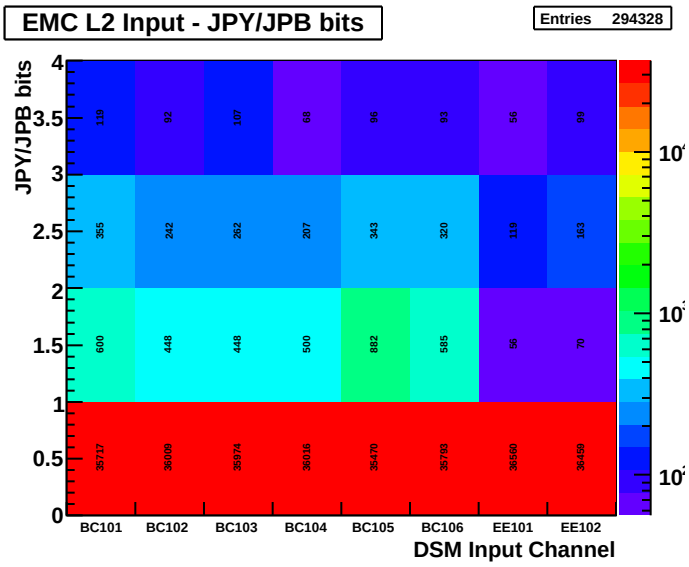

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

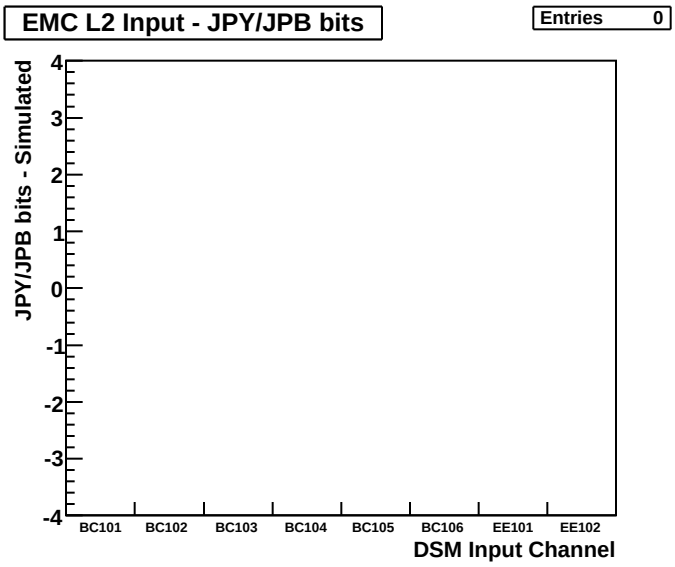

Endcap EMC L1 Input - High Tower Bits
Entries 441492
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

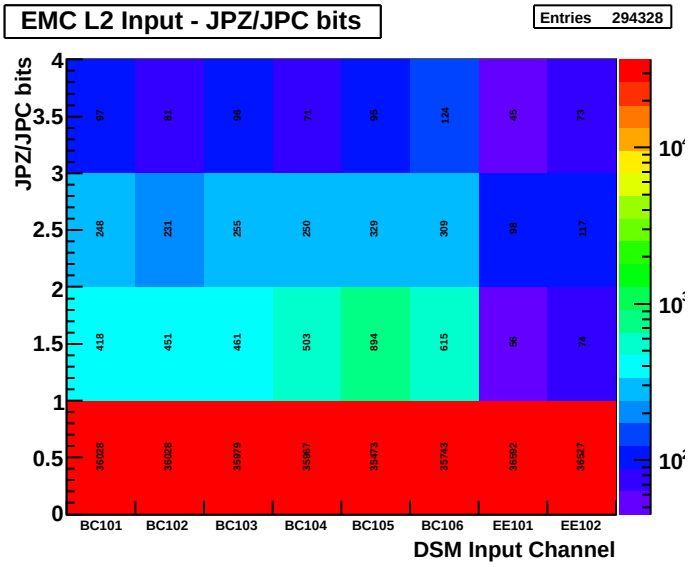

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

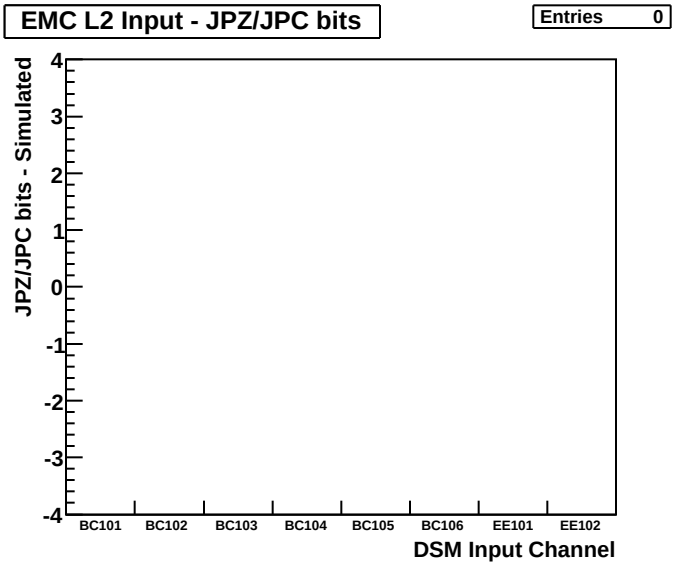

EMC L2 Input - JPX/JPA bits
Entries 294328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
207 148 126 90 97 105 55 128
461 355 294 268 258 280 98 213
713 654 533 536 559 488 66 57
35410 35634 35638 35897 35877 35918 36574 36393

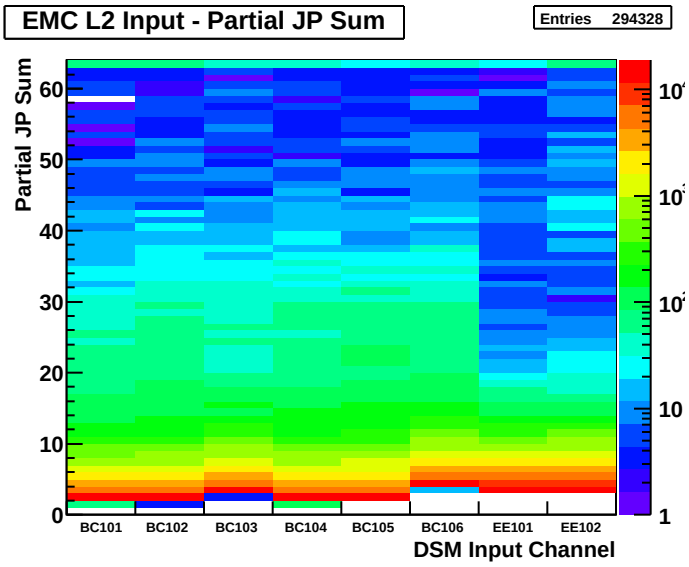

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

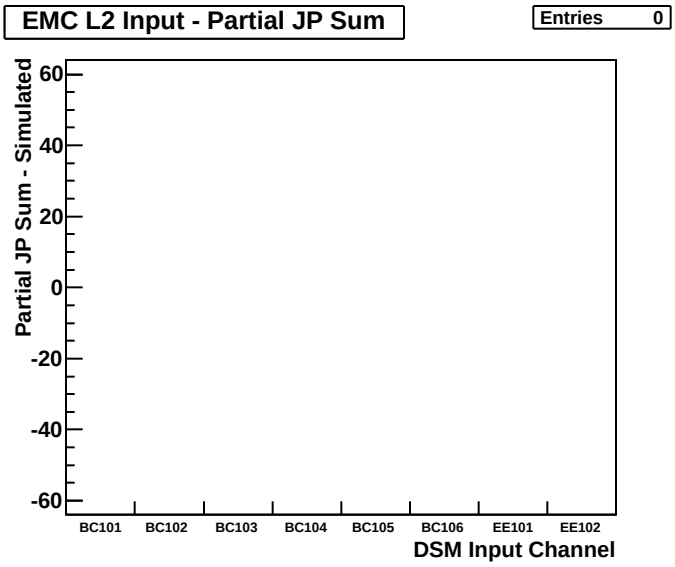

EMC L2 Input - JPY/JPB bits
Entries 294328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
119 92 107 68 96 93 56 99
355 242 262 207 343 320 119 163
600 448 448 500 882 585 56 70
35717 36009 35974 36016 35470 35793 36560 36459

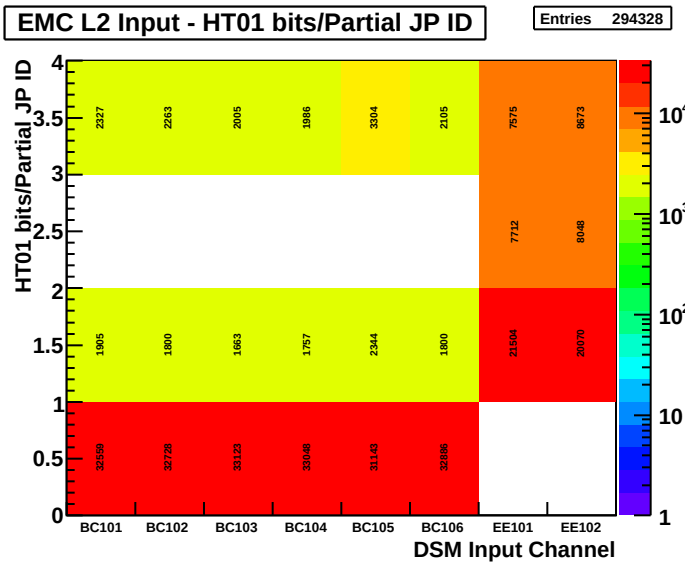

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

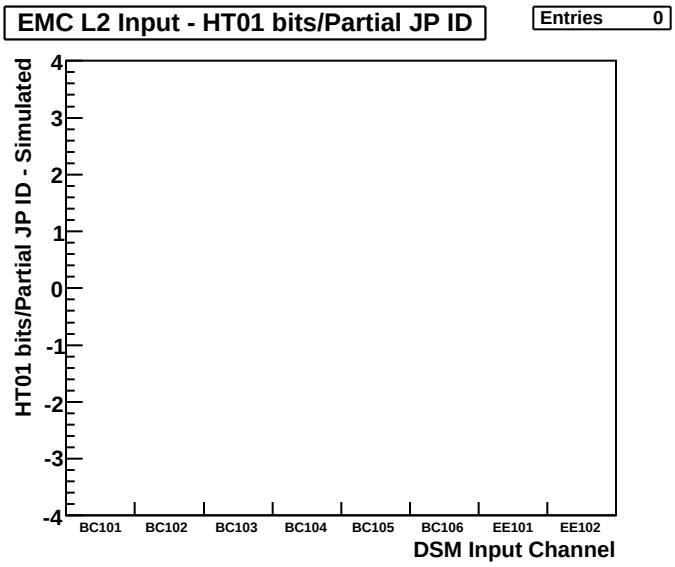

EMC L2 Input - JPZ/JPC bits
Entries 294328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
97 81 96 71 95 124 45 73
248 231 255 250 329 309 98 117
418 451 461 503 894 615 56 74
36028 36028 35979 35967 35473 35743 36592 36527

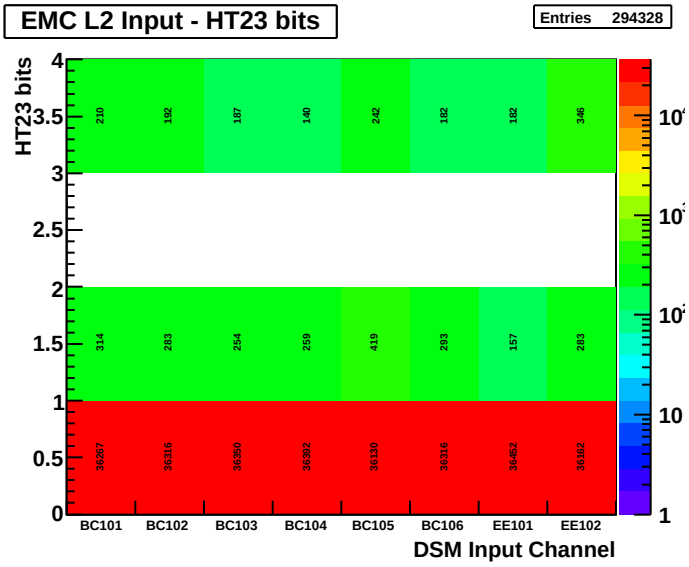

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

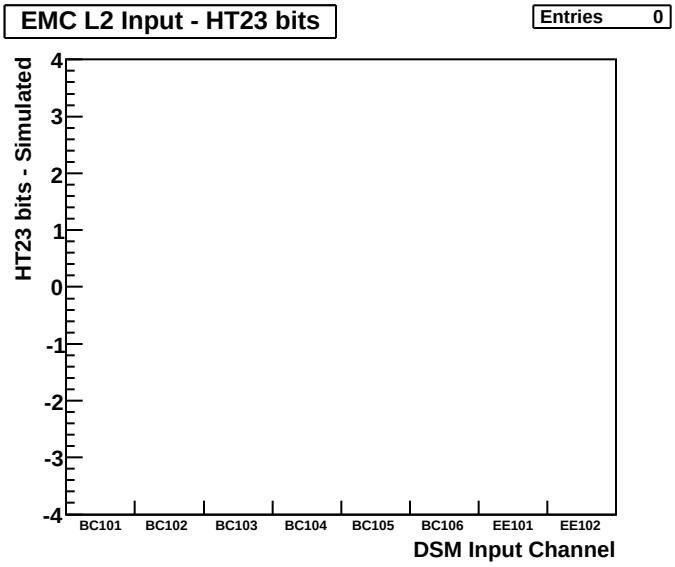

EMC L2 Input - Partial JP Sum
Entries 294328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

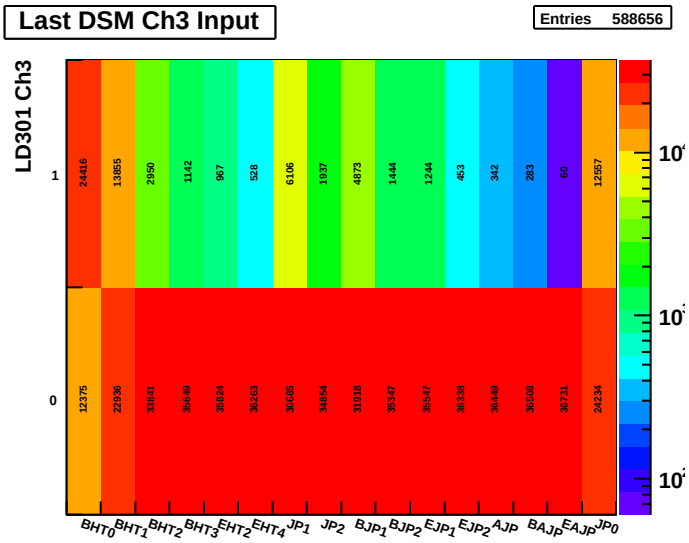

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

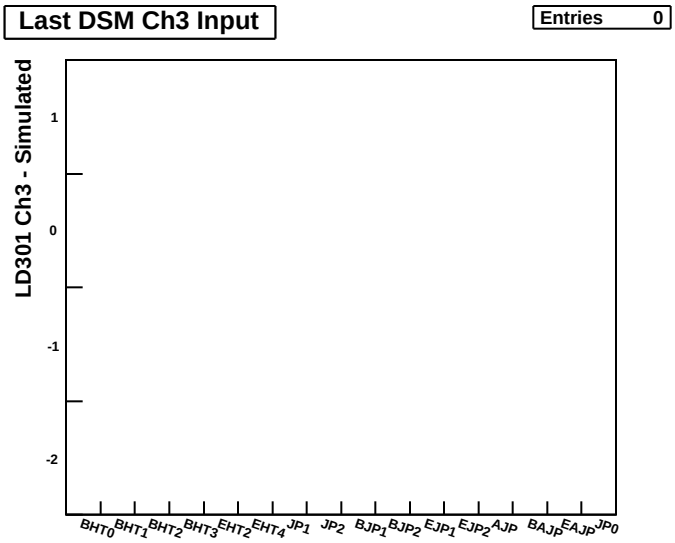

EMC L2 Input - HT01 bits/Partial JP ID
Entries 294328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

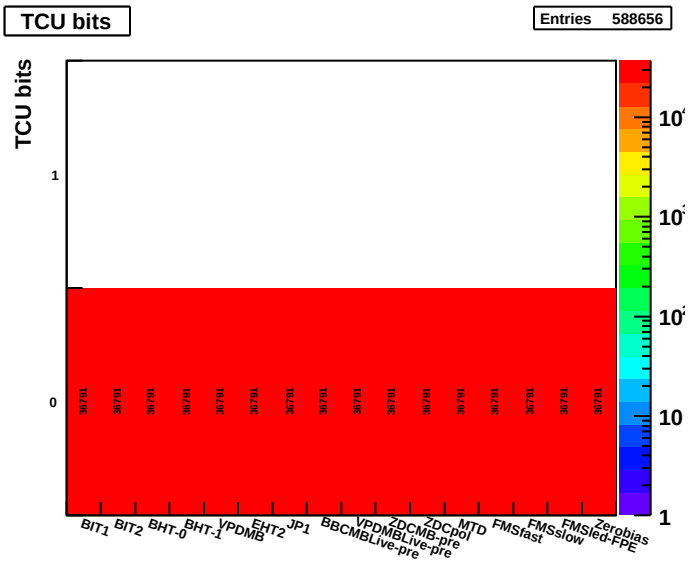

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

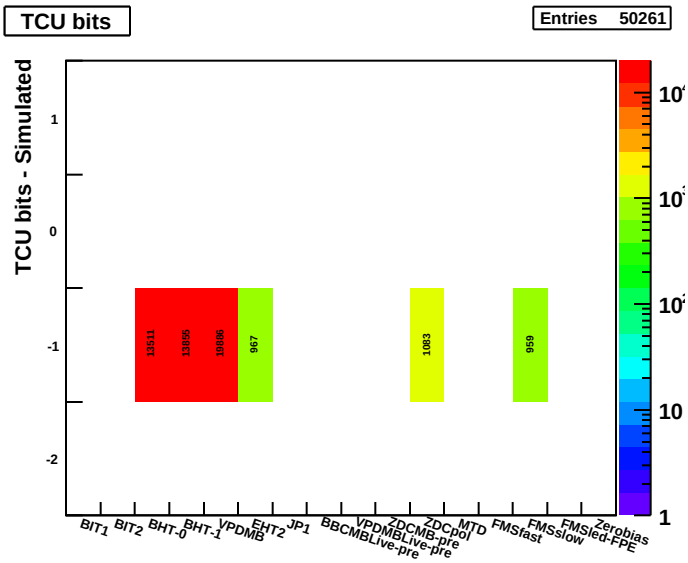

EMC L2 Input - HT23 bits
Entries 294328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

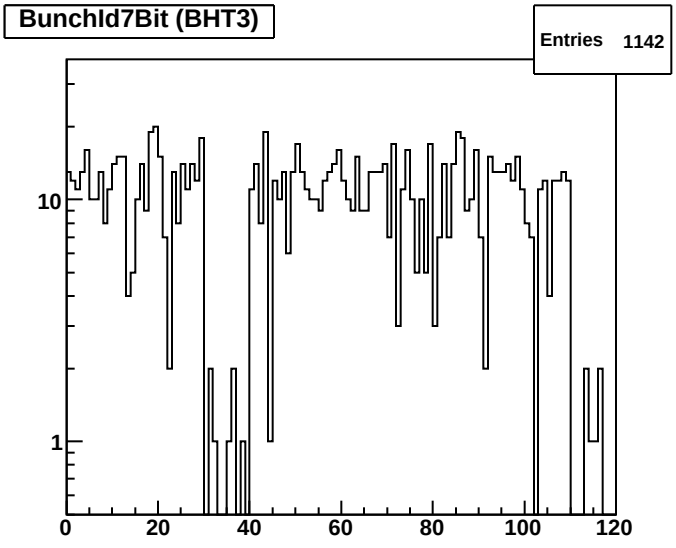

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

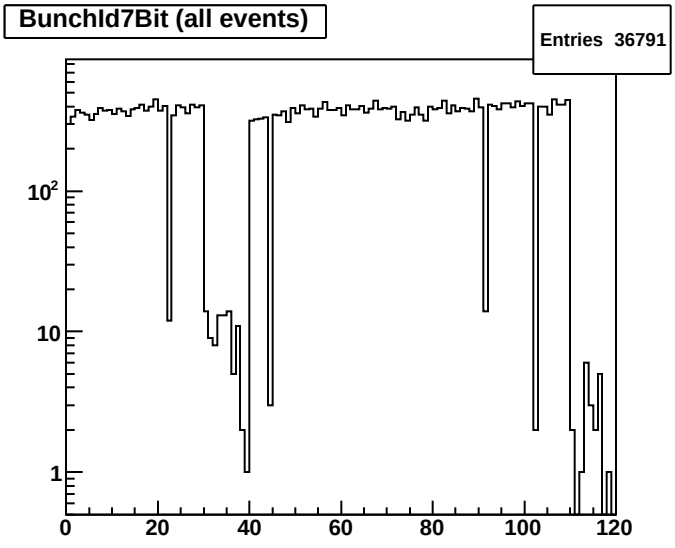

Last DSM Ch3 Input
Entries 588656
LD301 Ch3
24416 13855 2950 1142 967 528 6106 1937 4873 1444 1244 453 342 283 60 12557
12375 22936 33841 35649 35824 36263 30685 34854 31918 35347 35547 36338 36449 36508 36731 24234
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

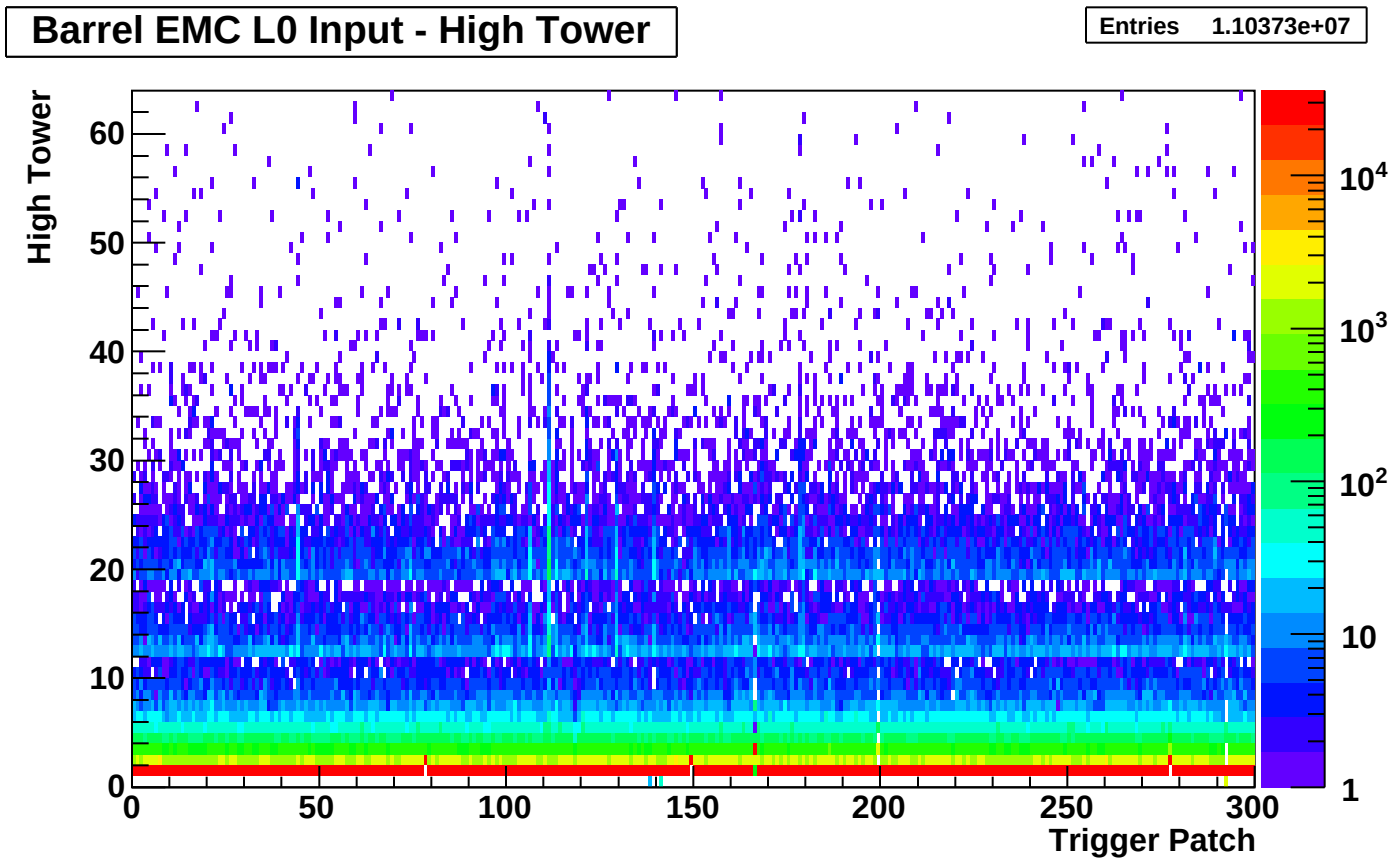

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

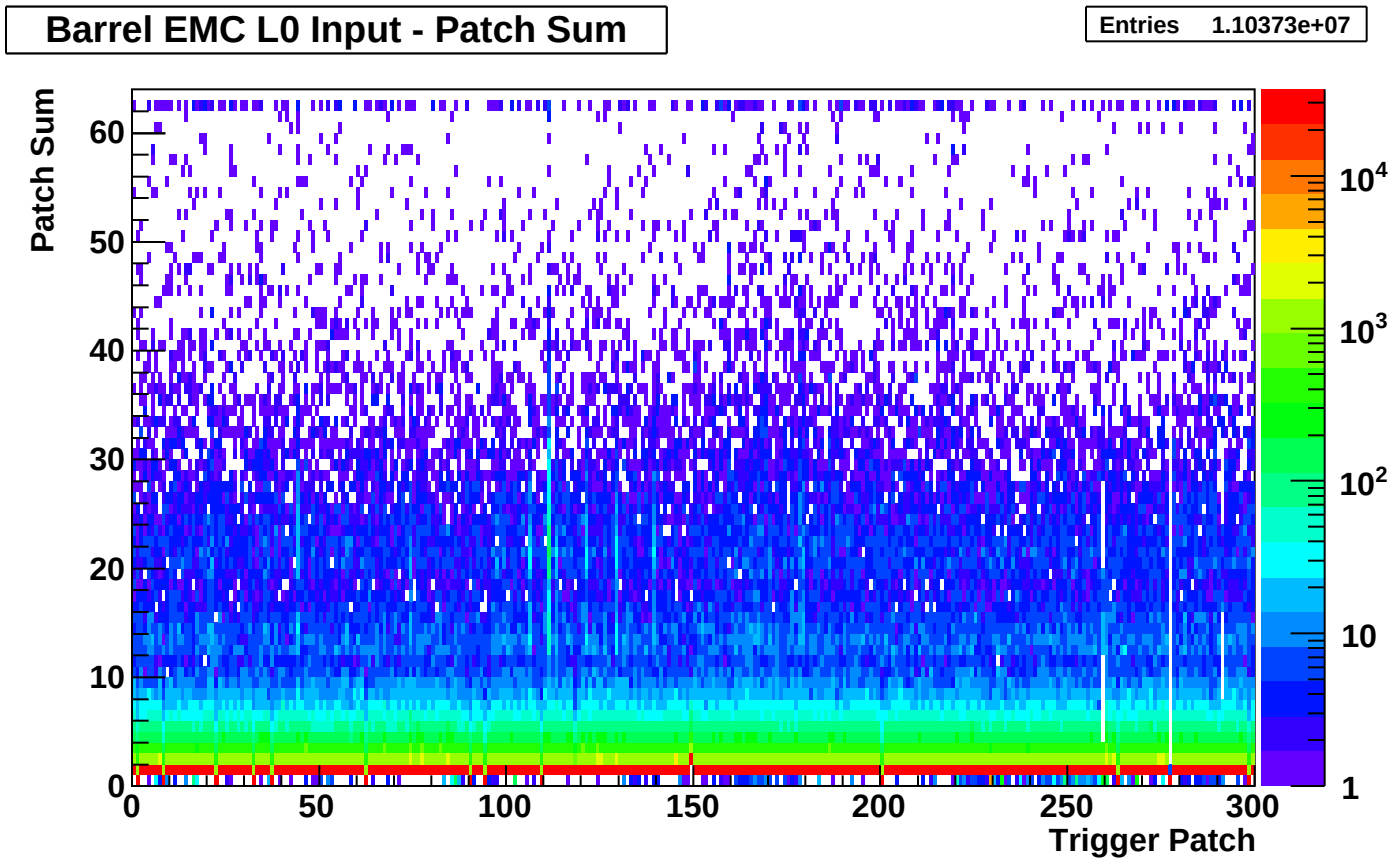

TCU bits
Entries 588656
TCU bits
36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791 36791
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

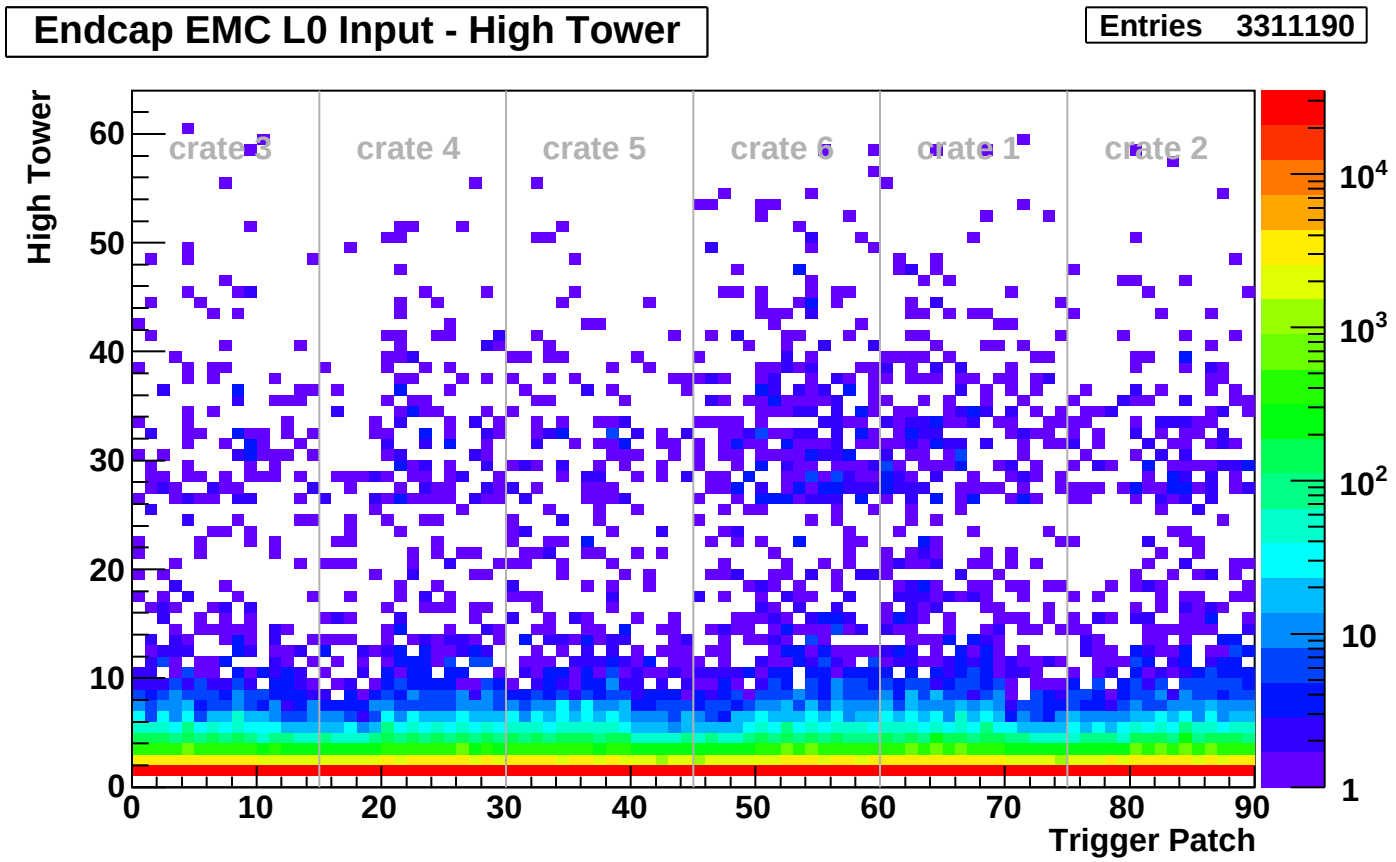

TCU bits
Entries 50261
TCU bits - Simulated
13511 13855 19886 967 1083 959
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

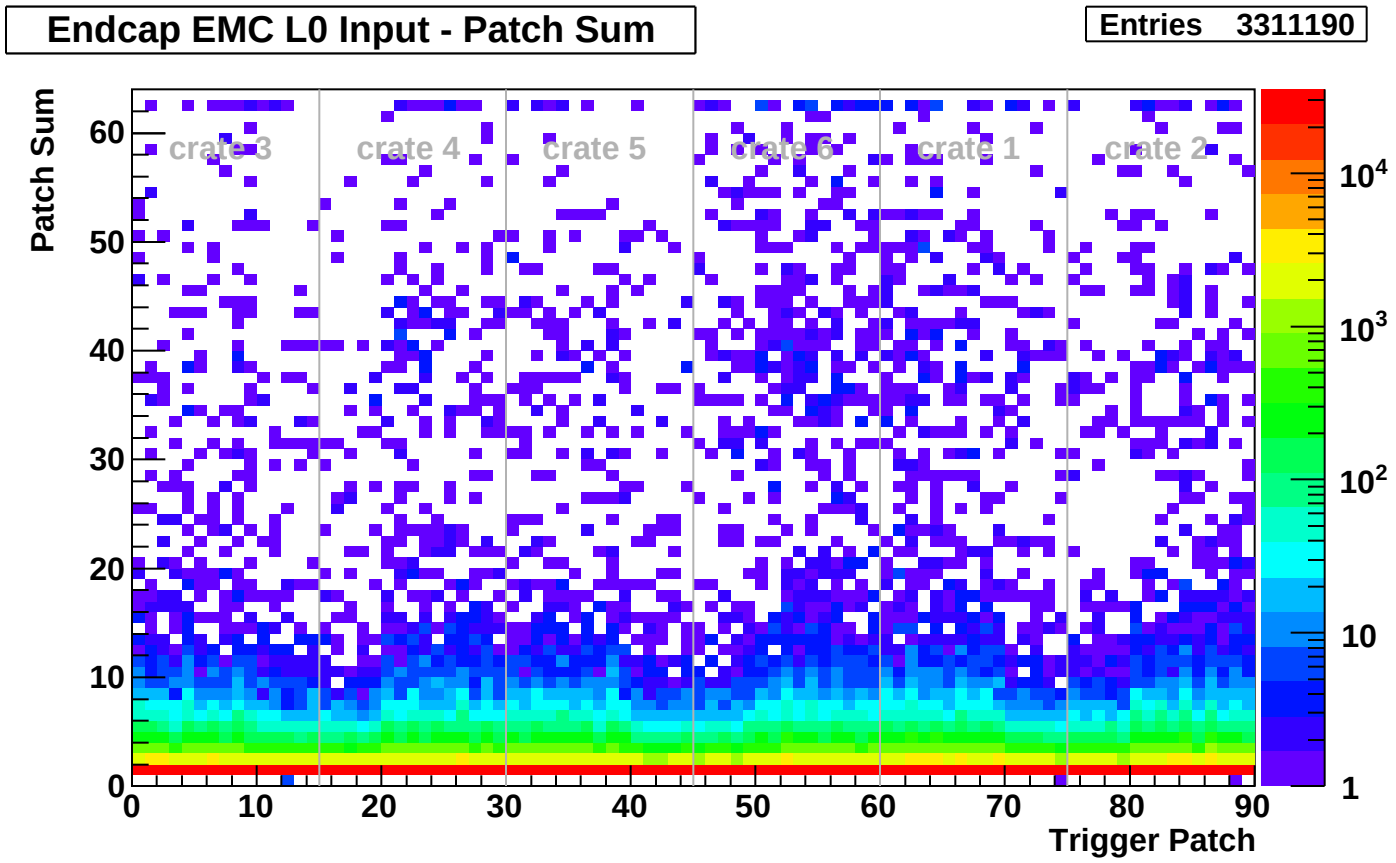

BunchId7Bit (BHT3)
Entries 1142

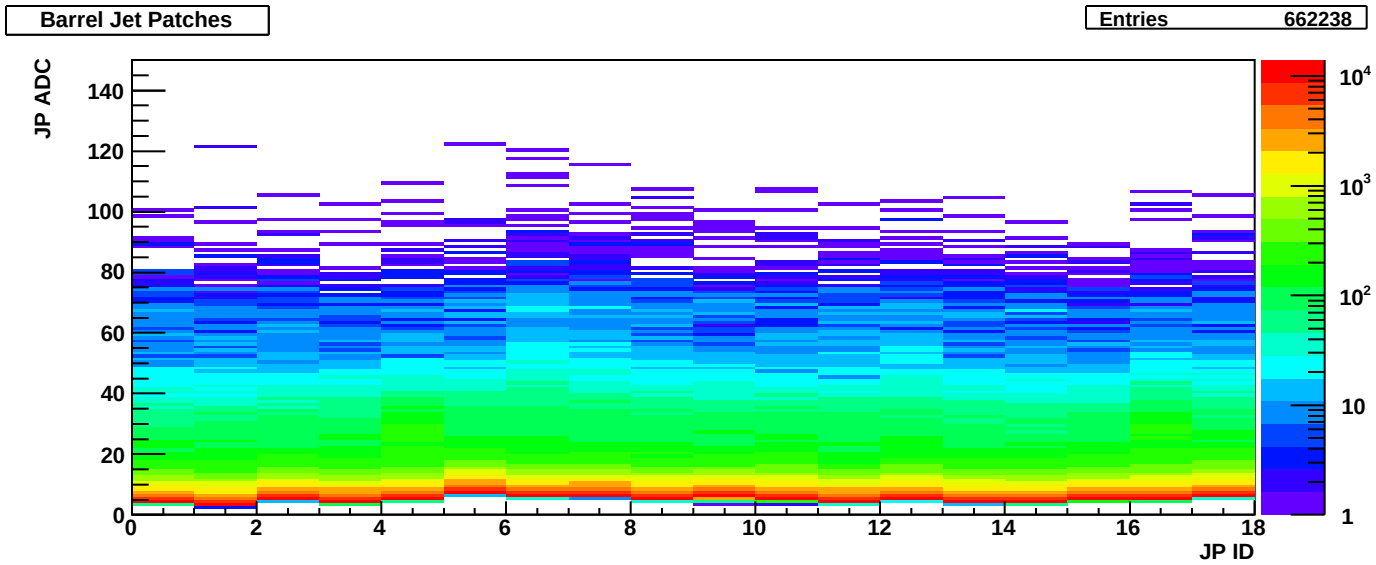

BunchId7Bit (all events)
Entries 36791

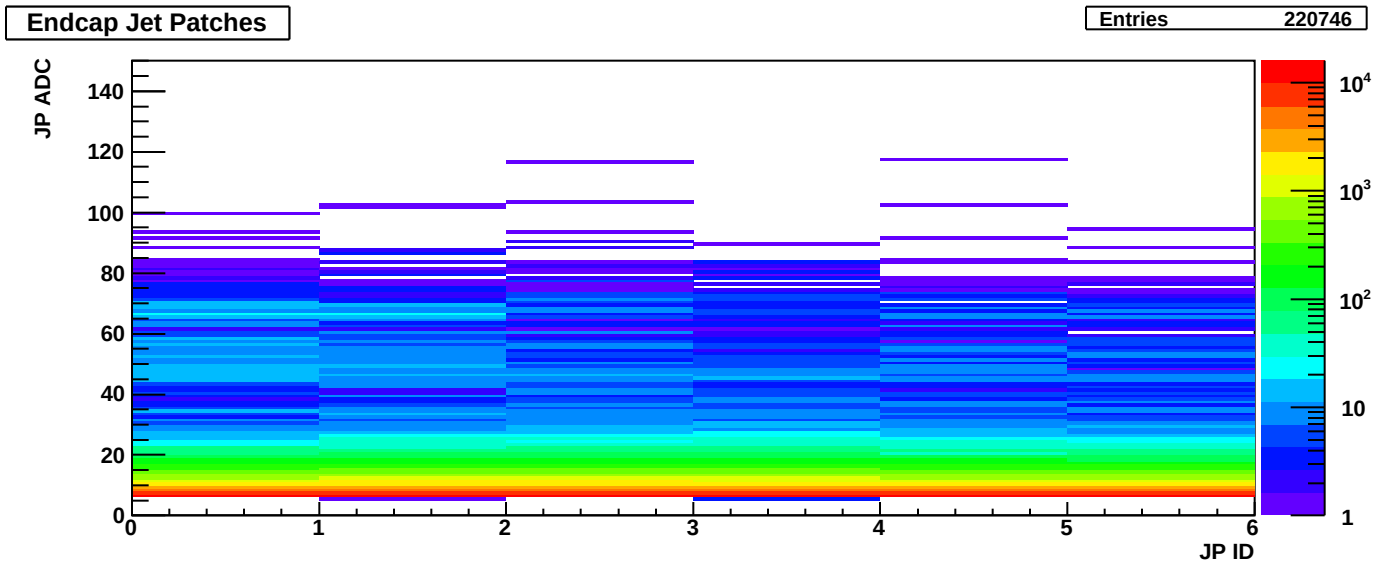

Barrel EMC L0 Input - High Tower
Entries 1.10373e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

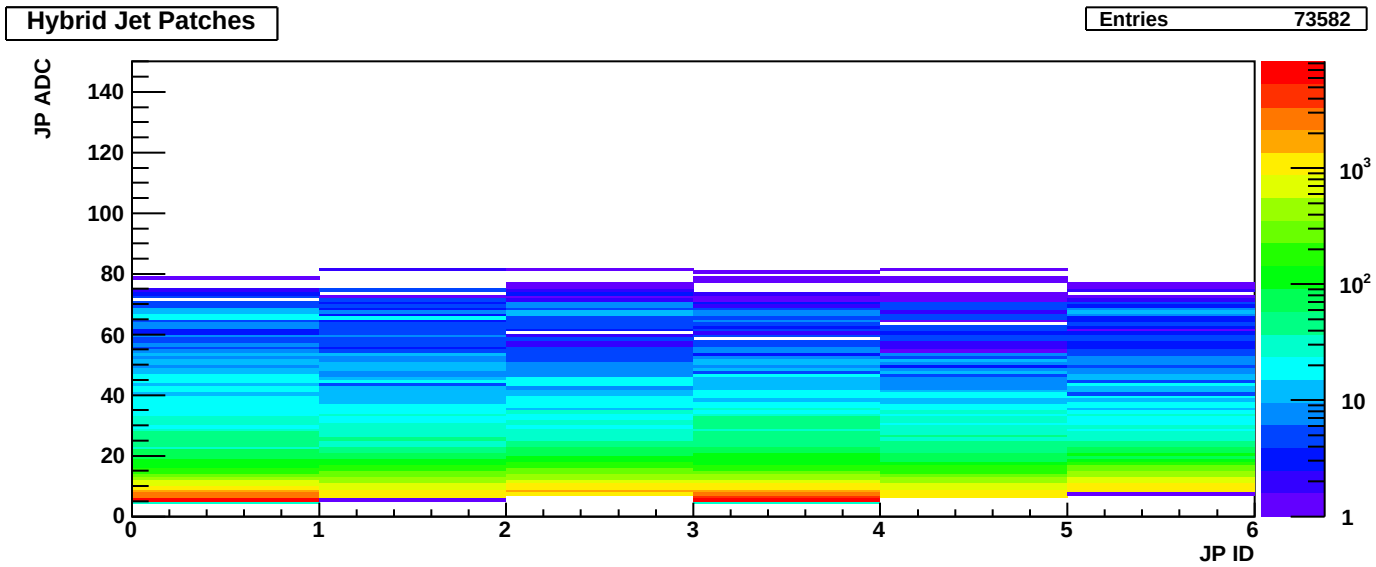

Barrel EMC L0 Input - Patch Sum
Entries 1.10373e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

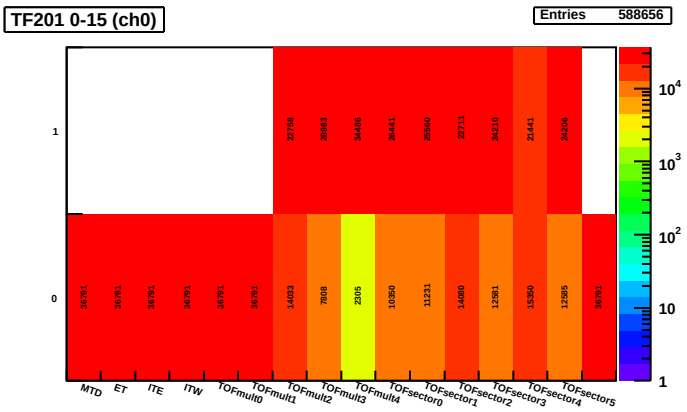

Endcap EMC L0 Input - High Tower
Entries 3311190
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

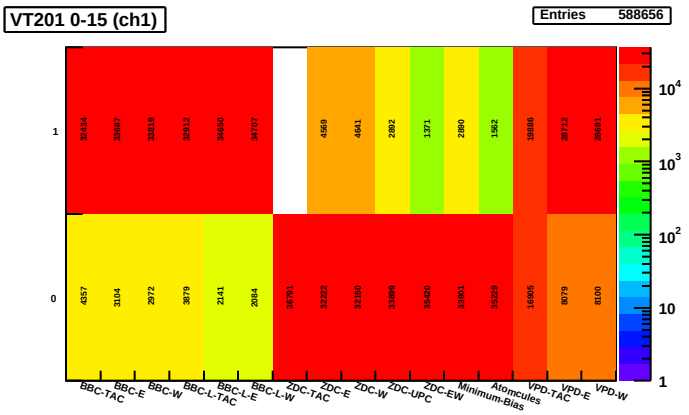

Endcap EMC L0 Input - Patch Sum
Entries 3311190
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

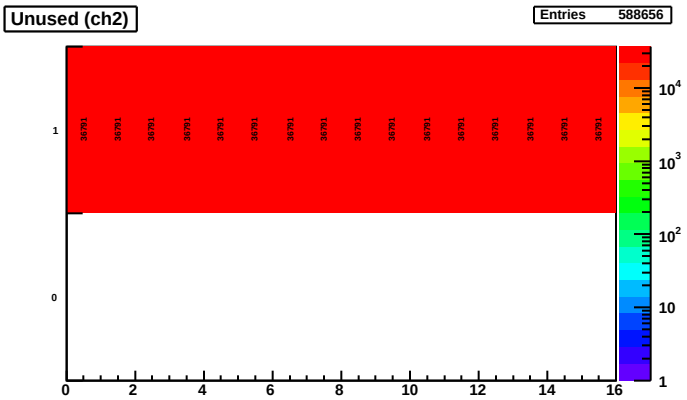

Barrel Jet Patches
Entries 662238
JP ADC
JP ID

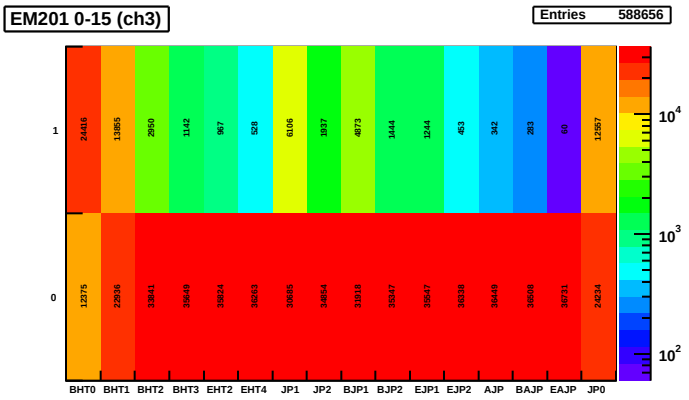

Endcap Jet Patches
Entries 220746
JP ADC
JP ID

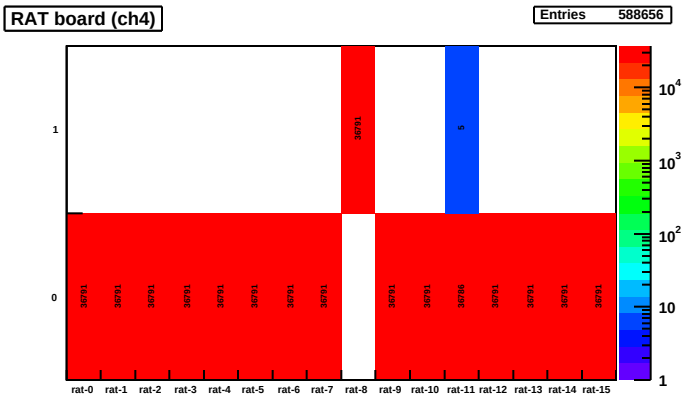

Hybrid Jet Patches
Entries 73582
JP ADC
JP ID

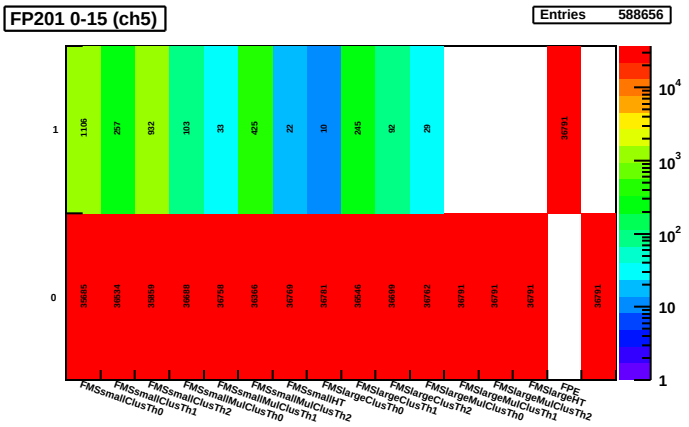

TF201 0-15 (ch0)
Entries 588656
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

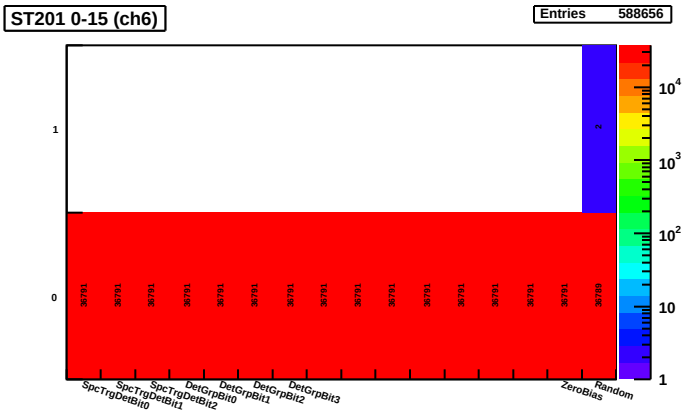

VT201 0-15 (ch1)
Entries 588656
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

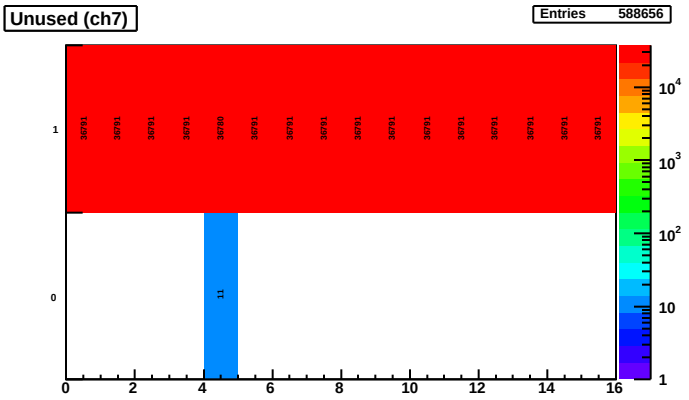

Unused (ch2)
Entries 588656

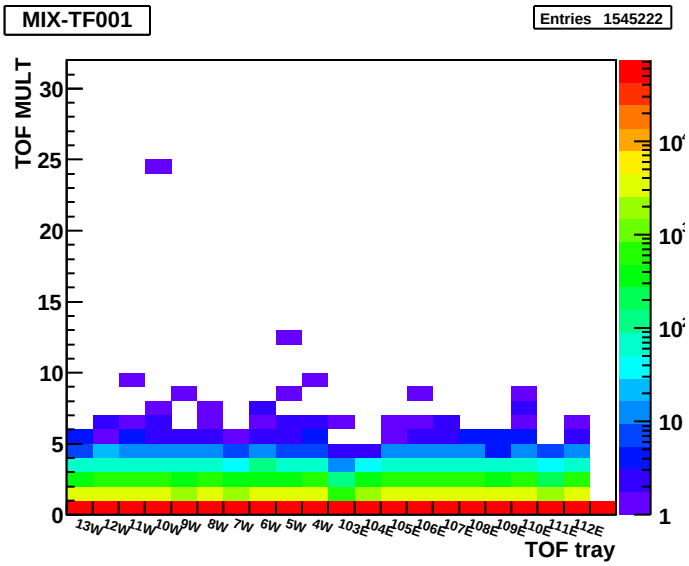

EM201 0-15 (ch3)
Entries 588656
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

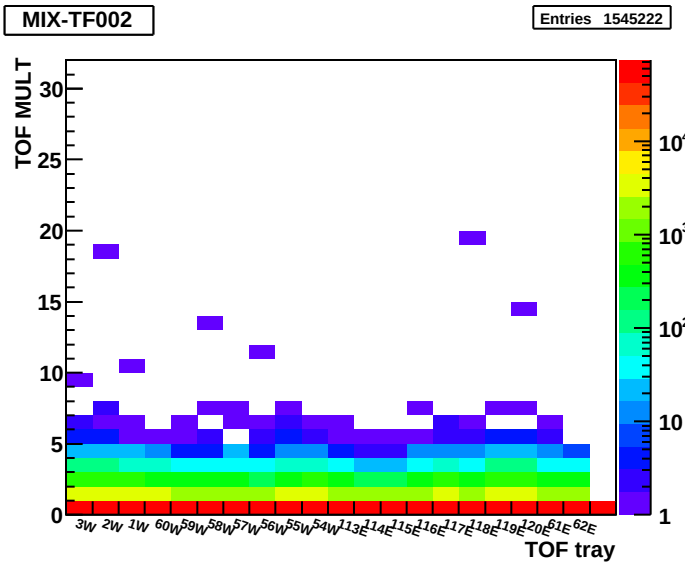

RAT board (ch4)
Entries 588656
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

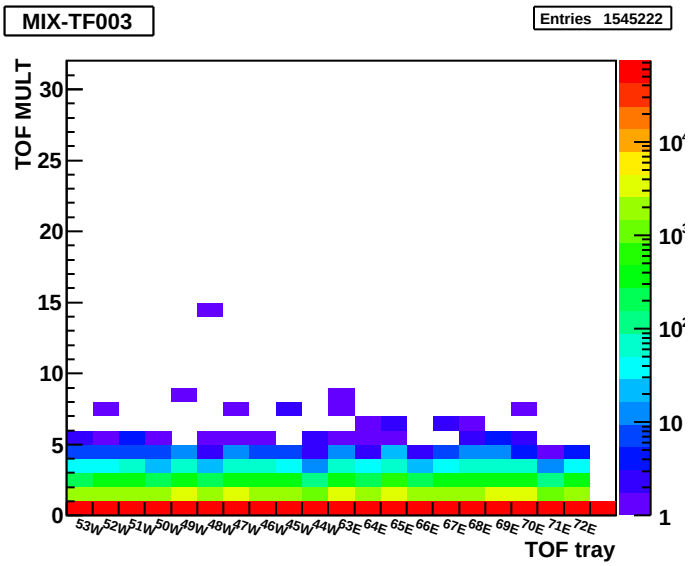

FP201 0-15 (ch5)
Entries 588656
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

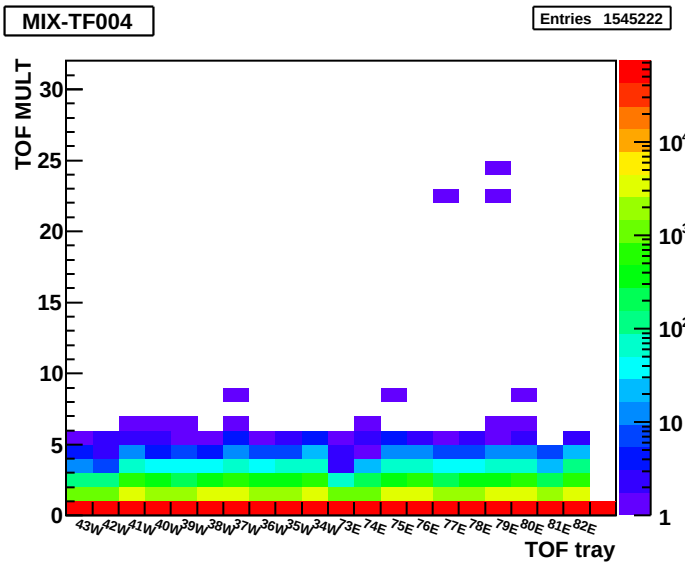

ST201 0-15 (ch6)
Entries 588656
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

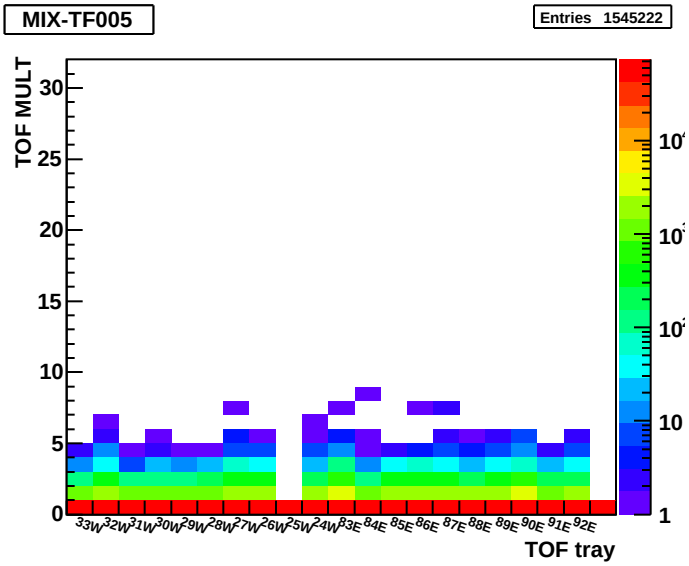

Unused (ch7)
Entries 588656

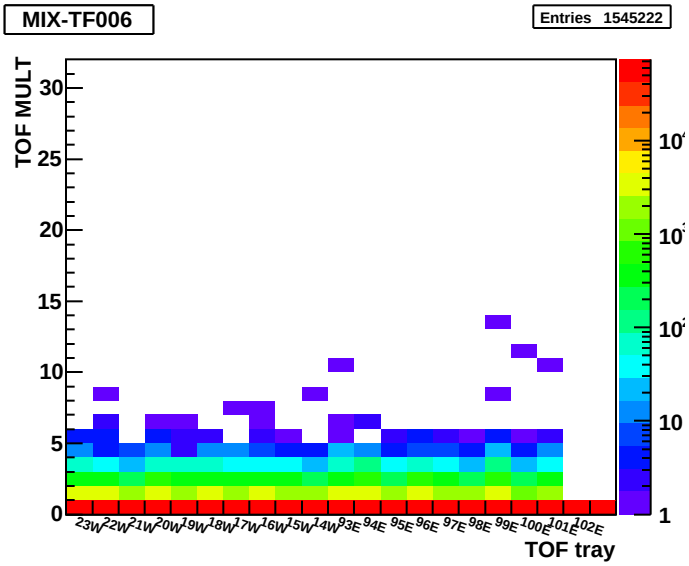

MIX-TF001
Entries 1545222
TOF MULT
TOF tray

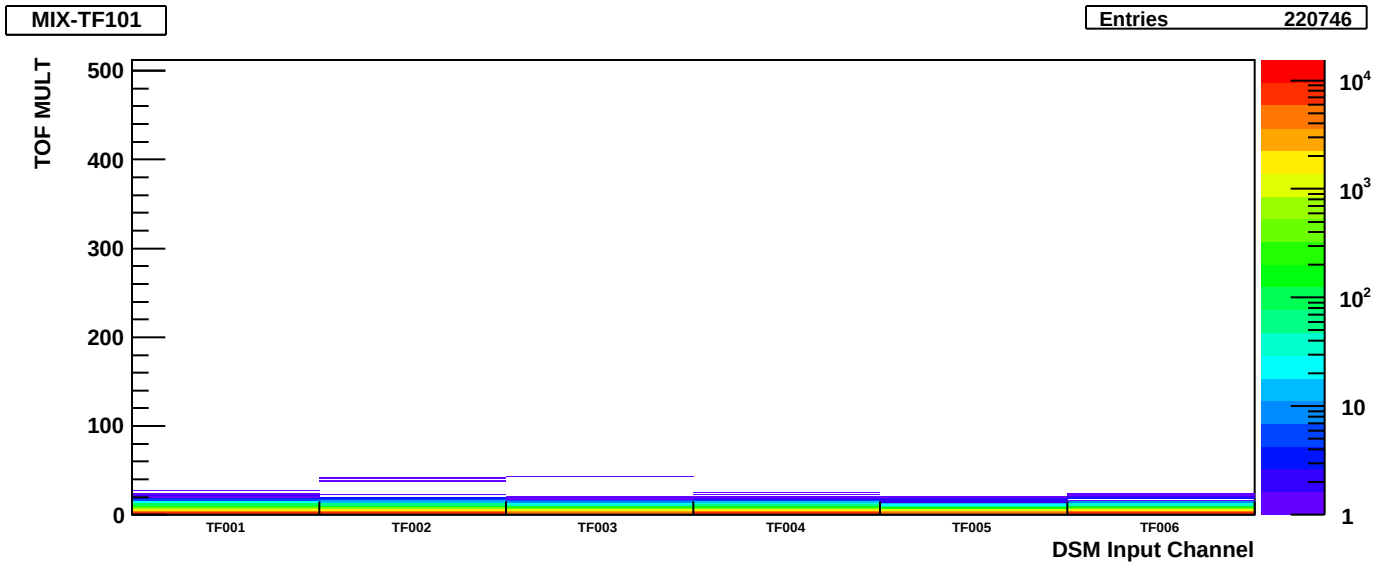

MIX-TF002
Entries 1545222
TOF MULT
TOF tray

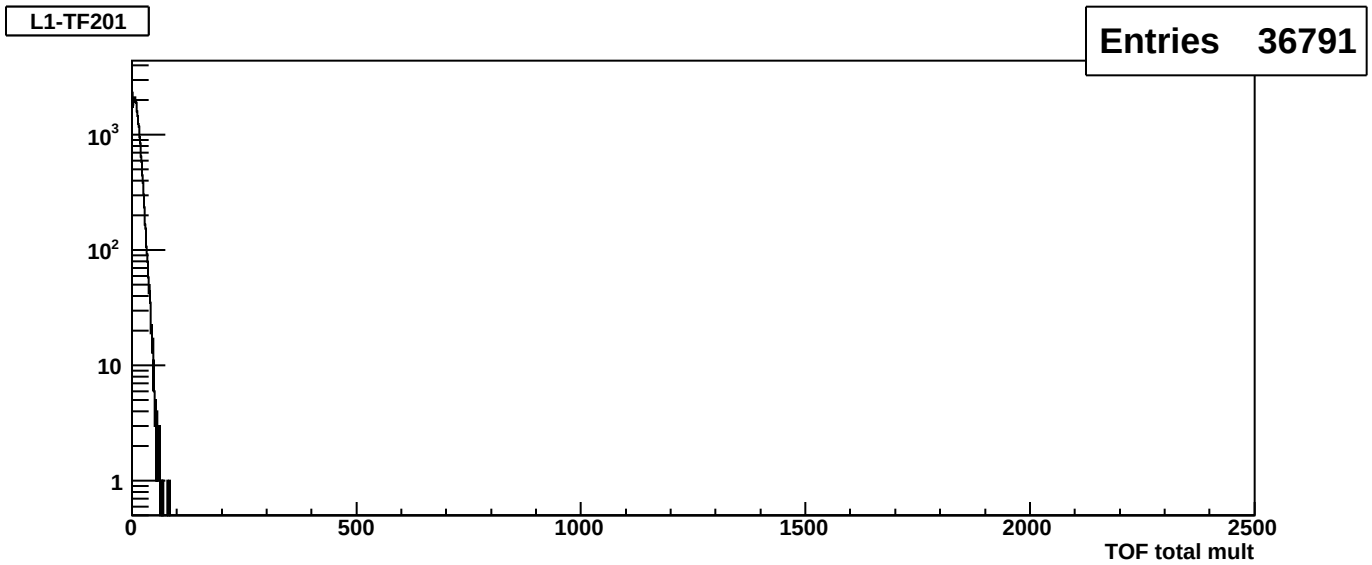

MIX-TF003
Entries 1545222
TOF MULT
TOF tray

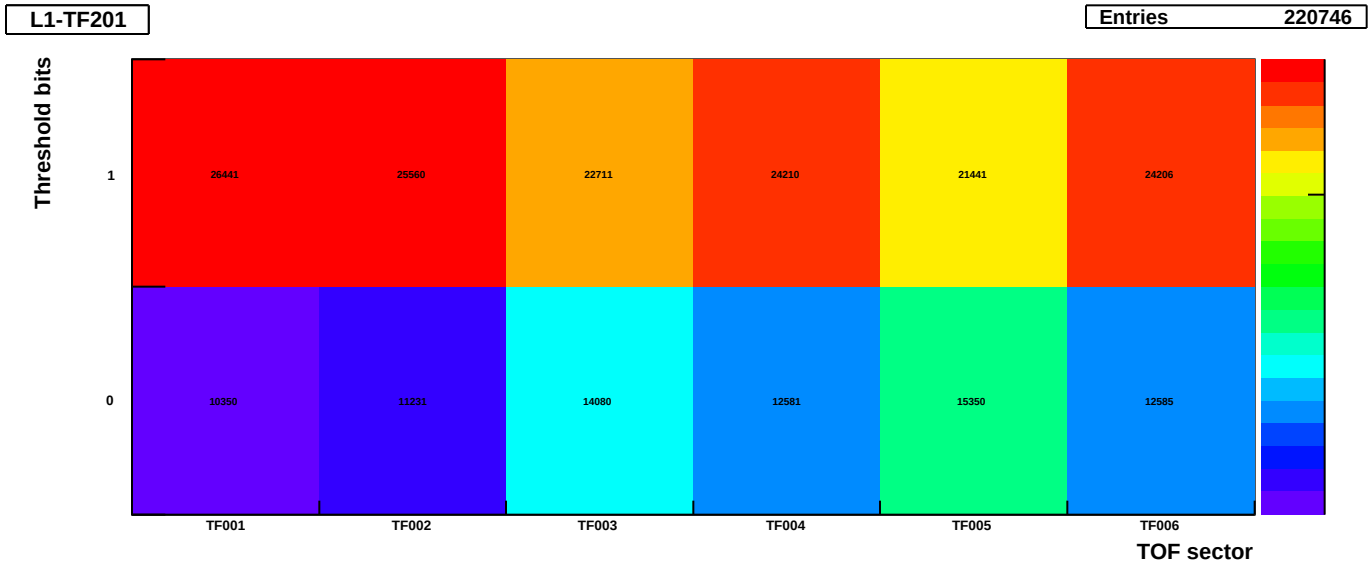

MIX-TF004
Entries 1545222
TOF MULT
TOF tray

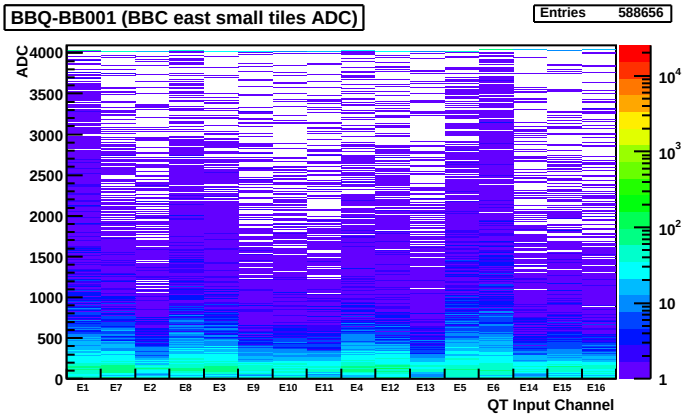

MIX-TF005
Entries 1545222
TOF MULT
TOF tray

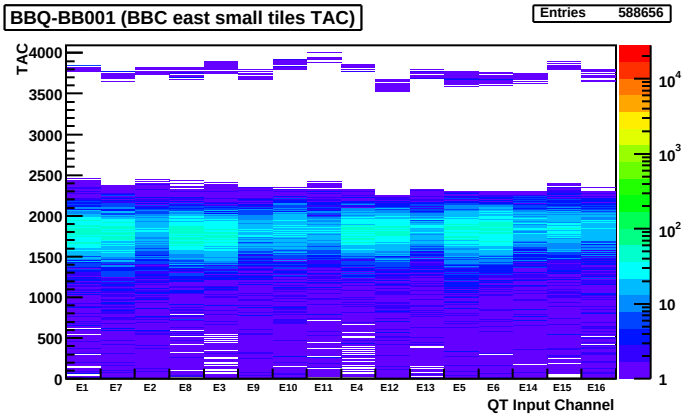

MIX-TF006
Entries 1545222
TOF MULT
TOF tray

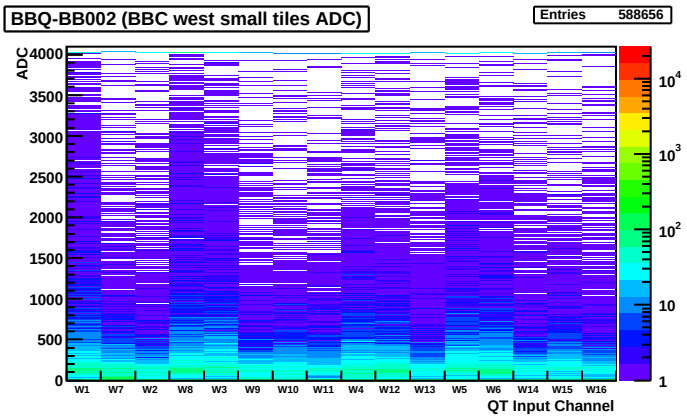

MIX-TF101
Entries 220746
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

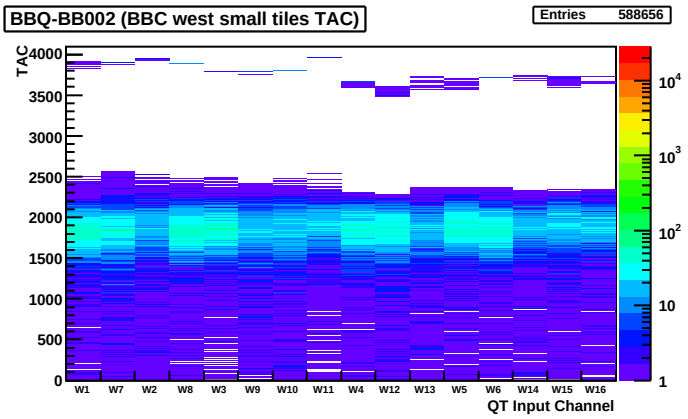

L1-TF201
Entries 36791
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

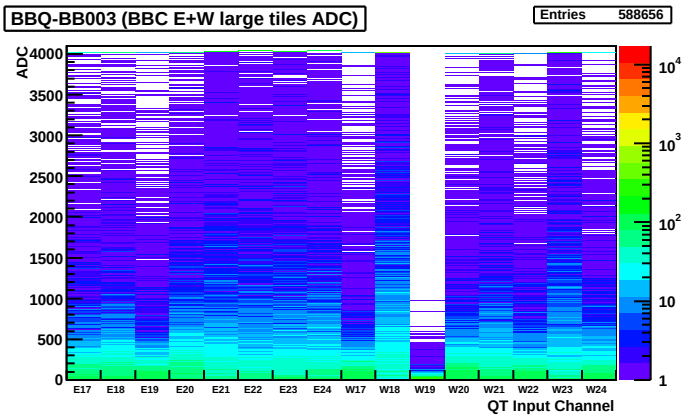

L1-TF201
Entries 220746
Threshold bits
1
0
26441
25560
22711
24210
21441
24206
10350
11231
14080
12581
15350
12585
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

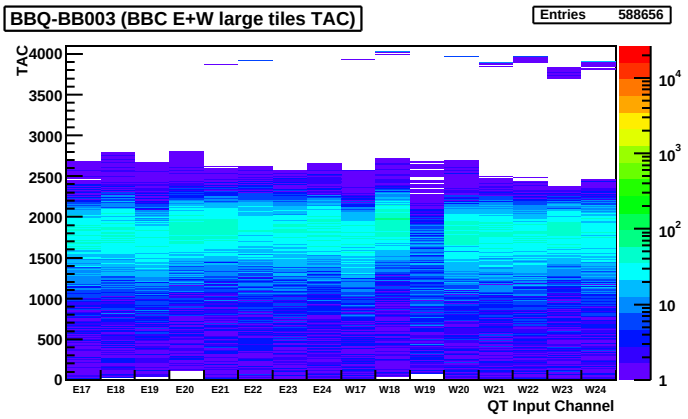

BBQ-BB001 (BBC east small tiles ADC)
Entries 588656
ADC
QT Input Channel

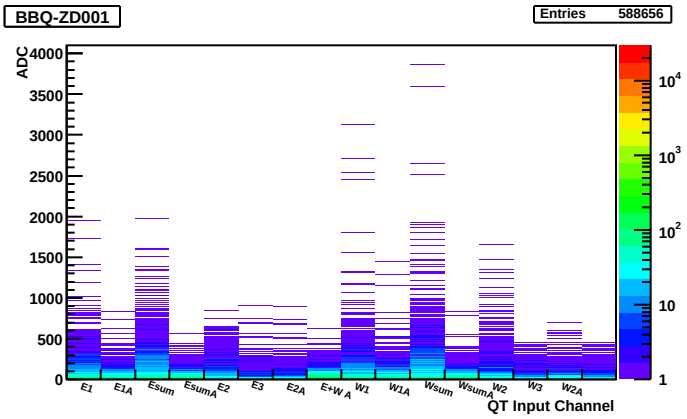

BBQ-BB001 (BBC east small tiles TAC)
Entries 588656
TAC
QT Input Channel

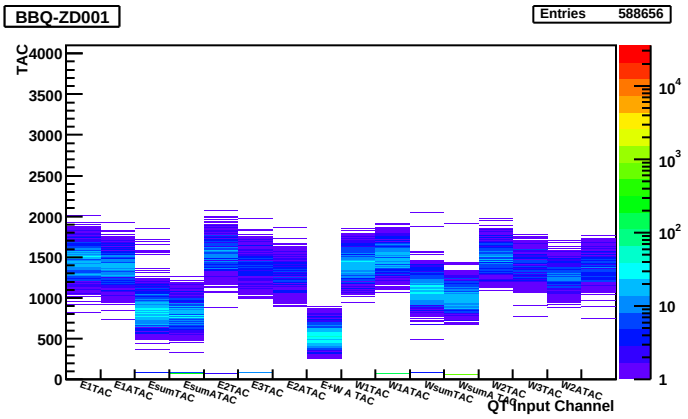

BBQ-BB002 (BBC west small tiles ADC)
Entries 588656
ADC
QT Input Channel

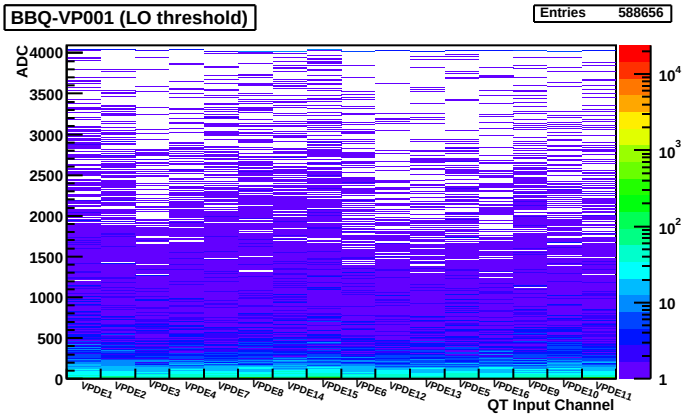

BBQ-BB002 (BBC west small tiles TAC)
Entries 588656
TAC
QT Input Channel

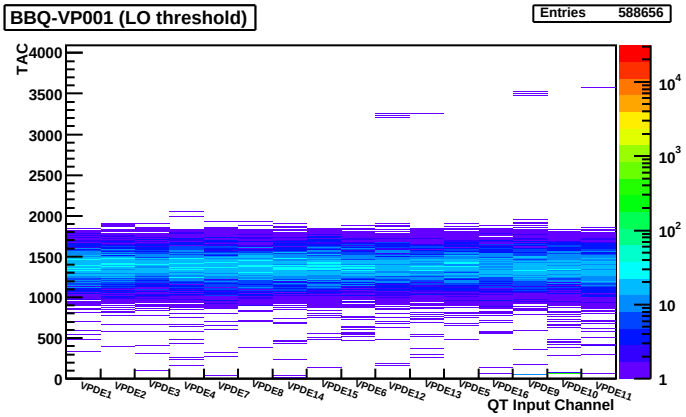

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 588656
ADC
QT Input Channel

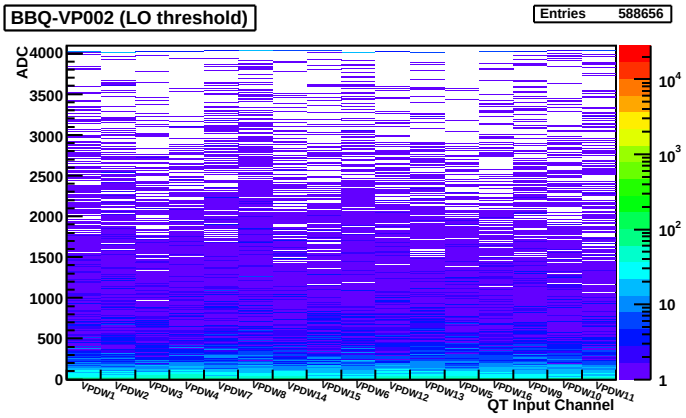

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 588656
TAC
QT Input Channel

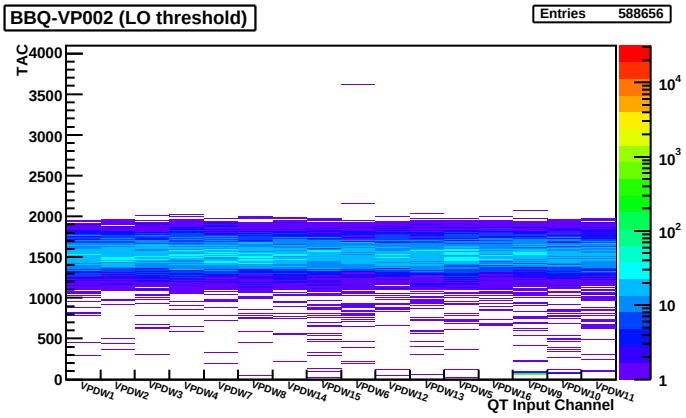

BBQ-ZD001
Entries 588656
ADC
QT Input Channel

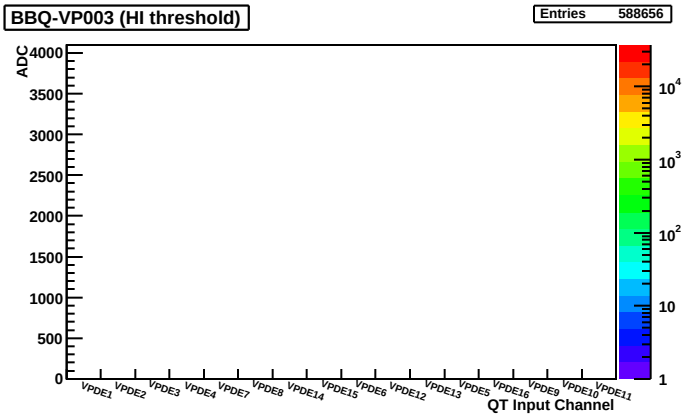

BBQ-ZD001
Entries 588656
TAC
QT Input Channel

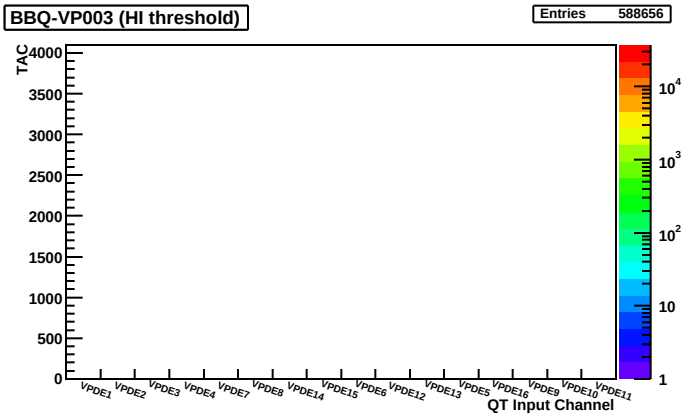

BBQ-VP001 (LO threshold)
Entries 588656
ADC
QT Input Channel

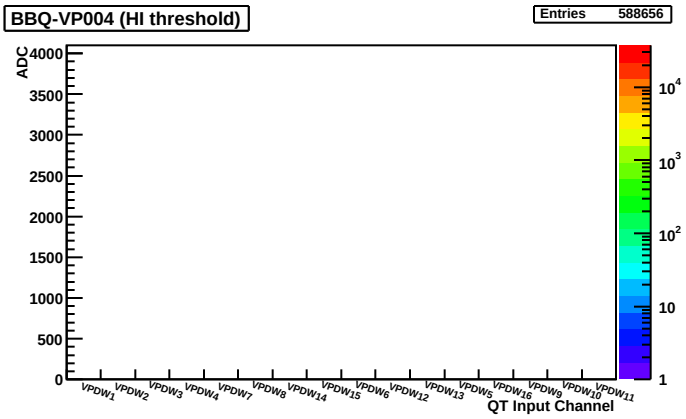

BBQ-VP001 (LO threshold)
Entries 588656
TAC
QT Input Channel

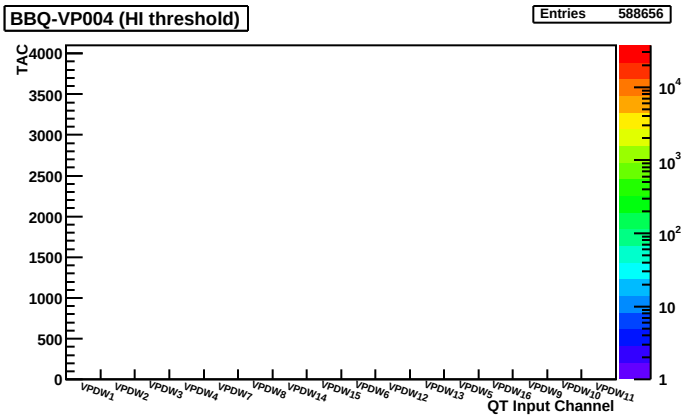

BBQ-VP002 (LO threshold)
Entries 588656
ADC
QT Input Channel

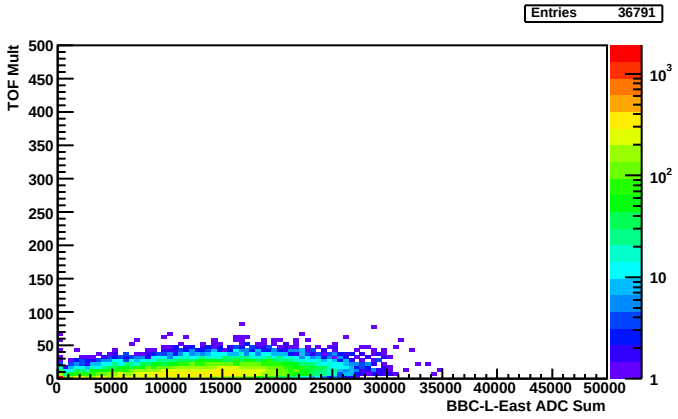

BBQ-VP002 (LO threshold)
Entries 588656
TAC
QT Input Channel

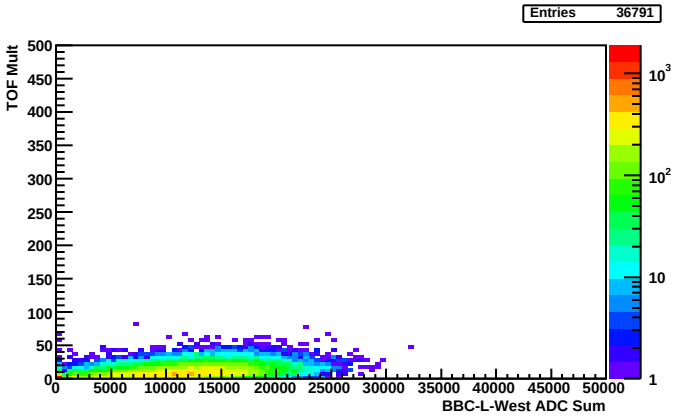

BBQ-VP003 (HI threshold)
Entries 588656
ADC
QT Input Channel

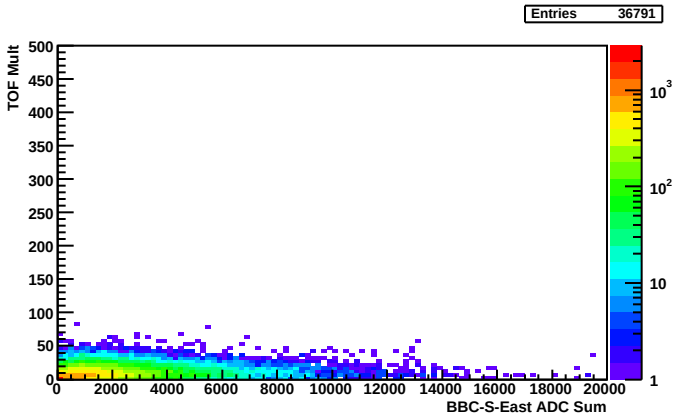

BBQ-VP003 (HI threshold)
Entries 588656
TAC
QT Input Channel

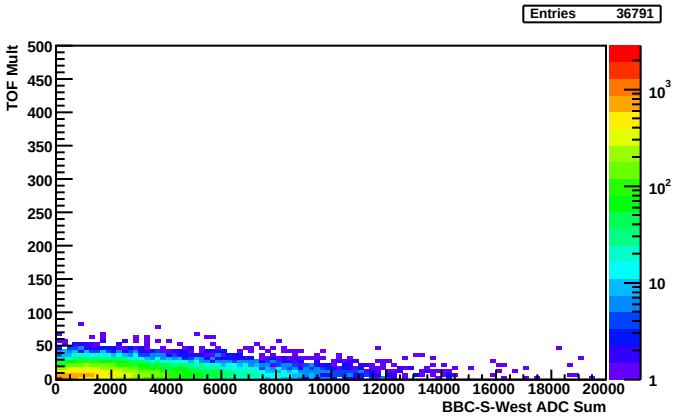

BBQ-VP004 (HI threshold)
Entries 588656
ADC
QT Input Channel

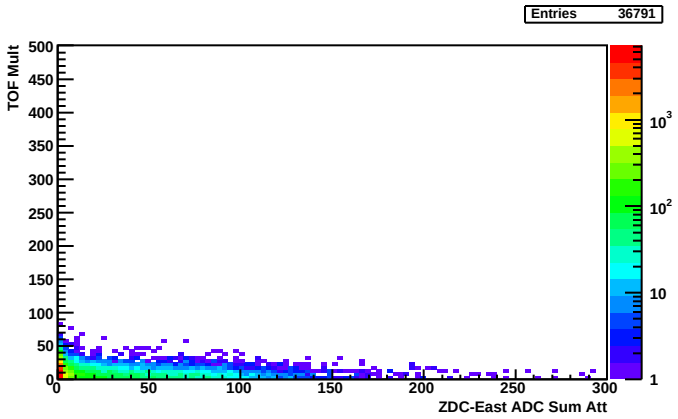

BBQ-VP004 (HI threshold)
Entries 588656
TAC
QT Input Channel

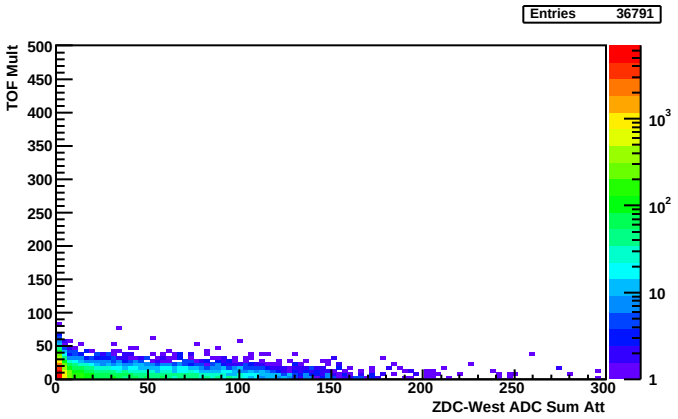

Entries 36791
TOF Mult
BBC-L-East ADC Sum

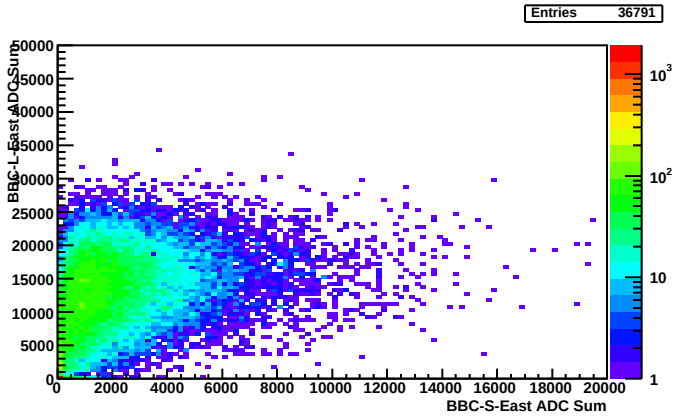

Entries 36791
TOF Mult
BBC-L-West ADC Sum

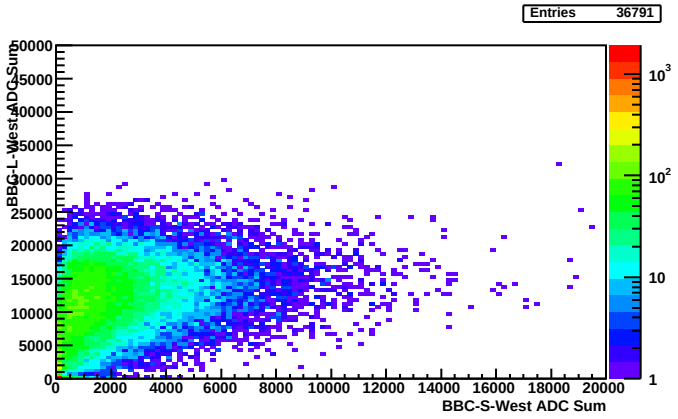

Entries 36791
TOF Mult
BBC-S-East ADC Sum

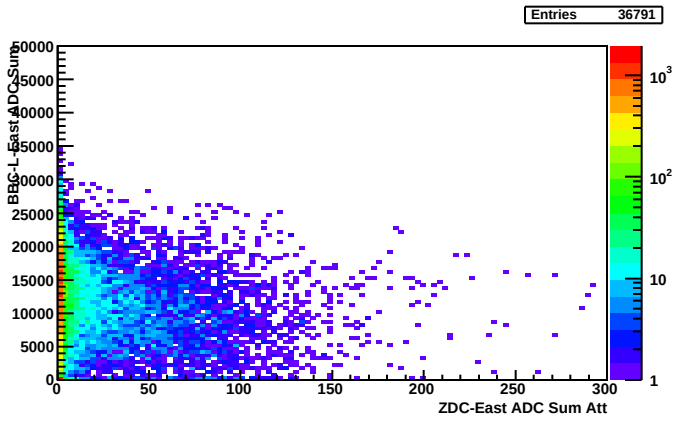

Entries 36791
TOF Mult
BBC-S-West ADC Sum

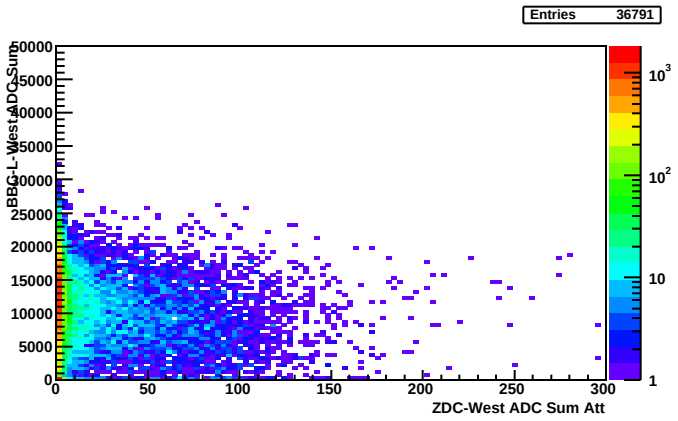

Entries 36791
TOF Mult
ZDC-East ADC Sum Att

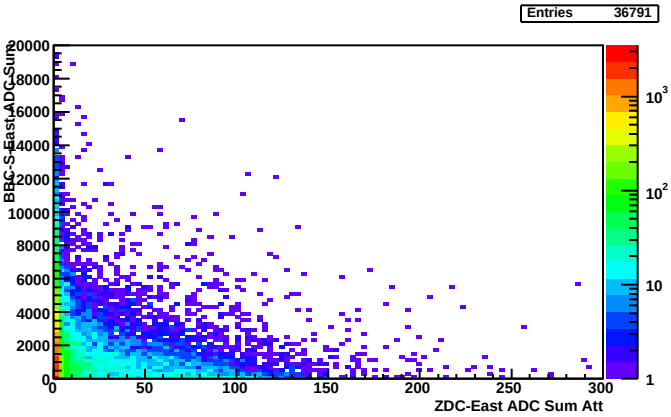

Entries 36791
TOF Mult
ZDC-West ADC Sum Att

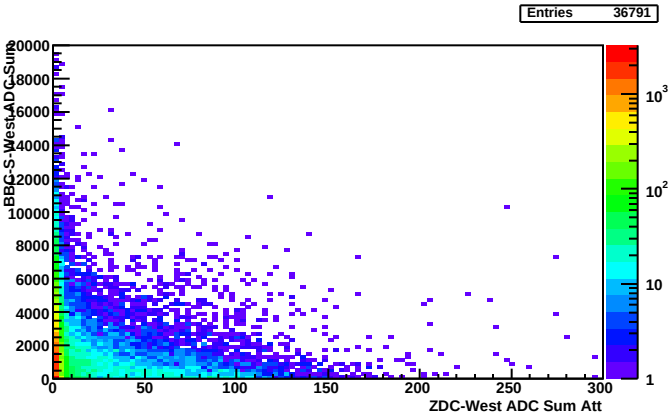

Entries 36791
BBC-L-East ADC Sum
BBC-S-East ADC Sum

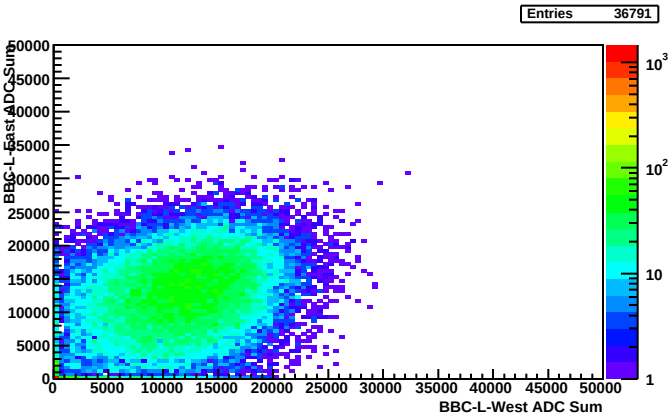

Entries 36791
BBC-L-West ADC Sum
BBC-S-West ADC Sum

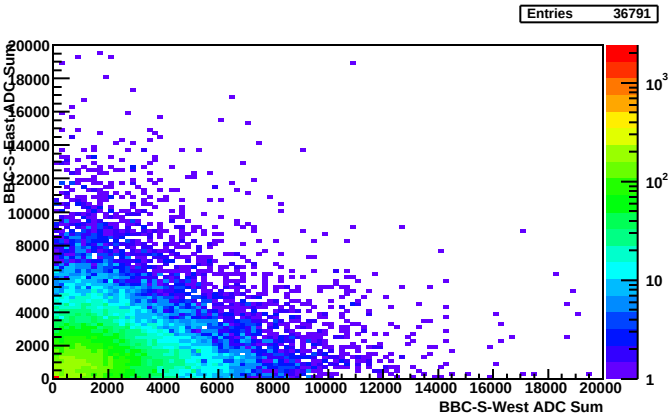

Entries 36791
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

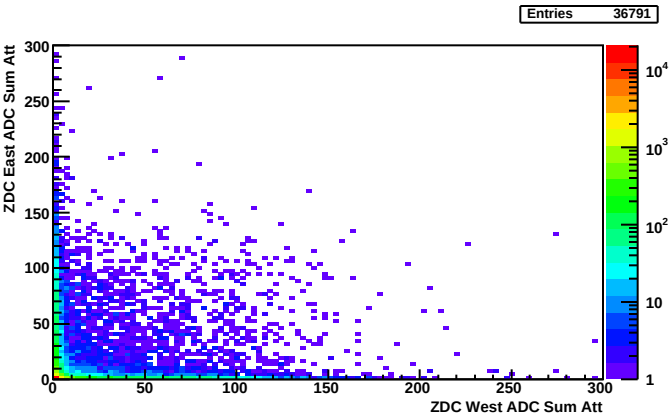

Entries 36791
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

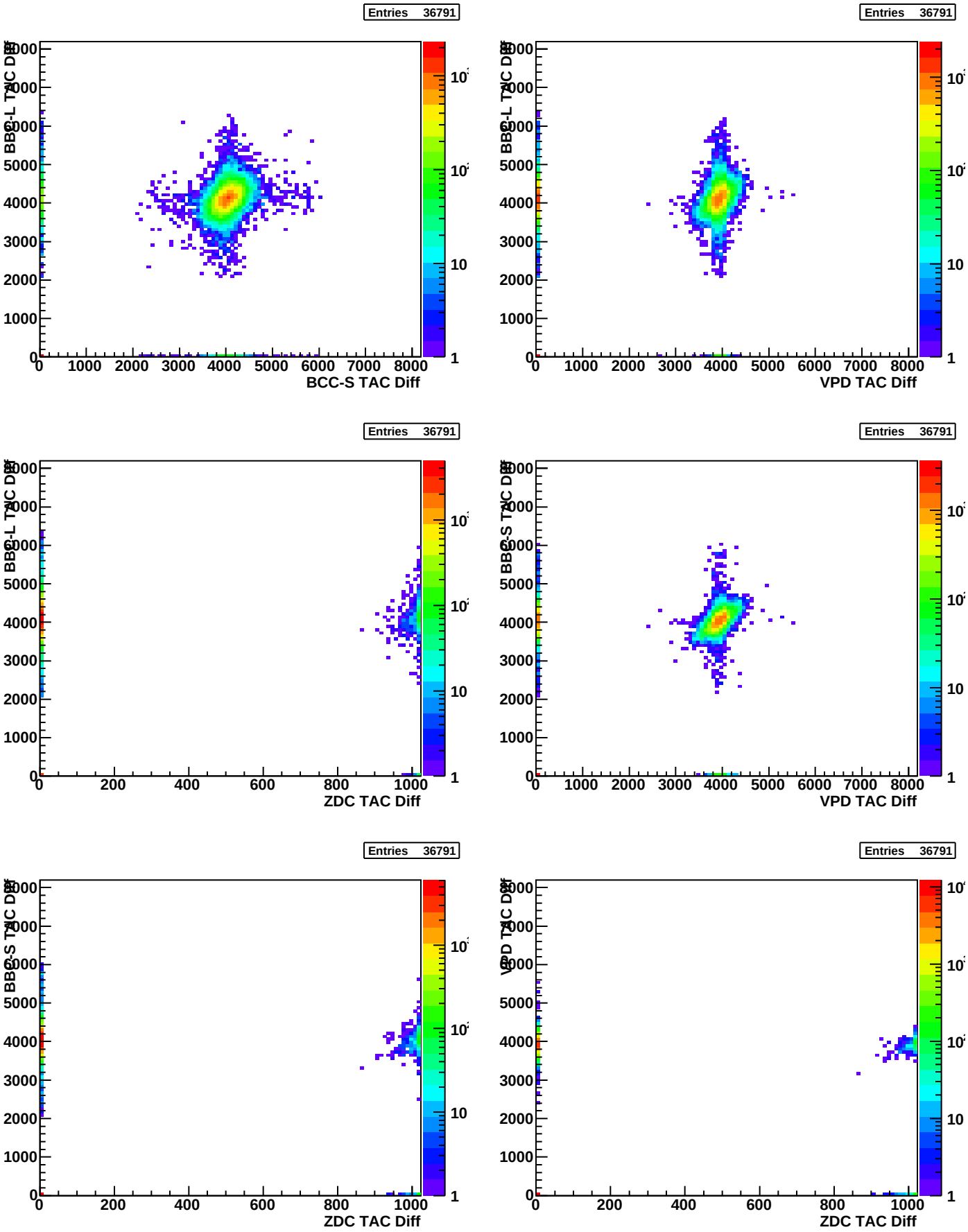

Entries 36791
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

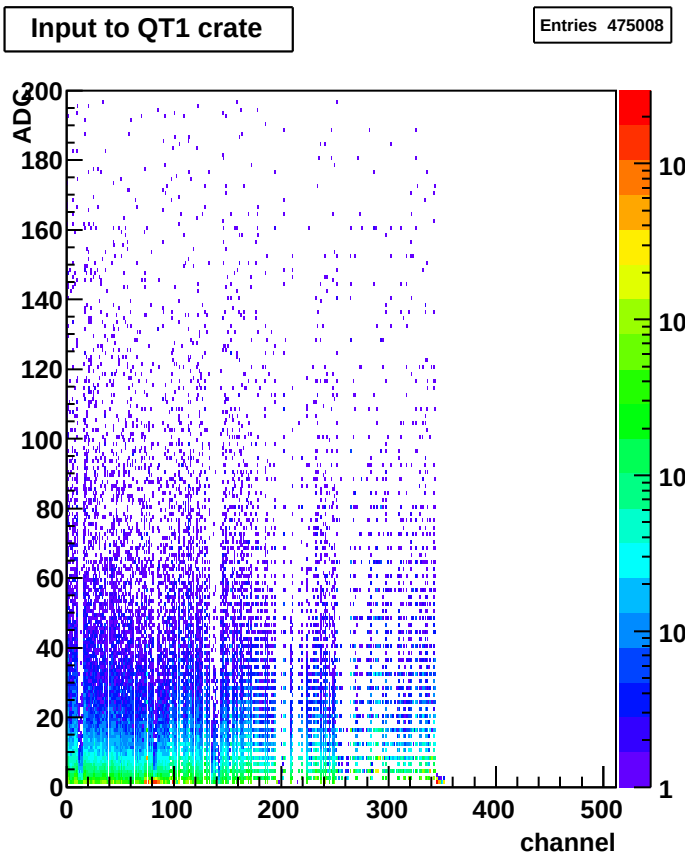

Entries 36791
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

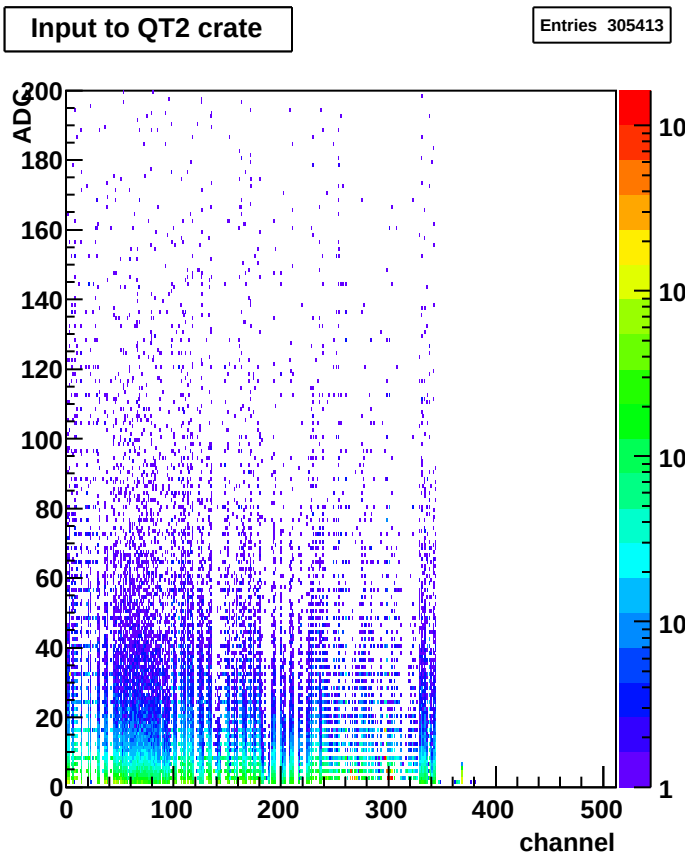

Entries 36791
BBC-L-East ADC Sum
BBC-L-West ADC Sum

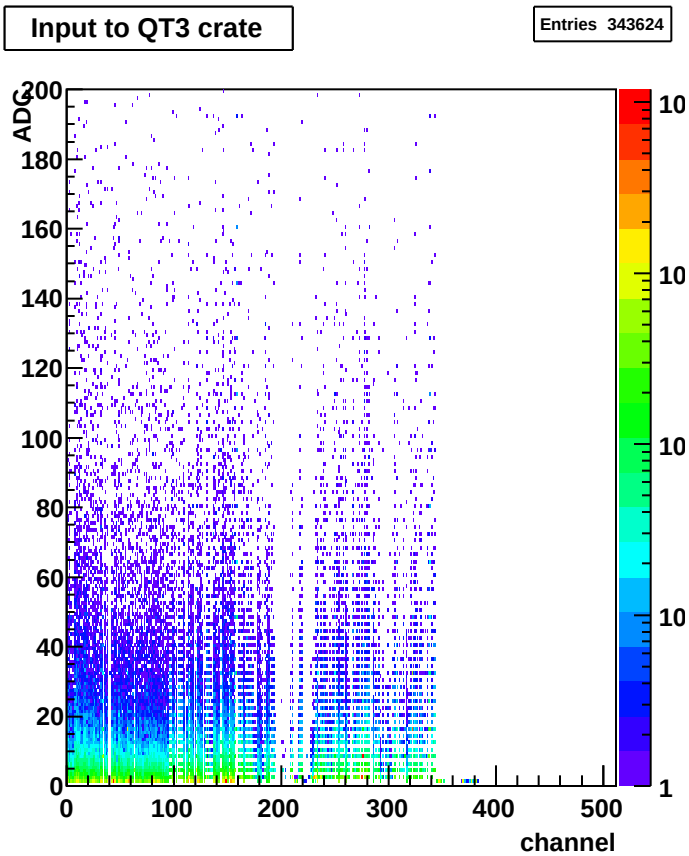

Entries 36791
BBC-S-East ADC Sum
BBC-S-West ADC Sum

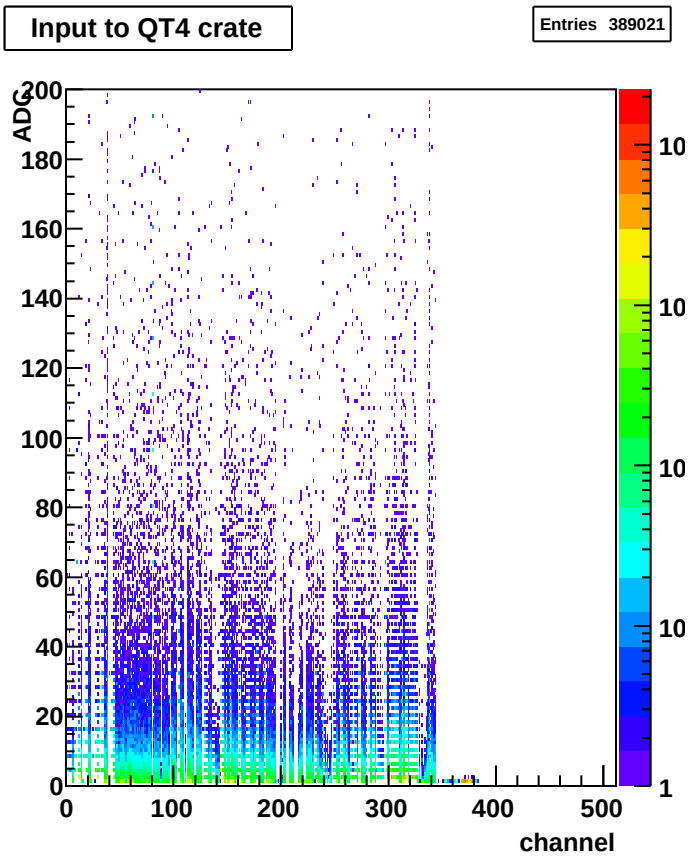

Entries 36791
ZDC East ADC Sum Att
ZDC West ADC Sum Att

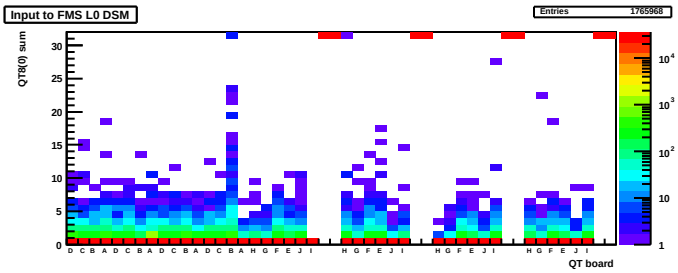

Entries 36791
BBC-L TAC Diff
BCC-S TAC Diff
Entries 36791
BBC-L TAC Diff
VPD TAC Diff
Entries 36791
BBC-L TAC Diff
ZDC TAC Diff
Entries 36791
BBC-S TAC Diff
VPD TAC Diff
Entries 36791
BBC-S TAC Diff
ZDC TAC Diff
Entries 36791
VPD TAC Diff
ZDC TAC Diff

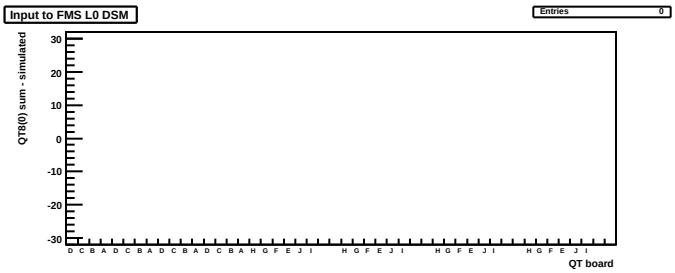

Input to QT1 crate
Entries 475008
ADC
channel

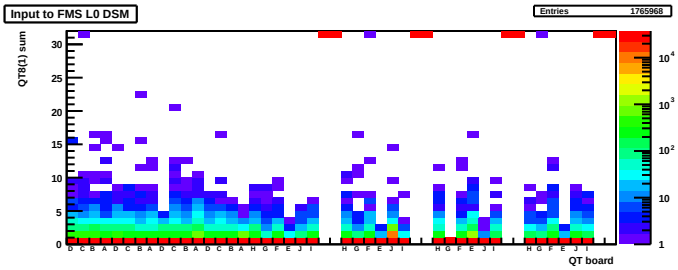

Input to QT2 crate
Entries 305413
ADC
channel

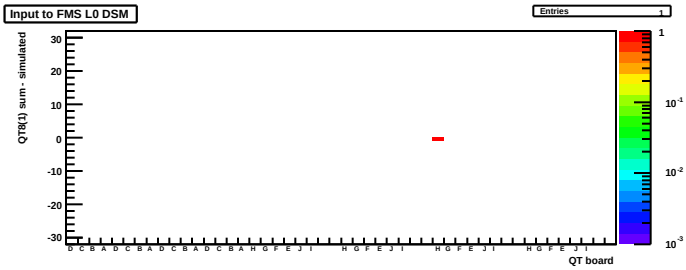

Input to QT3 crate
Entries 343624
ADC
channel

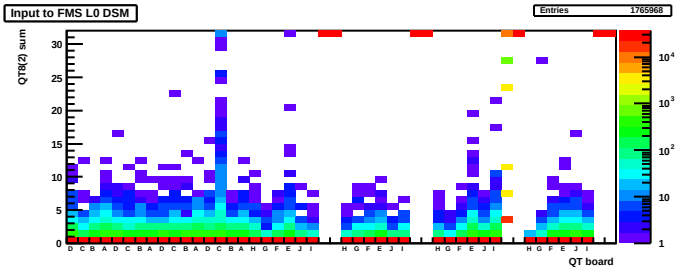

Input to QT4 crate
Entries 389021
ADC
channel

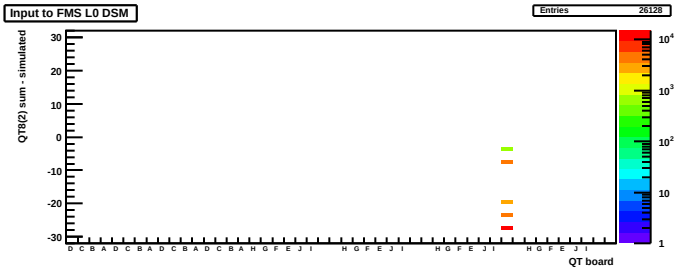

Input to FMS L0 DSM
Entries 1765968
QT8(0) sum
QT board

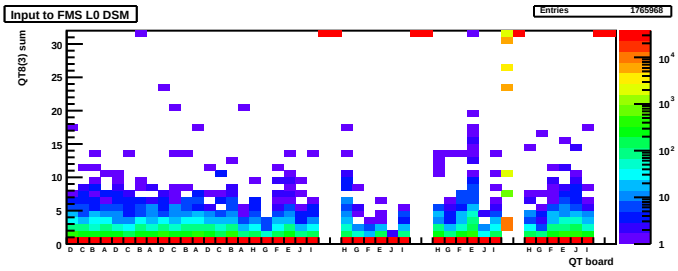

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

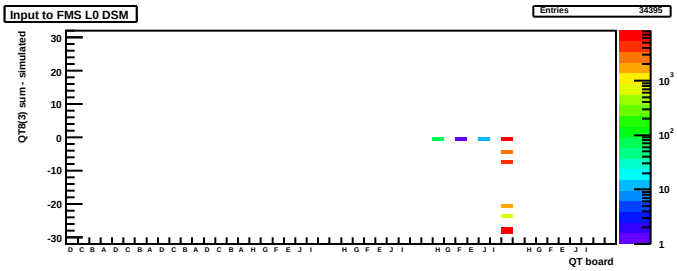

Input to FMS L0 DSM
Entries 1765968
QT8(1) sum
QT board

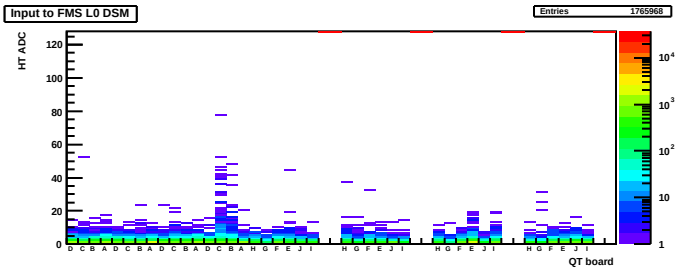

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

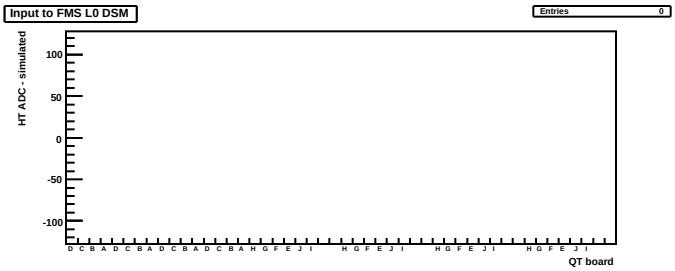

Input to FMS L0 DSM
Entries 1765968
QT8(2) sum
QT board

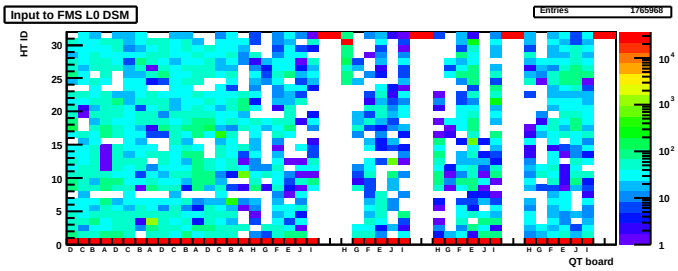

Input to FMS L0 DSM
Entries 26128
QT8(2) sum - simulated
QT board

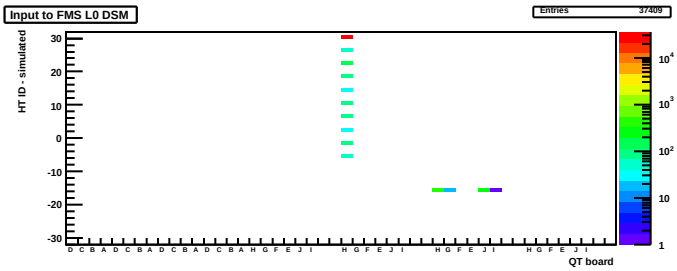

Input to FMS L0 DSM
Entries 1765968
QT8(3) sum
QT board

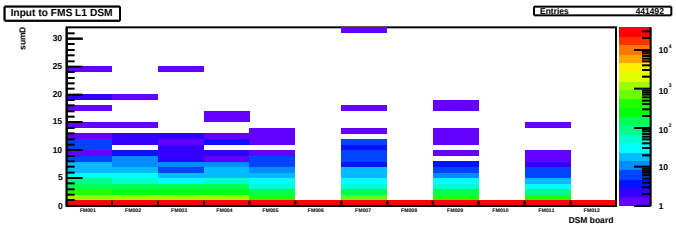

Input to FMS L0 DSM
Entries 34395
QT8(3) sum - simulated
QT board

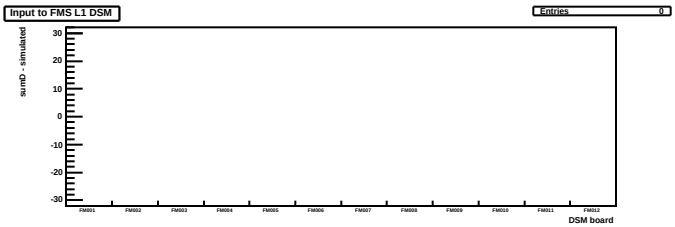

Input to FMS L0 DSM
Entries 1765968
HT ADC
QT board

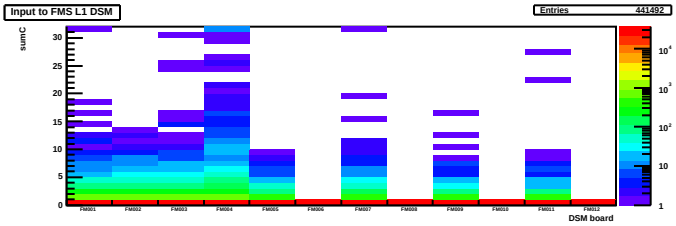

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

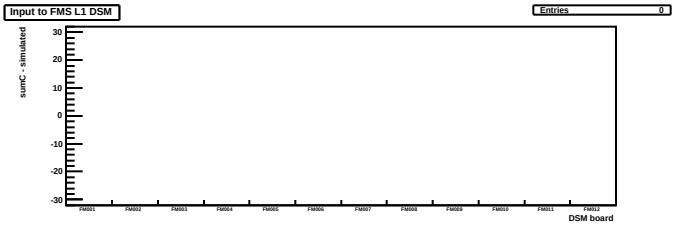

Input to FMS L0 DSM
Entries 1765968
HT ID
QT board

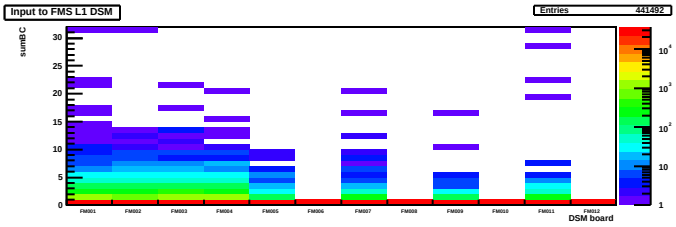

Input to FMS L0 DSM
Entries 37409
HT ID - simulated
QT board

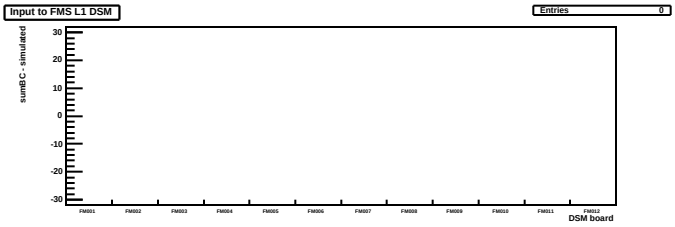

Input to FMS L1 DSM
Entries 441492
sumD
DSM board

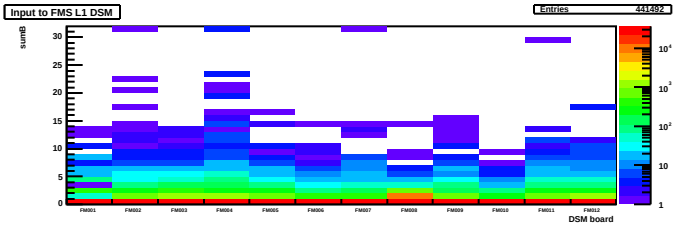

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

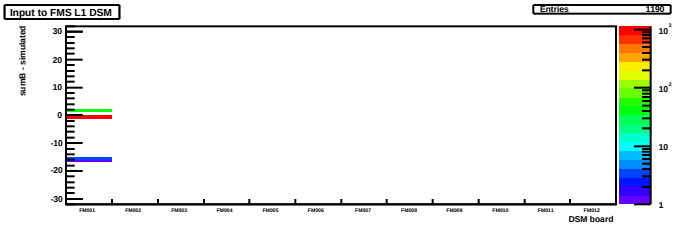

Input to FMS L1 DSM
Entries 441492
sumC
DSM board

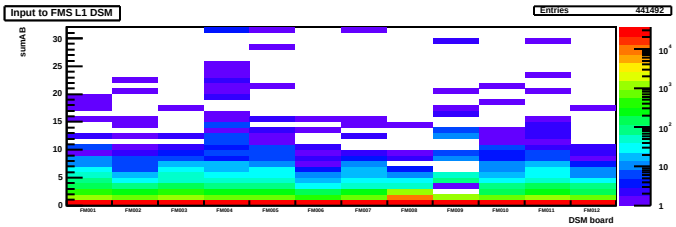

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

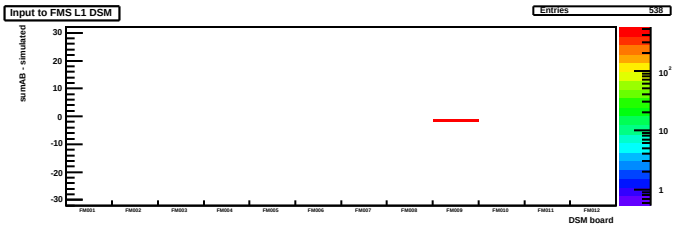

Input to FMS L1 DSM
Entries 441492
sumBC
DSM board

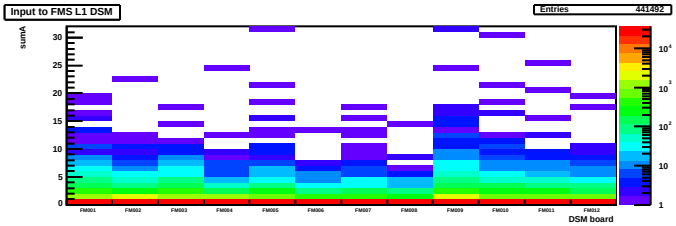

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

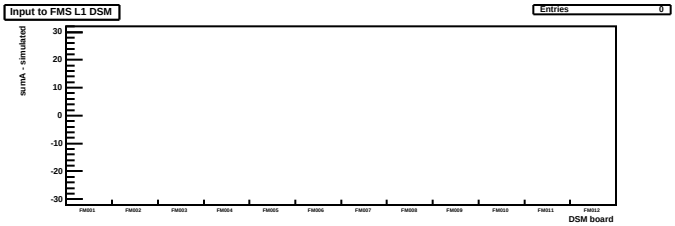

Input to FMS L1 DSM
Entries 441492
sumB
DSM board

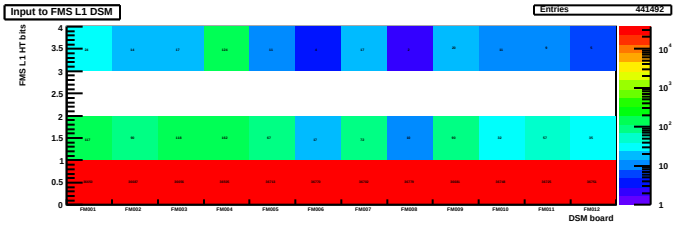

Input to FMS L1 DSM
Entries 1190
sumB - simulated
DSM board

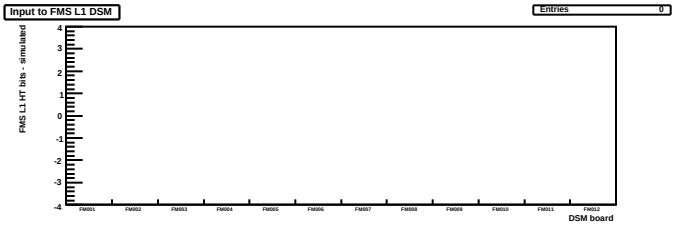

Input to FMS L1 DSM
Entries 441492
sumAB
DSM board

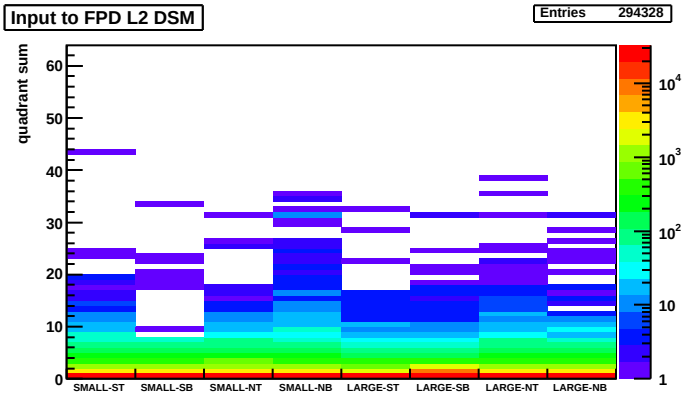

Input to FMS L1 DSM
Entries 538
sumAB - simulated
DSM board

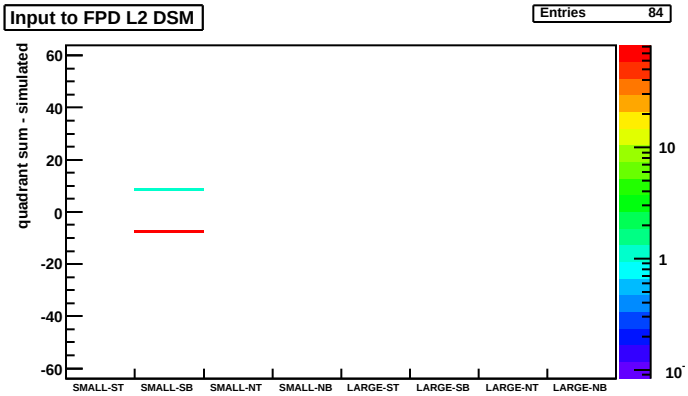

Input to FMS L1 DSM
Entries 441492
sumA
DSM board

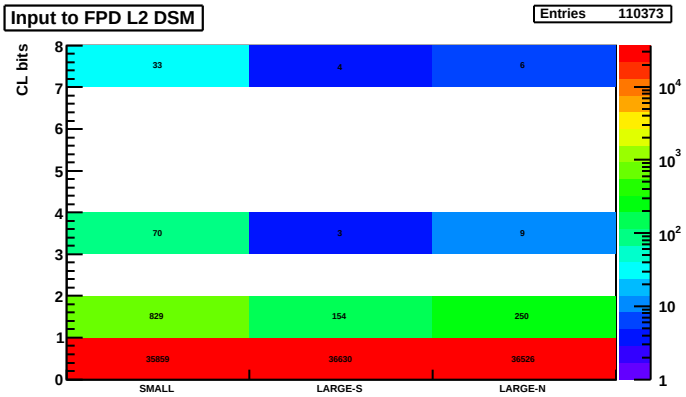

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

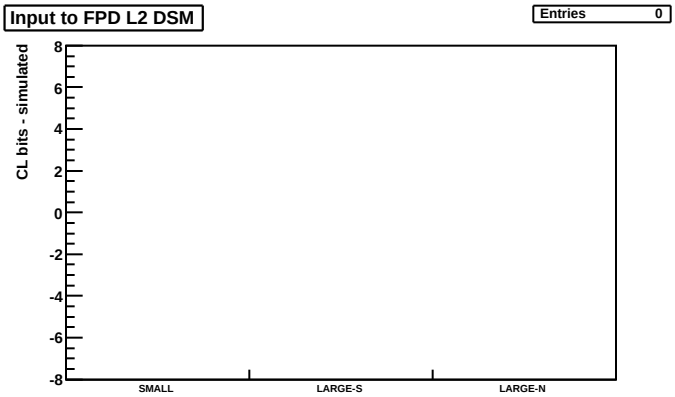

Input to FMS L1 DSM
Entries 441492
FMS L1 HT bits
DSM board

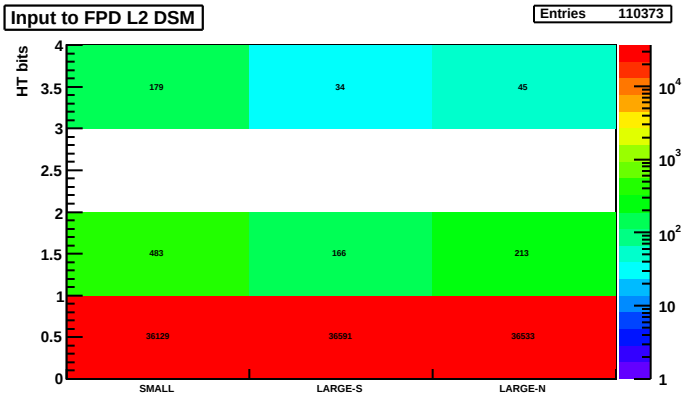

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

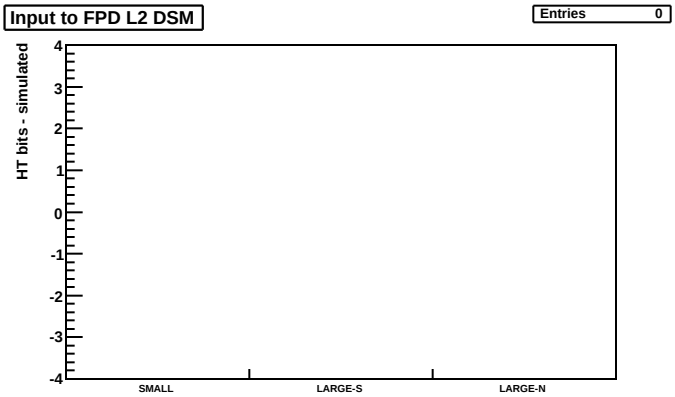

Input to FPD L2 DSM
Entries 294328
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

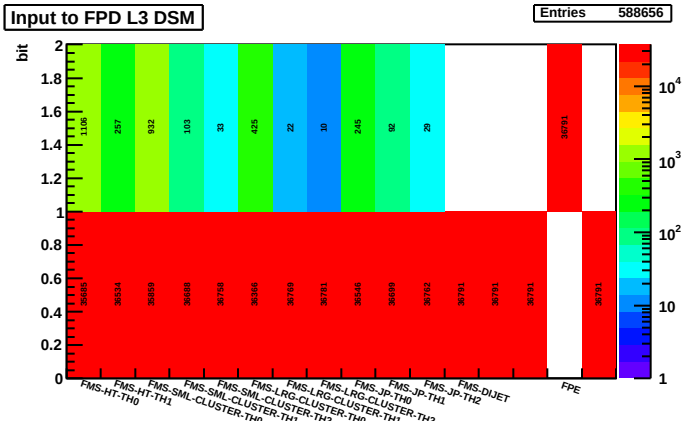

Input to FPD L2 DSM
Entries 84
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

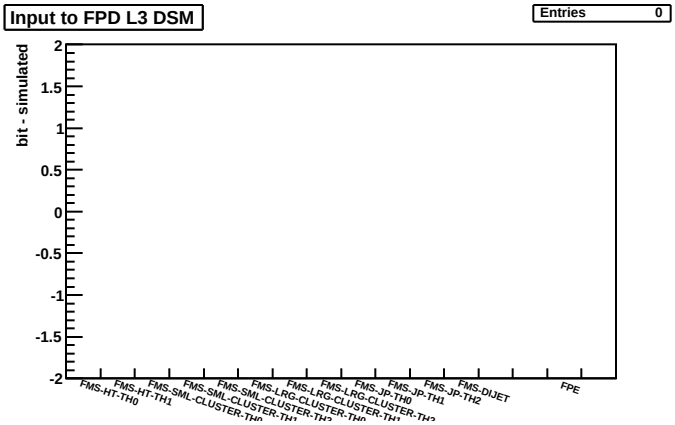

Input to FPD L2 DSM
Entries 110373
CL bits
33
4
6
70
3
9
829
154
250
35859
36630
36526
SMALL
LARGE-S
LARGE-N

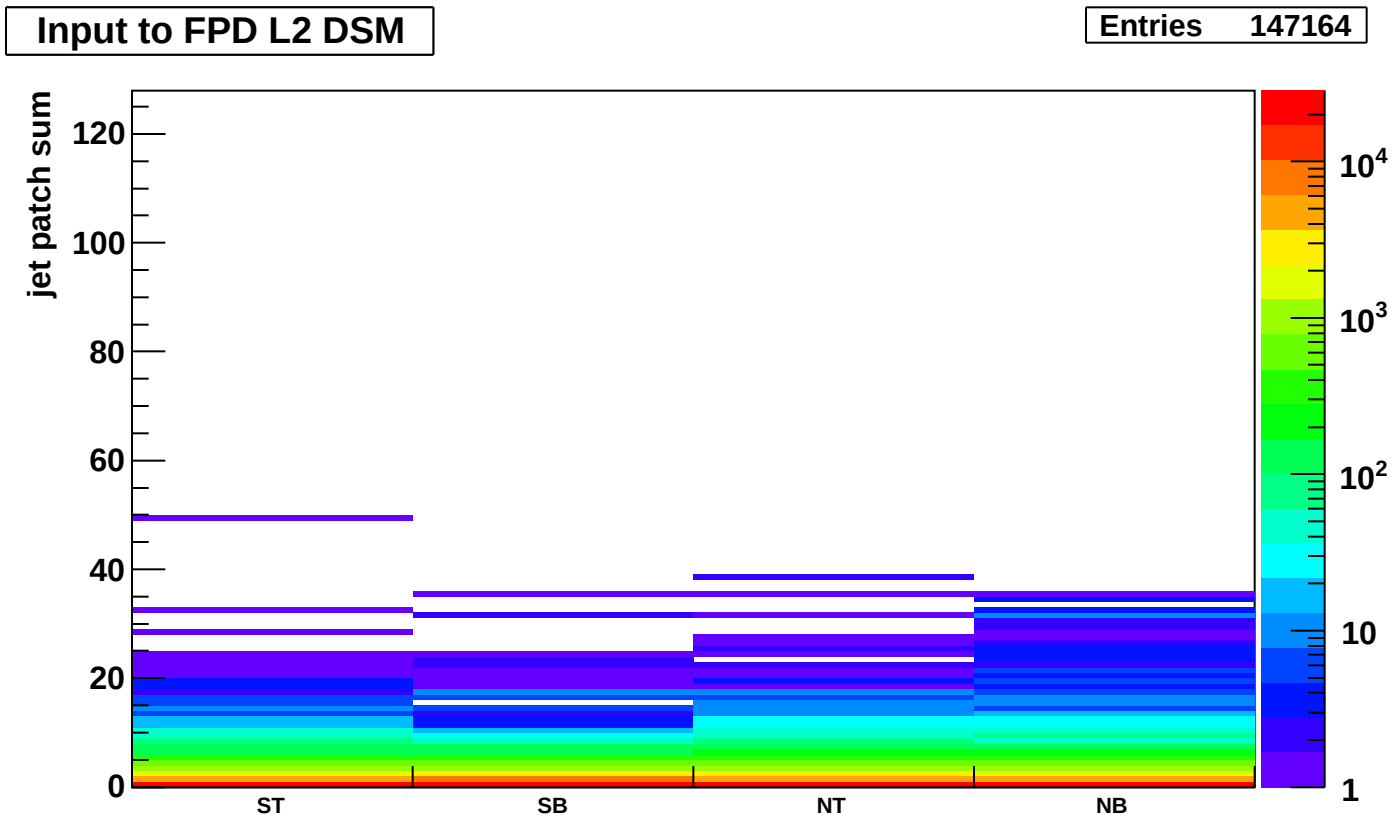

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

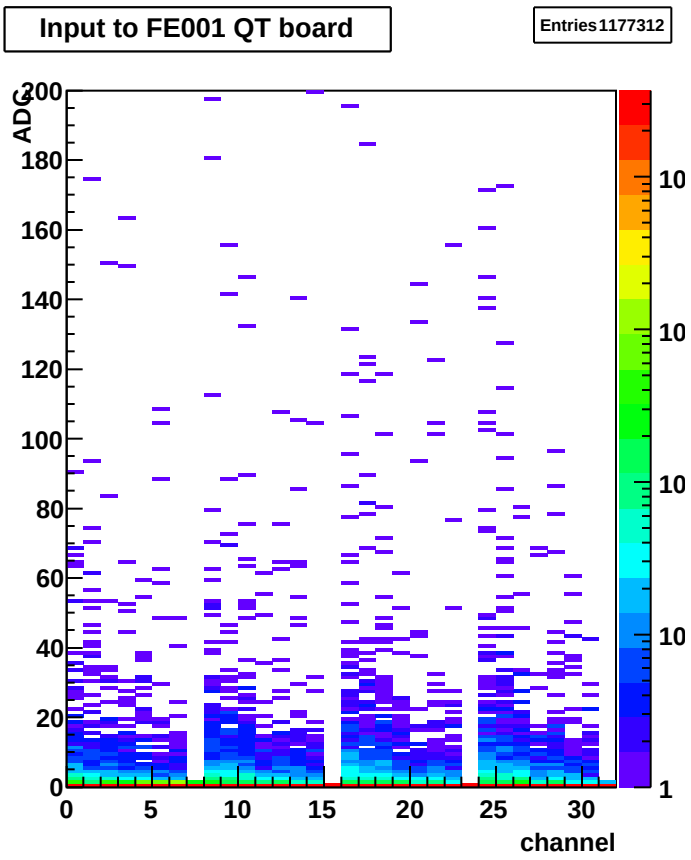

Input to FPD L2 DSM
Entries 110373
HT bits
179
34
45
483
166
213
36129
36591
36533
SMALL
LARGE-S
LARGE-N

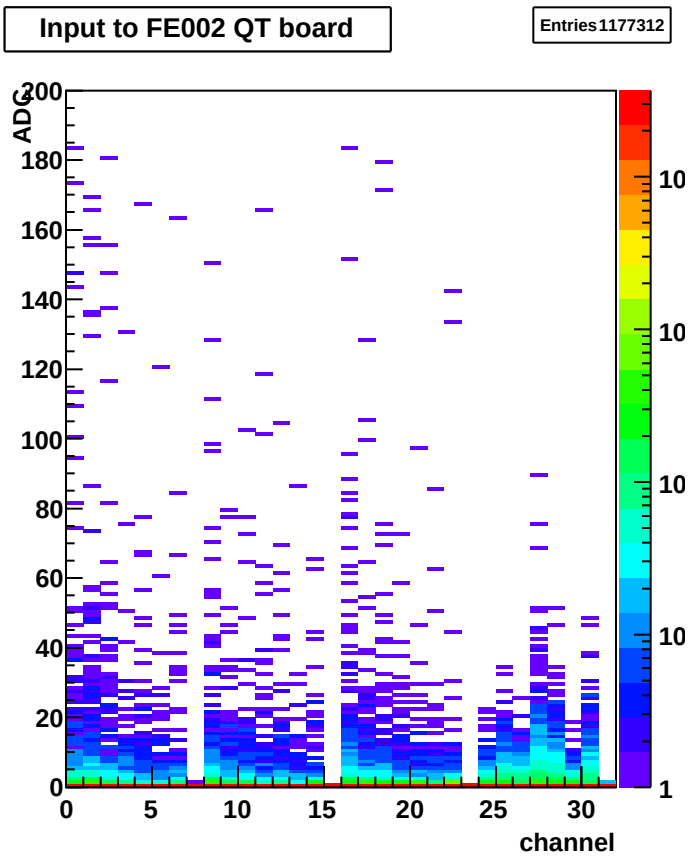

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

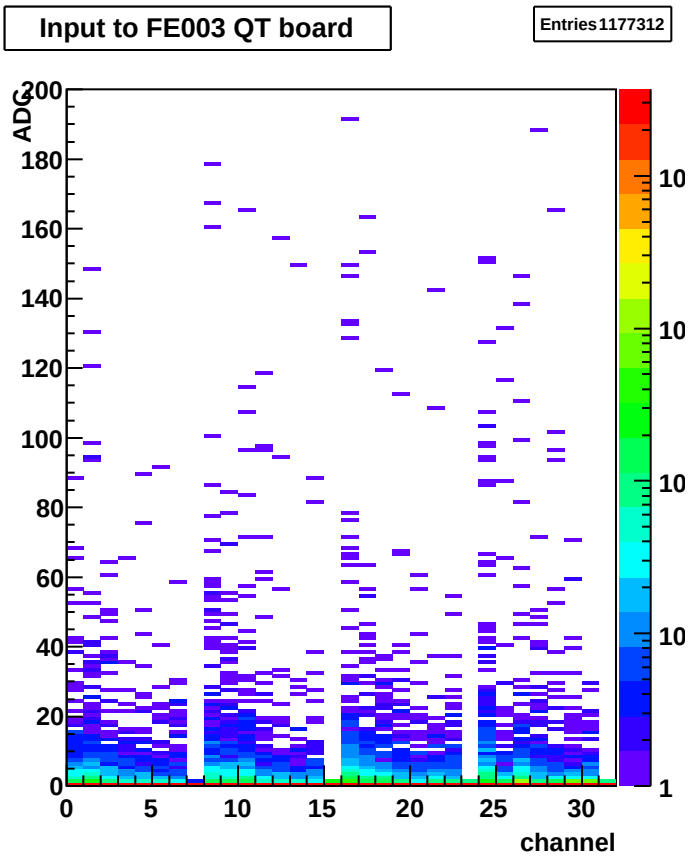

Input to FPD L3 DSM
Entries 588656
bit
1106
257
932
103
33
425
22
10
245
92
29
35791
35585
36534
35859
36688
39758
36366
39769
36781
36546
36589
36762
36791
36791
36791
39791
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

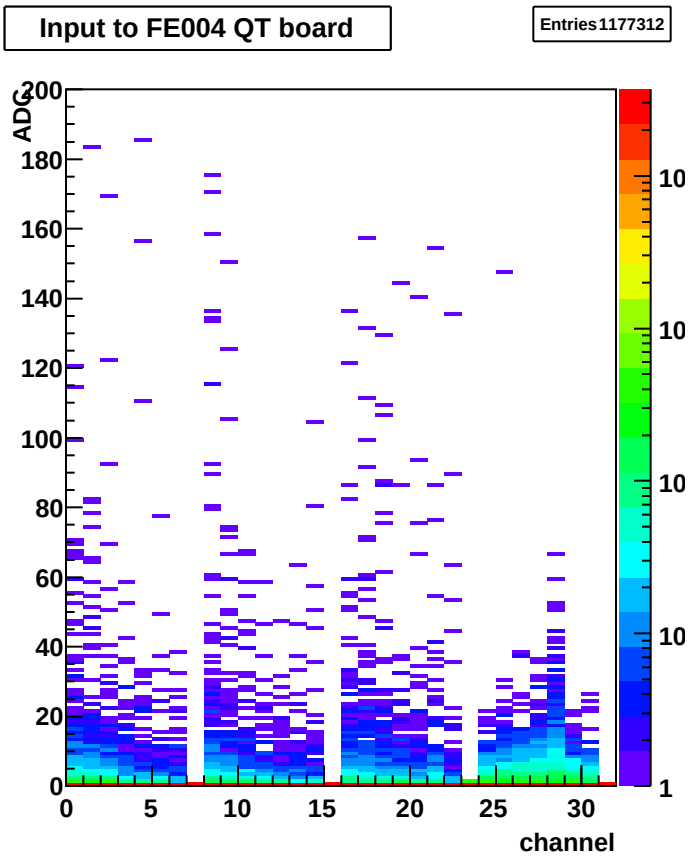

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

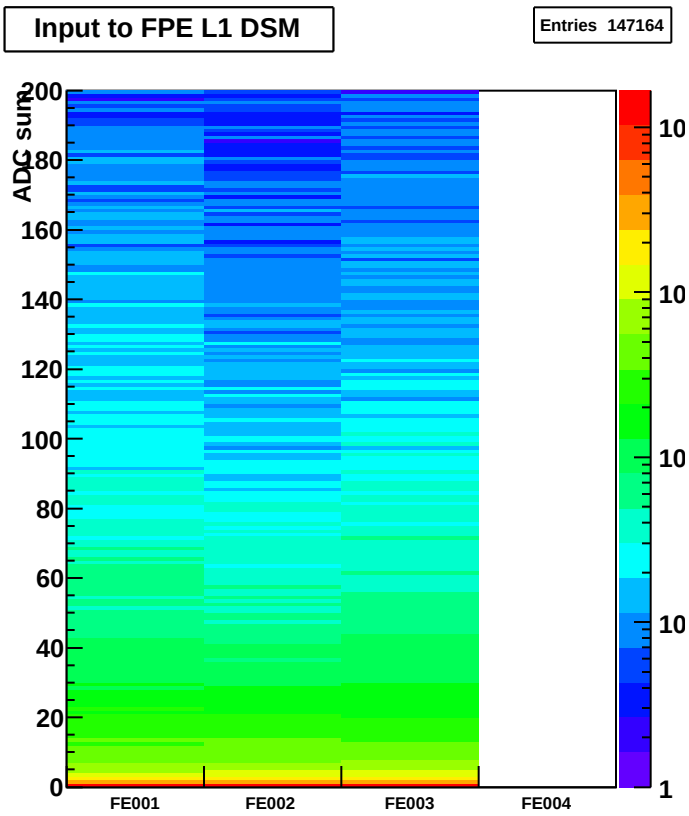

Input to FPD L2 DSM
Entries 147164
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

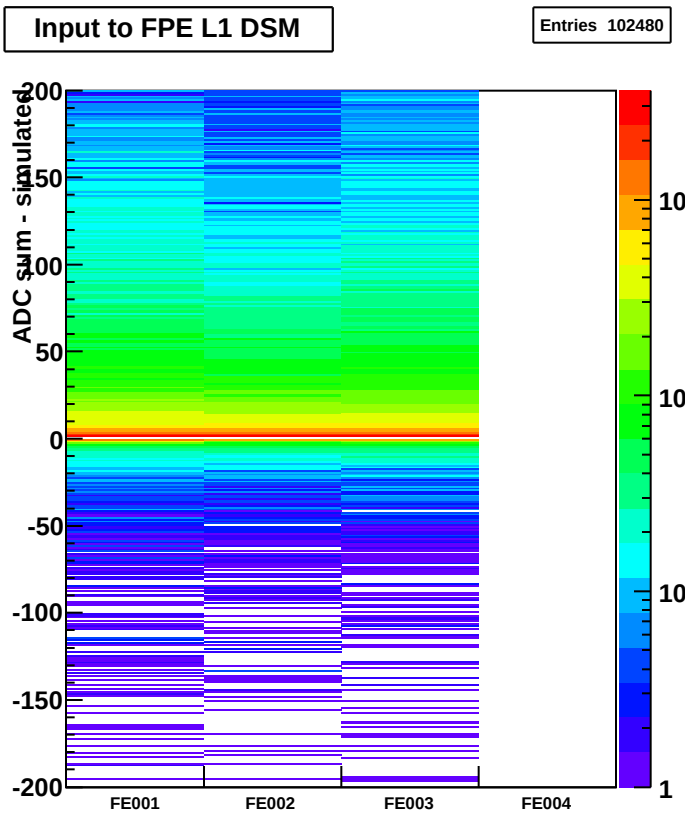

Input to FE001 QT board
Entries 1177312
ADC
channel

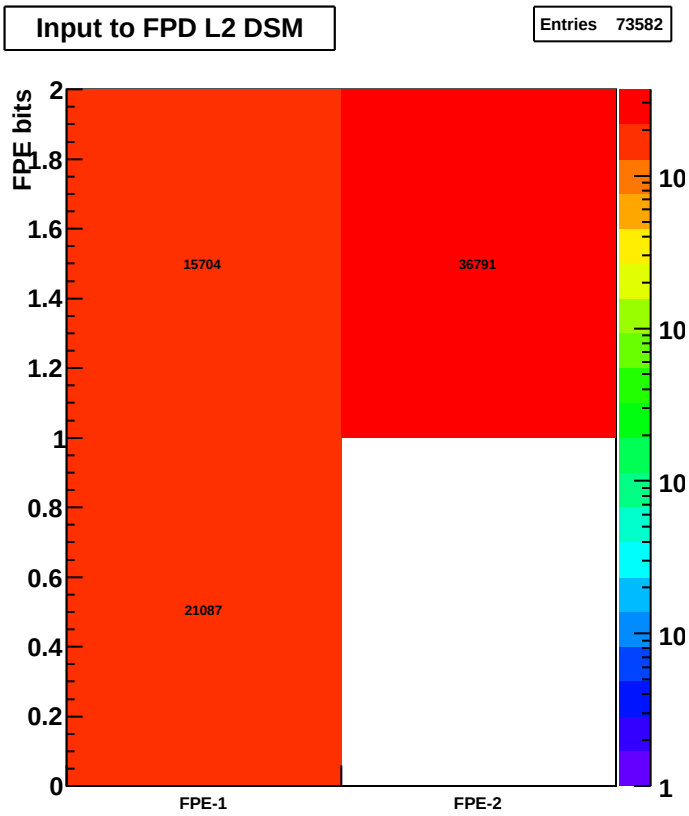

Input to FE002 QT board
Entries 1177312
ADC
channel

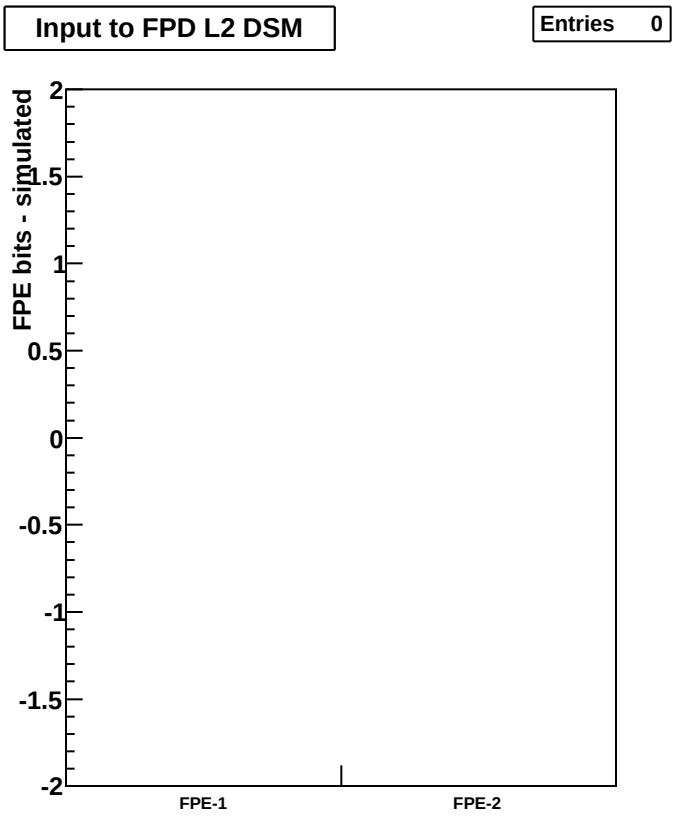

Input to FE003 QT board
Entries 1177312
ADC
channel


Input to FE004 QT board
Entries 1177312
ADC
channel


Input to FPE L1 DSM
Entries 147164
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 102480
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 73582
FPE bits
15704
36791
21087
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2