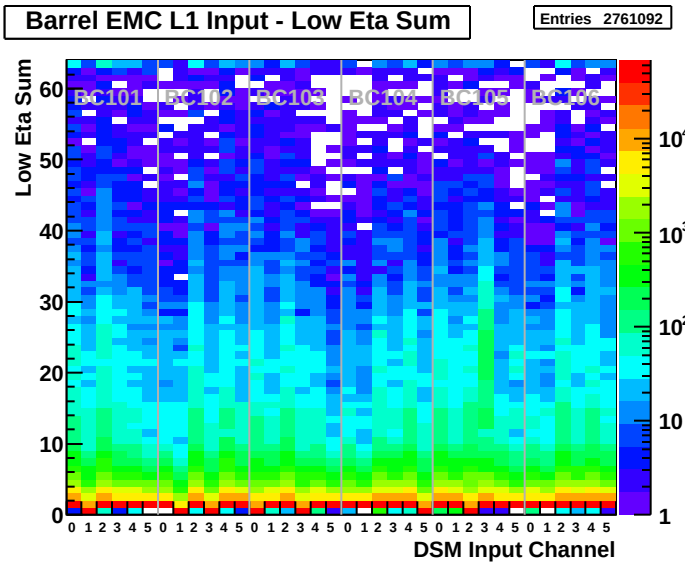
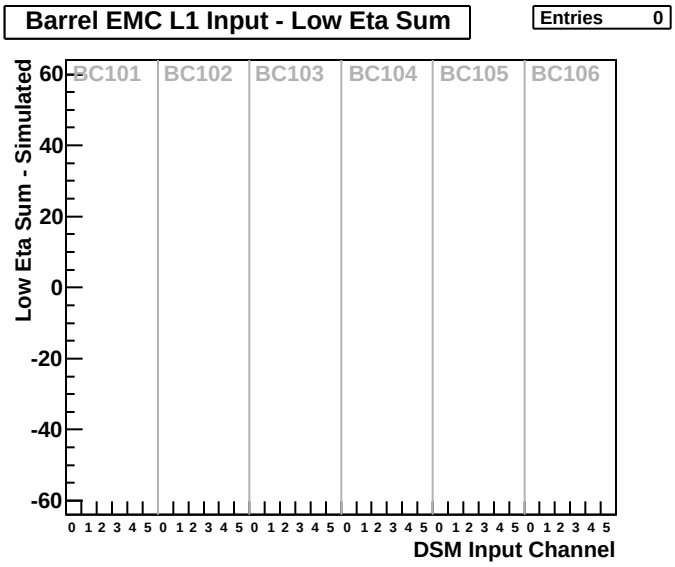
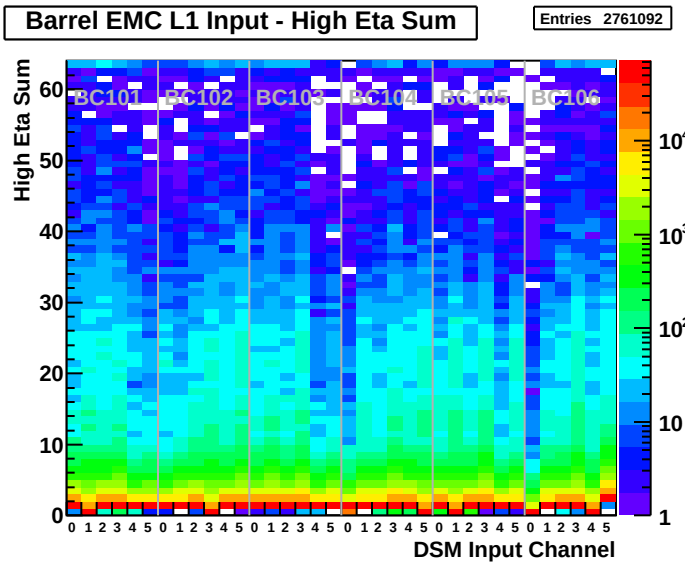
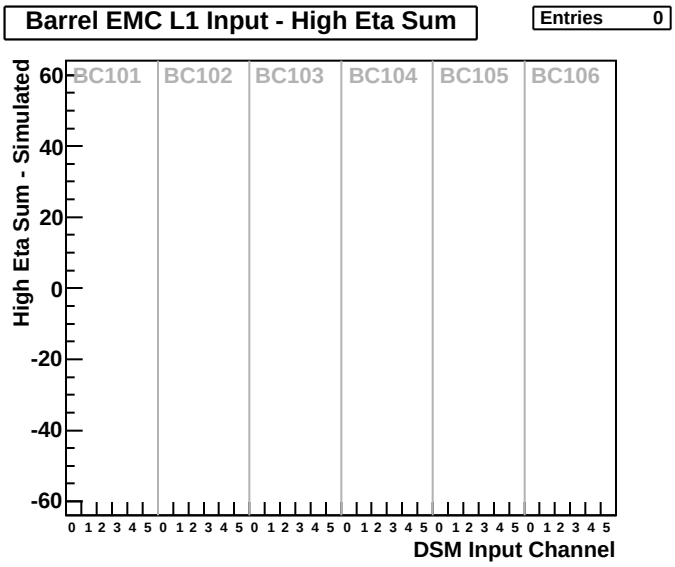
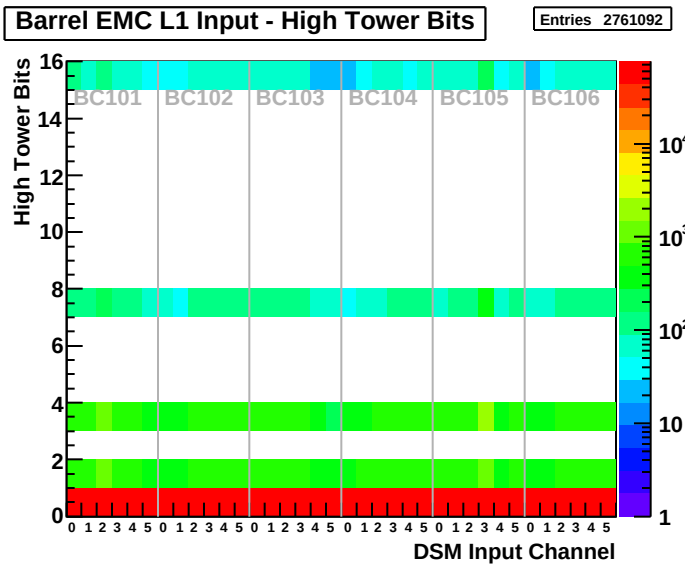
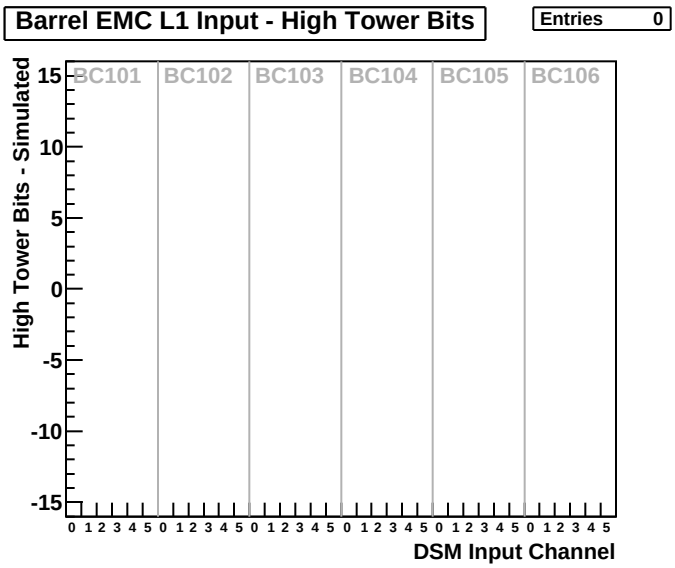

Barrel EMC L1 Input - Low Eta Sum
Entries 2761092
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

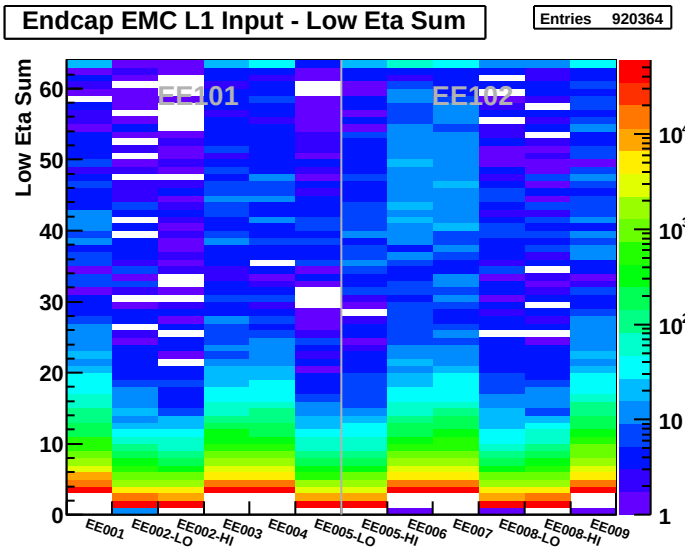

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

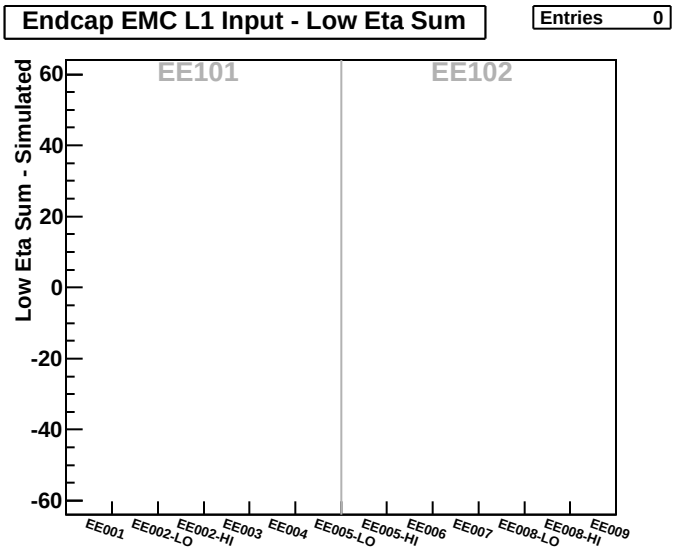

Barrel EMC L1 Input - High Eta Sum
Entries 2761092
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

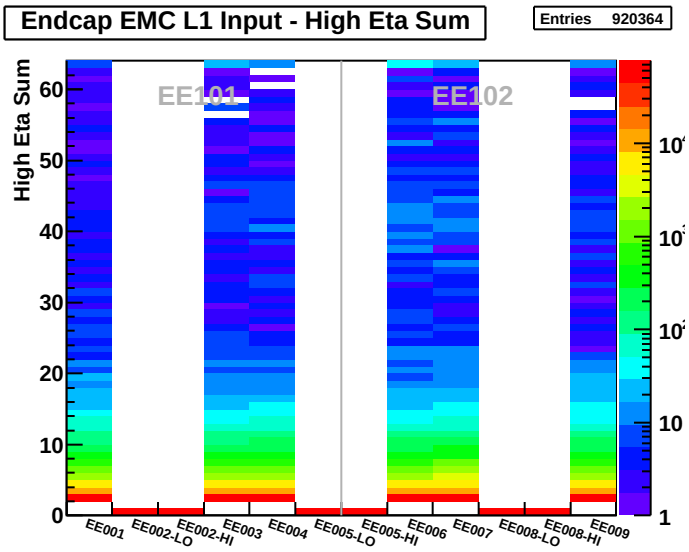

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

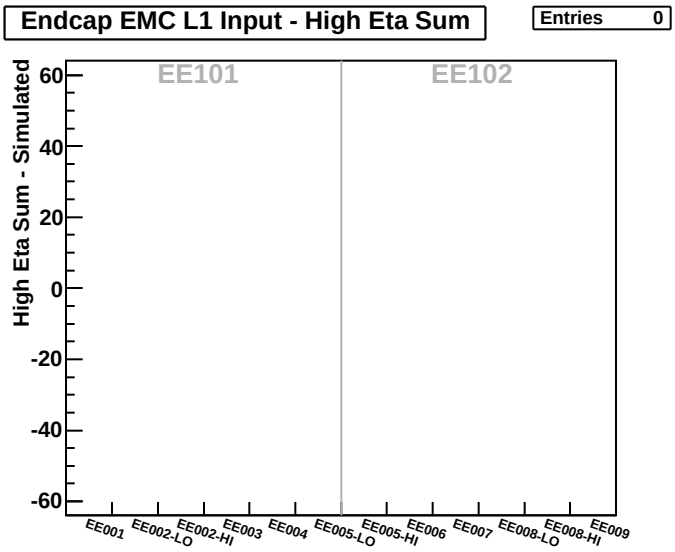

Barrel EMC L1 Input - High Tower Bits
Entries 2761092
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

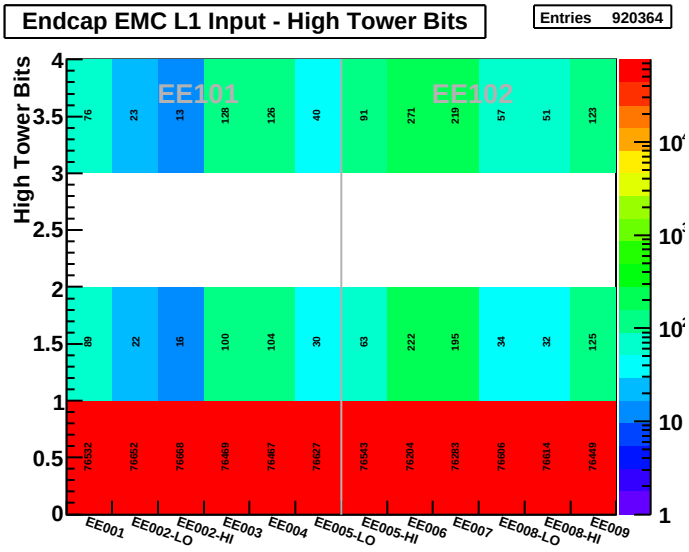

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

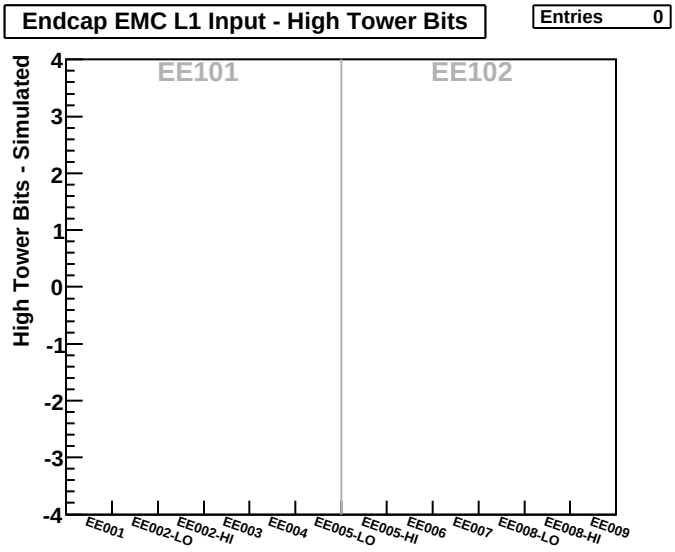

Endcap EMC L1 Input - Low Eta Sum
Entries 920364
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

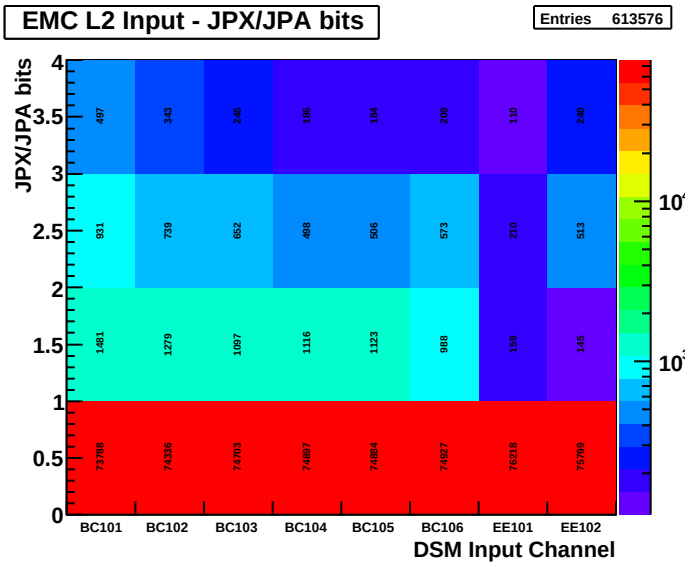

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

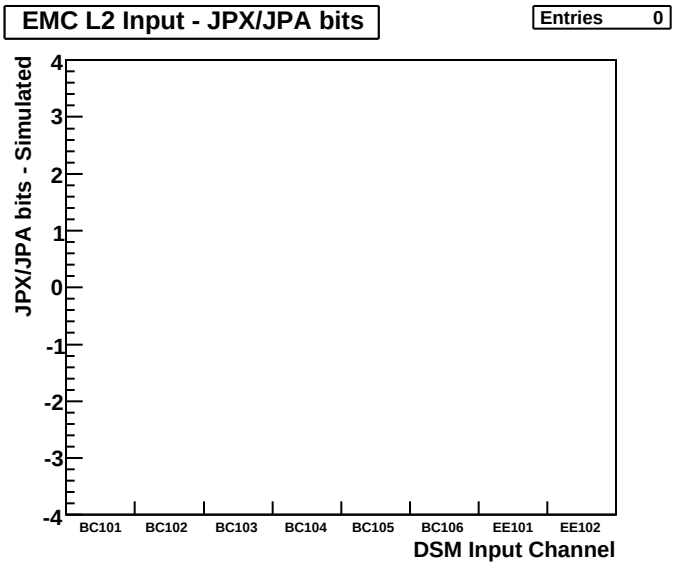

Endcap EMC L1 Input - High Eta Sum
Entries 920364
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

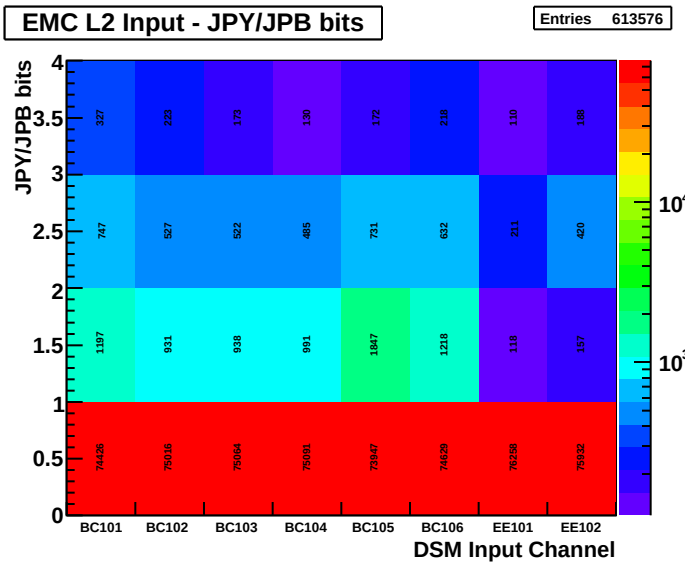

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

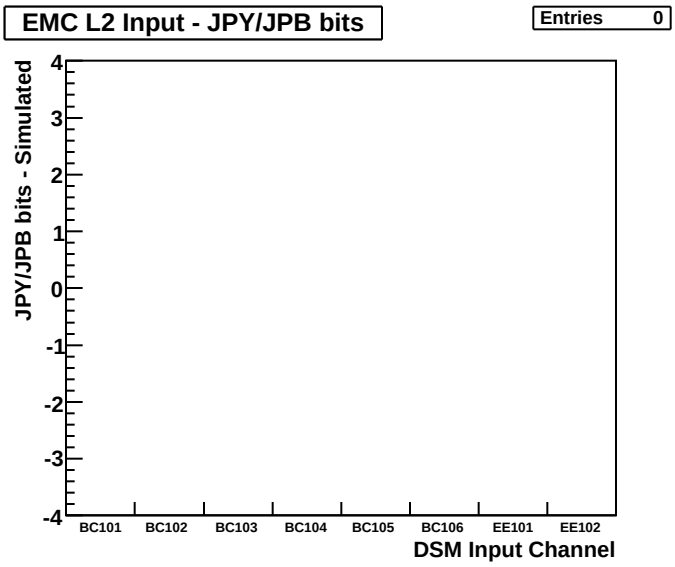

Endcap EMC L1 Input - High Tower Bits
Entries 920364
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

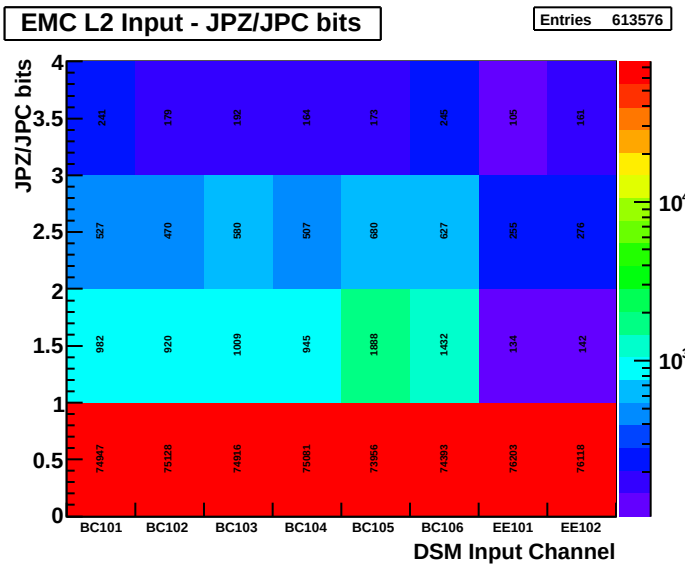

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

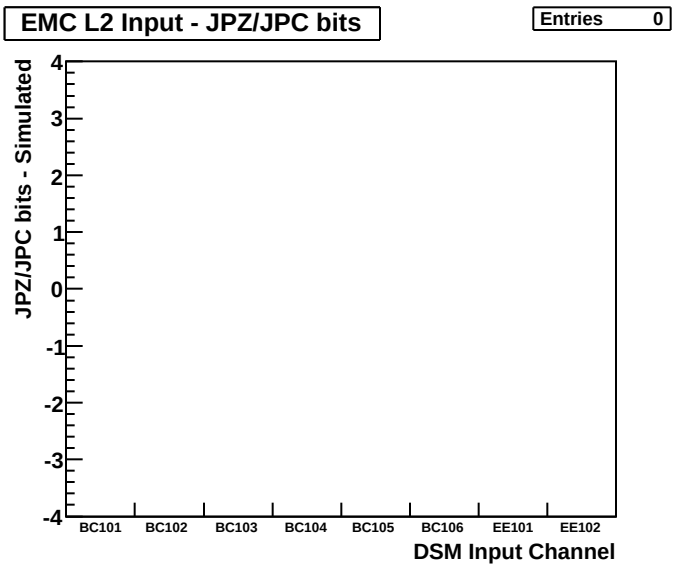

EMC L2 Input - JPX/JPA bits
Entries 613576
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
497 343 245 186 184 209 110 240
931 739 652 498 506 573 210 513
1481 1279 1097 1116 1123 988 159 145
73788 74336 74703 74897 74884 74927 76218 75799

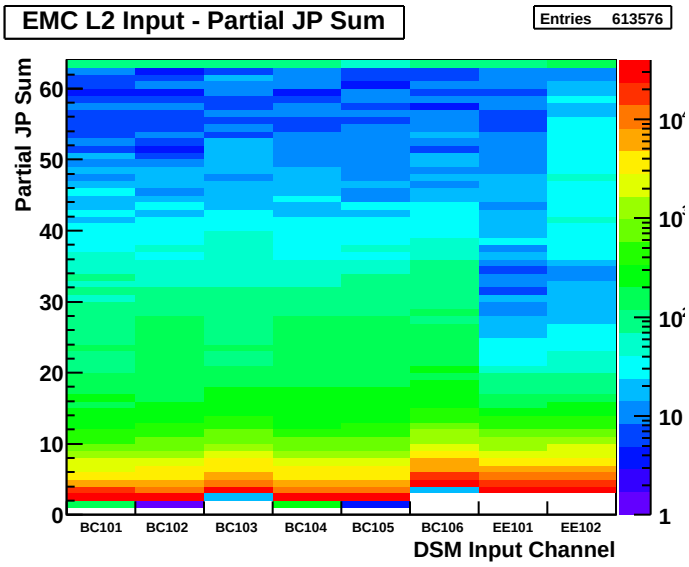

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

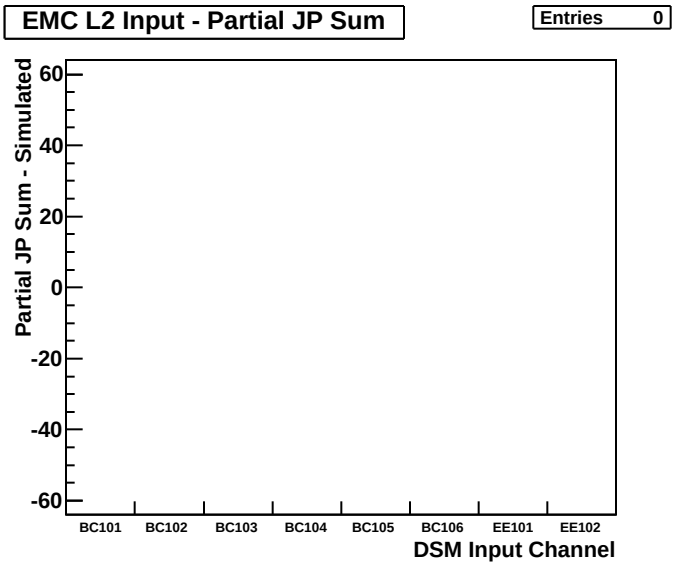

EMC L2 Input - JPY/JPB bits
Entries 613576
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
327 222 173 130 172 218 110 188
747 527 522 485 731 632 211 420
1197 931 938 991 1047 1218 118 157
74426 75016 75064 75091 73947 74629 76258 75932

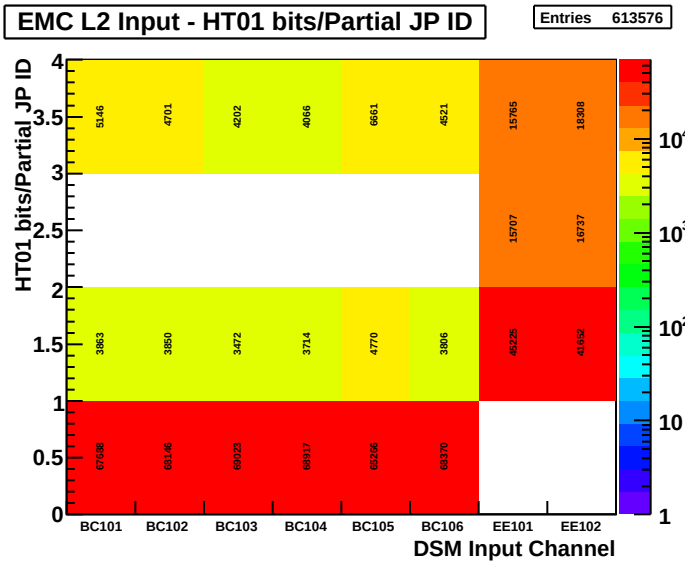

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

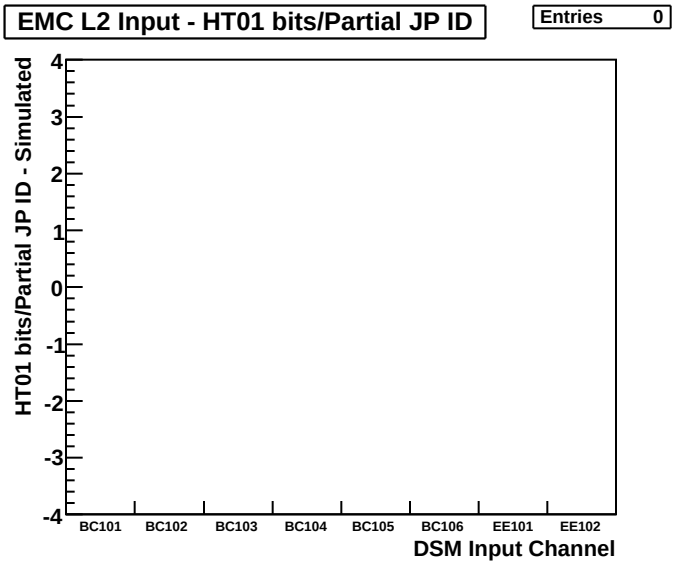

EMC L2 Input - JPZ/JPC bits
Entries 613576
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
241 179 192 164 173 245 105 161
527 470 580 507 680 627 255 276
982 920 1009 945 1888 1432 134 142
74947 75128 74916 75081 73956 74393 76203 76118

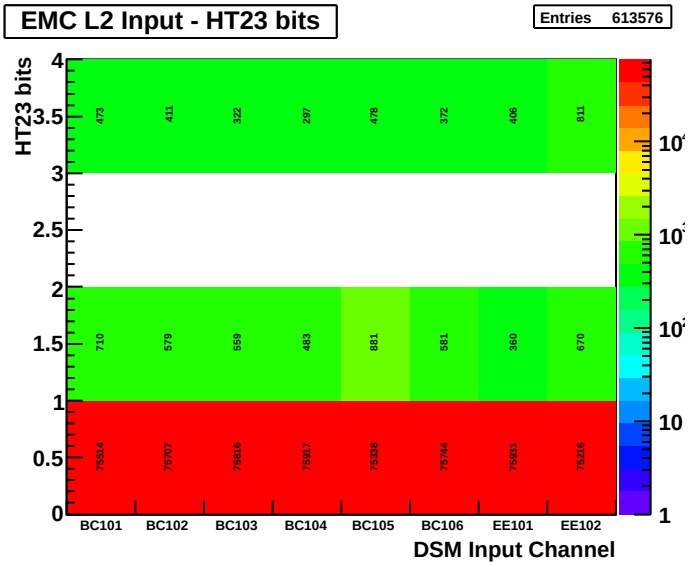

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

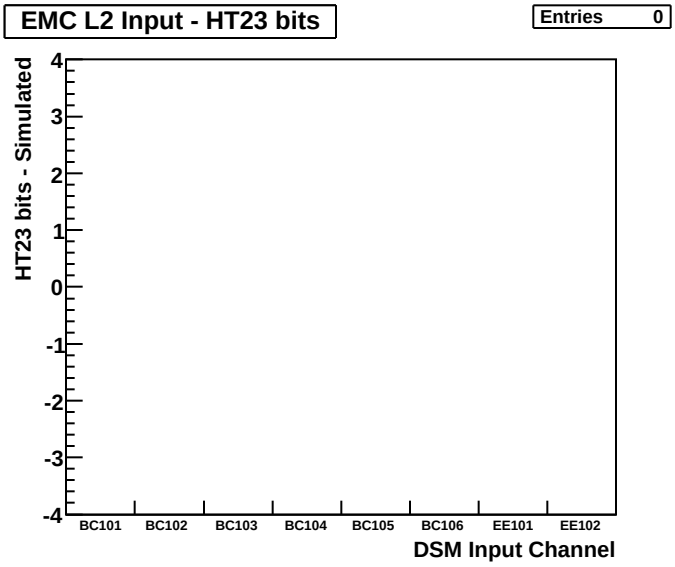

EMC L2 Input - Partial JP Sum
Entries 613576
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

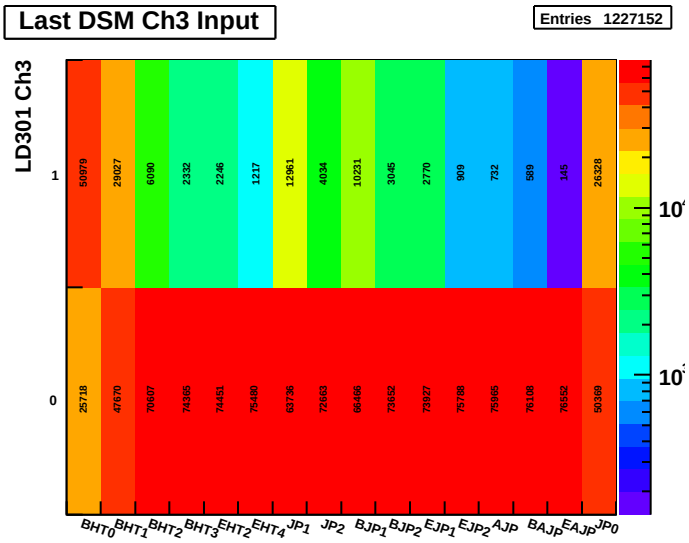

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

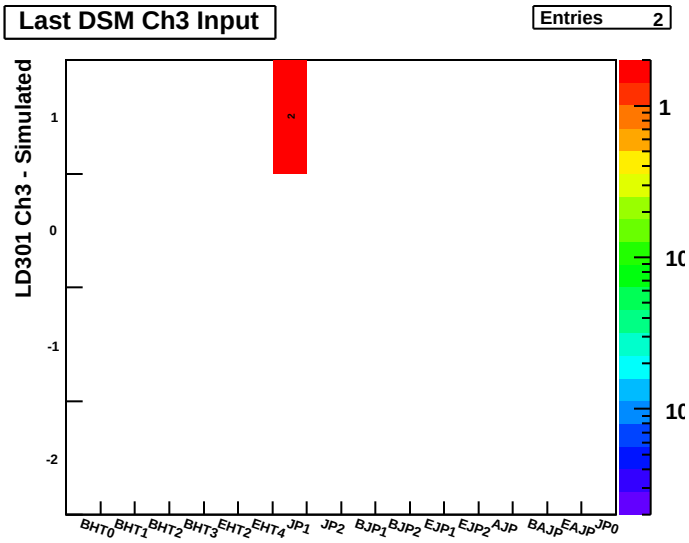

EMC L2 Input - HT01 bits/Partial JP ID
Entries 613576
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

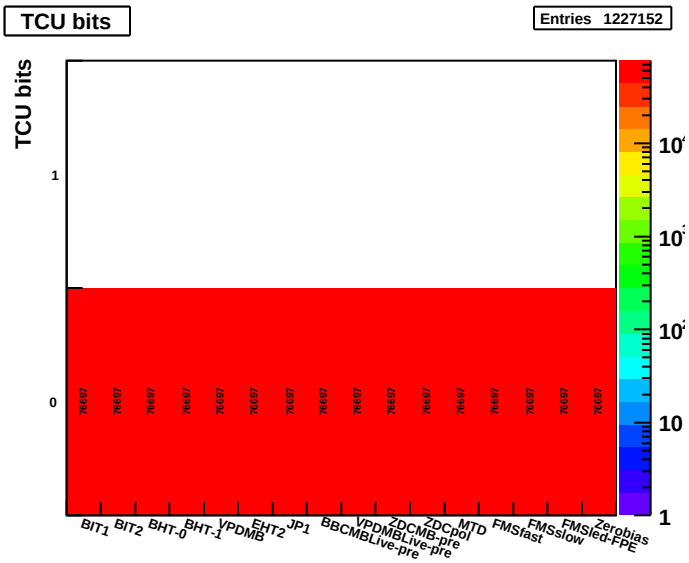

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

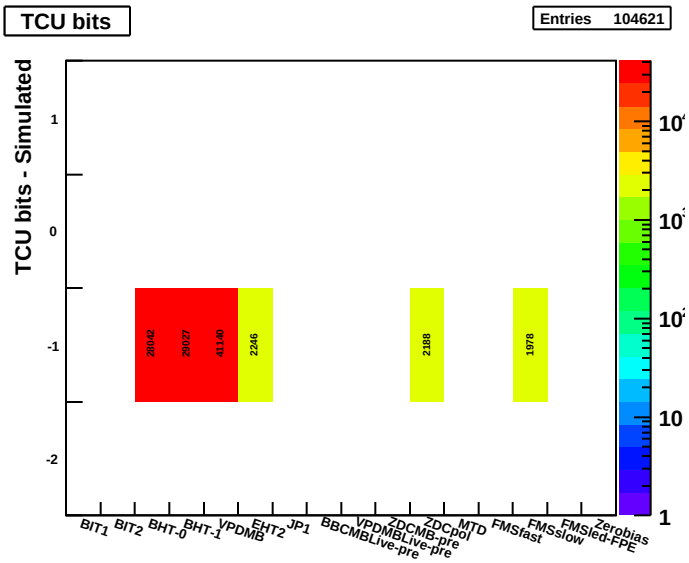

EMC L2 Input - HT23 bits
Entries 613576
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

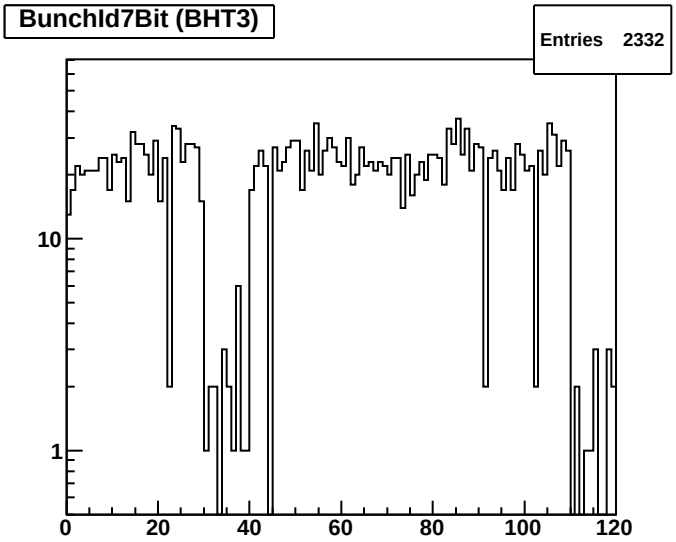

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

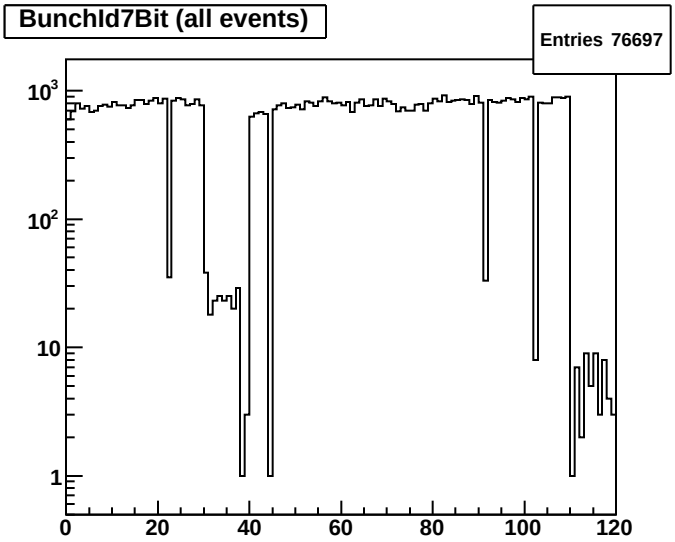

Last DSM Ch3 Input
Entries 1227152
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

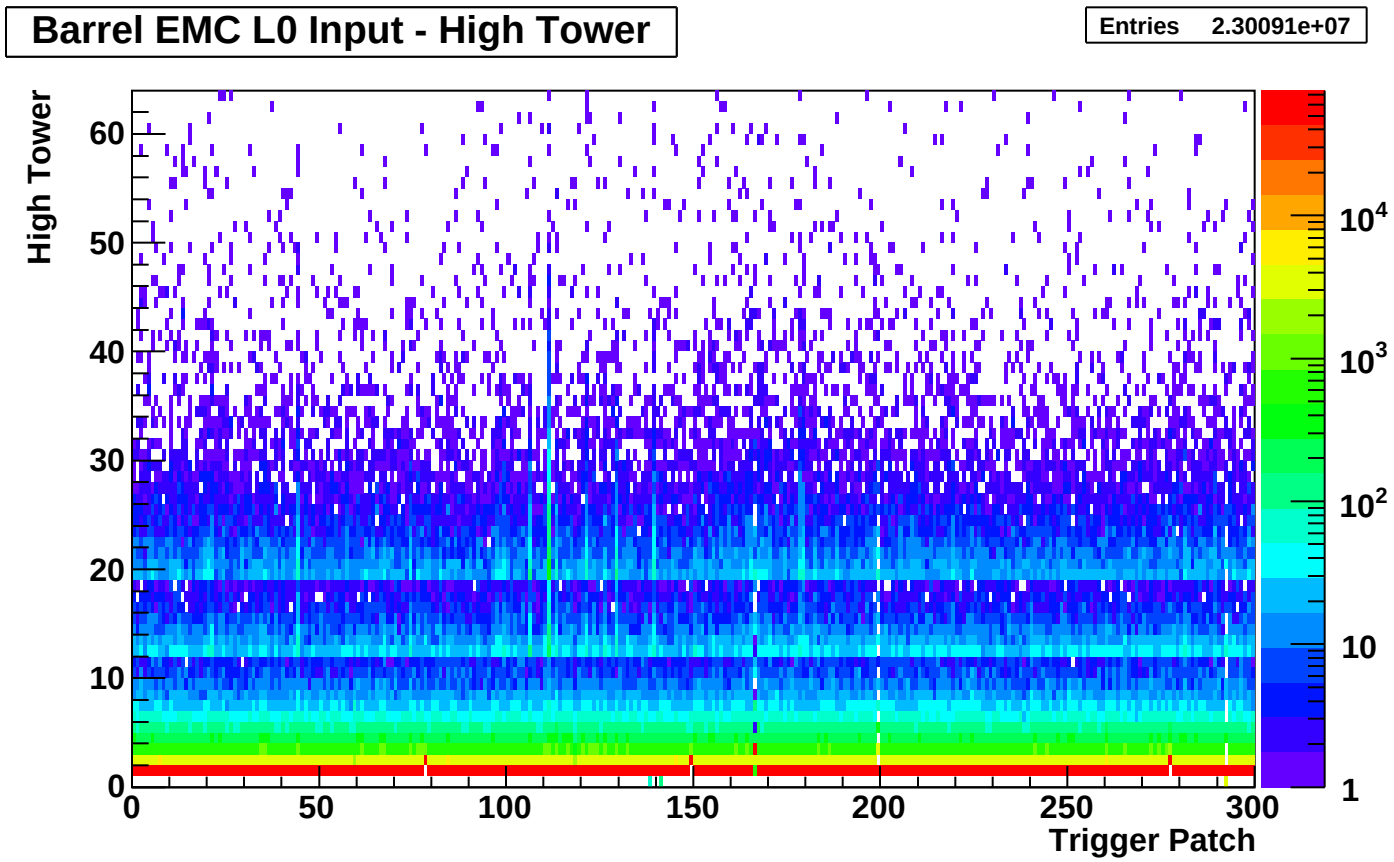

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

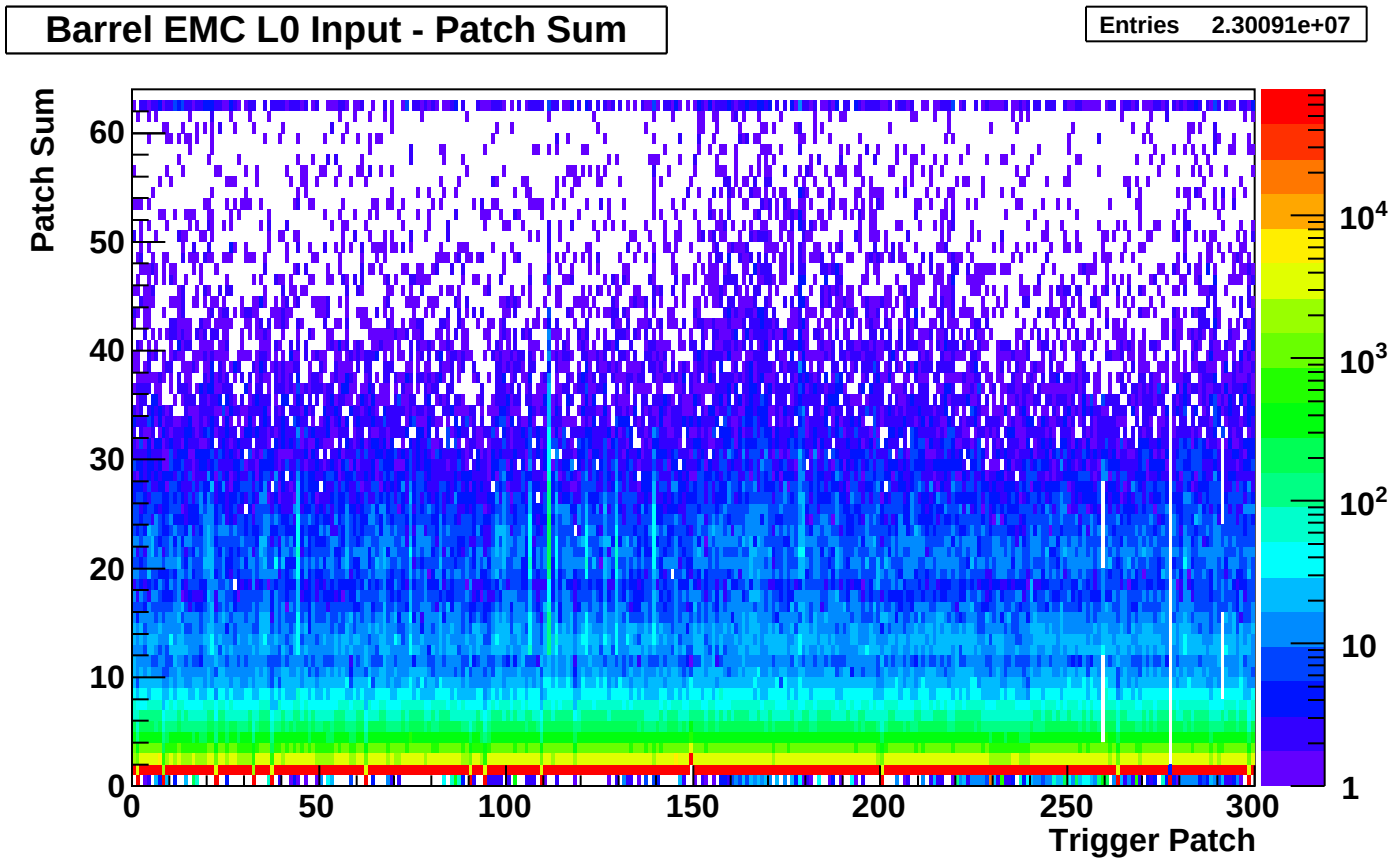

TCU bits
Entries 1227152
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

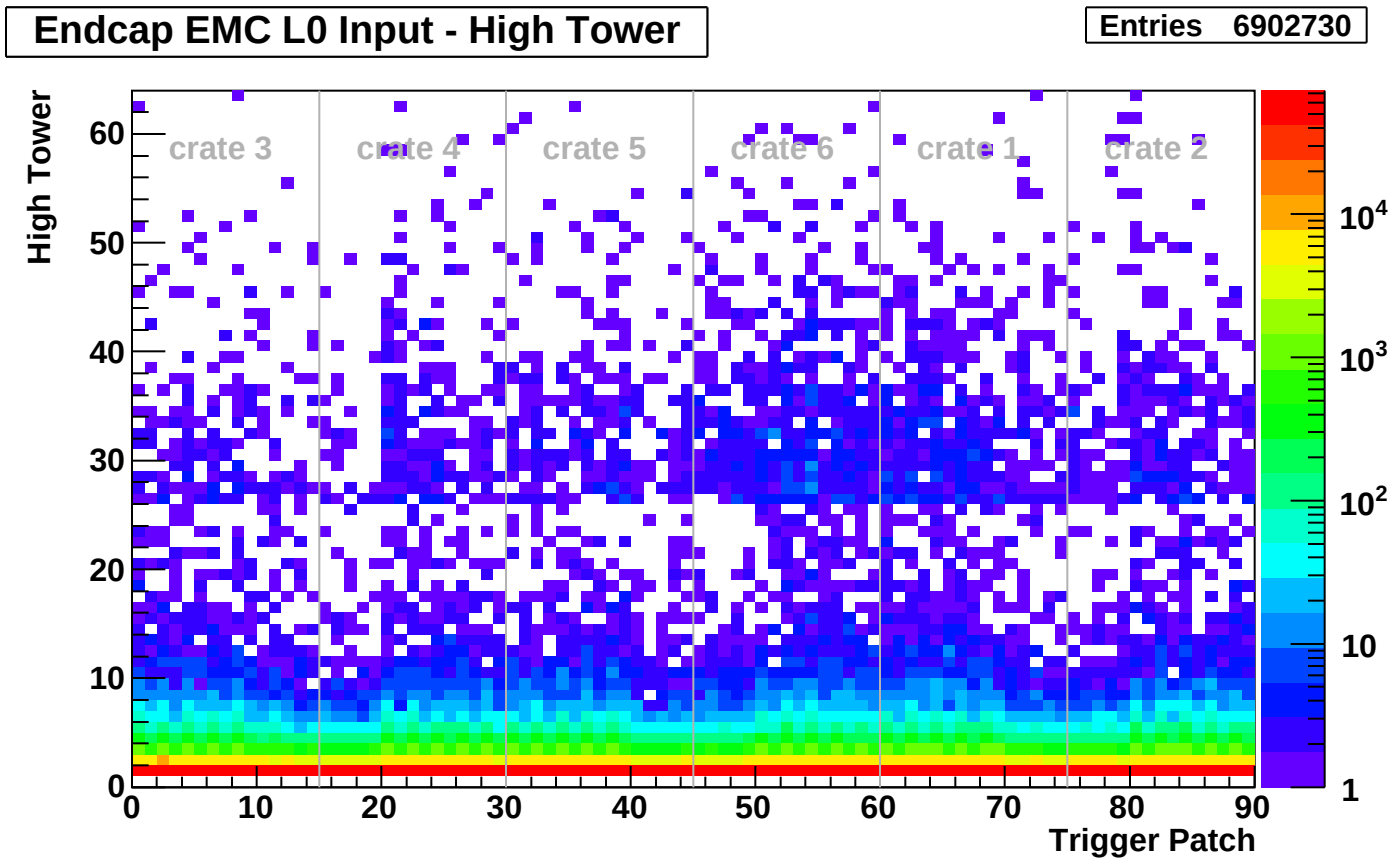

TCU bits
Entries 104621
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

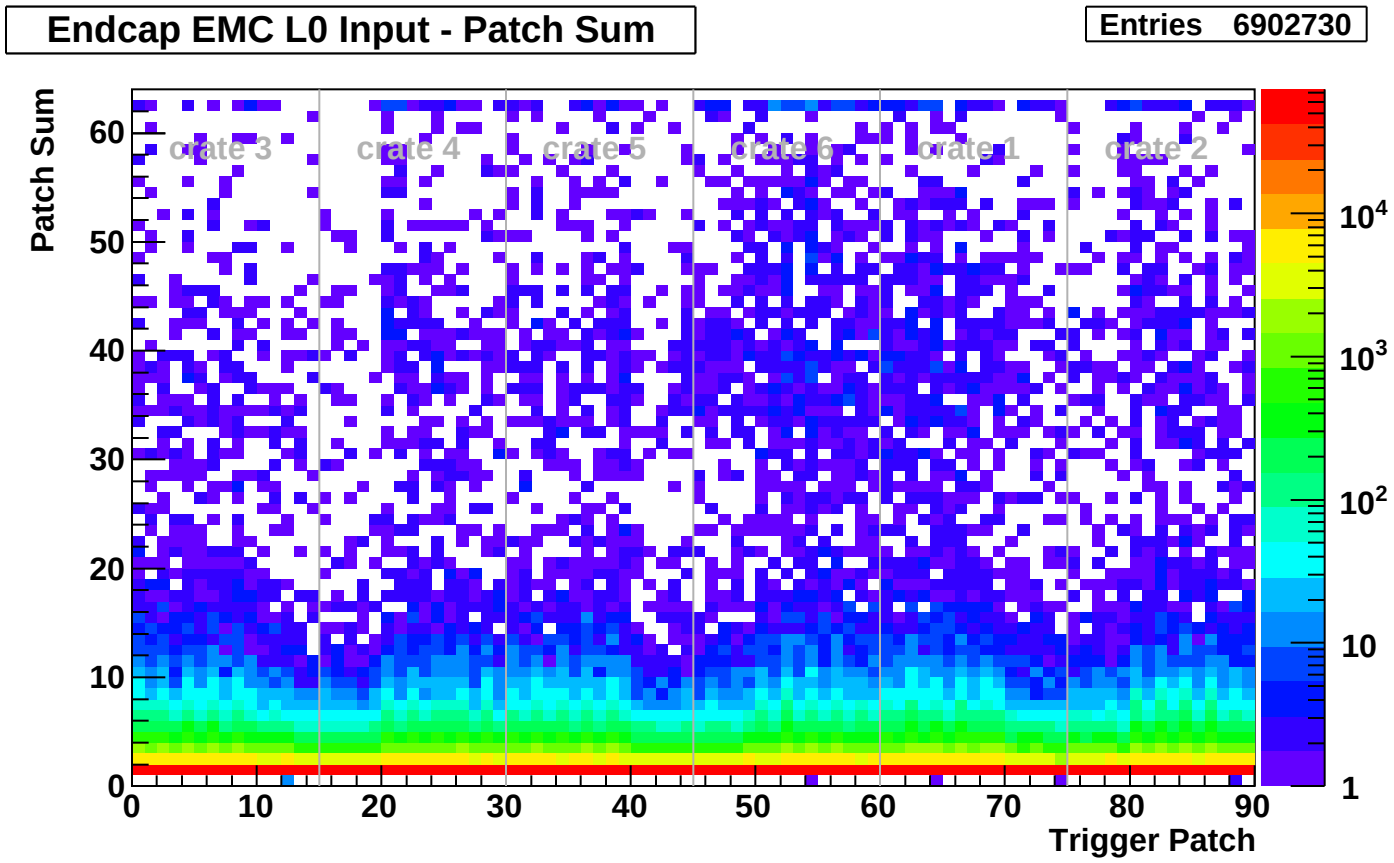

BunchId7Bit (BHT3)
Entries 2332

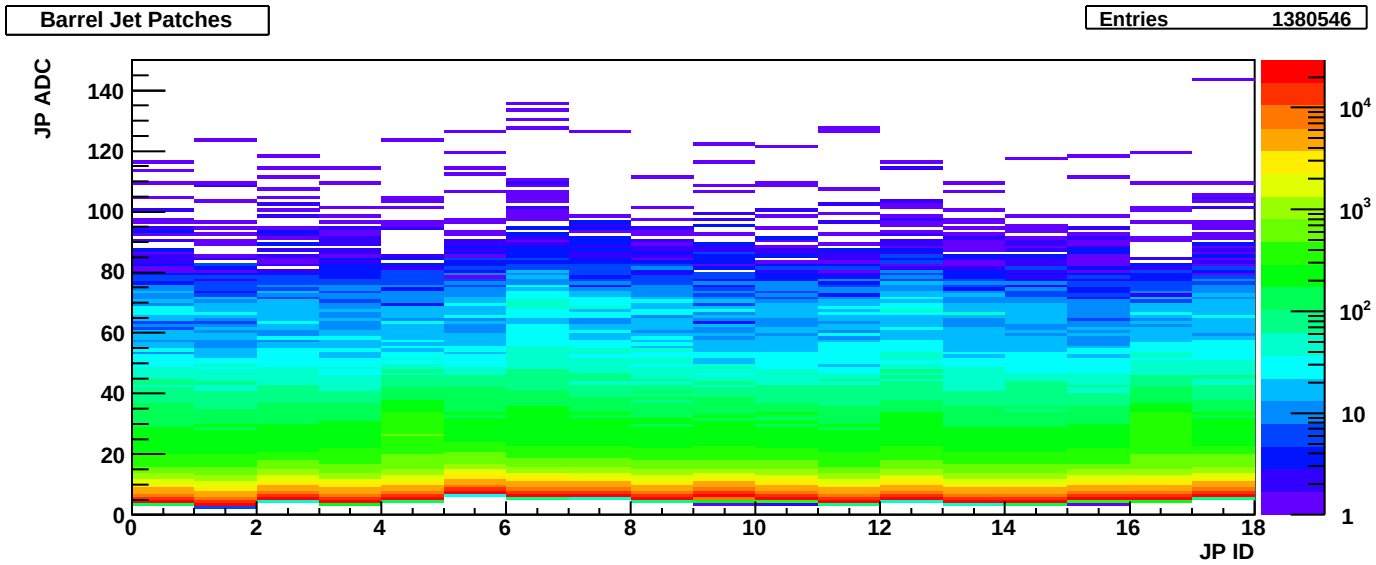

BunchId7Bit (all events)
Entries 76697

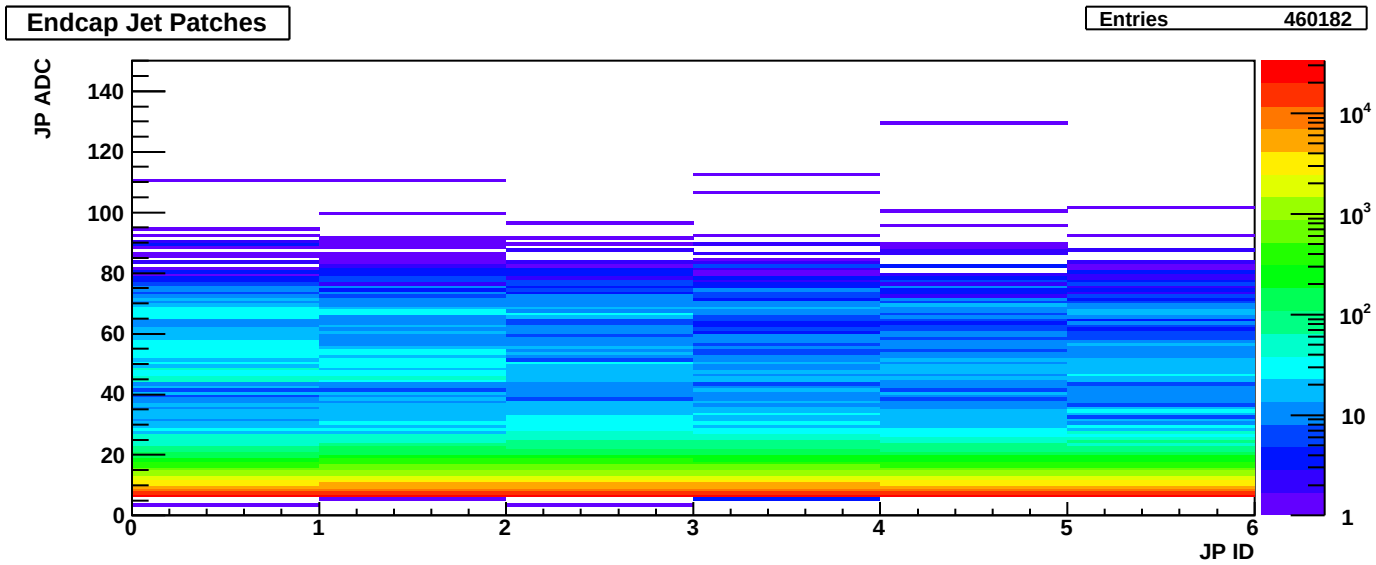

Barrel EMC L0 Input - High Tower
Entries 2.30091e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

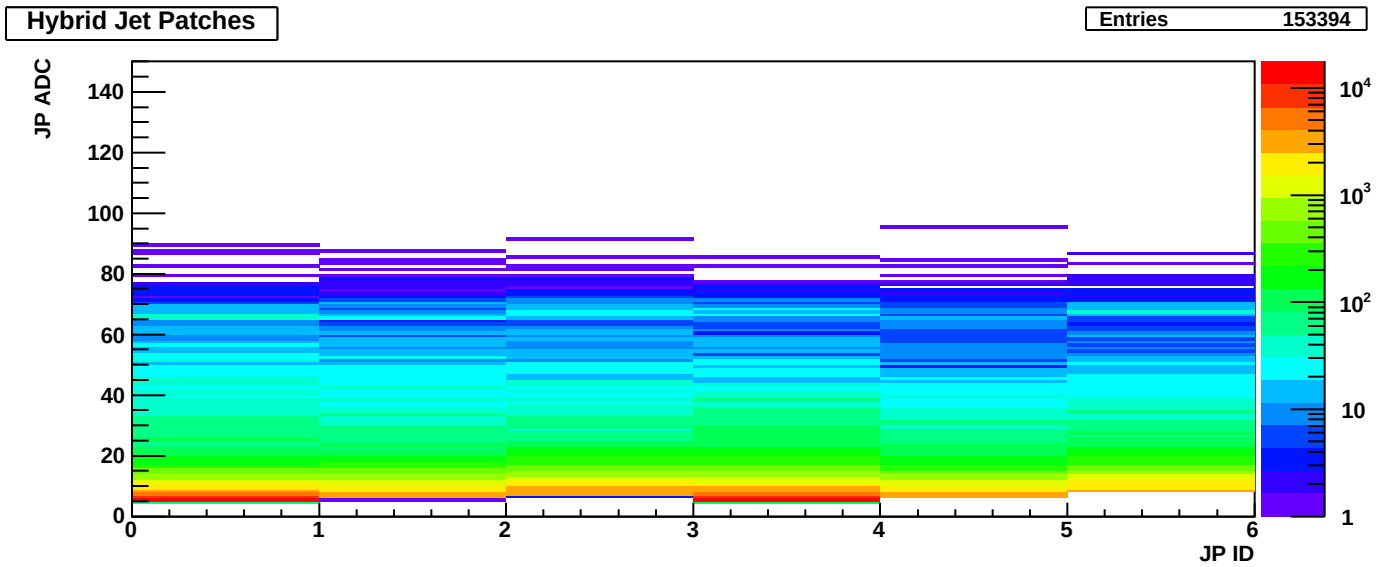

Barrel EMC L0 Input - Patch Sum
Entries 2.30091e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

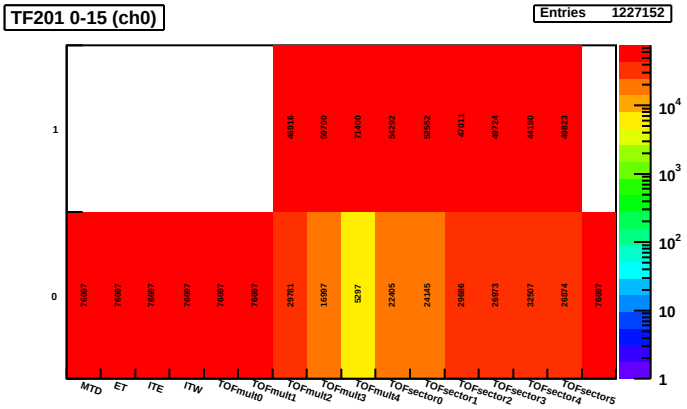

Endcap EMC L0 Input - High Tower
Entries 6902730
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

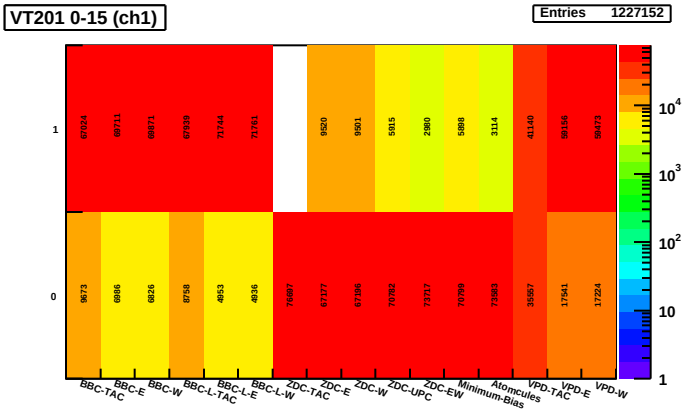

Endcap EMC L0 Input - Patch Sum
Entries 6902730
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

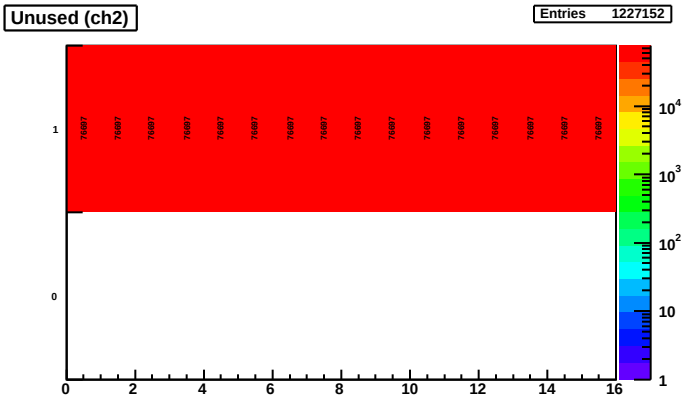

Barrel Jet Patches
Entries 1380546
JP ADC
JP ID

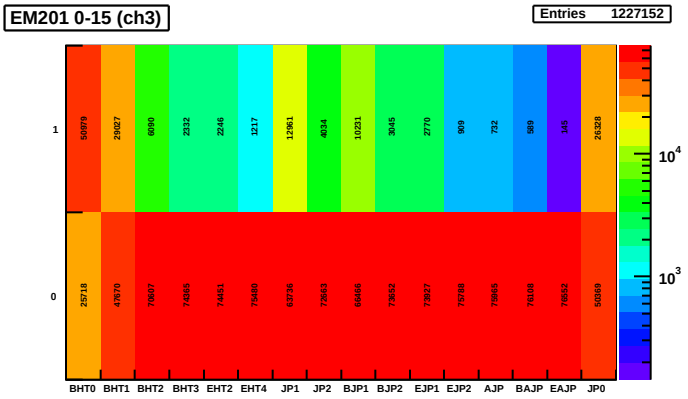

Endcap Jet Patches
Entries 460182
JP ADC
JP ID

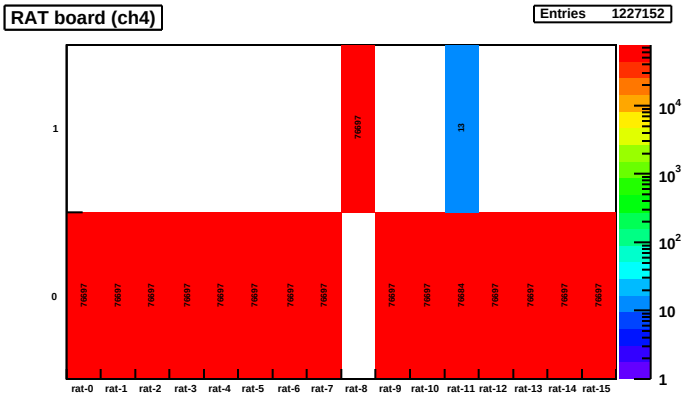

Hybrid Jet Patches
Entries 153394
JP ADC
JP ID

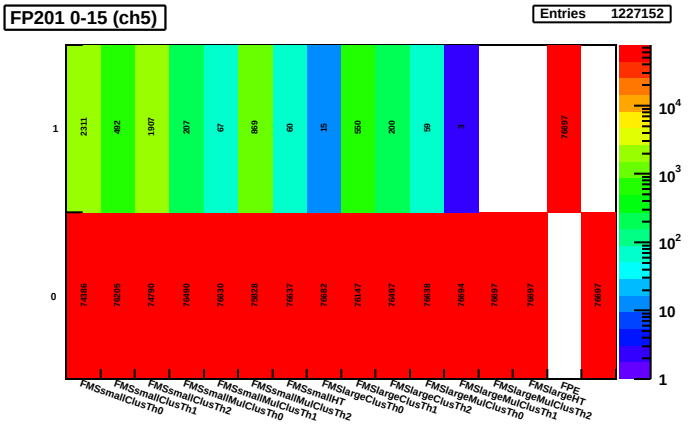

TF201 0-15 (ch0)
Entries 1227152
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

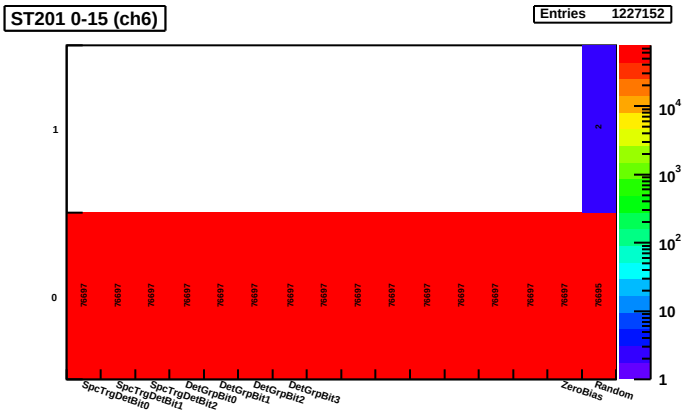

VT201 0-15 (ch1)
Entries 1227152
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

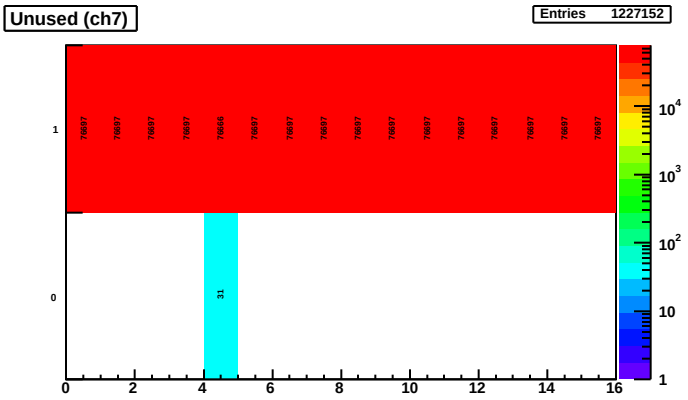

Unused (ch2)
Entries 1227152

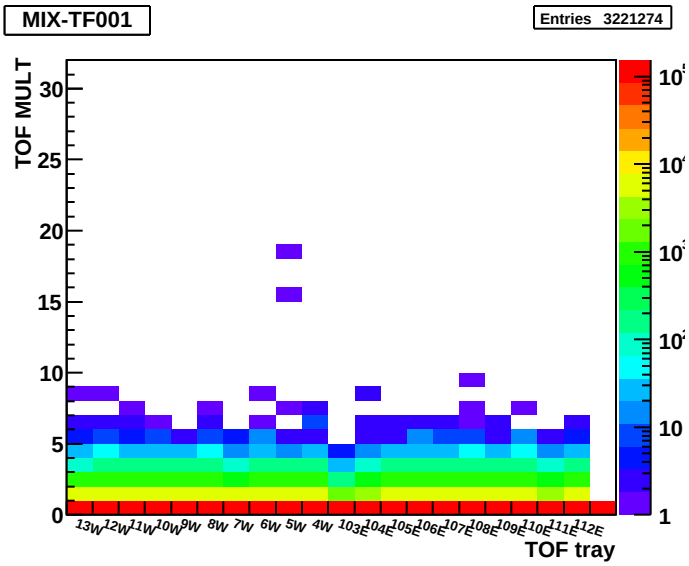

EM201 0-15 (ch3)
Entries 1227152
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

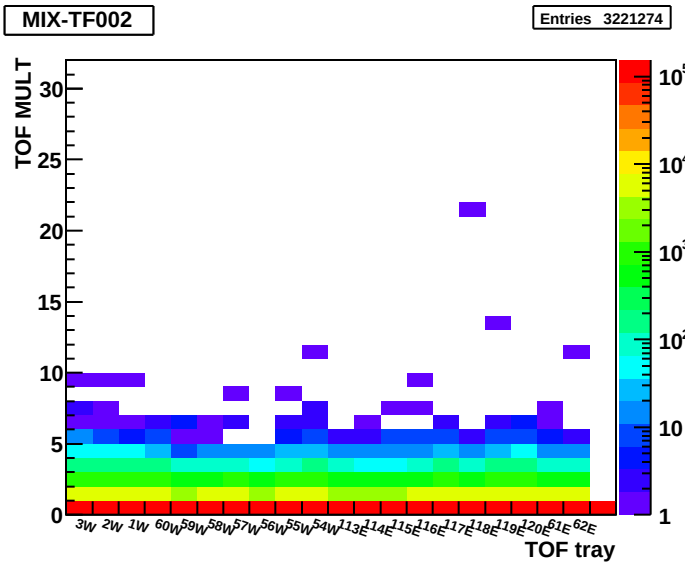

RAT board (ch4)
Entries 1227152
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

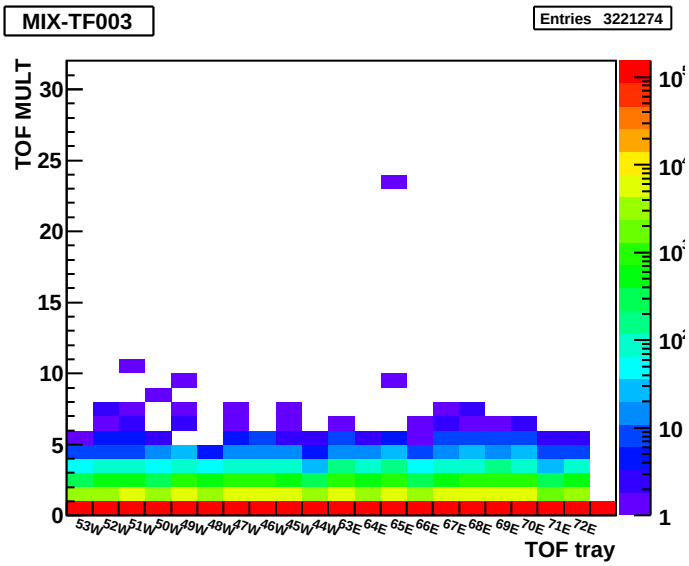

FP201 0-15 (ch5)
Entries 1227152
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

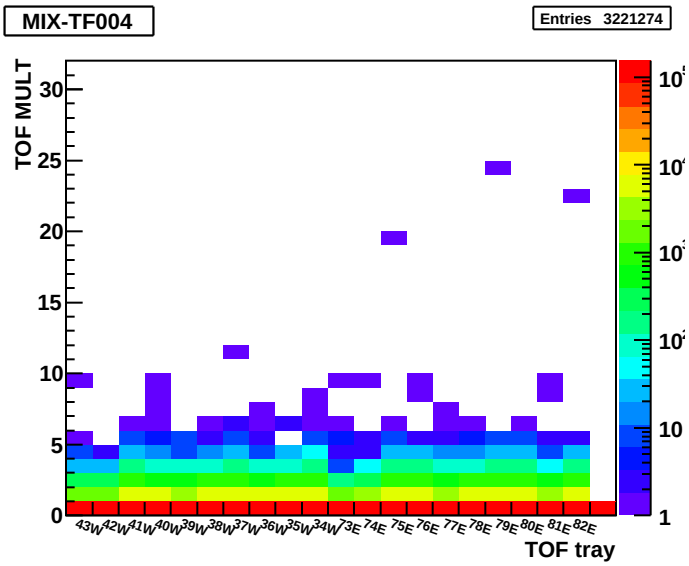

ST201 0-15 (ch6)
Entries 1227152
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

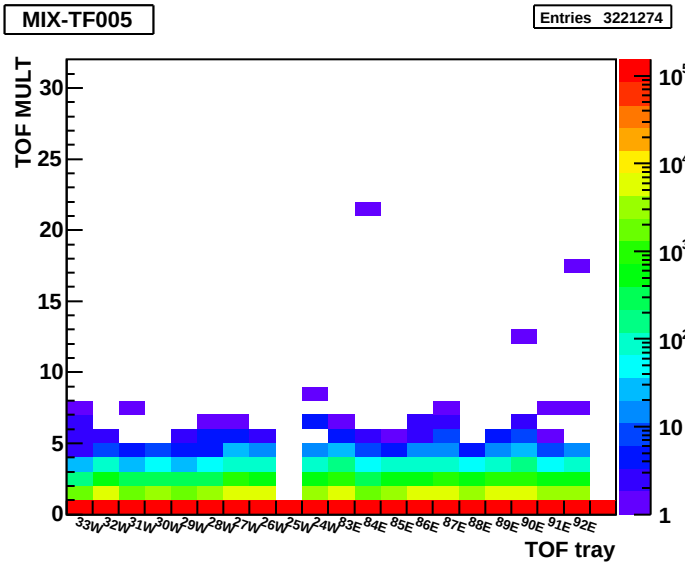

Unused (ch7)
Entries 1227152

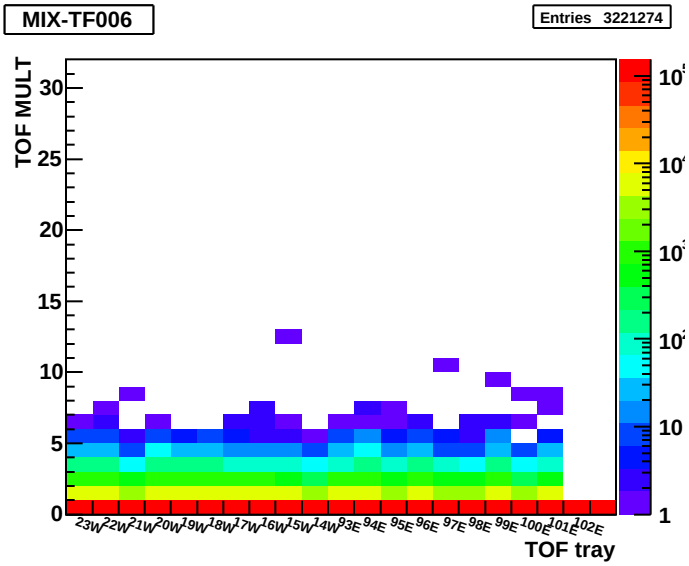

MIX-TF001
Entries 3221274
TOF MULT
TOF tray

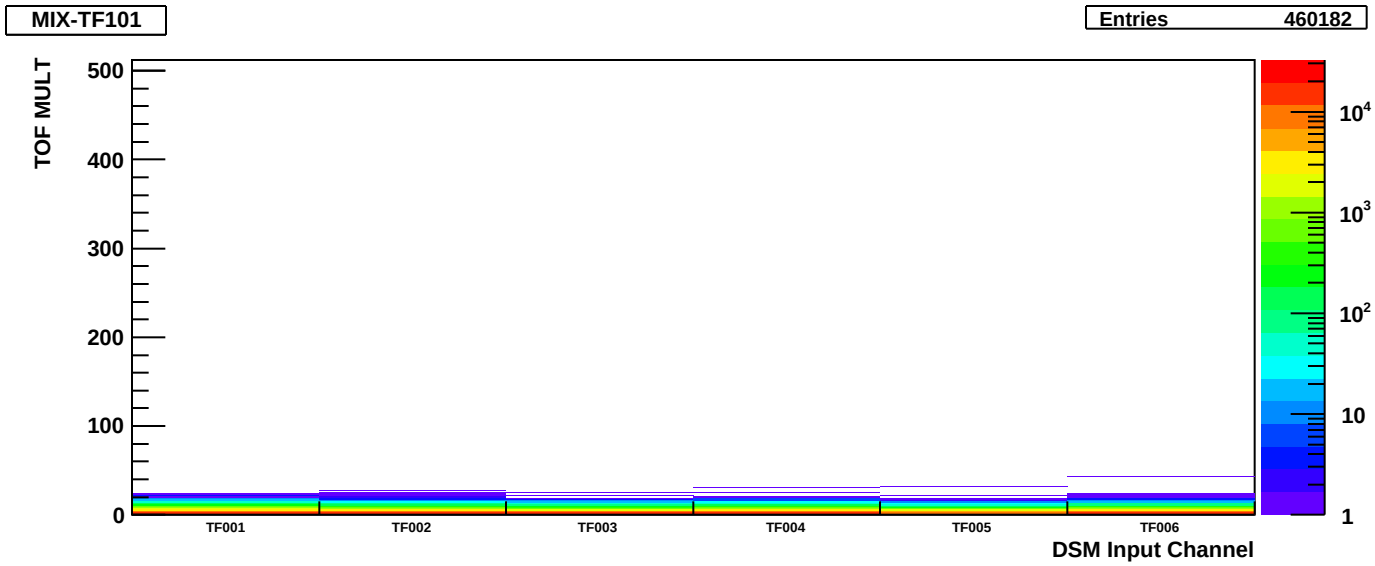

MIX-TF002
Entries 3221274
TOF MULT
TOF tray

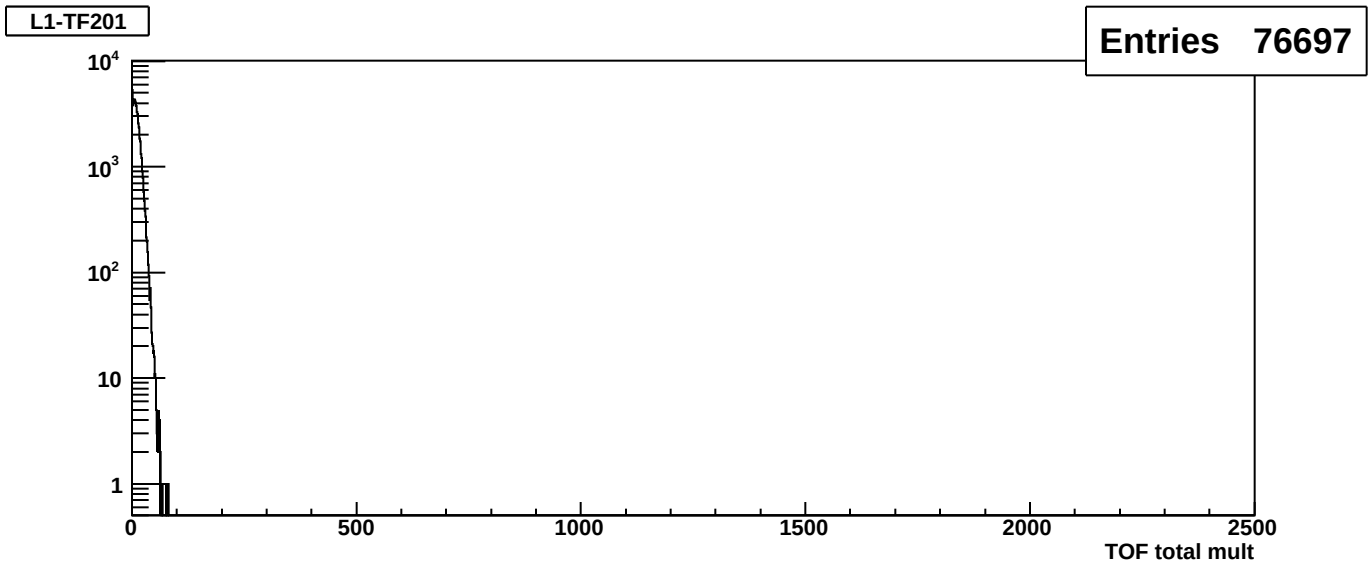

MIX-TF003
Entries 3221274
TOF MULT
TOF tray

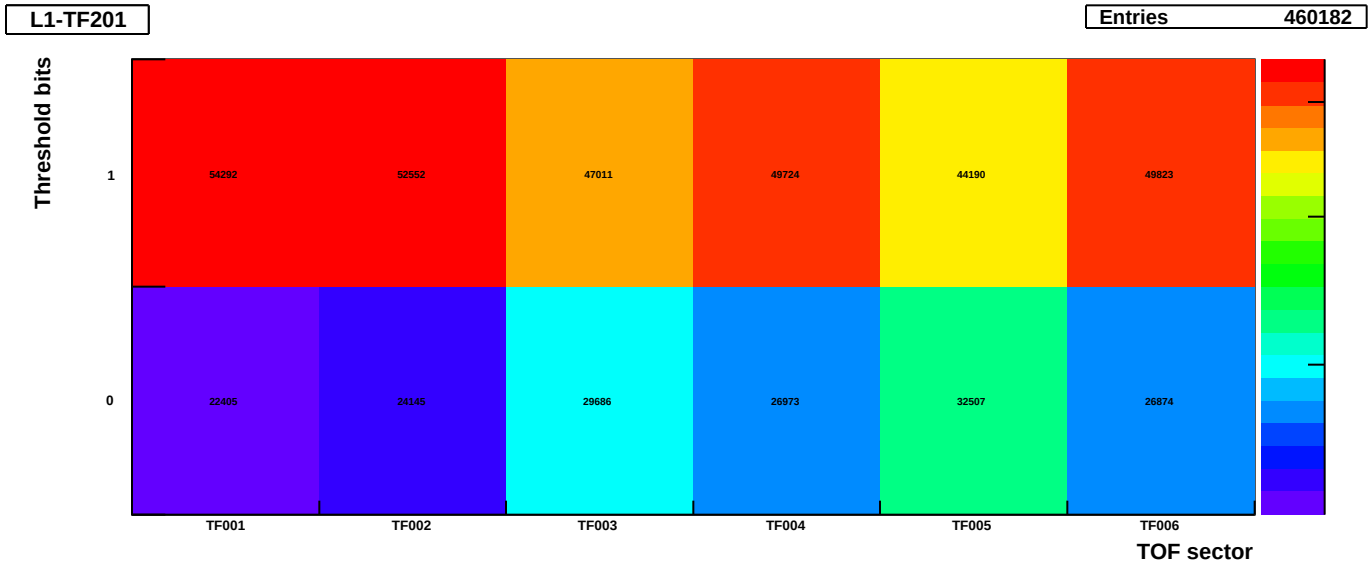

MIX-TF004
Entries 3221274
TOF MULT
TOF tray

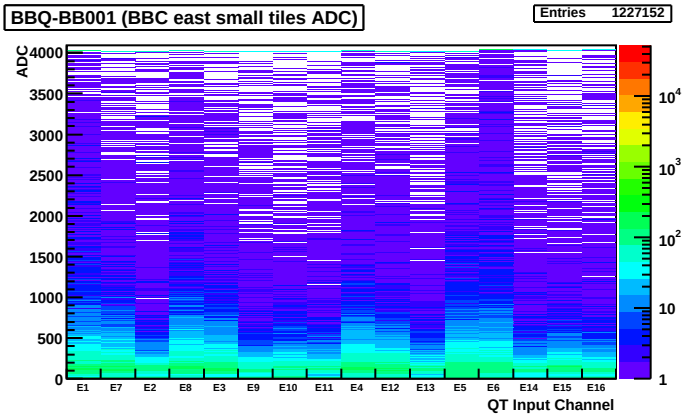

MIX-TF005
Entries 3221274
TOF MULT
TOF tray

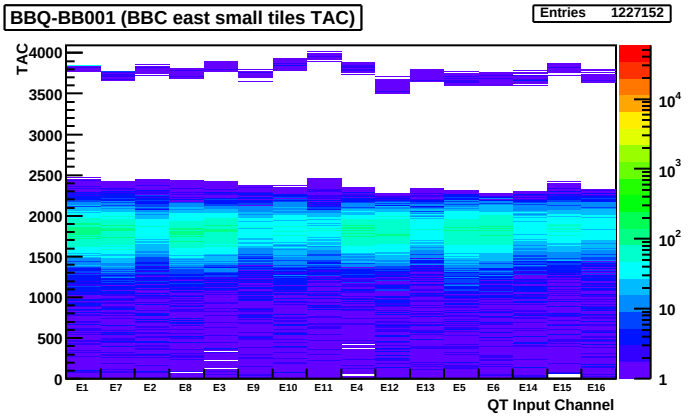

MIX-TF006
Entries 3221274
TOF MULT
TOF tray

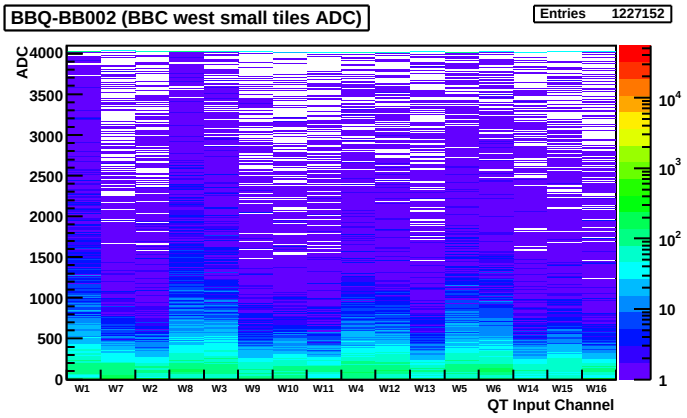

MIX-TF101
Entries 460182
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

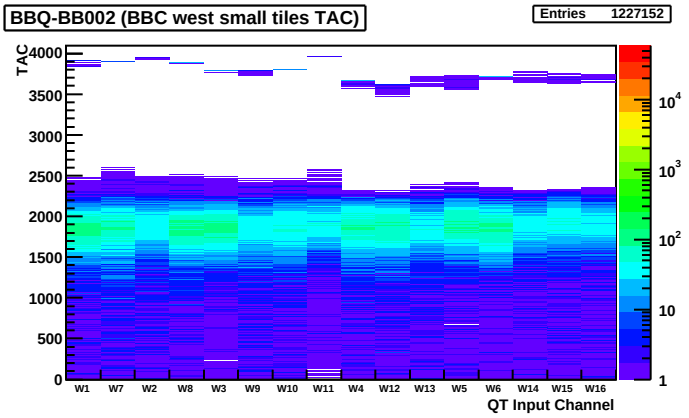

L1-TF201
Entries 76697
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

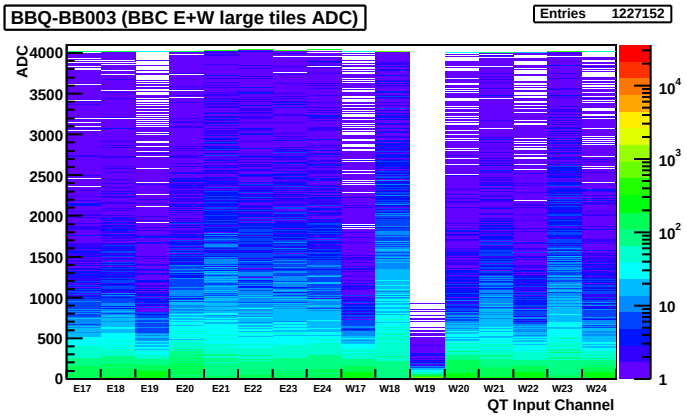

L1-TF201
Entries 460182
Threshold bits
1
0
54292
52552
47011
49724
44190
49823
22405
24145
29686
26973
32507
26874
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

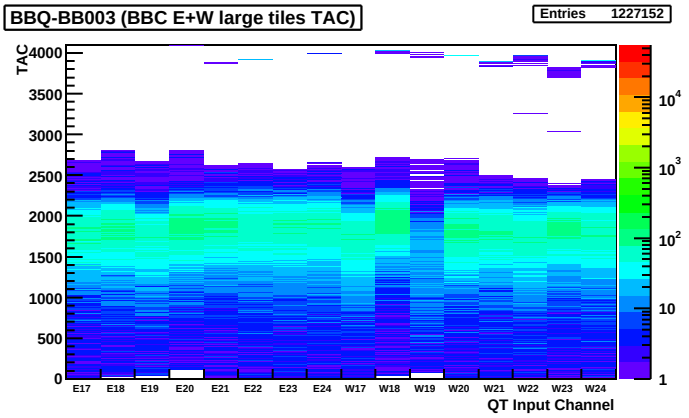

BBQ-BB001 (BBC east small tiles ADC)
Entries 1227152
ADC
QT Input Channel

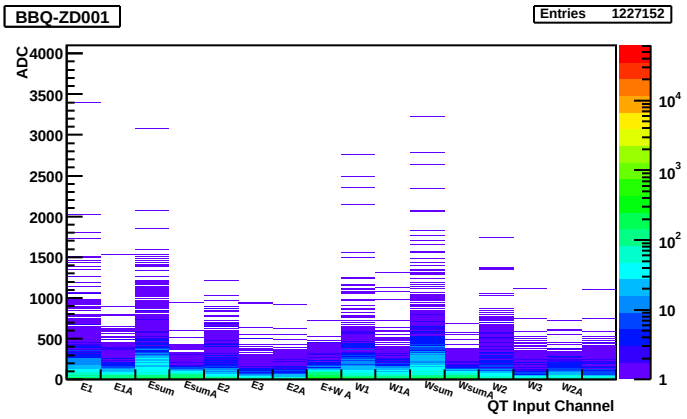

BBQ-BB001 (BBC east small tiles TAC)
Entries 1227152
TAC
QT Input Channel

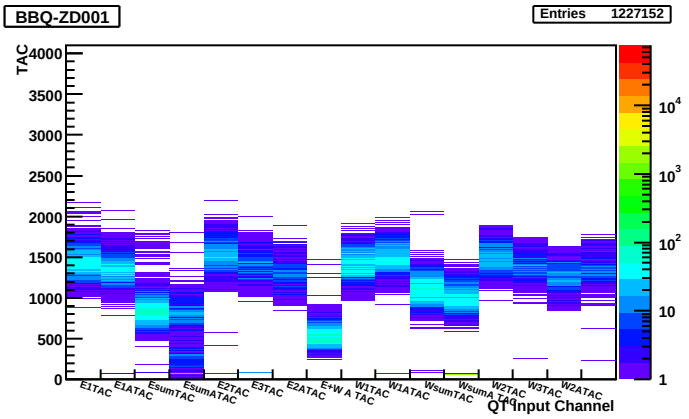

BBQ-BB002 (BBC west small tiles ADC)
Entries 1227152
ADC
QT Input Channel

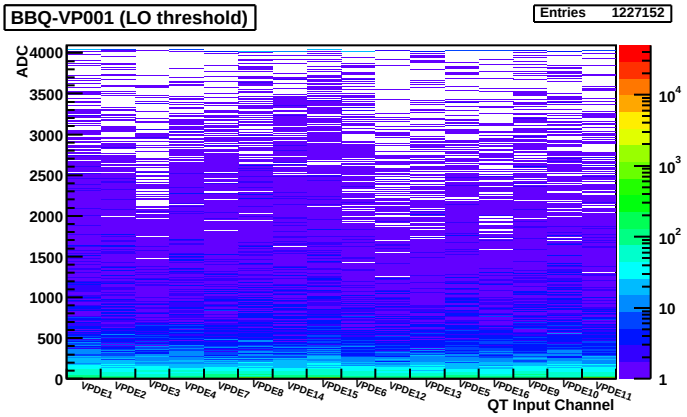

BBQ-BB002 (BBC west small tiles TAC)
Entries 1227152
TAC
QT Input Channel

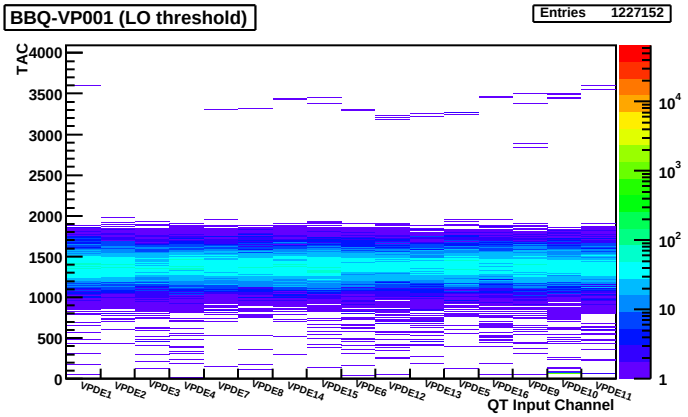

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1227152
ADC
QT Input Channel

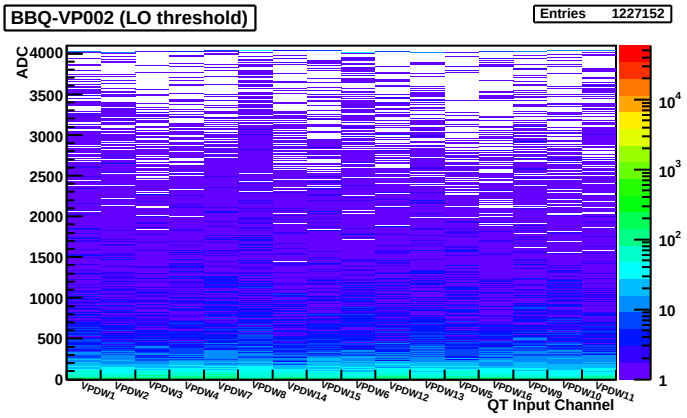

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1227152
TAC
QT Input Channel

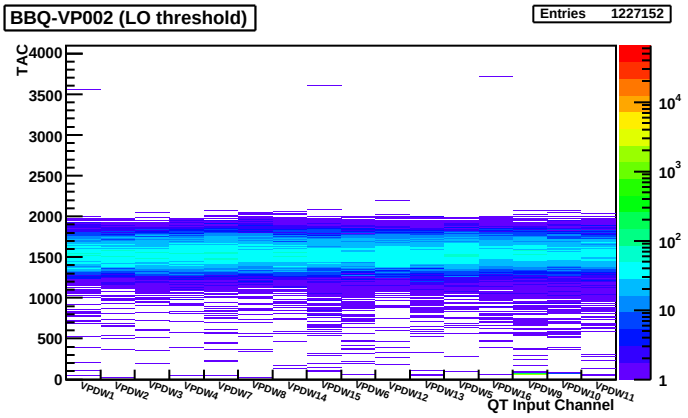

BBQ-ZD001
Entries 1227152
ADC
QT Input Channel

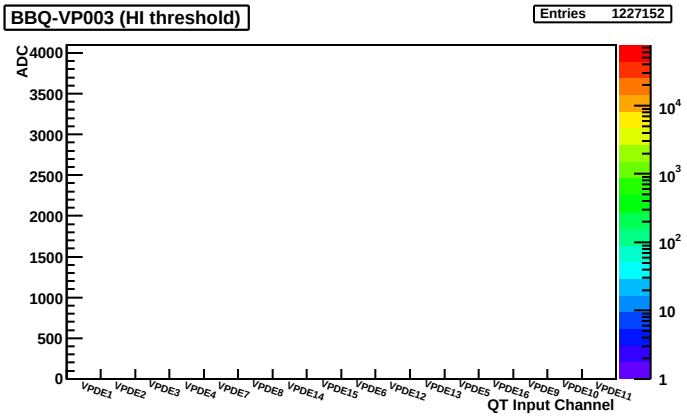

BBQ-ZD001
Entries 1227152
TAC
QT Input Channel

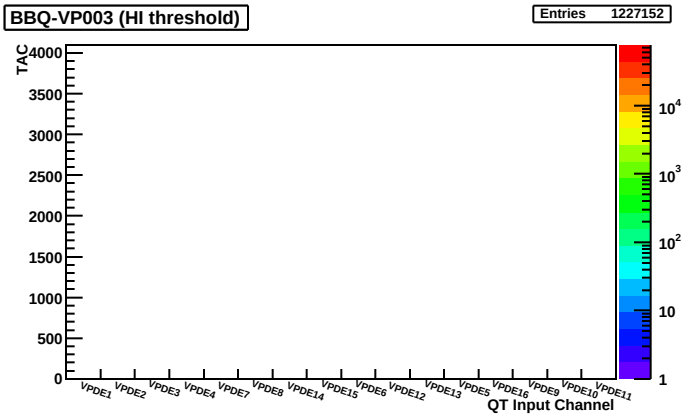

BBQ-VP001 (LO threshold)
Entries 1227152
ADC
QT Input Channel

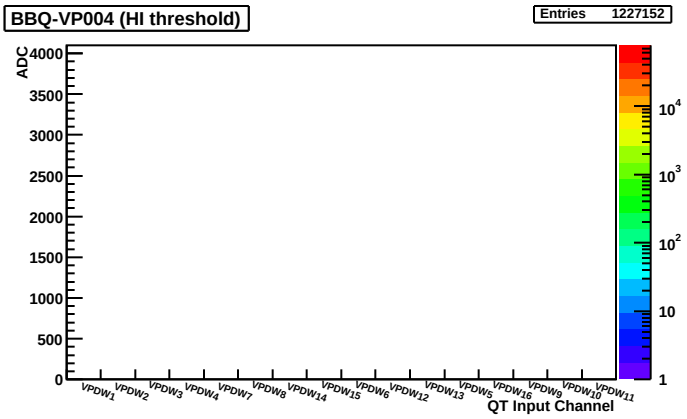

BBQ-VP001 (LO threshold)
Entries 1227152
TAC
QT Input Channel

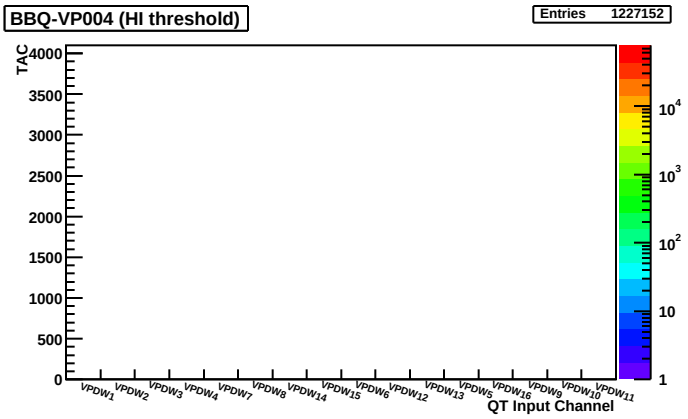

BBQ-VP002 (LO threshold)
Entries 1227152
ADC
QT Input Channel

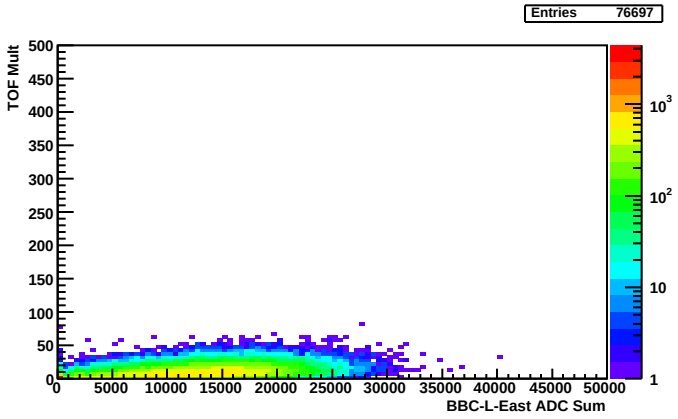

BBQ-VP002 (LO threshold)
Entries 1227152
TAC
QT Input Channel

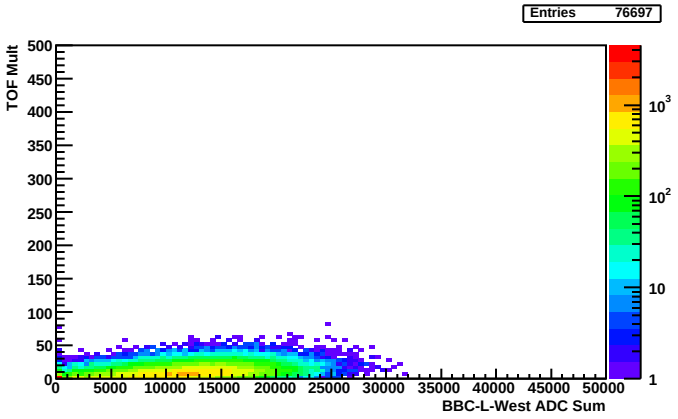

BBQ-VP003 (HI threshold)
Entries 1227152
ADC
QT Input Channel

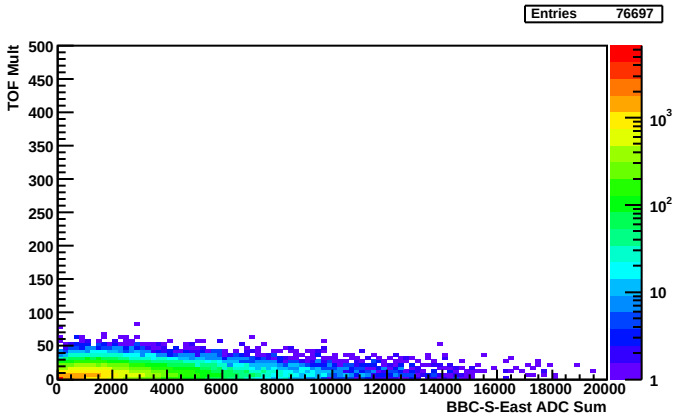

BBQ-VP003 (HI threshold)
Entries 1227152
TAC
QT Input Channel

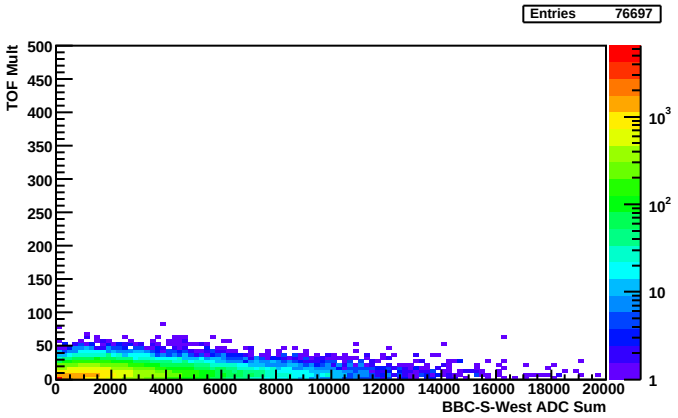

BBQ-VP004 (HI threshold)
Entries 1227152
ADC
QT Input Channel

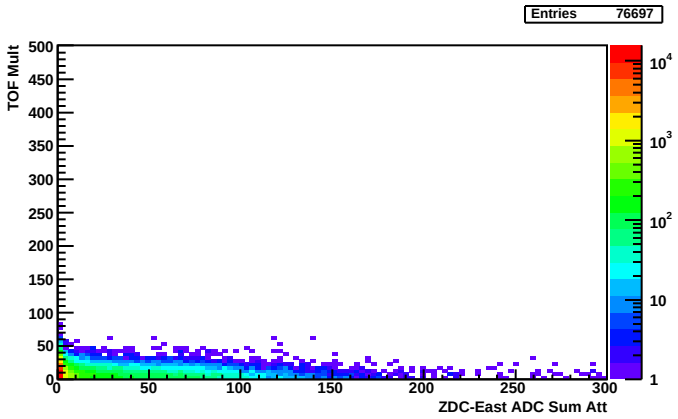

BBQ-VP004 (HI threshold)
Entries 1227152
TAC
QT Input Channel

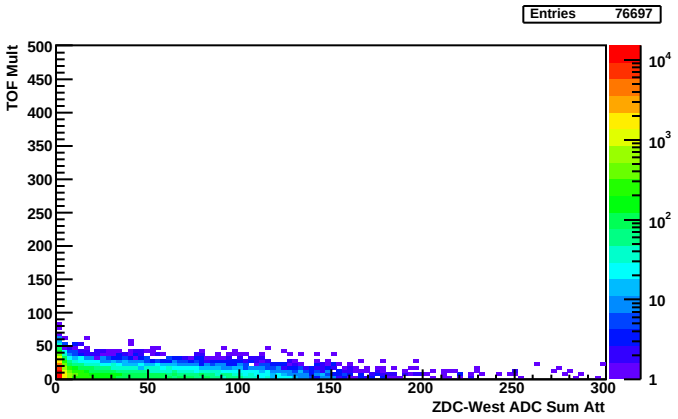

Entries 76697
TOF Mult
BBC-L-East ADC Sum

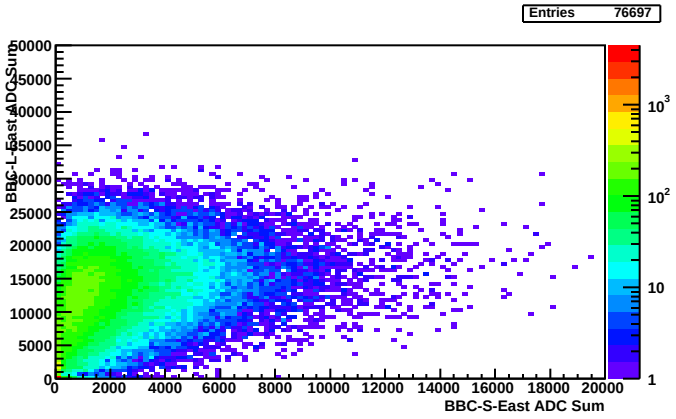

Entries 76697
TOF Mult
BBC-L-West ADC Sum

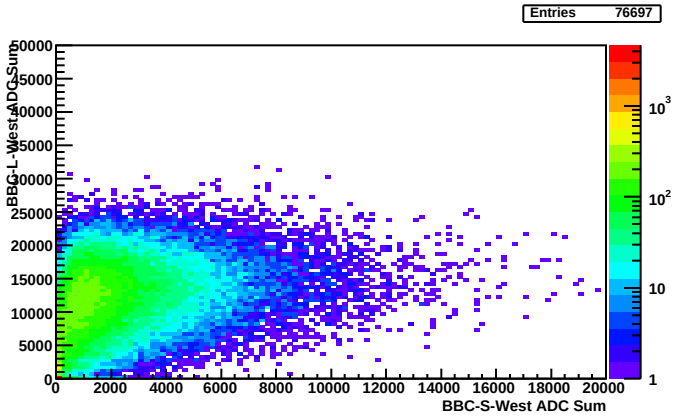

Entries 76697
TOF Mult
BBC-S-East ADC Sum

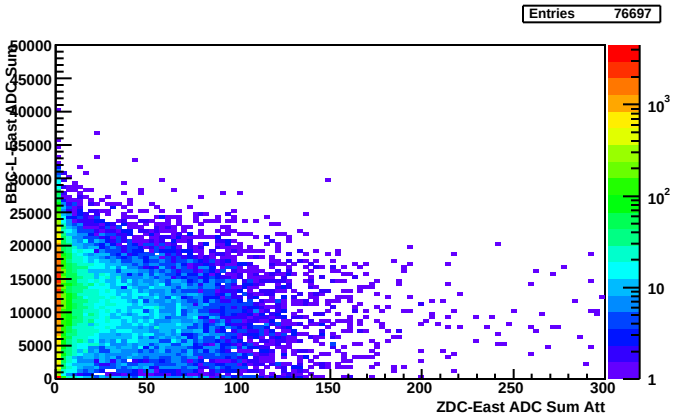

Entries 76697
TOF Mult
BBC-S-West ADC Sum

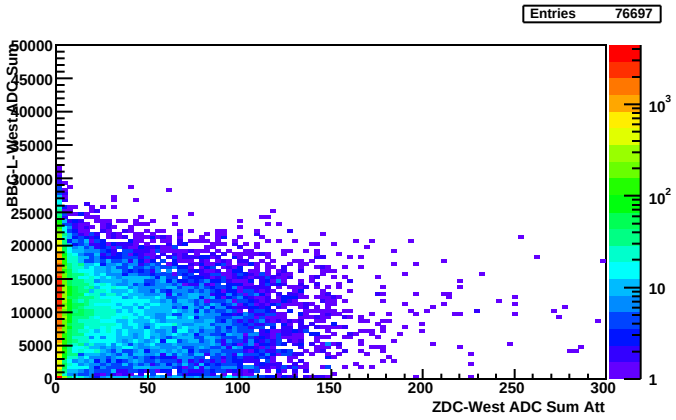

Entries 76697
TOF Mult
ZDC-East ADC Sum Att

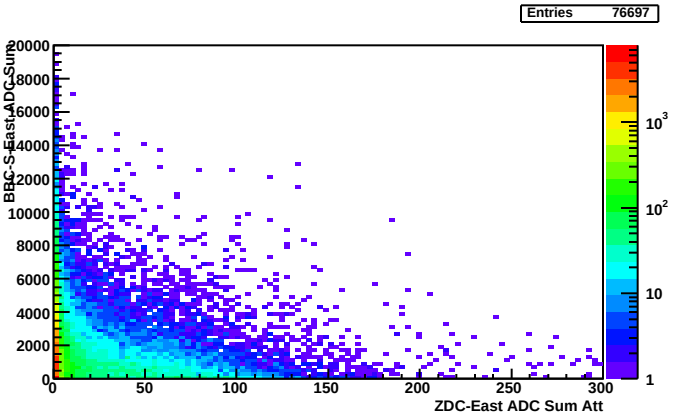

Entries 76697
TOF Mult
ZDC-West ADC Sum Att

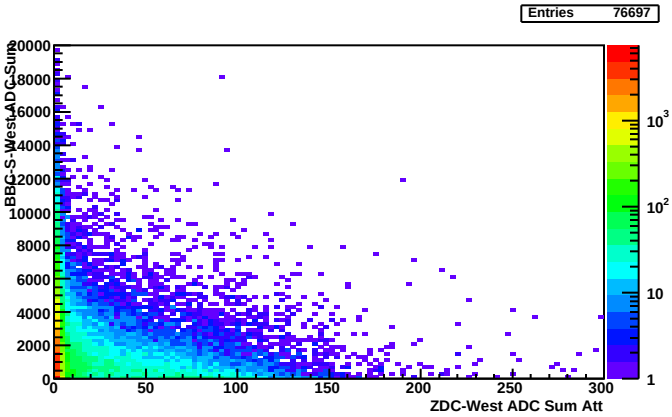

Entries 76697
BBC-L-East ADC Sum
BBC-S-East ADC Sum

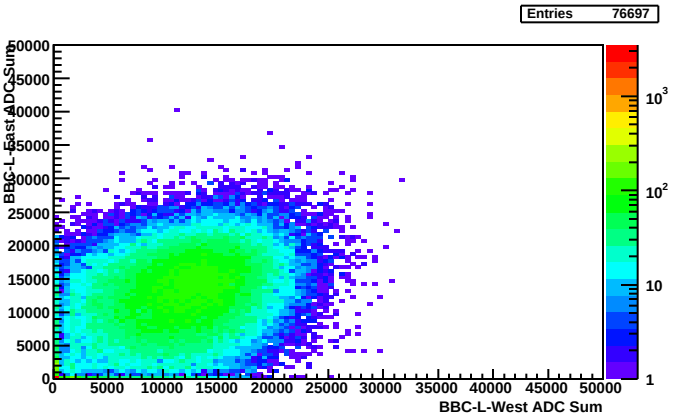

Entries 76697
BBC-L-West ADC Sum
BBC-S-West ADC Sum

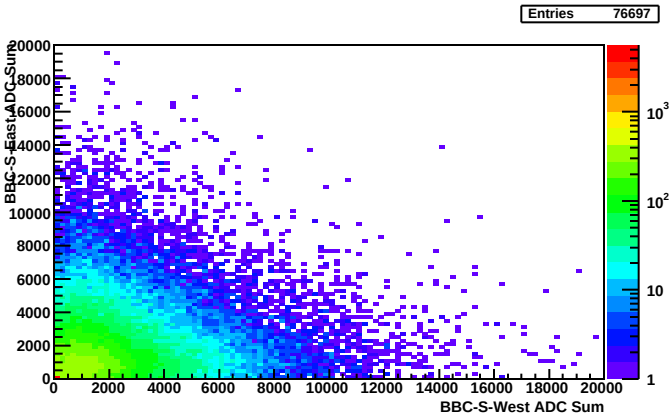

Entries 76697
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

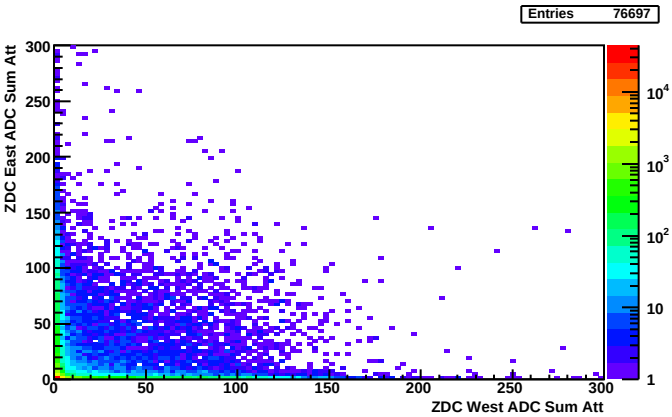

Entries 76697
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

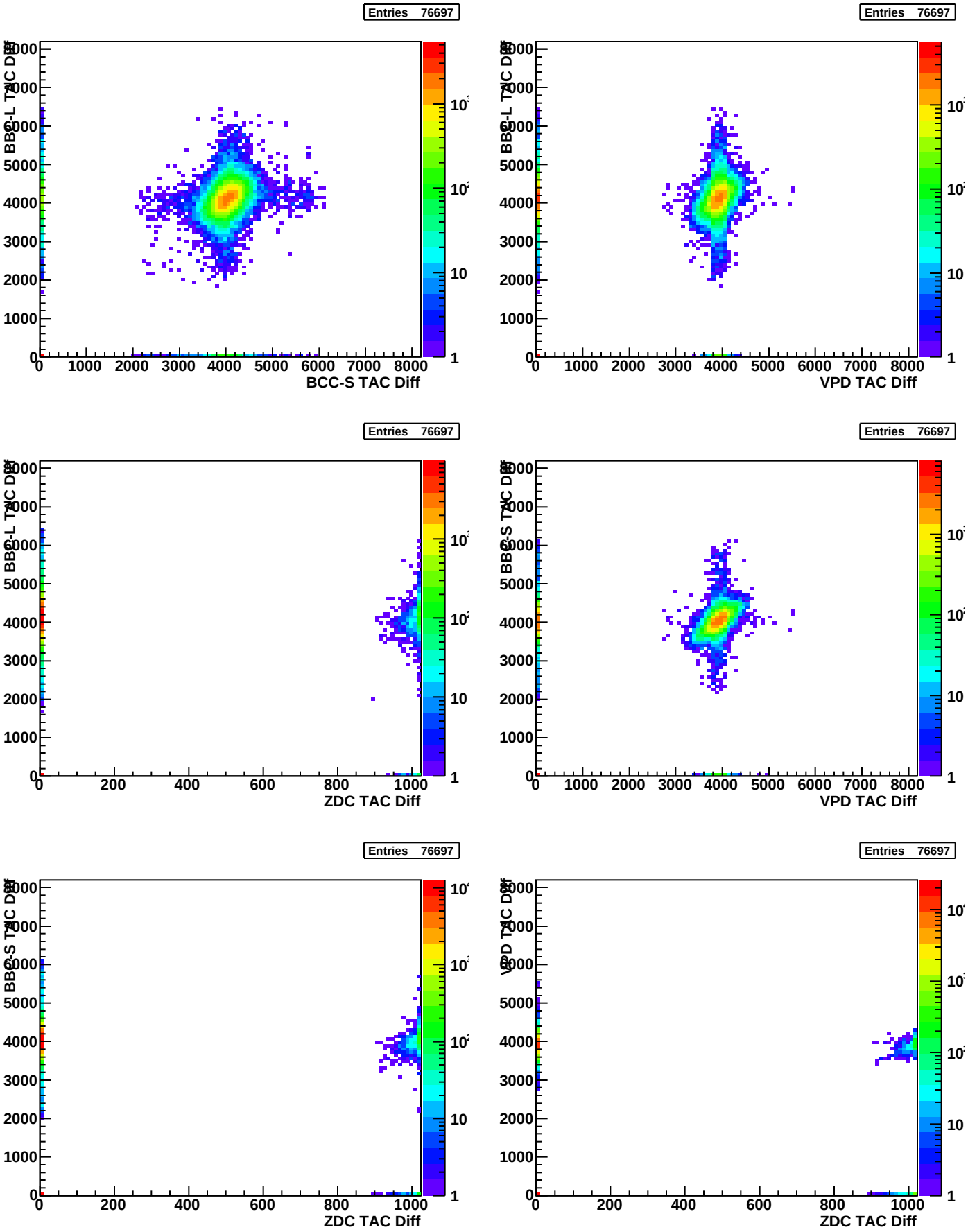

Entries 76697
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

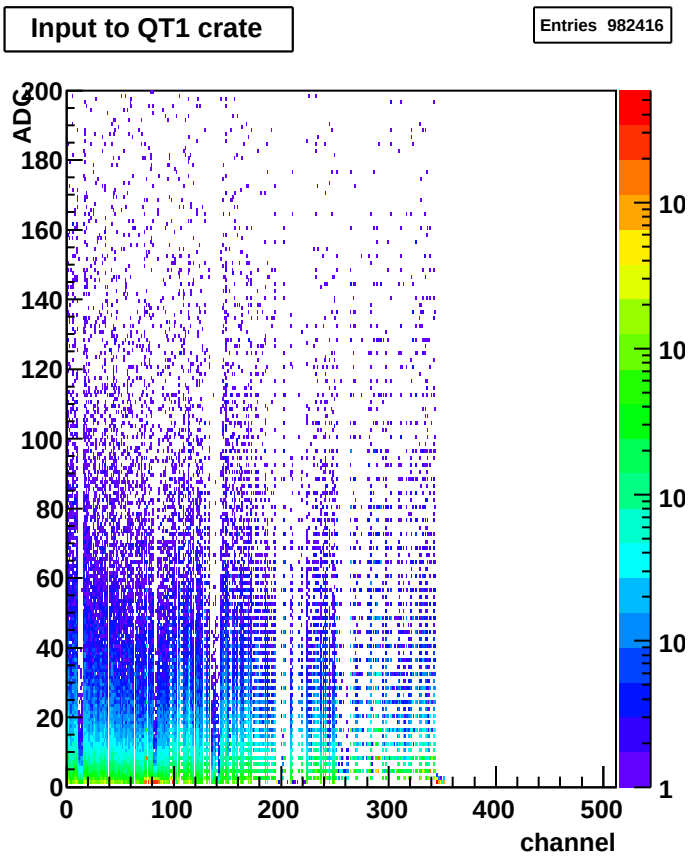

Entries 76697
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

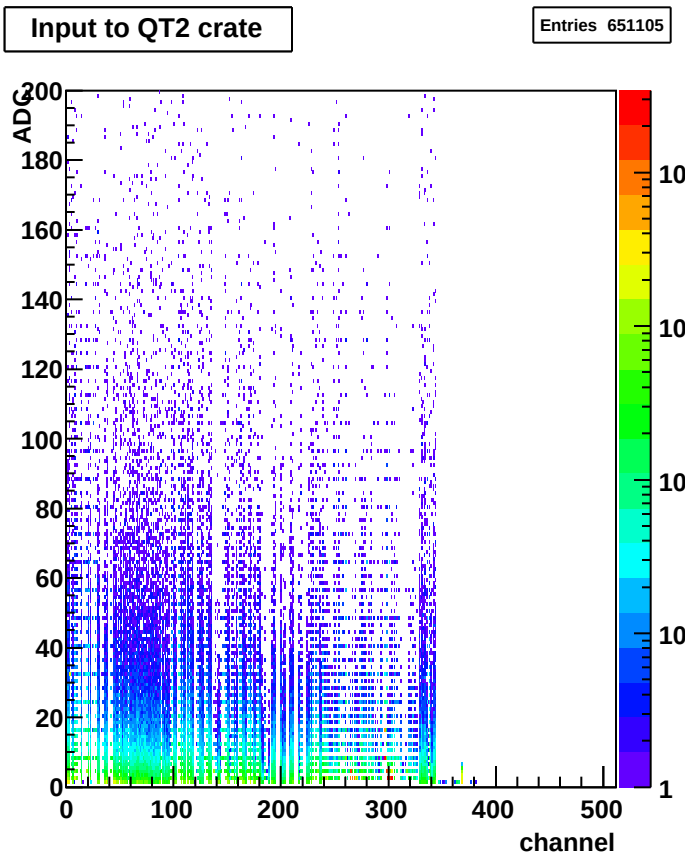

Entries 76697
BBC-L-East ADC Sum
BBC-L-West ADC Sum

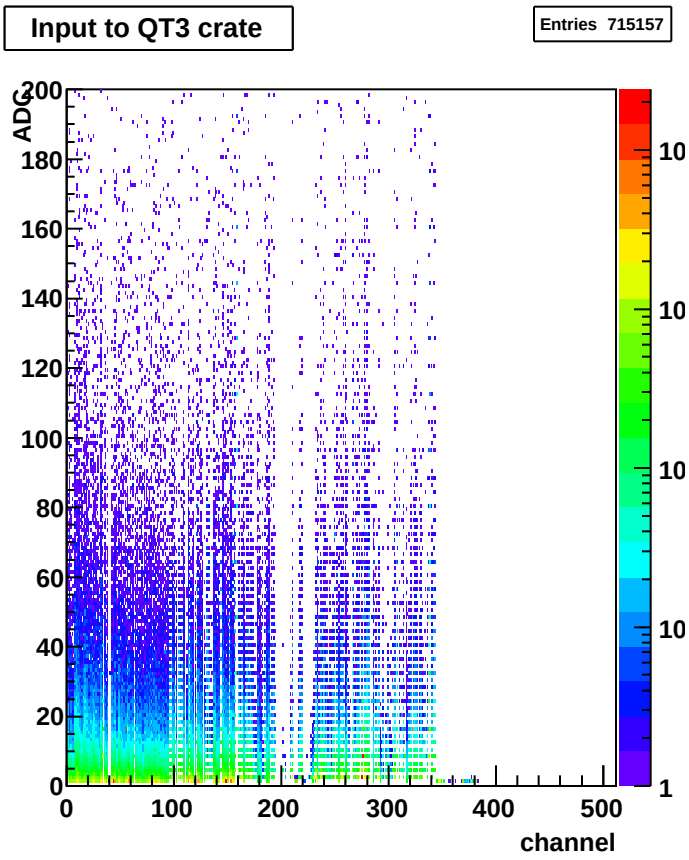

Entries 76697
BBC-S-East ADC Sum
BBC-S-West ADC Sum

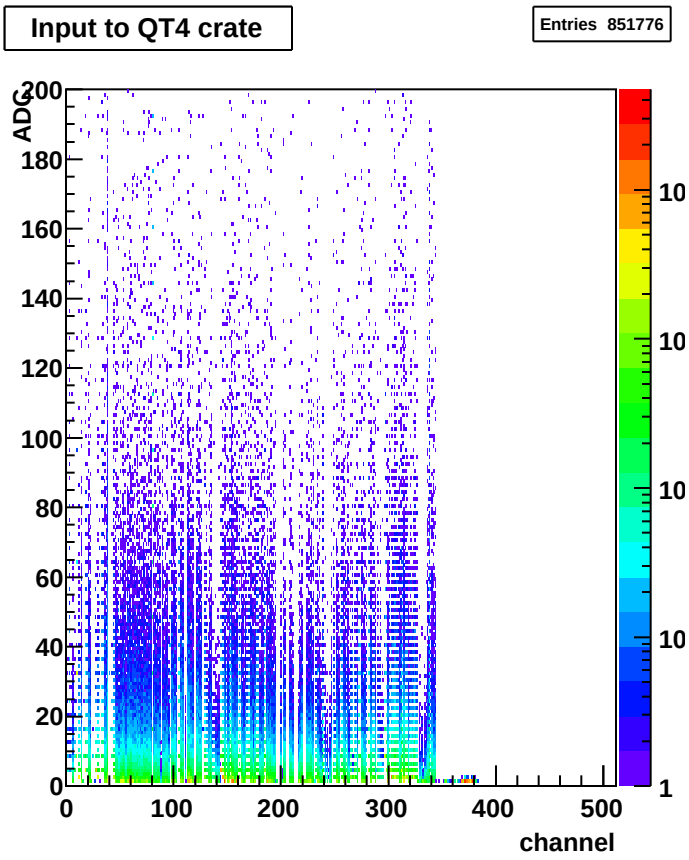

Entries 76697
ZDC East ADC Sum Att
ZDC West ADC Sum Att

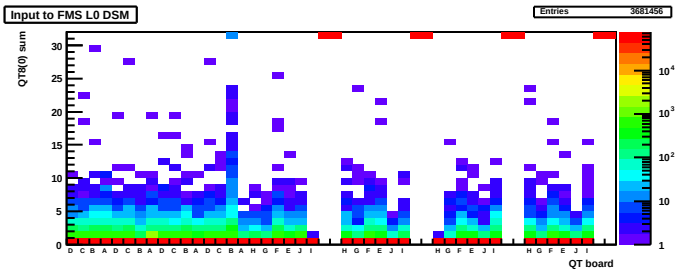

Entries 76697
BBC-L TAC Diff
BCC-S TAC Diff
Entries 76697
BBC-L TAC Diff
VPD TAC Diff
Entries 76697
BBC-L TAC Diff
ZDC TAC Diff
Entries 76697
BBC-S TAC Diff
VPD TAC Diff
Entries 76697
BBC-S TAC Diff
ZDC TAC Diff
Entries 76697
VPD TAC Diff
ZDC TAC Diff

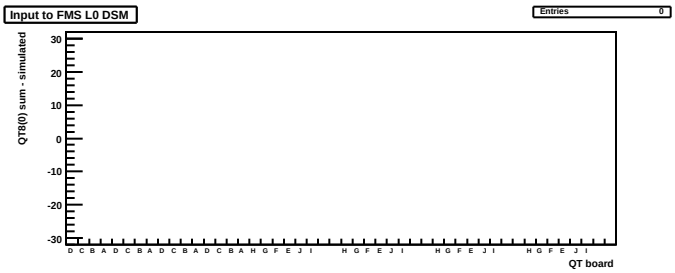

Input to QT1 crate
Entries 982416
ADC
channel

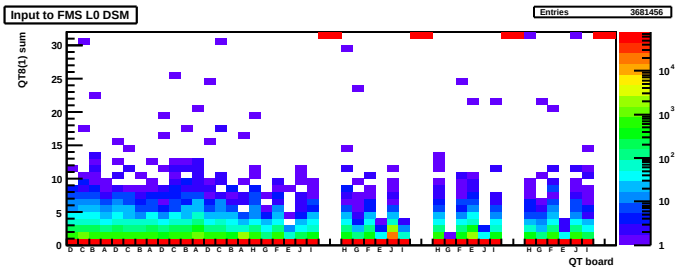

Input to QT2 crate
Entries 651105
ADC
channel

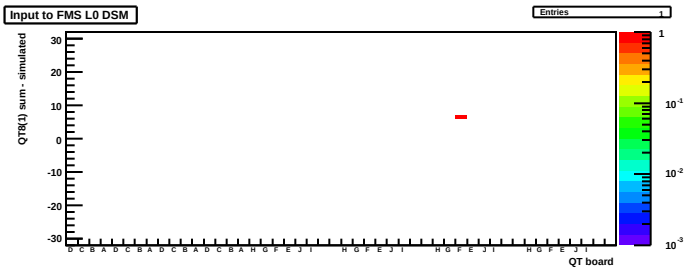

Input to QT3 crate
Entries 715157
ADC
channel

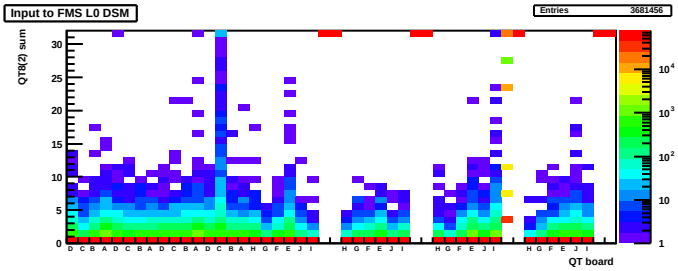

Input to QT4 crate
Entries 851776
ADC
channel

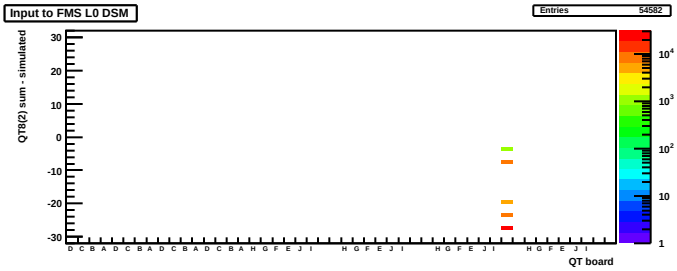

Input to FMS L0 DSM
Entries 3681456
QT8(0) sum
QT board

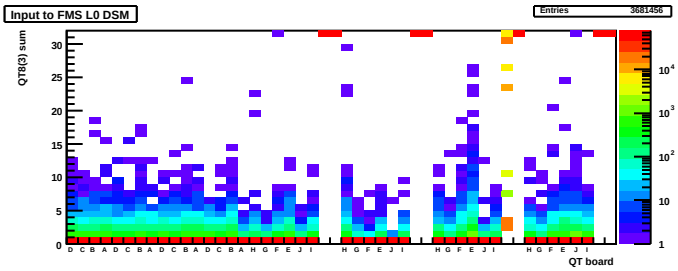

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

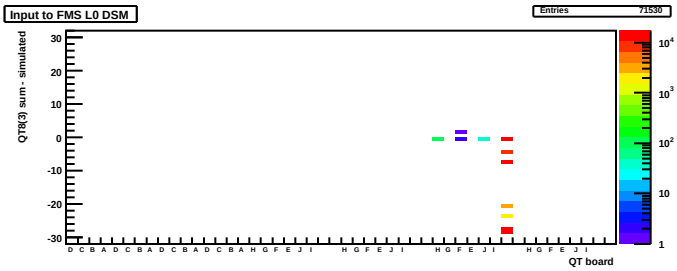

Input to FMS L0 DSM
Entries 3681456
QT8(1) sum
QT board

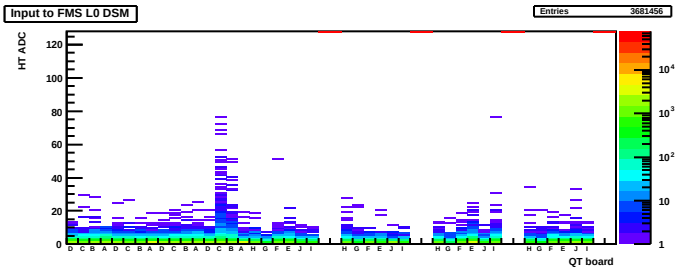

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

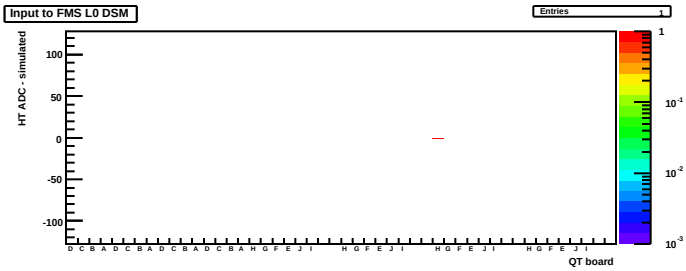

Input to FMS L0 DSM
Entries 3681456
QT8(2) sum
QT board

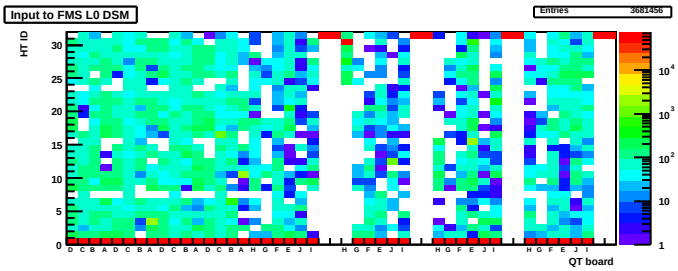

Input to FMS L0 DSM
Entries 54582
QT8(2) sum - simulated
QT board

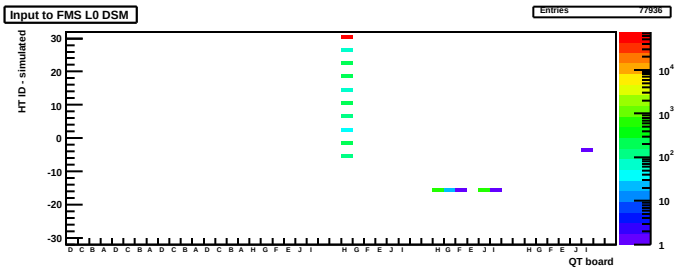

Input to FMS L0 DSM
Entries 3681456
QT8(3) sum
QT board

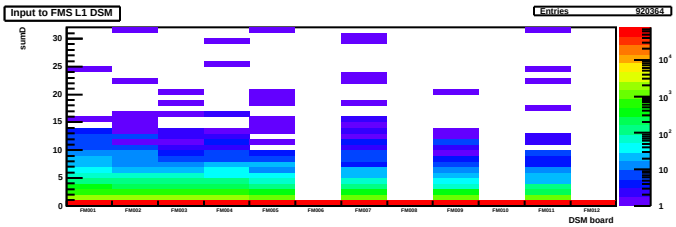

Input to FMS L0 DSM
Entries 71530
QT8(3) sum - simulated
QT board

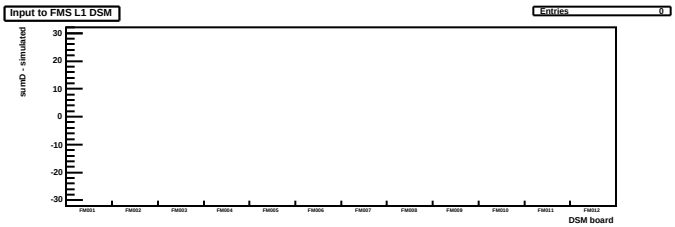

Input to FMS L0 DSM
Entries 3681456
HT ADC
QT board

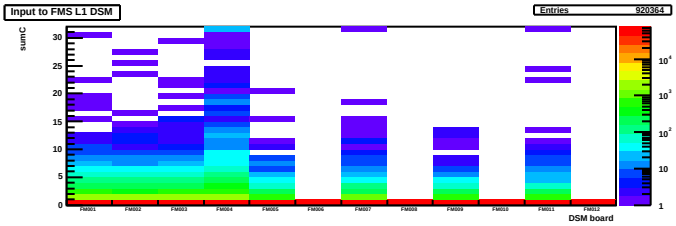

Input to FMS L0 DSM
Entries 1
HT ADC - simulated
QT board

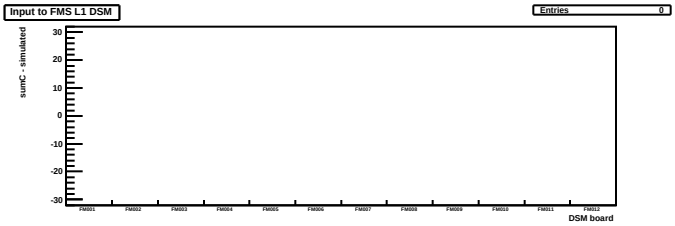

Input to FMS L0 DSM
Entries 3681456
HT ID
QT board

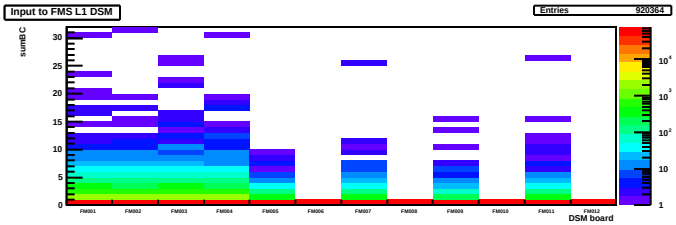

Input to FMS L0 DSM
Entries 77936
HT ID - simulated
QT board

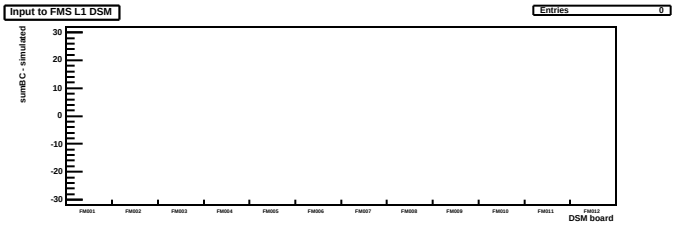

Input to FMS L1 DSM
Entries 920364
sumD
DSM board

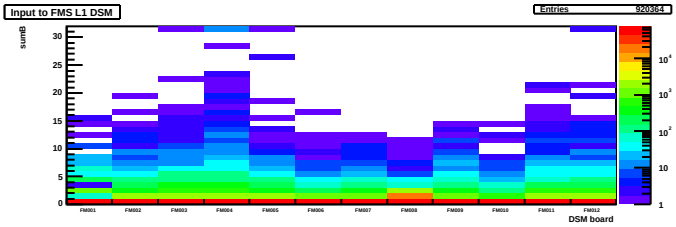

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

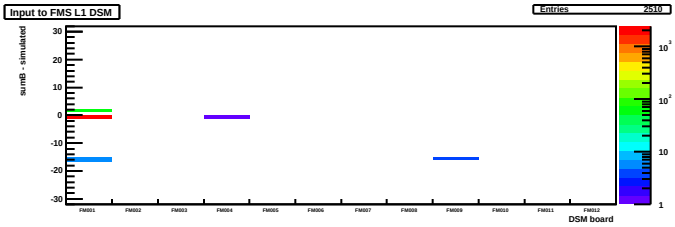

Input to FMS L1 DSM
Entries 920364
sumC
DSM board

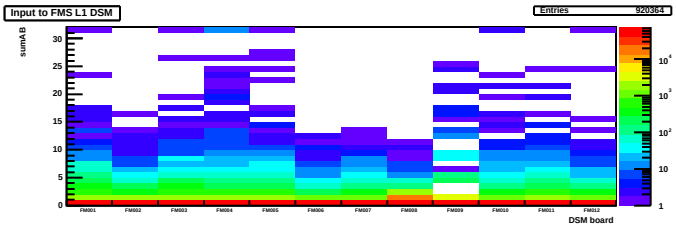

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

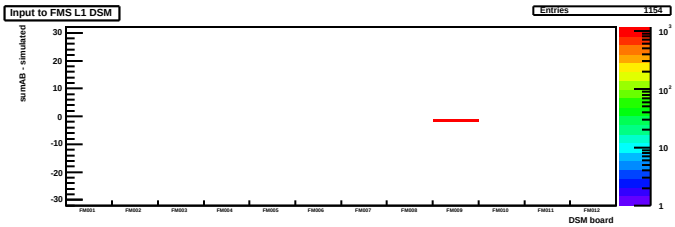

Input to FMS L1 DSM
Entries 920364
sumBC
DSM board

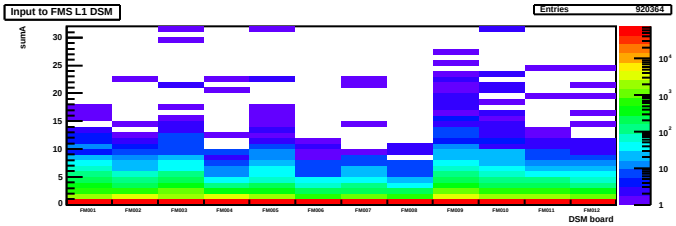

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

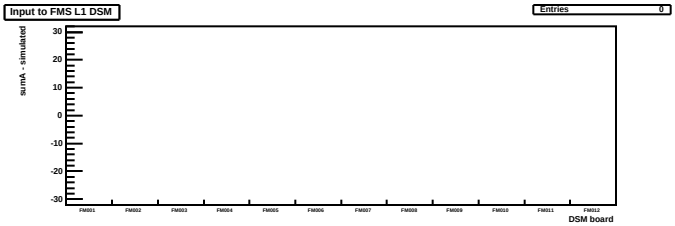

Input to FMS L1 DSM
Entries 920364
sumB
DSM board

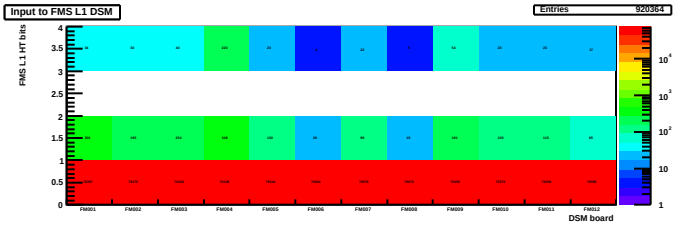

Input to FMS L1 DSM
Entries 2510
sumB - simulated
DSM board

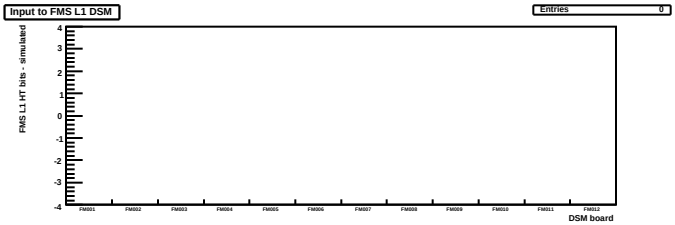

Input to FMS L1 DSM
Entries 920364
sumAB
DSM board

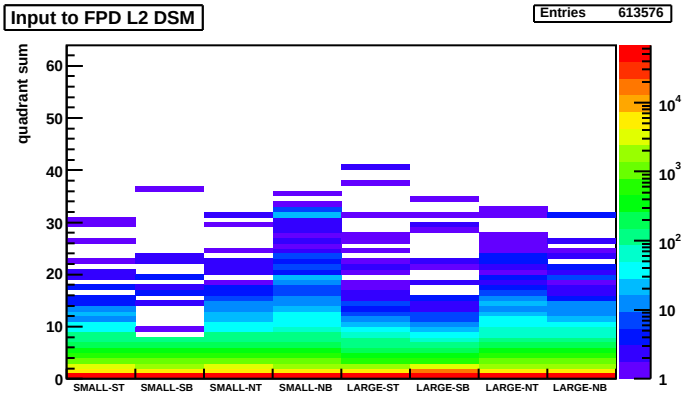

Input to FMS L1 DSM
Entries 1154
sumAB - simulated
DSM board

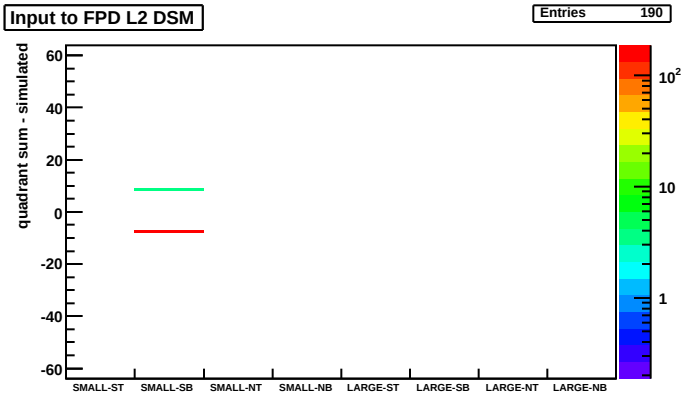

Input to FMS L1 DSM
Entries 920364
sumA
DSM board

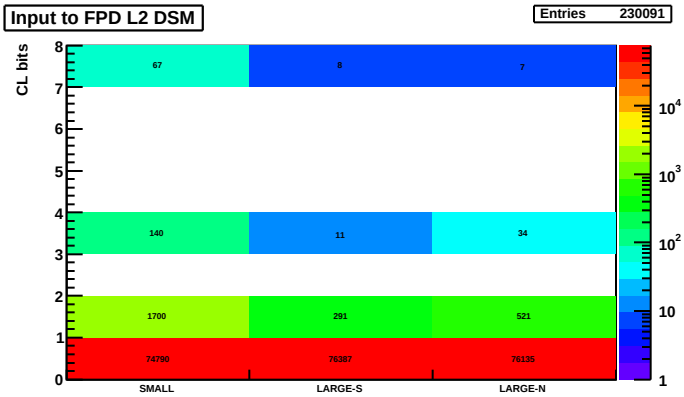

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

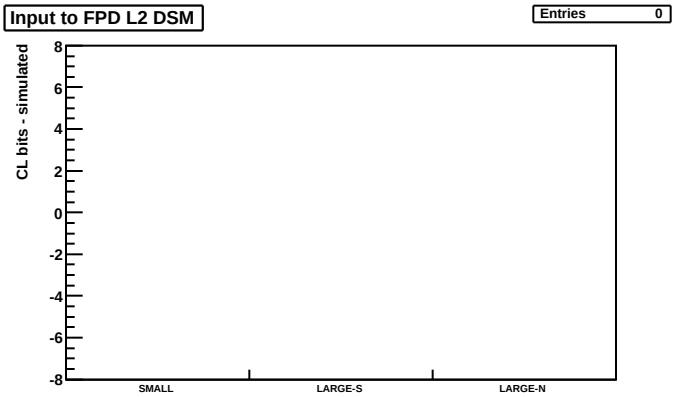

Input to FMS L1 DSM
Entries 920364
FMS L1 HT bits
DSM board

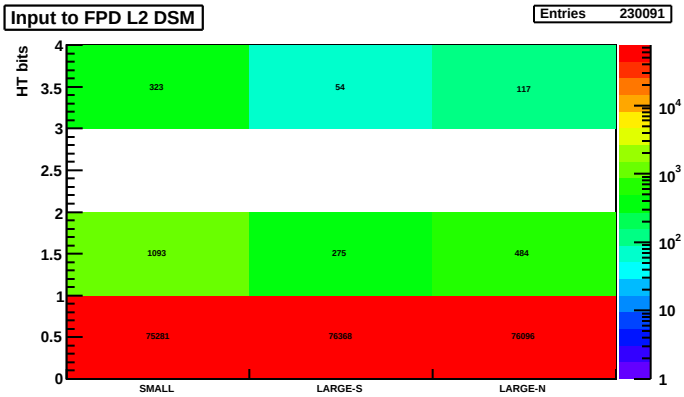

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

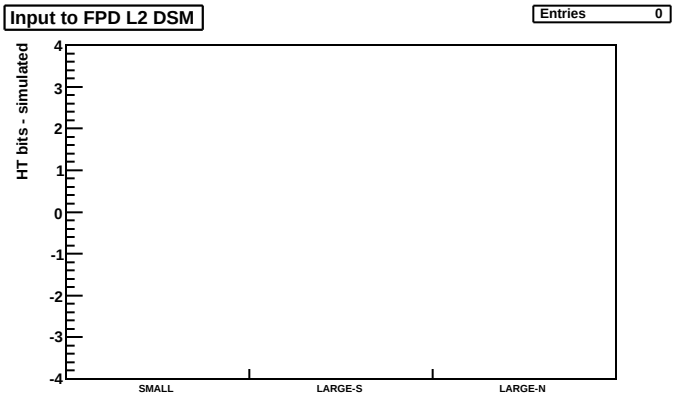

Input to FPD L2 DSM
Entries 613576
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

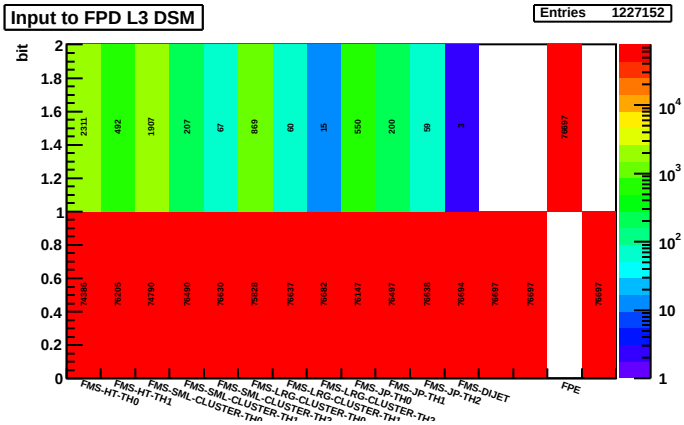

Input to FPD L2 DSM
Entries 190
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

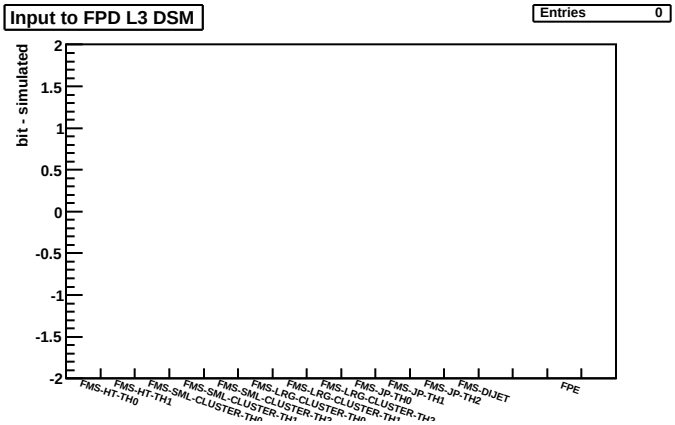

Input to FPD L2 DSM
Entries 230091
CL bits
67
8
7
140
11
34
1700
291
521
74790
76387
76135
SMALL
LARGE-S
LARGE-N

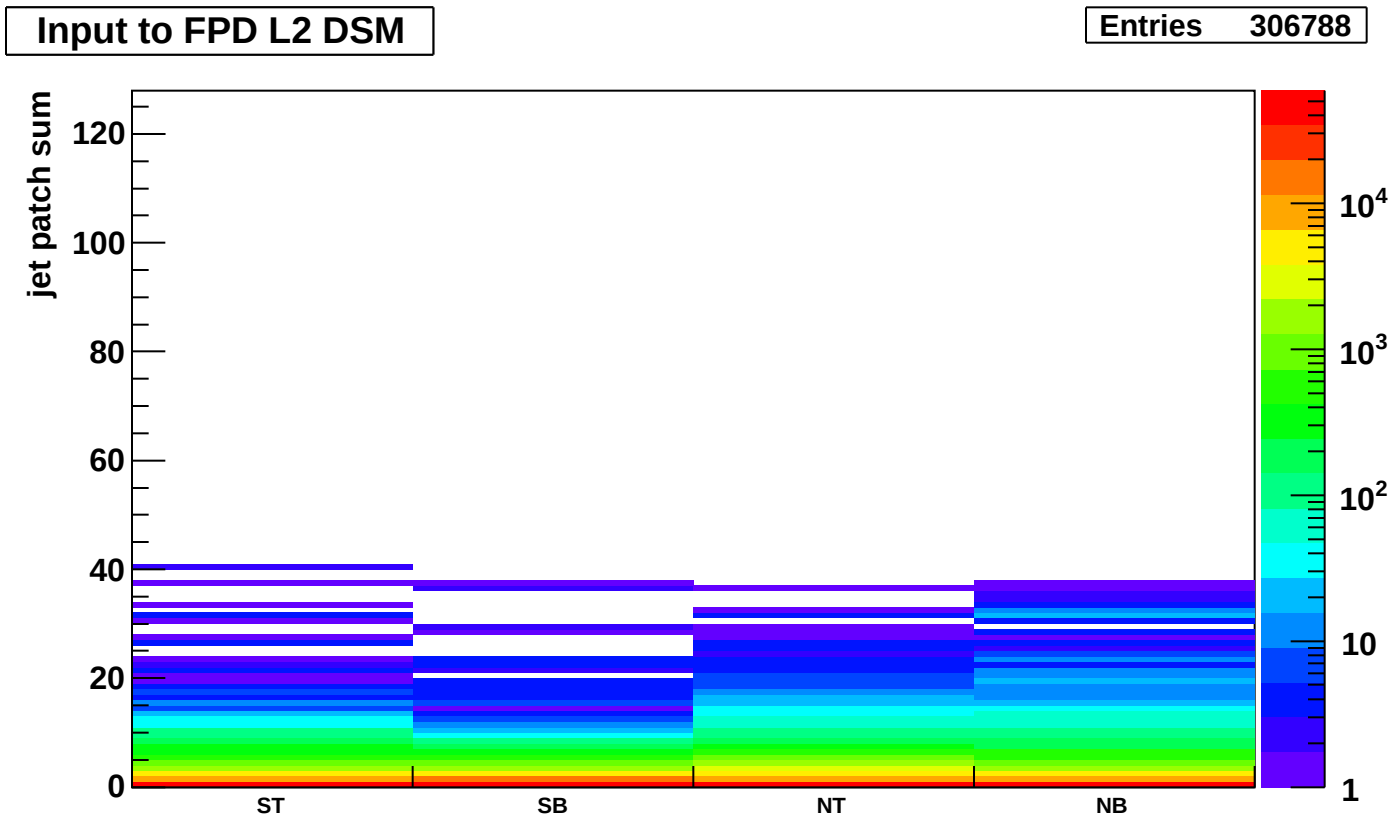

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

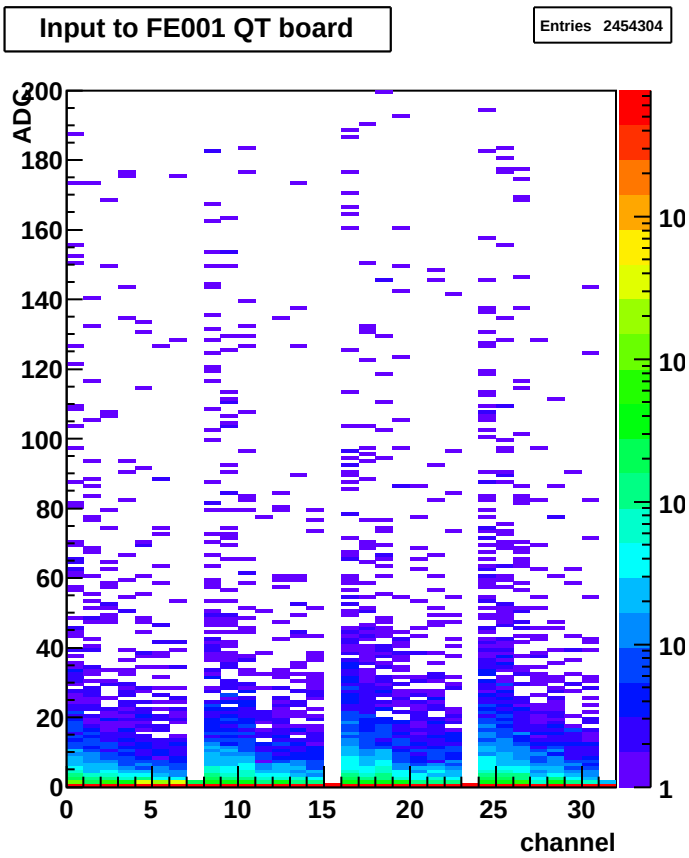

Input to FPD L2 DSM
Entries 230091
HT bits
323
54
117
1093
275
484
75281
76368
76096
SMALL
LARGE-S
LARGE-N

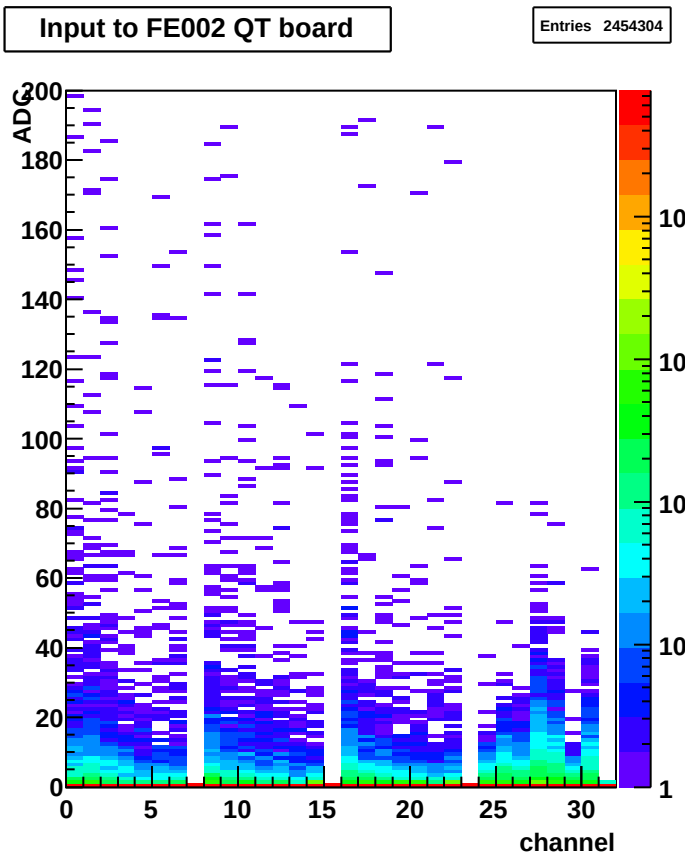

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

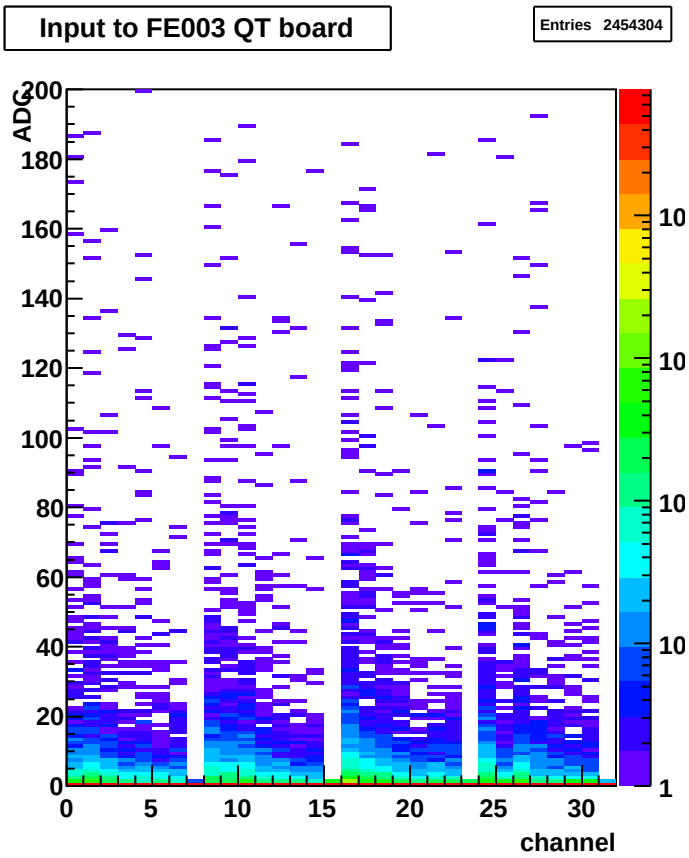

Input to FPD L3 DSM
Entries 1227152
bit
2311
492
1907
207
67
869
60
15
550
200
59
3
76697
76385
76205
74790
76490
76630
75828
76637
76652
76147
76497
76638
76694
76697
76697
76697
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

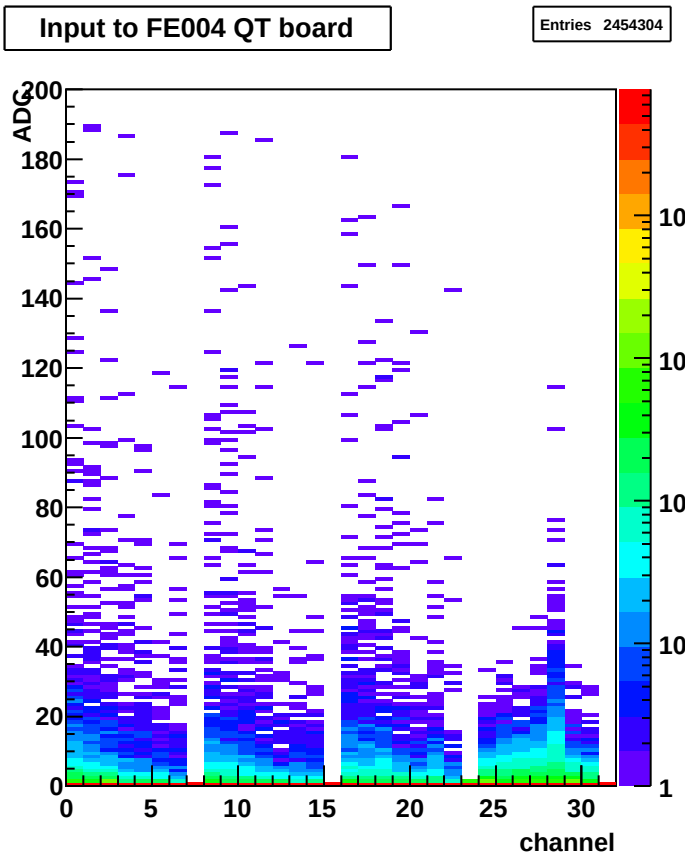

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

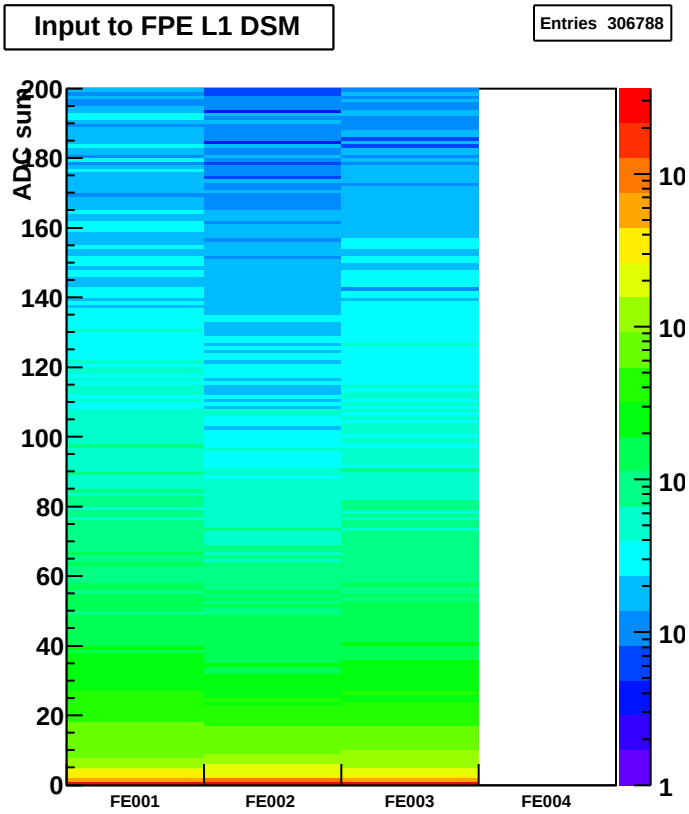

Input to FPD L2 DSM
Entries 306788
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

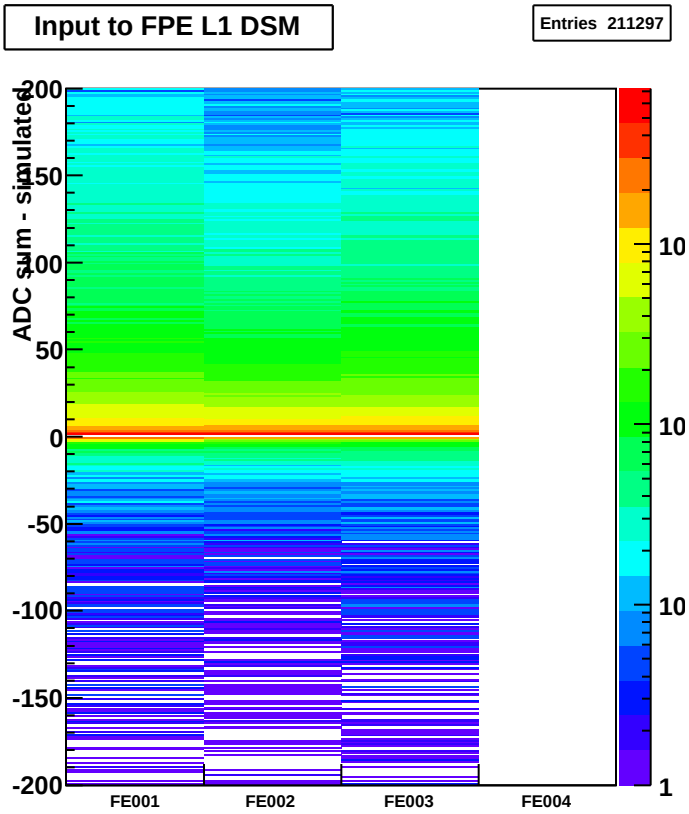

Input to FE001 QT board
Entries 2454304
ADC
channel

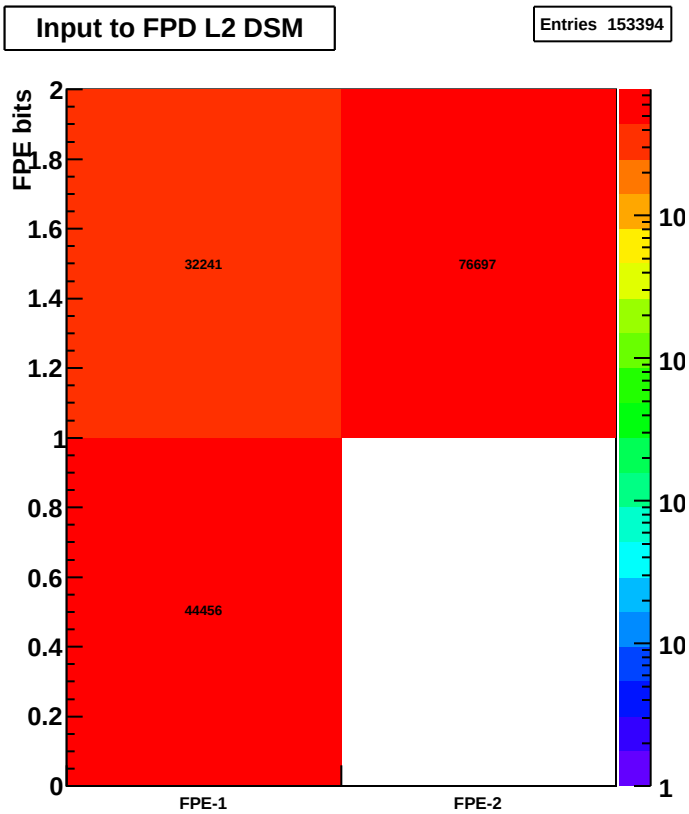

Input to FE002 QT board
Entries 2454304
ADC
channel

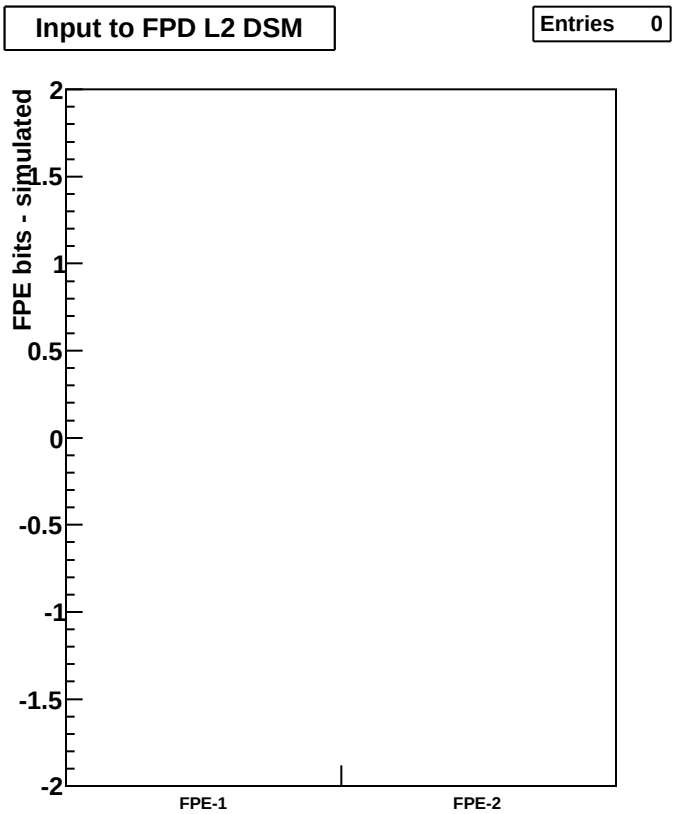

Input to FE003 QT board
Entries 2454304
ADC
channel


Input to FE004 QT board
Entries 2454304
ADC
channel


Input to FPE L1 DSM
Entries 306788
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 211297
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 153394
FPE bits
32241
76697
44456
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2