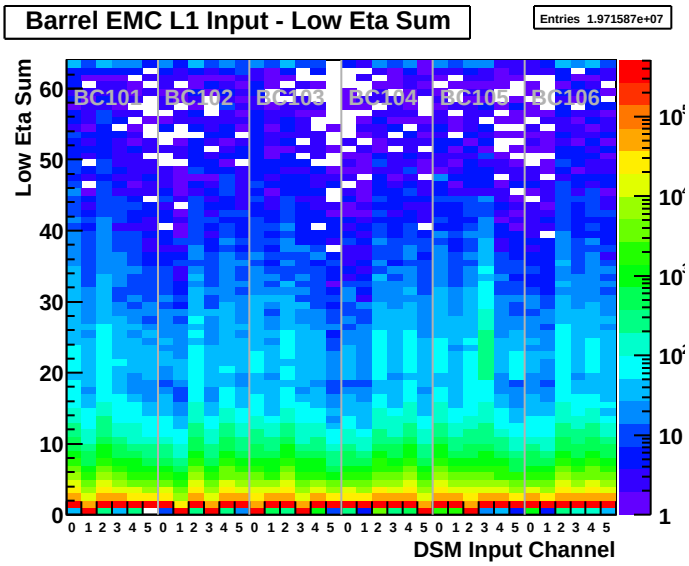
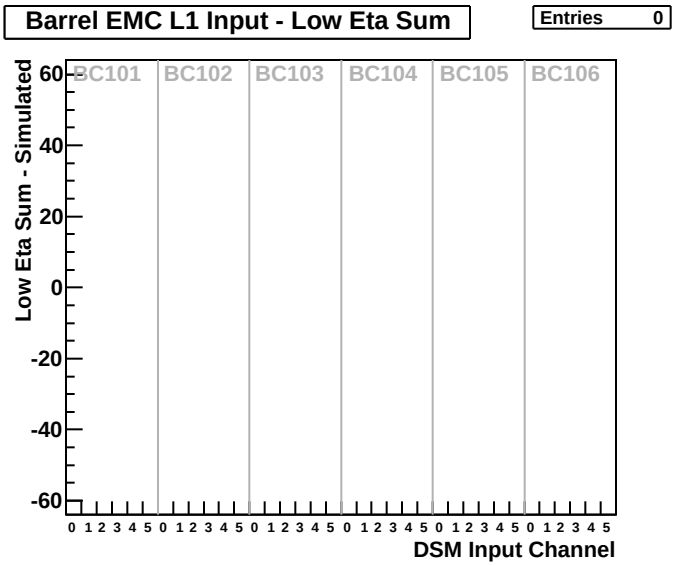
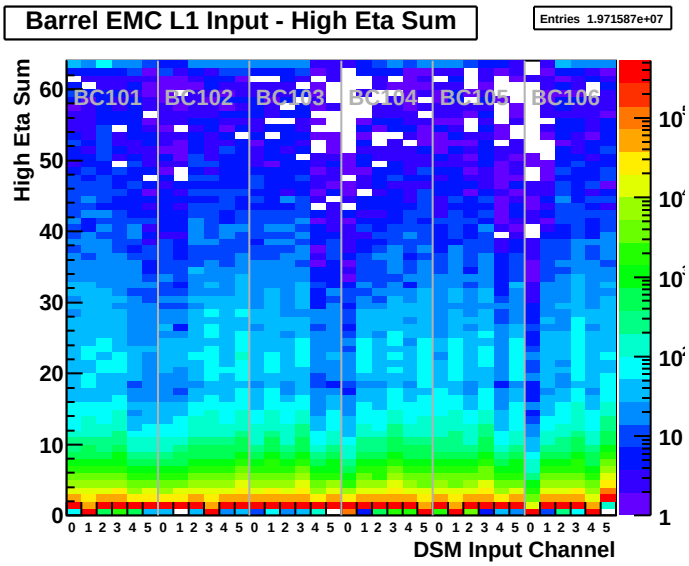
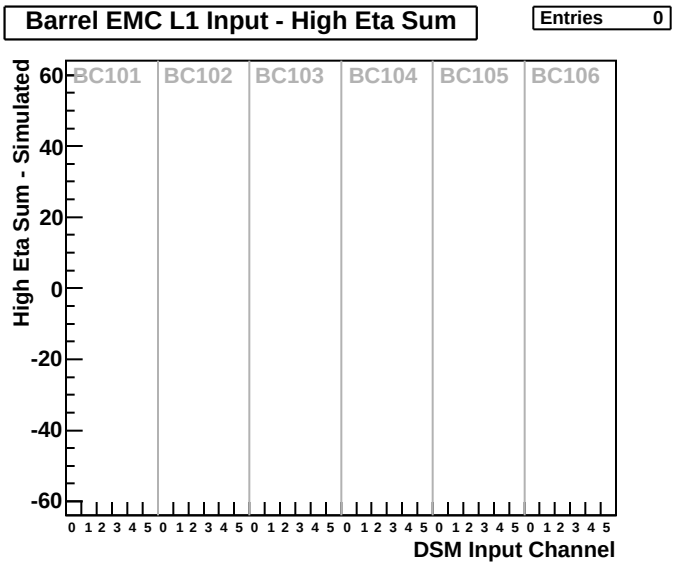
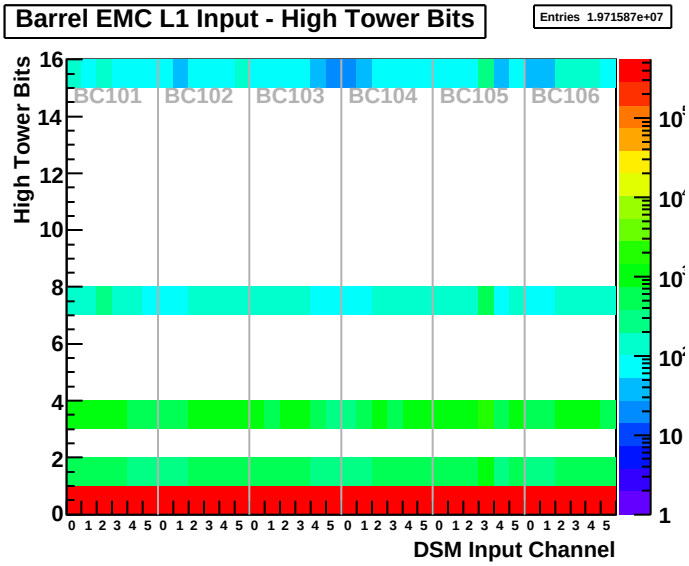
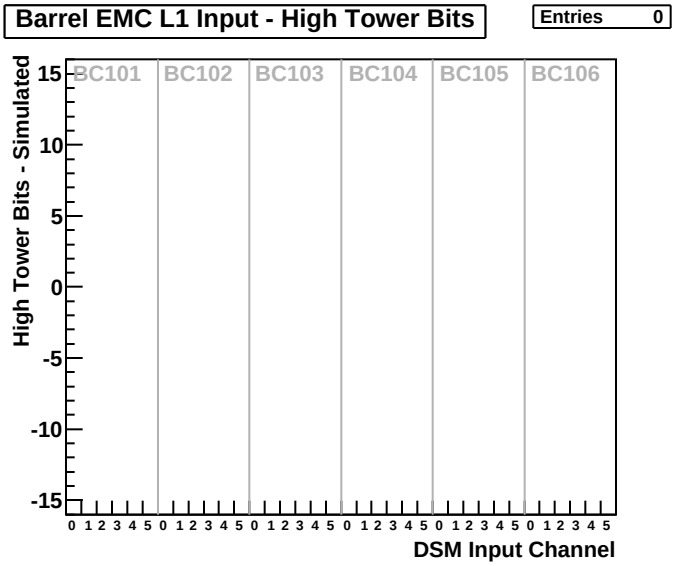

Barrel EMC L1 Input - Low Eta Sum
Entries 1.971587e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

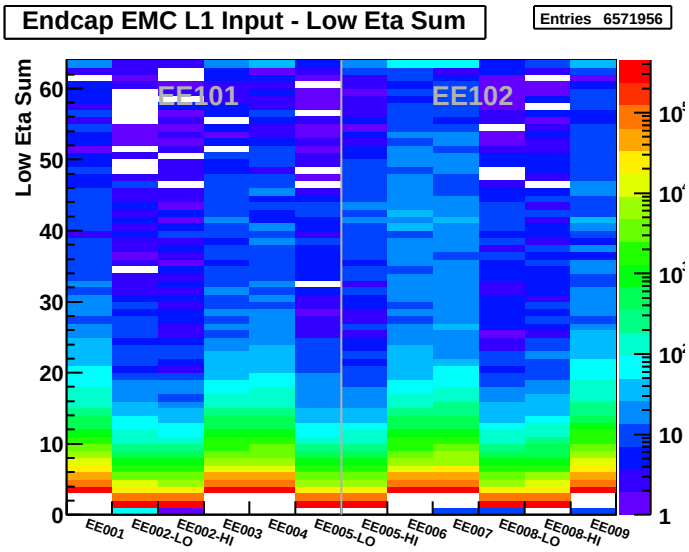

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

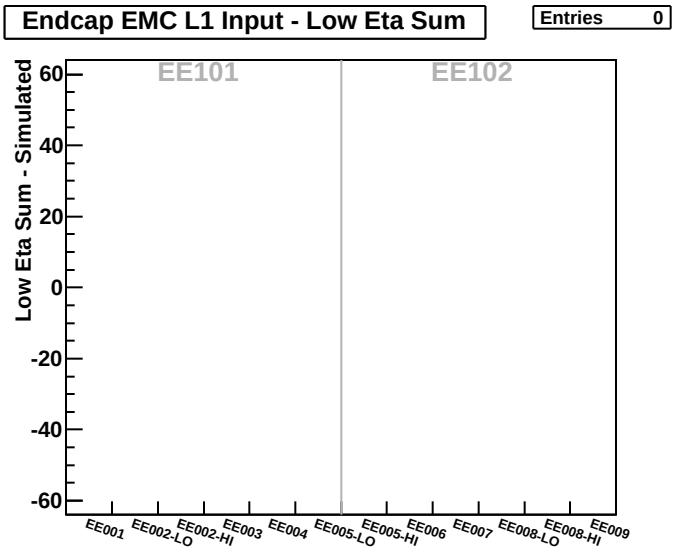

Barrel EMC L1 Input - High Eta Sum
Entries 1.971587e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

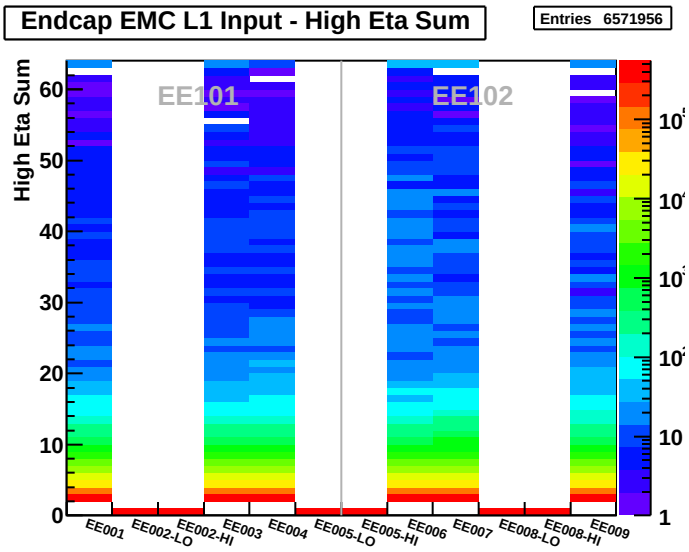

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

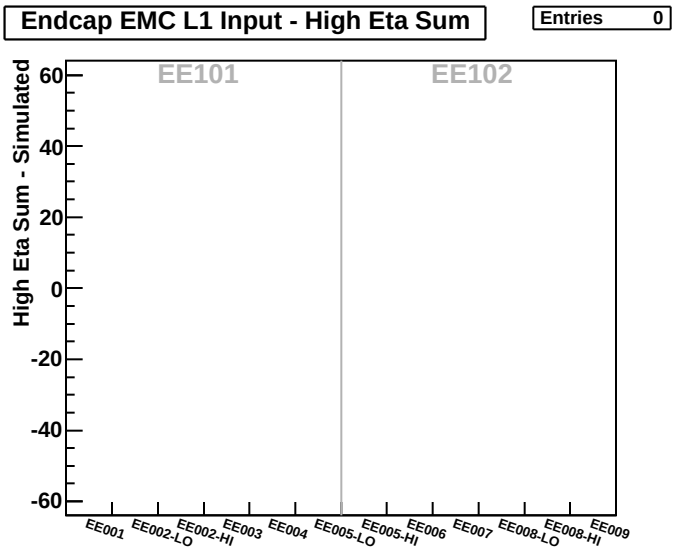

Barrel EMC L1 Input - High Tower Bits
Entries 1.971587e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

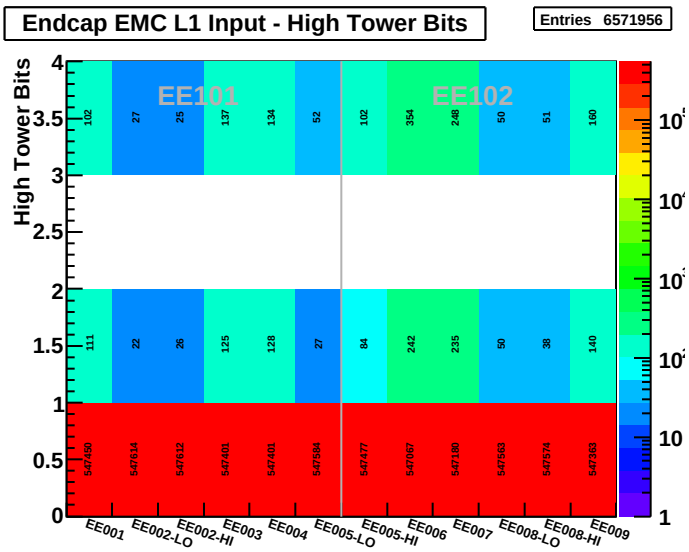

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

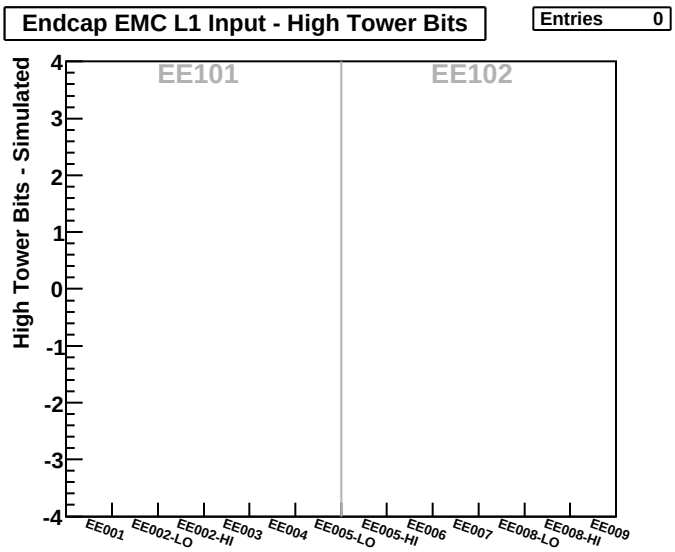

Endcap EMC L1 Input - Low Eta Sum
Entries 6571956
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

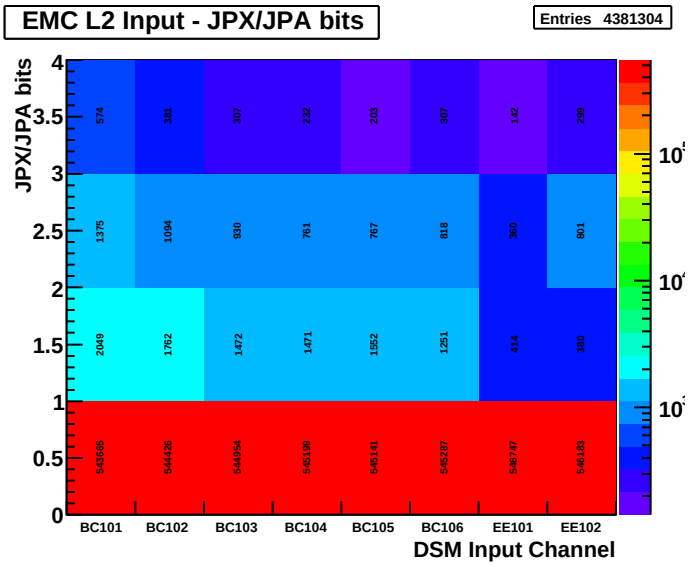

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

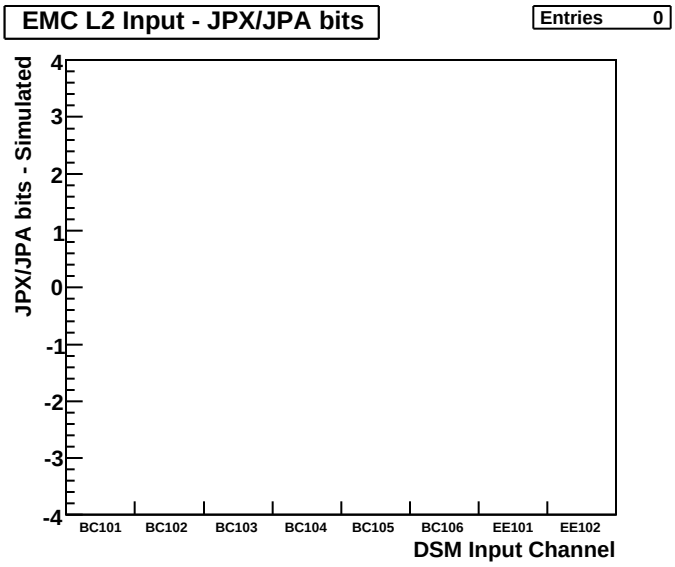

Endcap EMC L1 Input - High Eta Sum
Entries 6571956
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

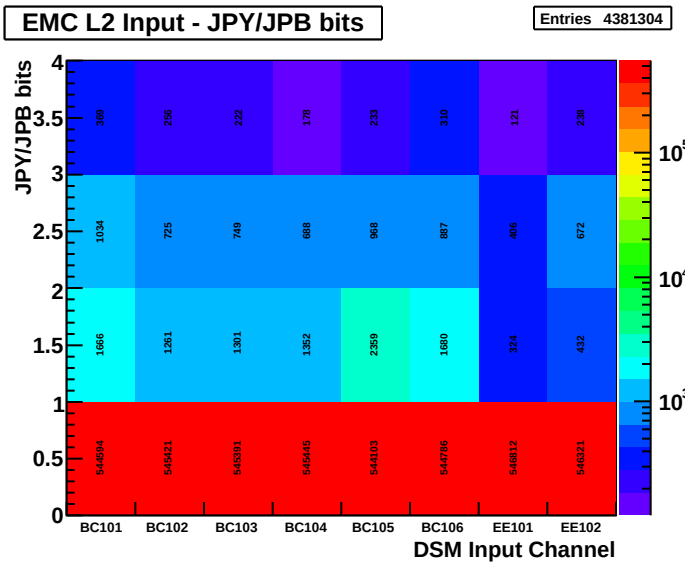

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

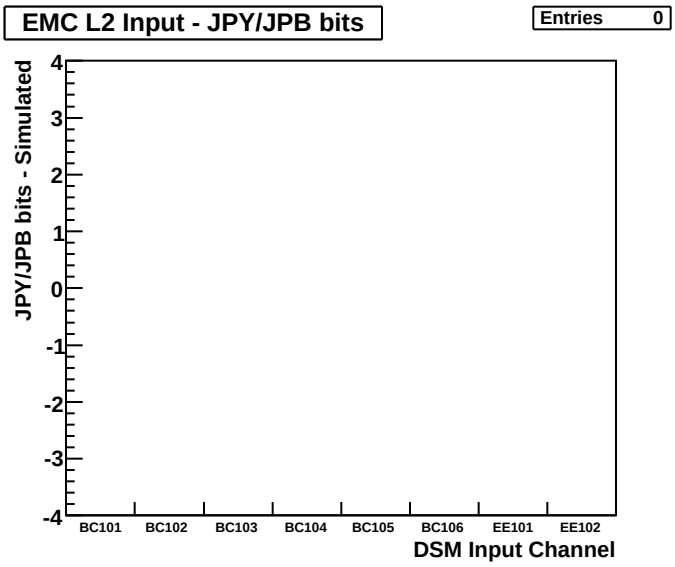

Endcap EMC L1 Input - High Tower Bits
Entries 6571956
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

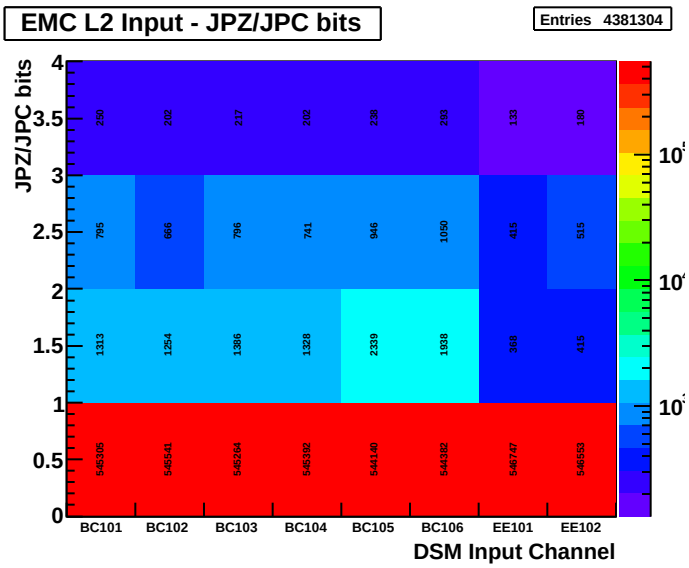

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

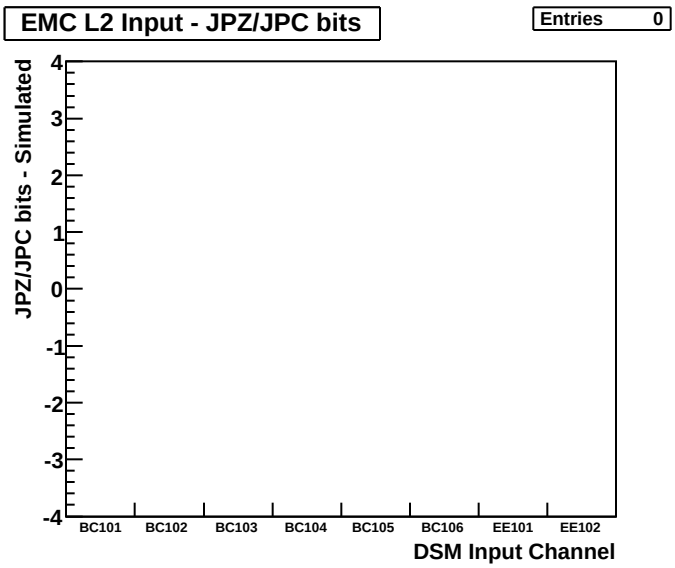

EMC L2 Input - JPX/JPA bits
Entries 4381304
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
574 381 307 232 203 307 142 299
1375 1094 920 761 767 818 360 801
2049 1762 1472 1471 1552 1251 414 380
543665 544426 544954 545199 545141 545287 546747 546183

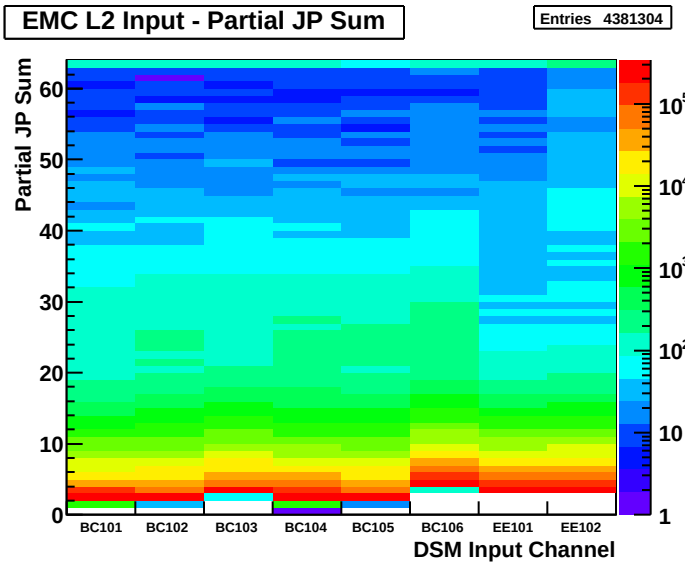

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

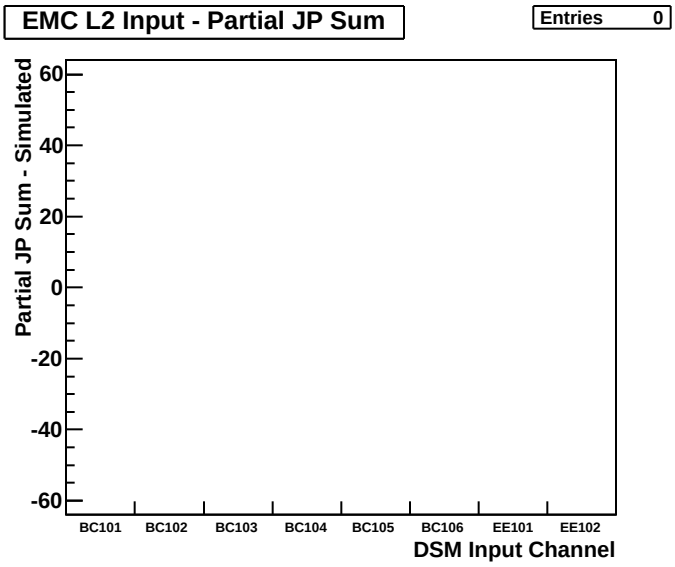

EMC L2 Input - JPY/JPB bits
Entries 4381304
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
389 256 222 178 233 310 121 238
1034 725 749 688 968 887 408 672
1666 1261 1301 1352 2359 1680 324 432
544594 545421 545391 545445 544103 544786 546812 546321

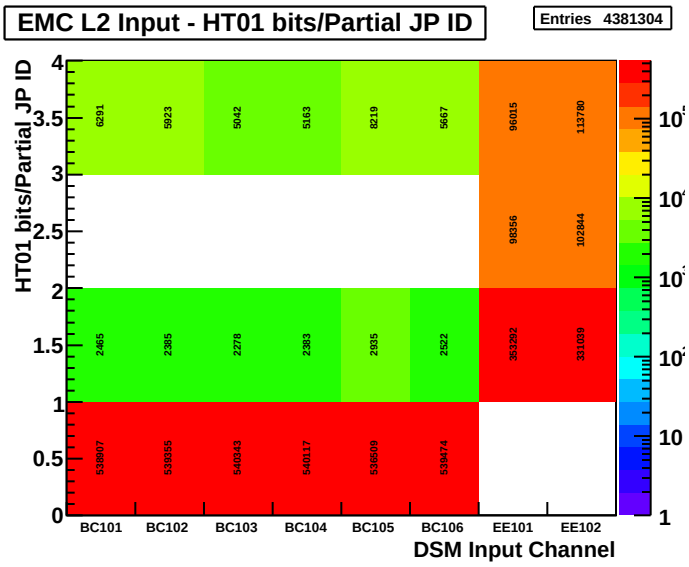

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

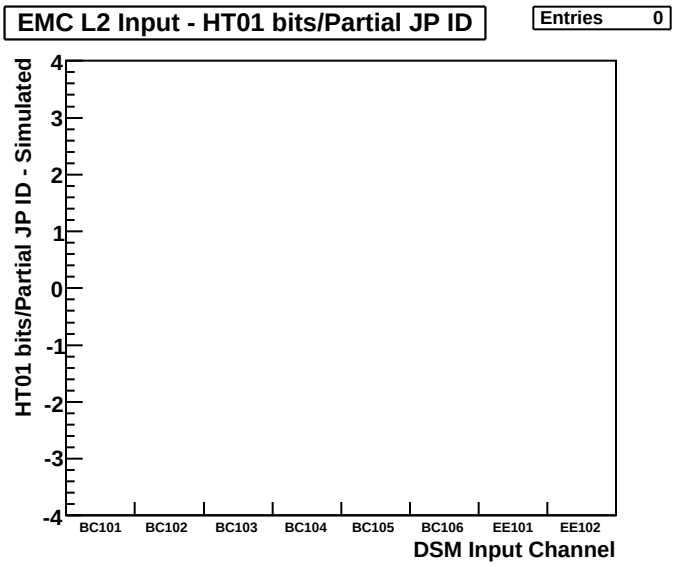

EMC L2 Input - JPZ/JPC bits
Entries 4381304
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
250 202 237 202 238 239 133 180
795 666 796 741 946 1050 415 515
1313 1254 1386 1328 2339 1938 368 415
545305 545541 545264 545392 544140 544382 546747 546553

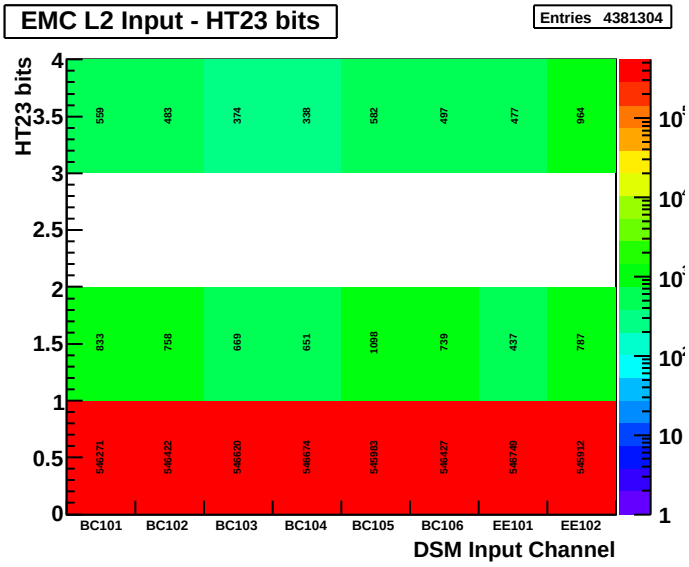

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

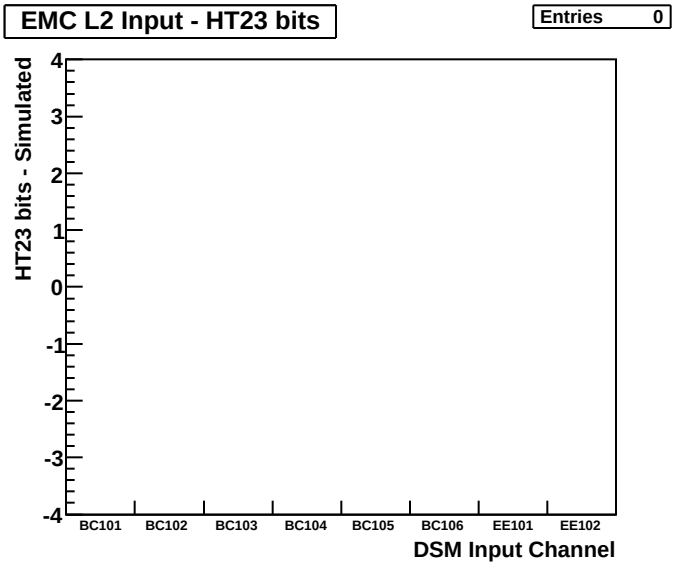

EMC L2 Input - Partial JP Sum
Entries 4381304
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

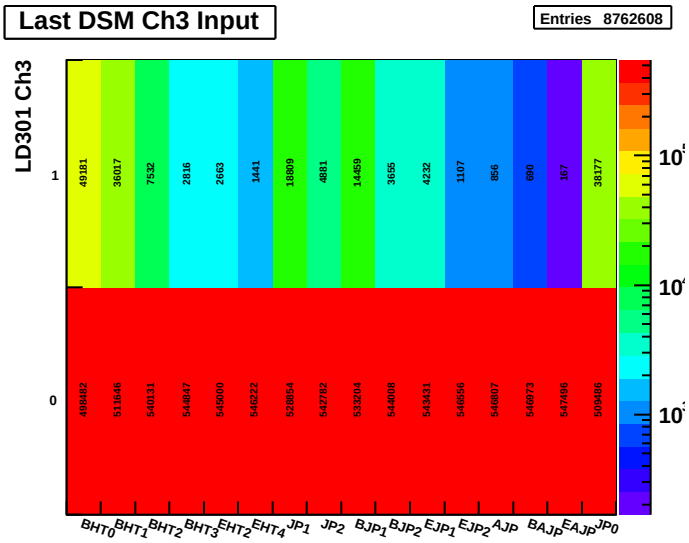

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

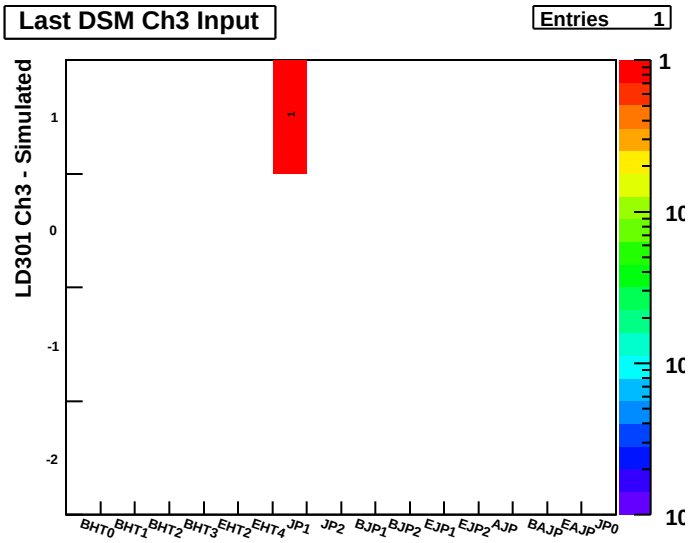

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4381304
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

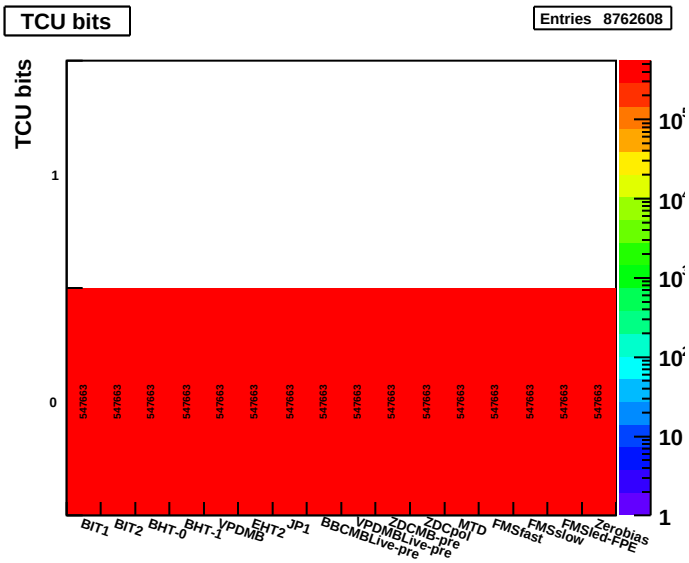

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

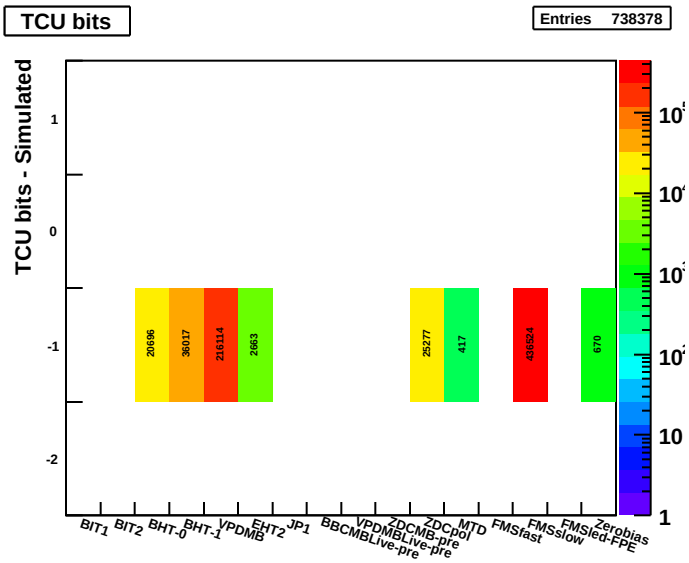

EMC L2 Input - HT23 bits
Entries 4381304
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

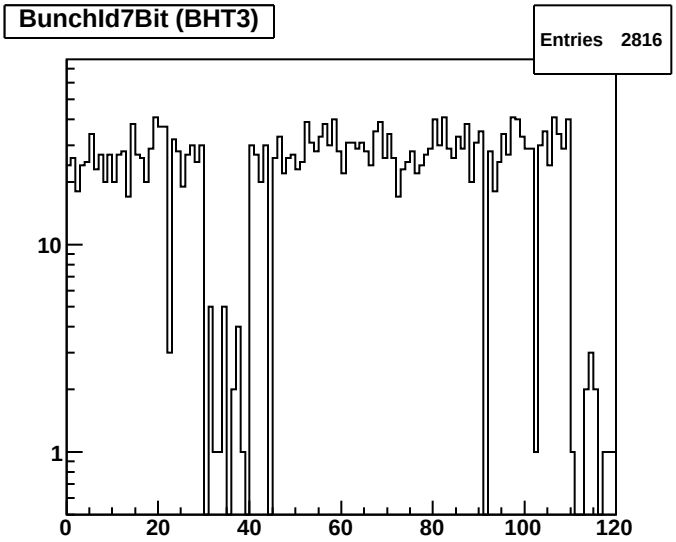

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

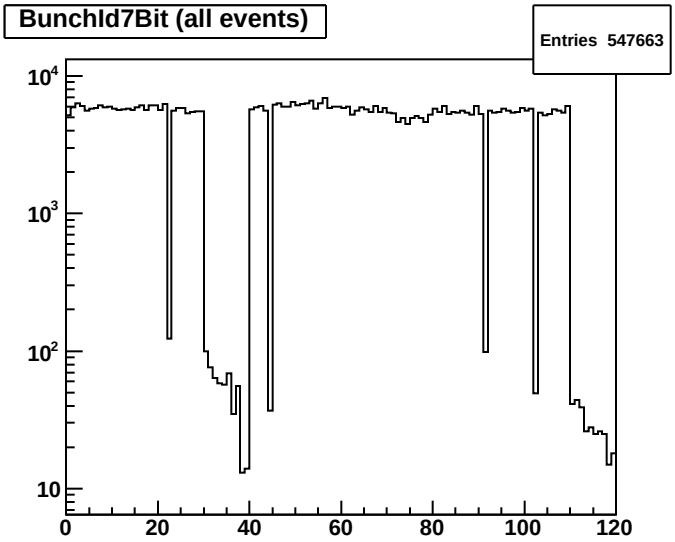

Last DSM Ch3 Input
Entries 8762608
LD301 Ch3
1
0
49181 36017 7532 2816 2663 1441 18809 4881 14459 3655 4232 1107 856 690 167 38177
498482 511646 540131 544847 545000 546222 528854 542762 533204 544008 543431 546556 546807 546973 547496 509486
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

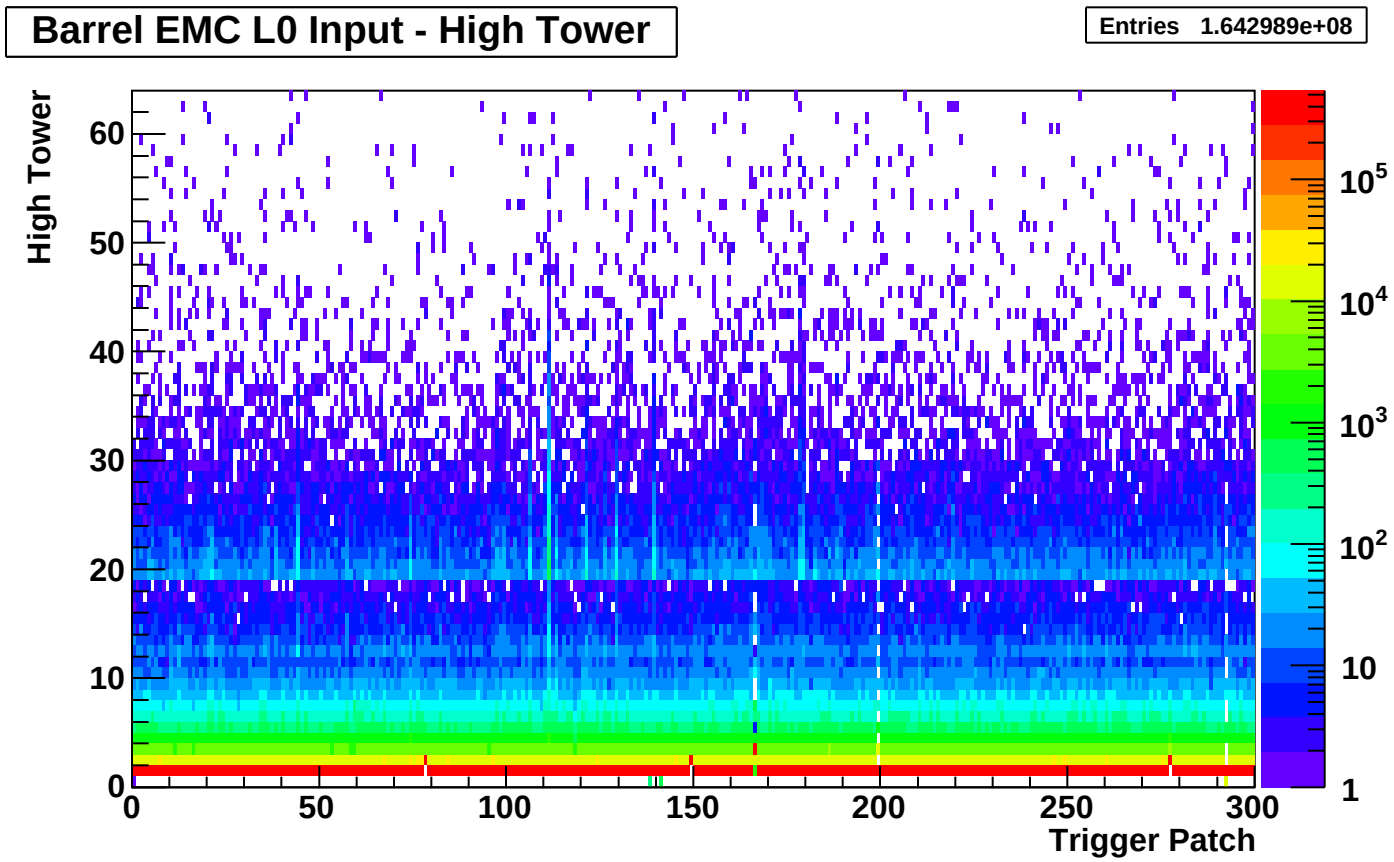

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
0
-1
-2
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

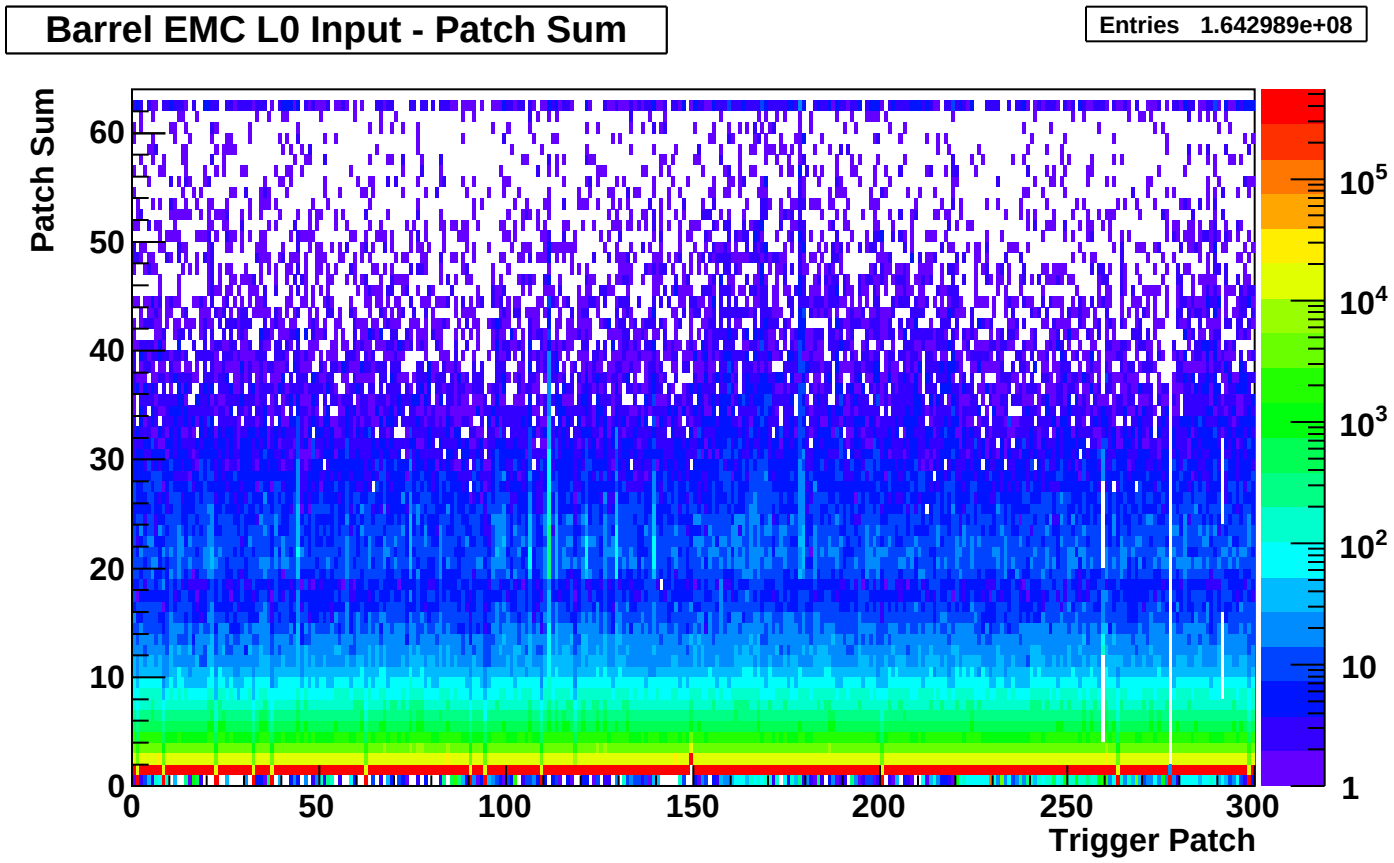

TCU bits
Entries 8762608
TCU bits
1
0
547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663 547663
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

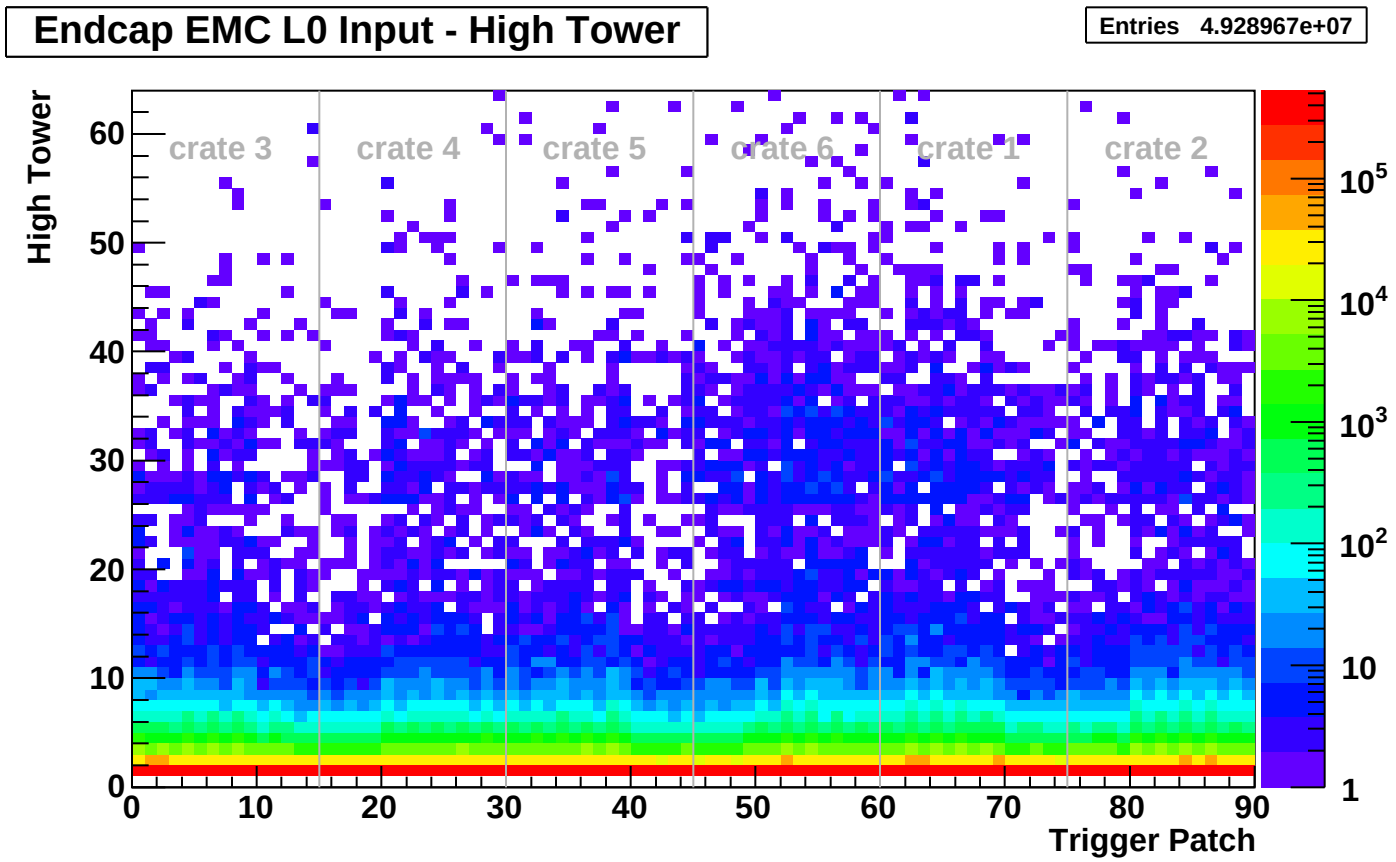

TCU bits
Entries 738378
TCU bits - Simulated
1
0
-1
-2
20696 36017 216114 2693 25277 417 436524 670
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

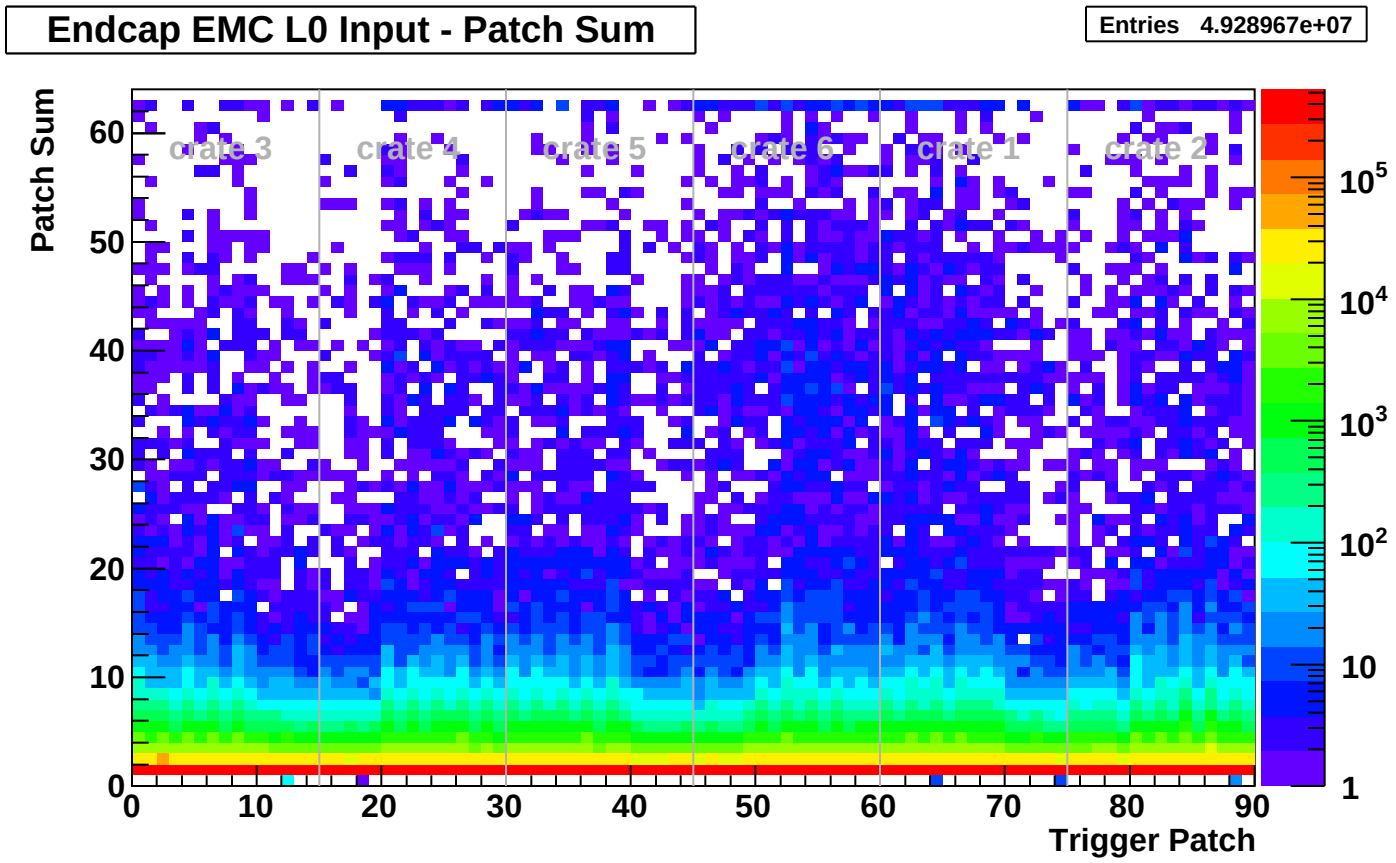

BunchId7Bit (BHT3)
Entries 2816
10
1
0 20 40 60 80 100 120

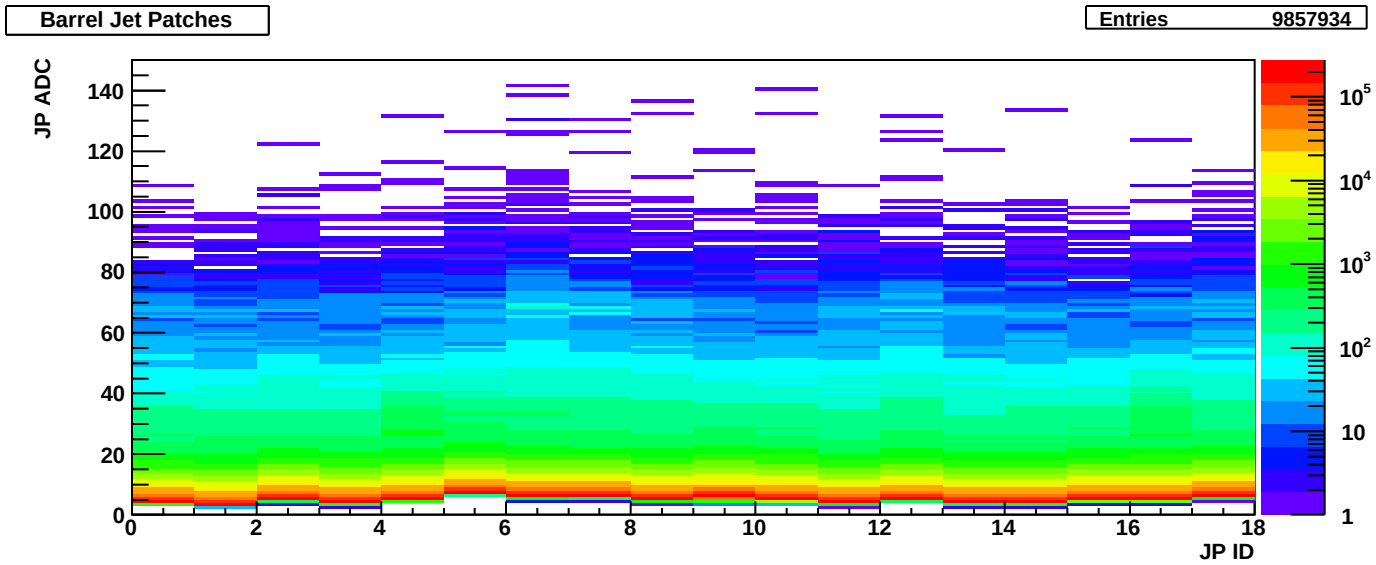

BunchId7Bit (all events)
Entries 547663
10^4
10^3
10^2
10
0 20 40 60 80 100 120

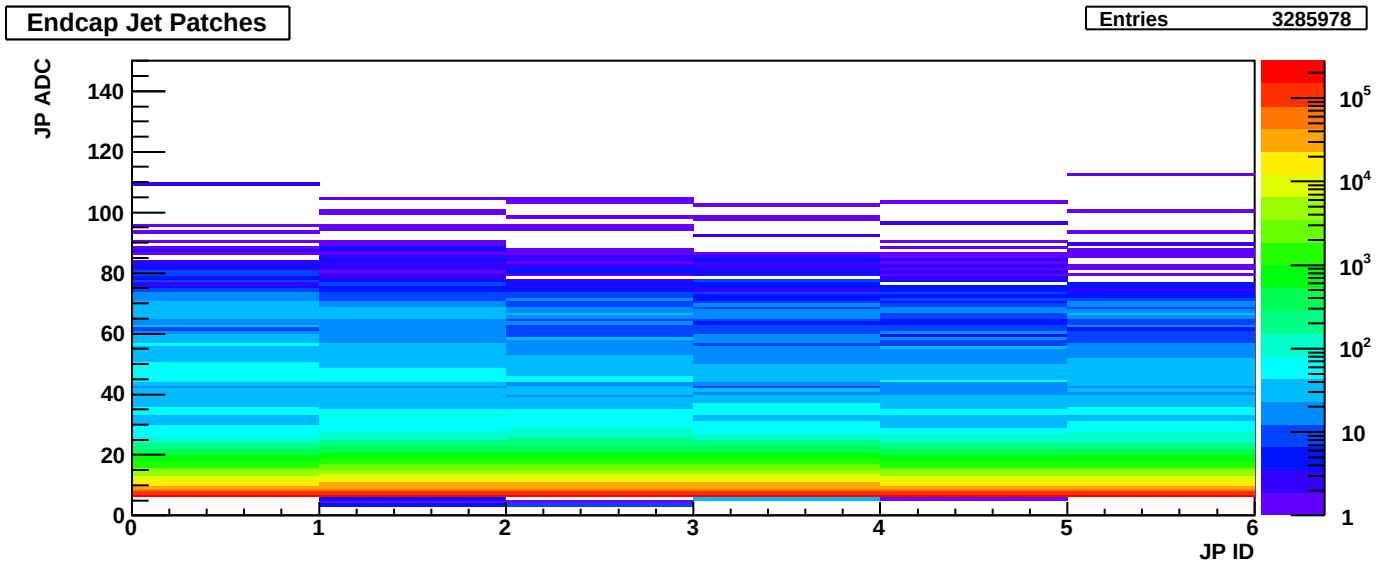

Barrel EMC L0 Input - High Tower
Entries 1.642989e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

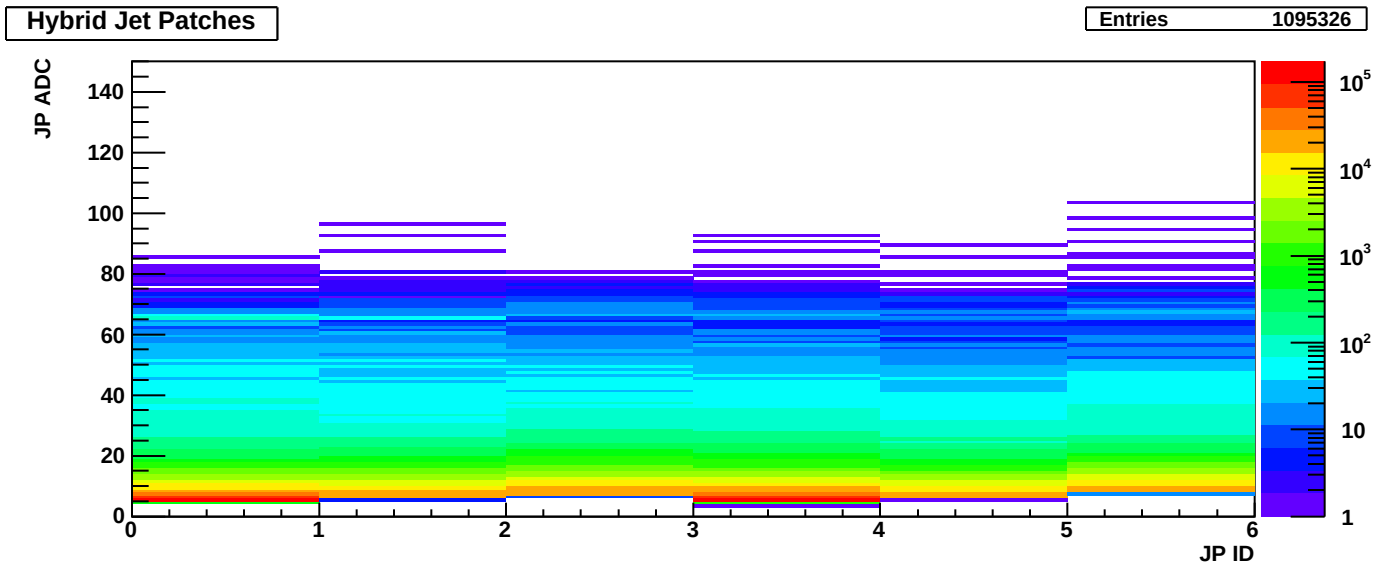

Barrel EMC L0 Input - Patch Sum
Entries 1.642989e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

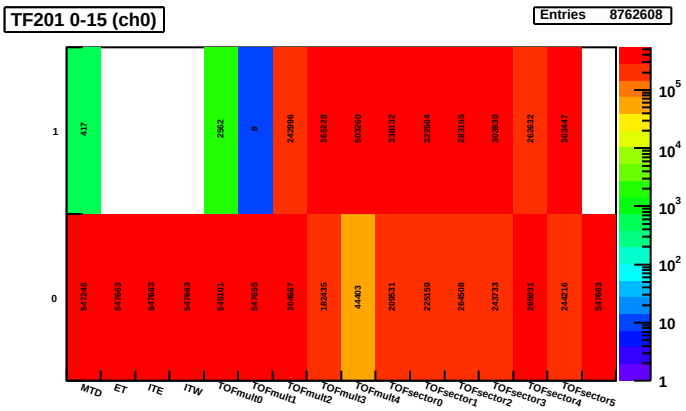

Endcap EMC L0 Input - High Tower
Entries 4.928967e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

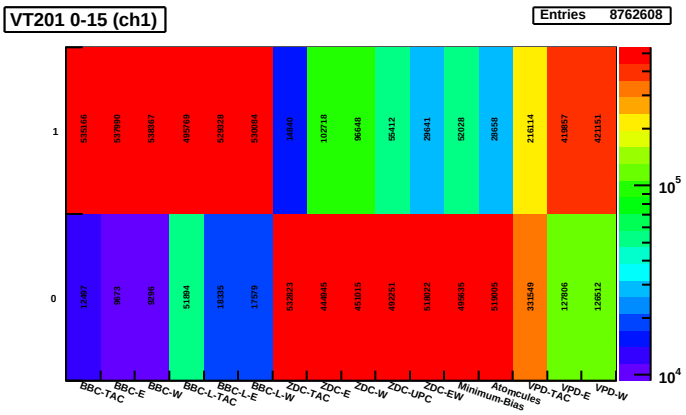

Endcap EMC L0 Input - Patch Sum
Entries 4.928967e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

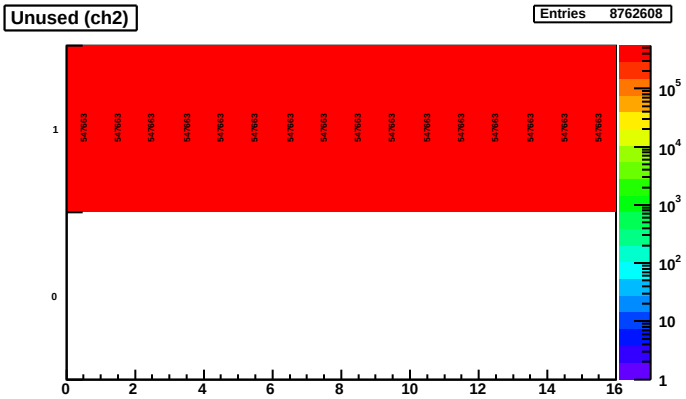

Barrel Jet Patches
Entries 9857934
JP ADC
JP ID

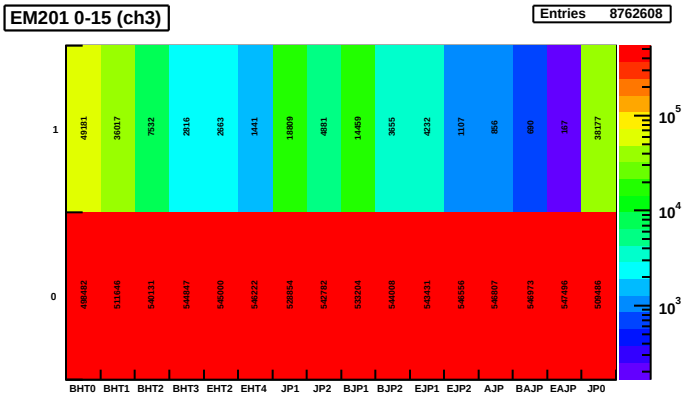

Endcap Jet Patches
Entries 3285978
JP ADC
JP ID

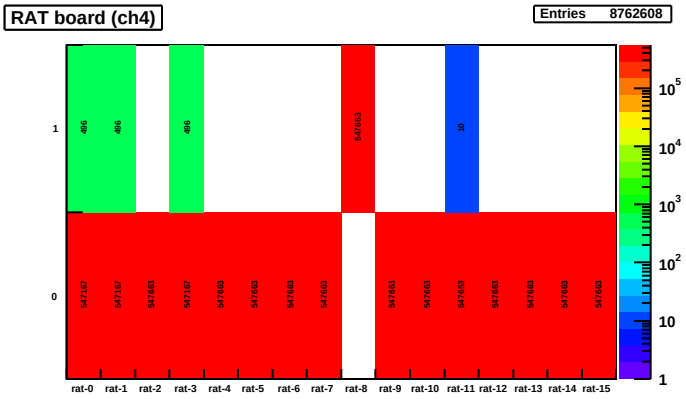

Hybrid Jet Patches
Entries 1095326
JP ADC
JP ID

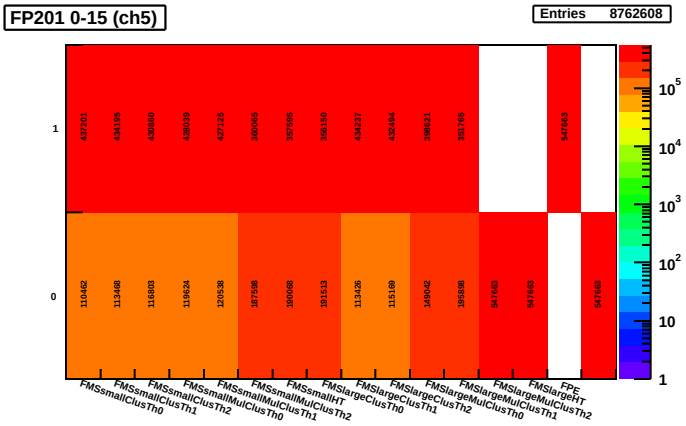

TF201 0-15 (ch0)
Entries 8762608
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

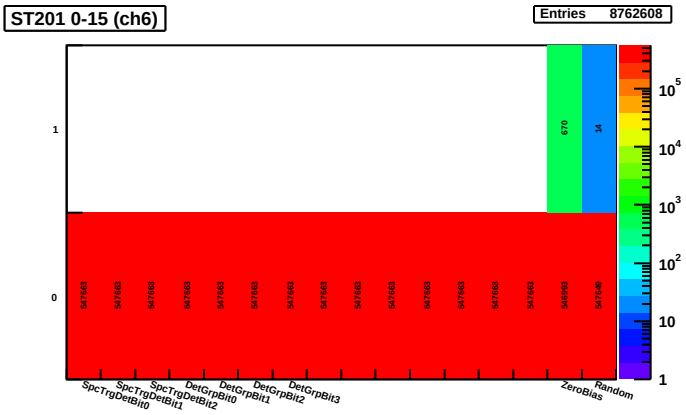

VT201 0-15 (ch1)
Entries 8762608
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

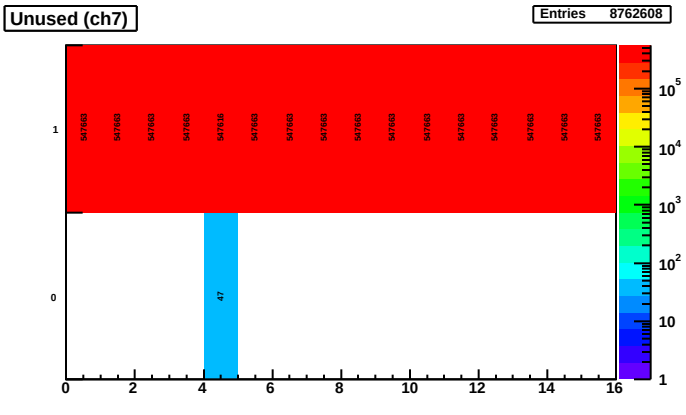

Unused (ch2)
Entries 8762608

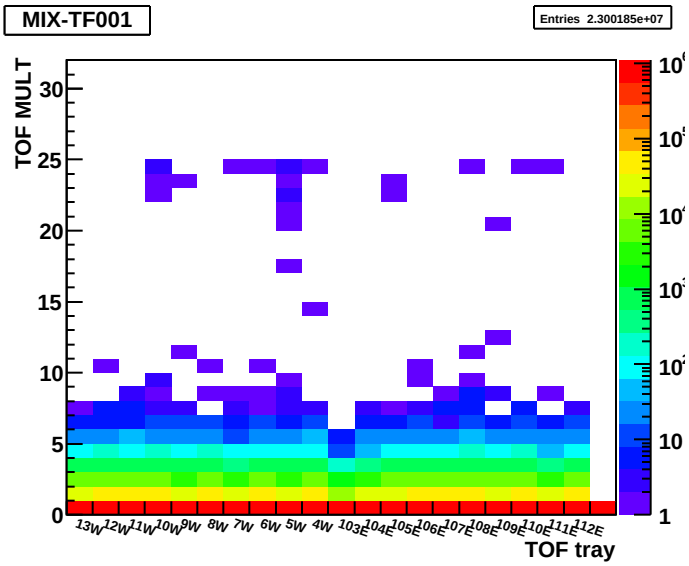

EM201 0-15 (ch3)
Entries 8762608
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

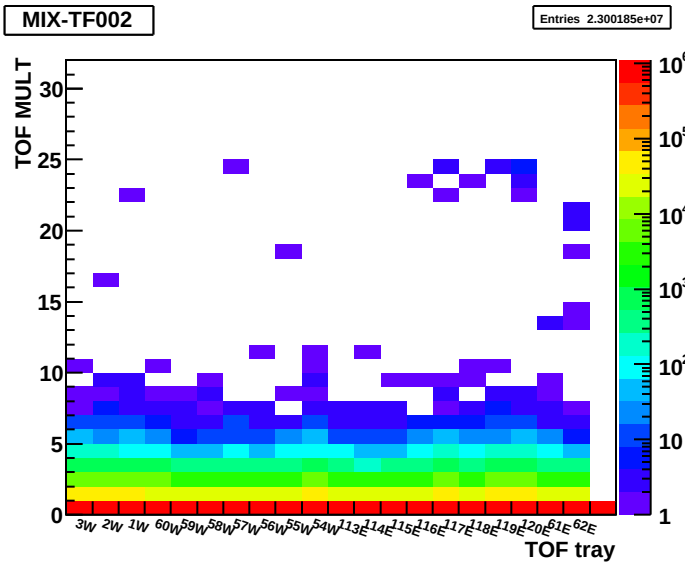

RAT board (ch4)
Entries 8762608
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

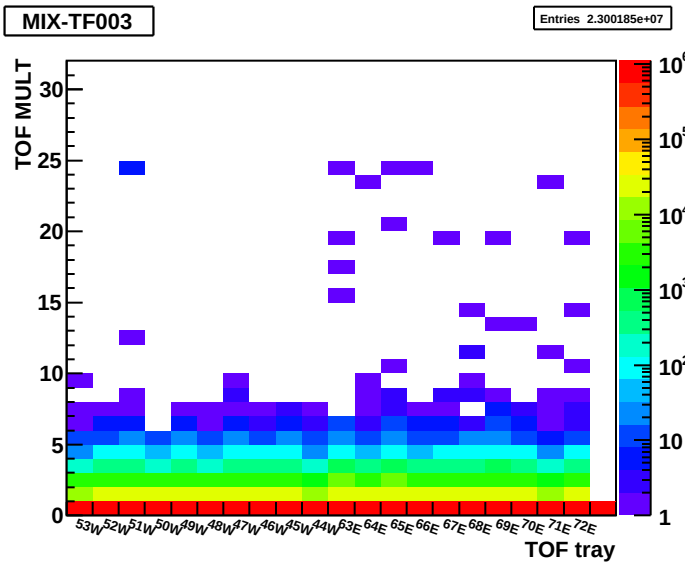

FP201 0-15 (ch5)
Entries 8762608
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

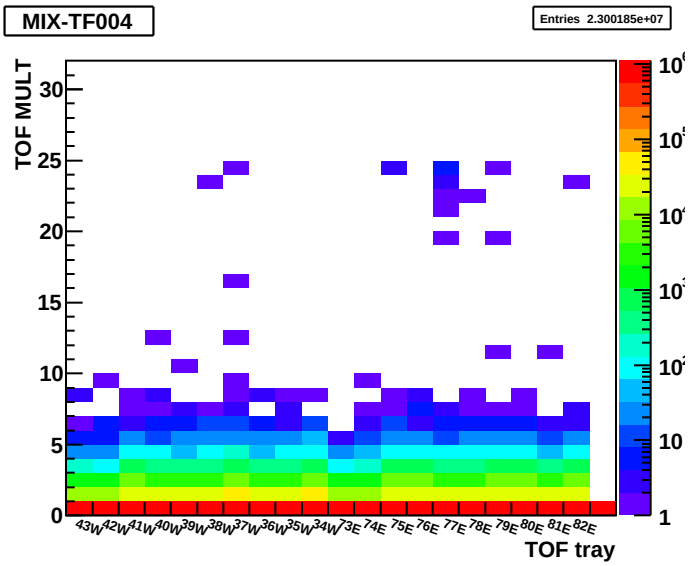

ST201 0-15 (ch6)
Entries 8762608
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

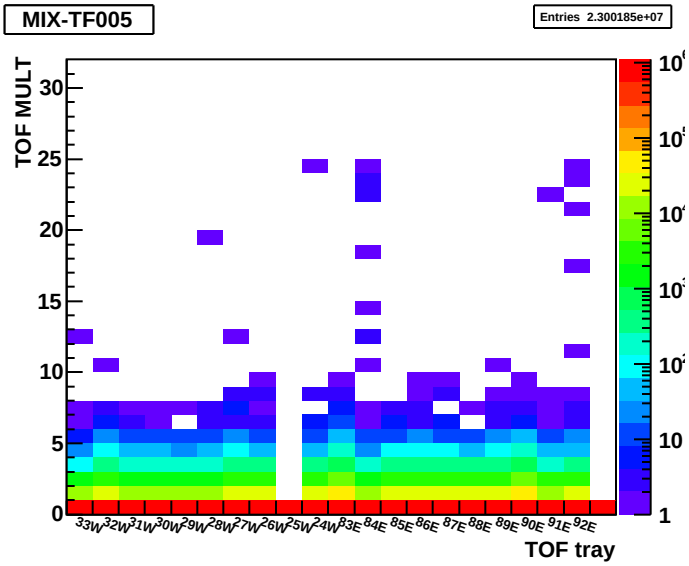

Unused (ch7)
Entries 8762608

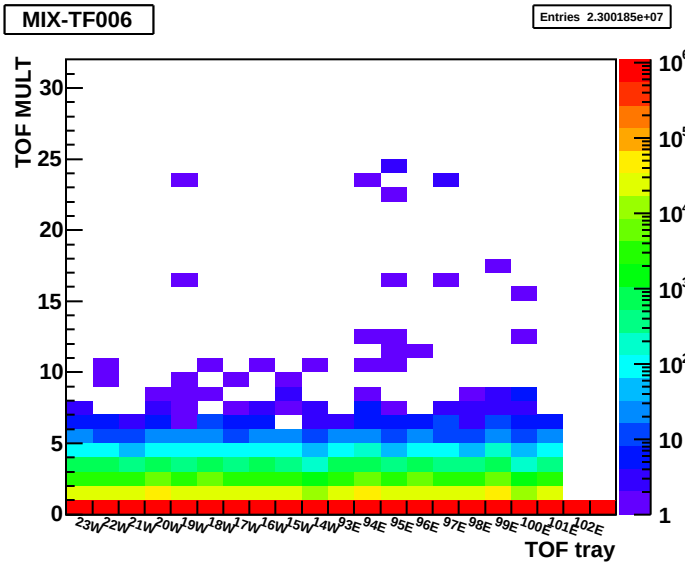

MIX-TF001
Entries 2.300185e+07
TOF MULT
TOF tray

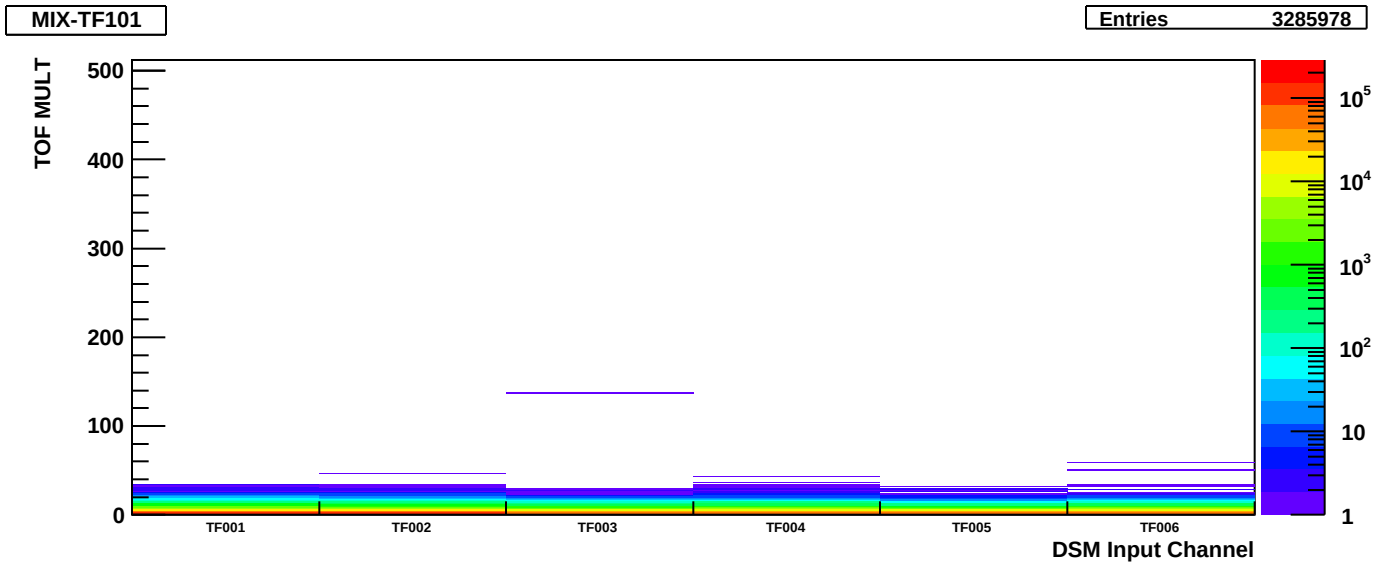

MIX-TF002
Entries 2.300185e+07
TOF MULT
TOF tray

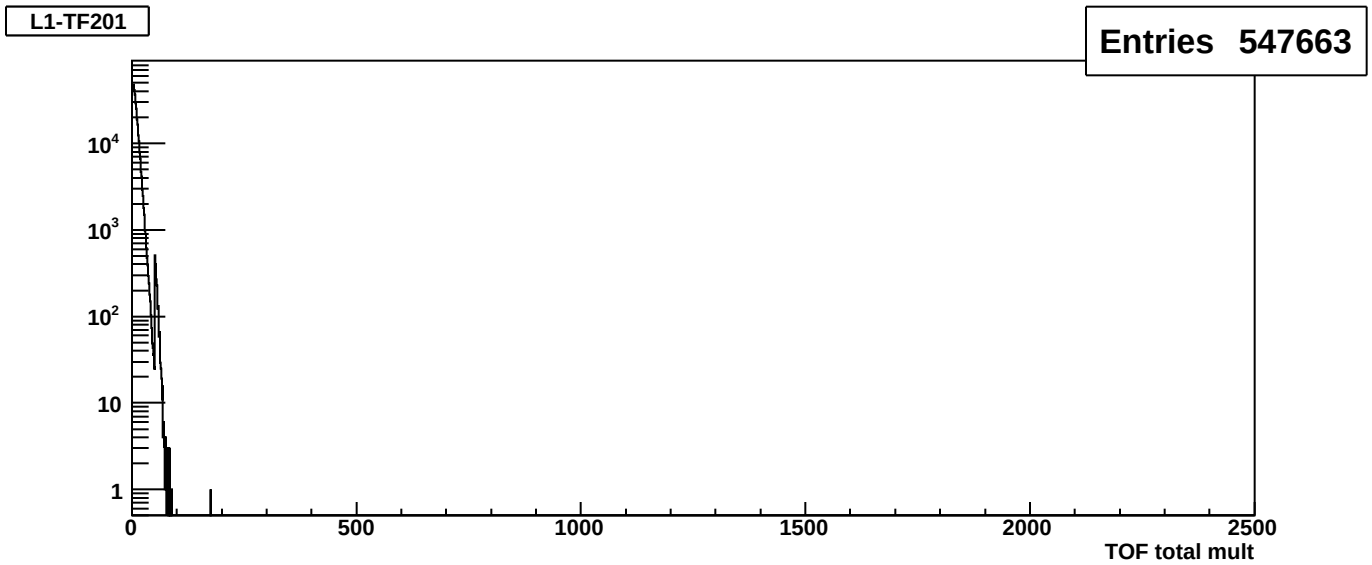

MIX-TF003
Entries 2.300185e+07
TOF MULT
TOF tray

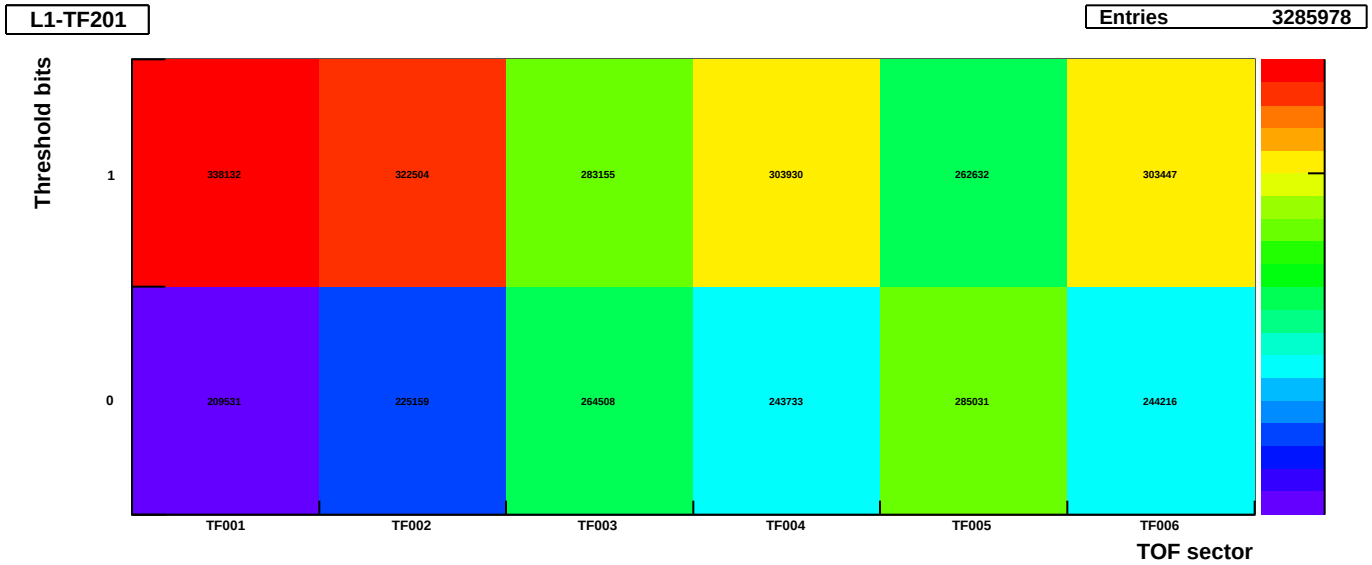

MIX-TF004
Entries 2.300185e+07
TOF MULT
TOF tray

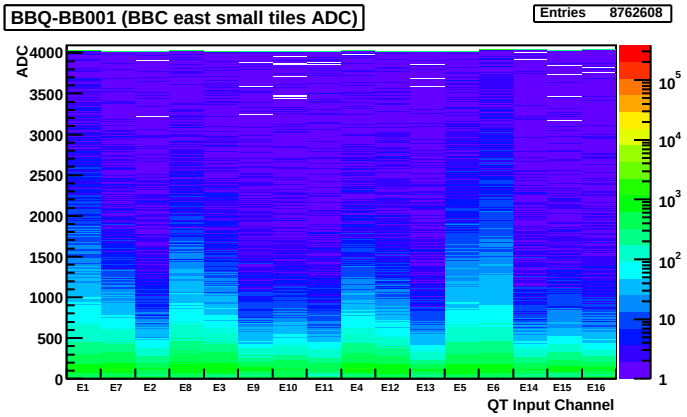

MIX-TF005
Entries 2.300185e+07
TOF MULT
TOF tray

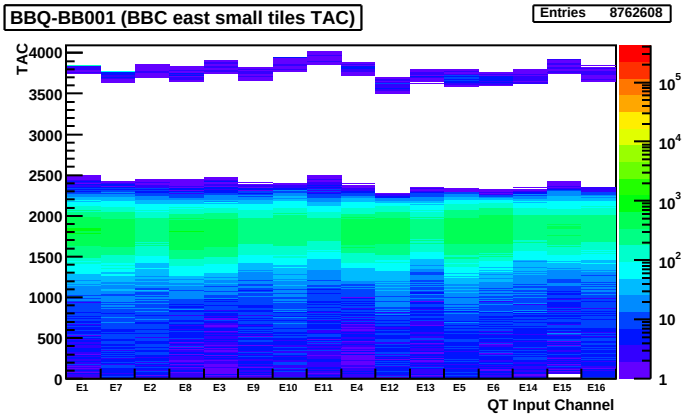

MIX-TF006
Entries 2.300185e+07
TOF MULT
TOF tray

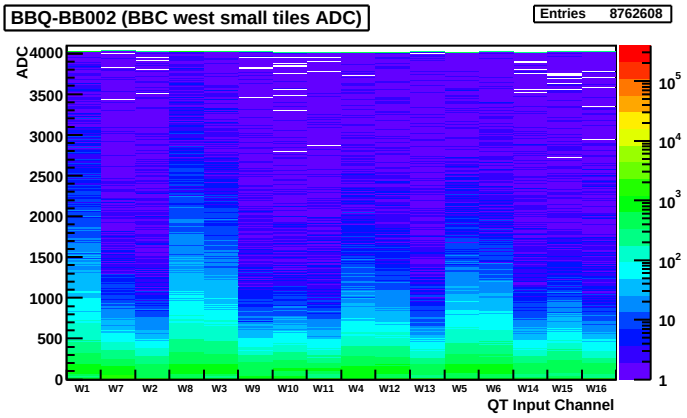

MIX-TF101
Entries 3285978
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

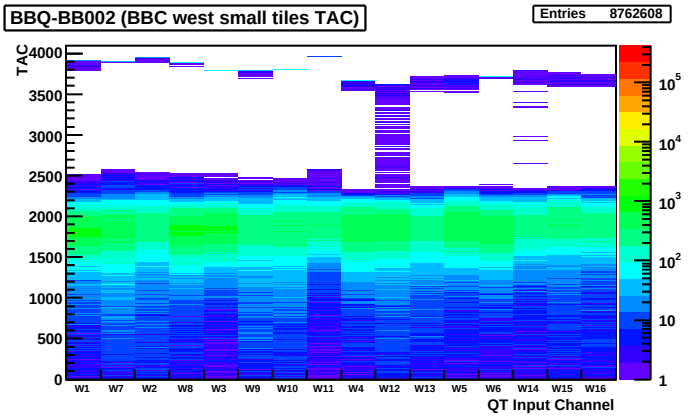

L1-TF201
Entries 547663
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

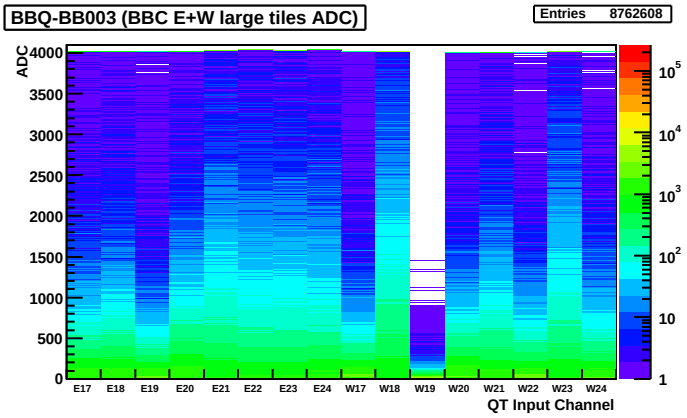

L1-TF201
Entries 3285978
Threshold bits
1
0
338132
322504
283155
303930
262632
303447
209531
225159
264508
243733
285031
244216
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

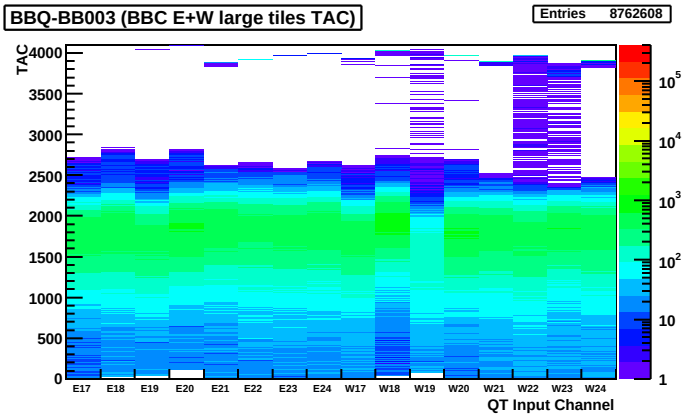

BBQ-BB001 (BBC east small tiles ADC)
Entries 8762608
ADC
QT Input Channel

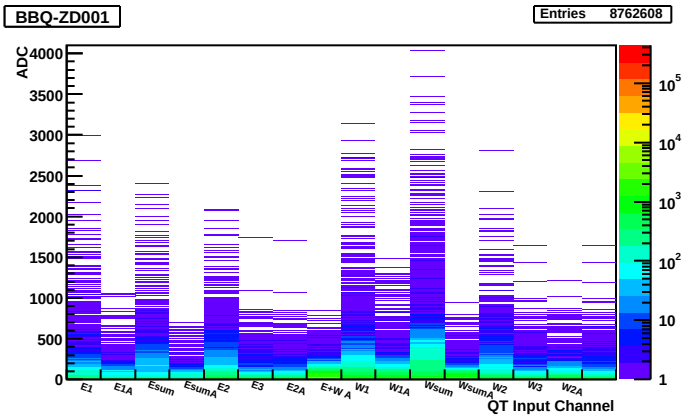

BBQ-BB001 (BBC east small tiles TAC)
Entries 8762608
TAC
QT Input Channel

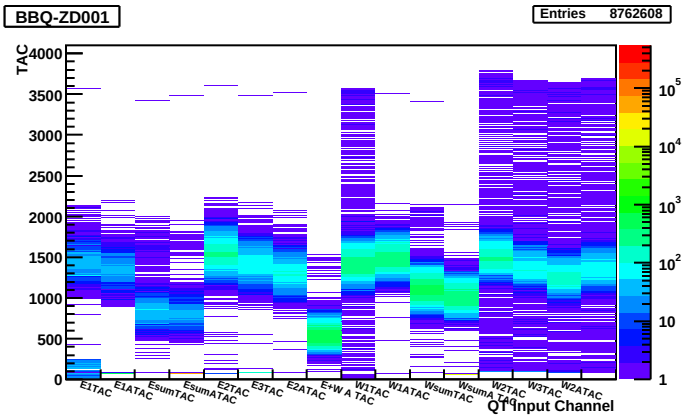

BBQ-BB002 (BBC west small tiles ADC)
Entries 8762608
ADC
QT Input Channel

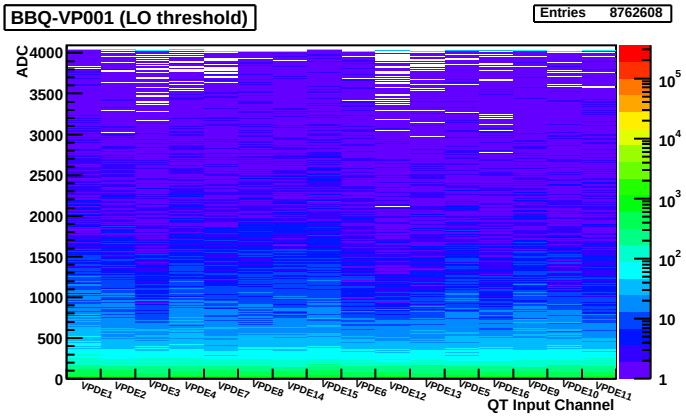

BBQ-BB002 (BBC west small tiles TAC)
Entries 8762608
TAC
QT Input Channel

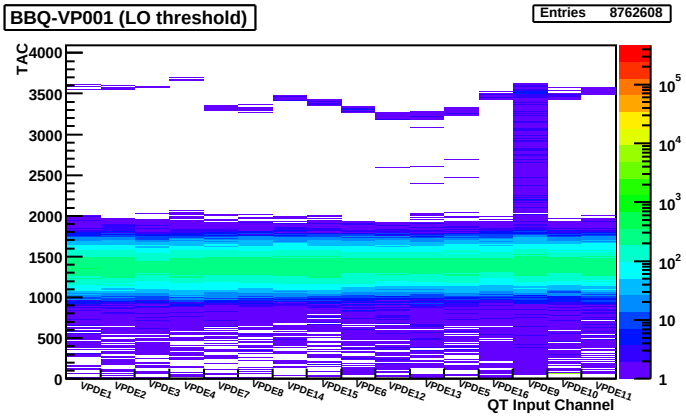

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8762608
ADC
QT Input Channel

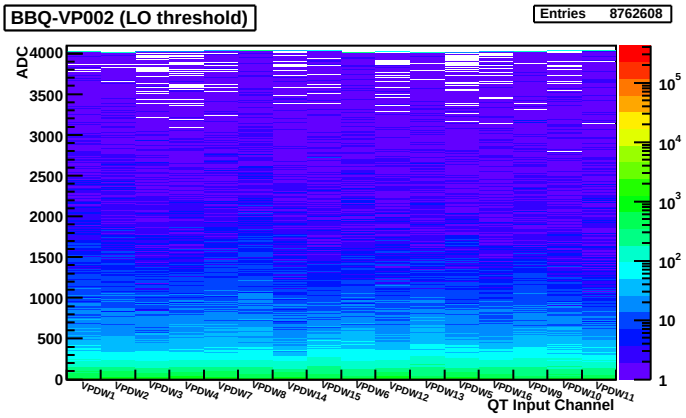

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8762608
TAC
QT Input Channel

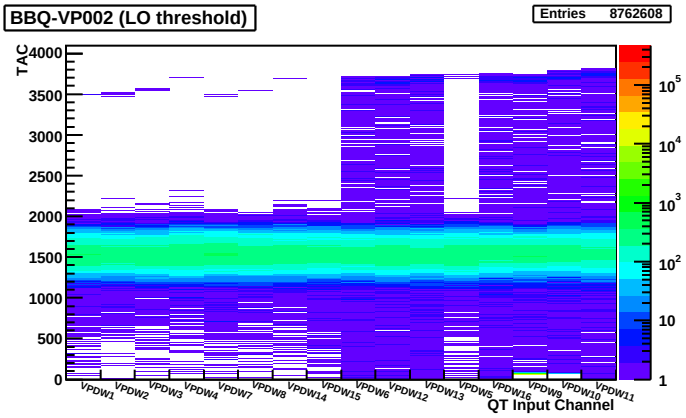

BBQ-ZD001
Entries 8762608
ADC
QT Input Channel

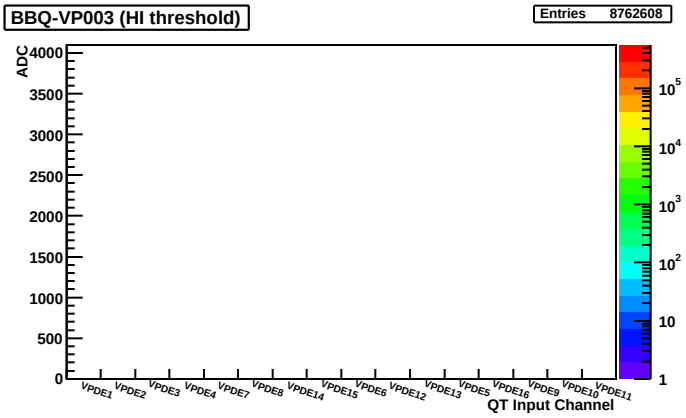

BBQ-ZD001
Entries 8762608
TAC
QT Input Channel

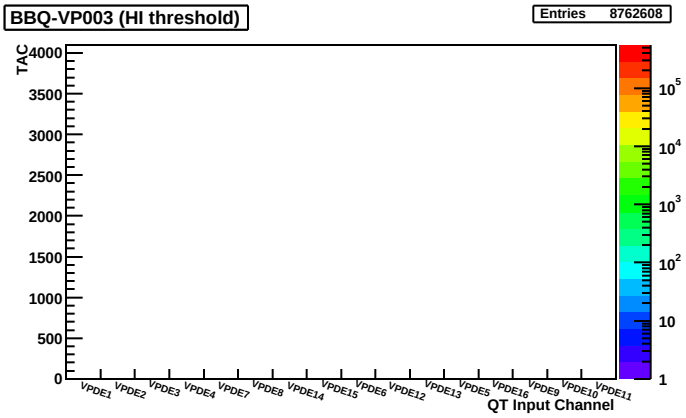

BBQ-VP001 (LO threshold)
Entries 8762608
ADC
QT Input Channel

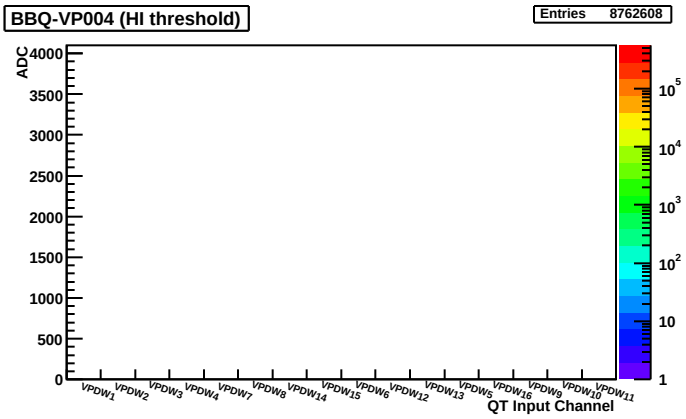

BBQ-VP001 (LO threshold)
Entries 8762608
TAC
QT Input Channel

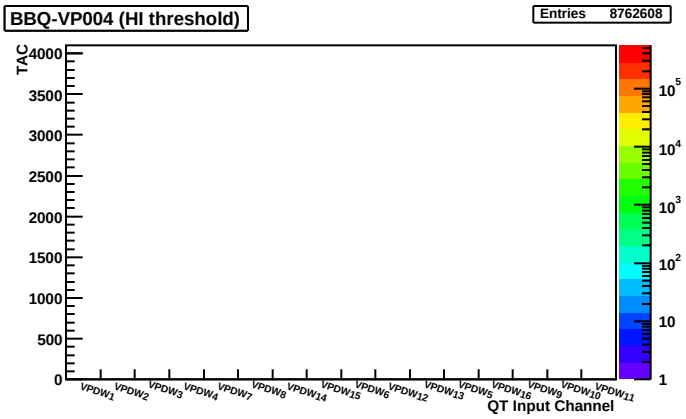

BBQ-VP002 (LO threshold)
Entries 8762608
ADC
QT Input Channel

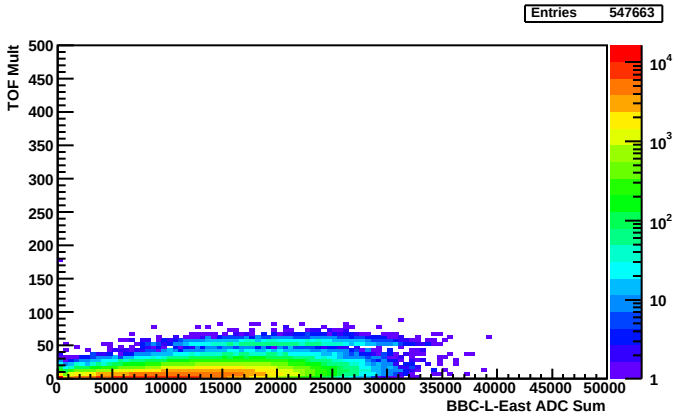

BBQ-VP002 (LO threshold)
Entries 8762608
TAC
QT Input Channel

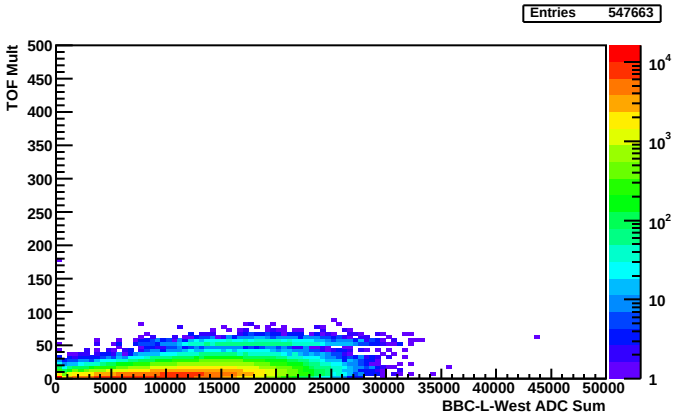

BBQ-VP003 (HI threshold)
Entries 8762608
ADC
QT Input Channel

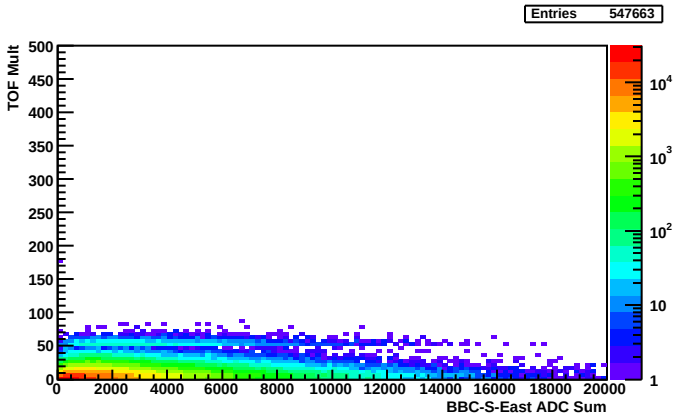

BBQ-VP003 (HI threshold)
Entries 8762608
TAC
QT Input Channel

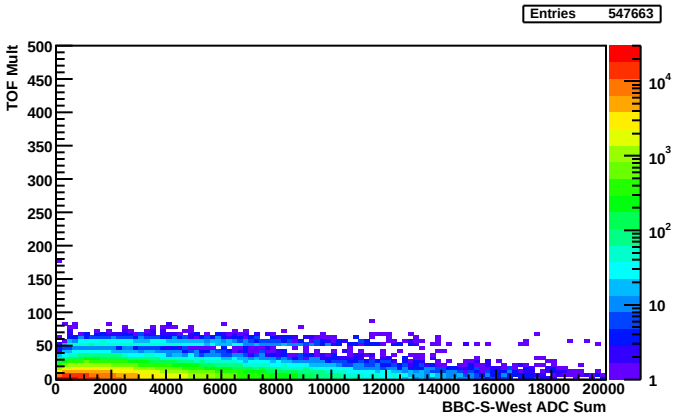

BBQ-VP004 (HI threshold)
Entries 8762608
ADC
QT Input Channel

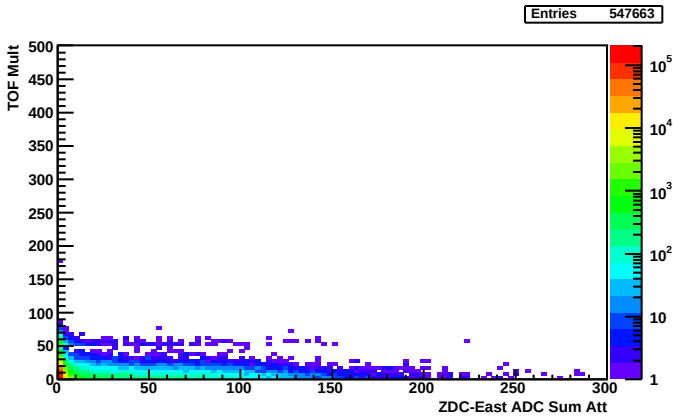

BBQ-VP004 (HI threshold)
Entries 8762608
TAC
QT Input Channel

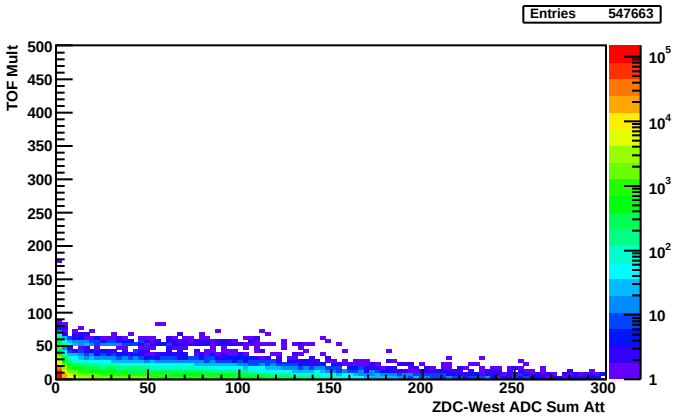

Entries 547663
TOF Mult
BBC-L-East ADC Sum

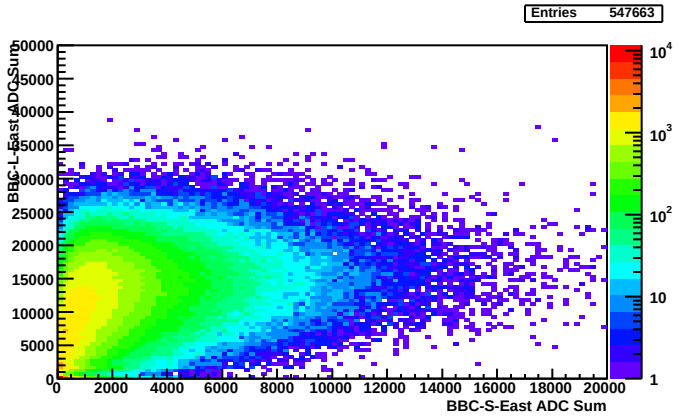

Entries 547663
TOF Mult
BBC-L-West ADC Sum

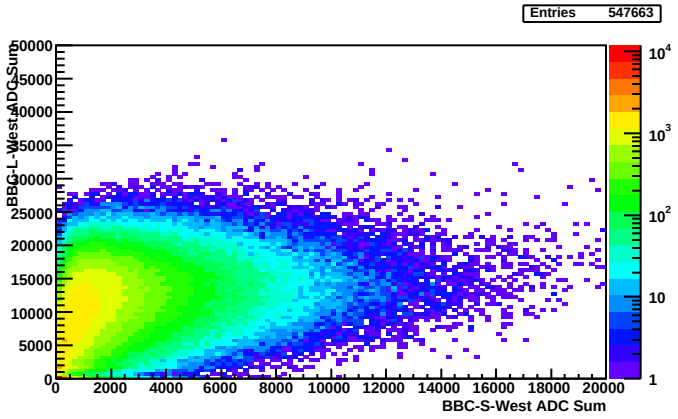

Entries 547663
TOF Mult
BBC-S-East ADC Sum

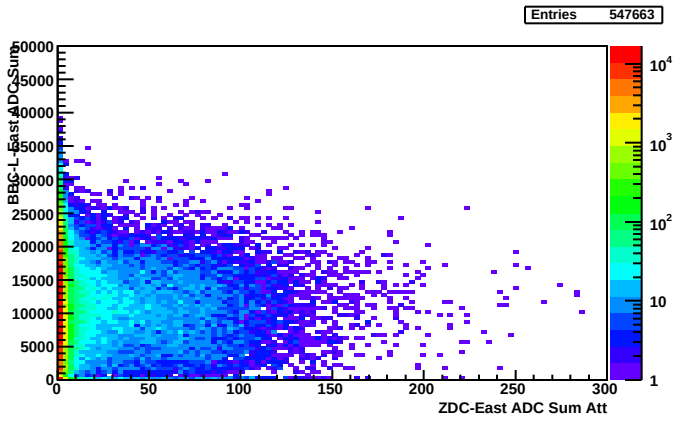

Entries 547663
TOF Mult
BBC-S-West ADC Sum

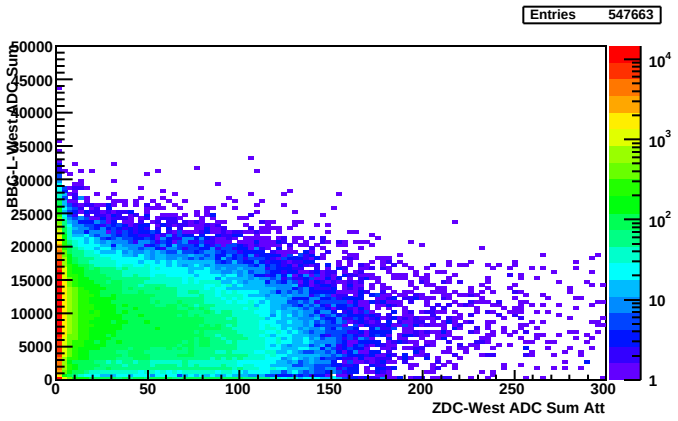

Entries 547663
TOF Mult
ZDC-East ADC Sum Att

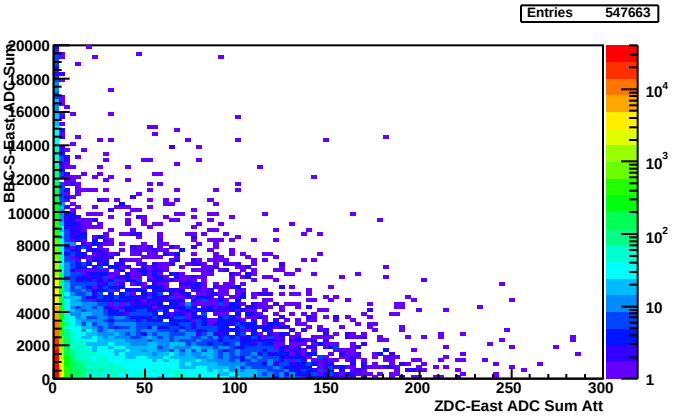

Entries 547663
TOF Mult
ZDC-West ADC Sum Att

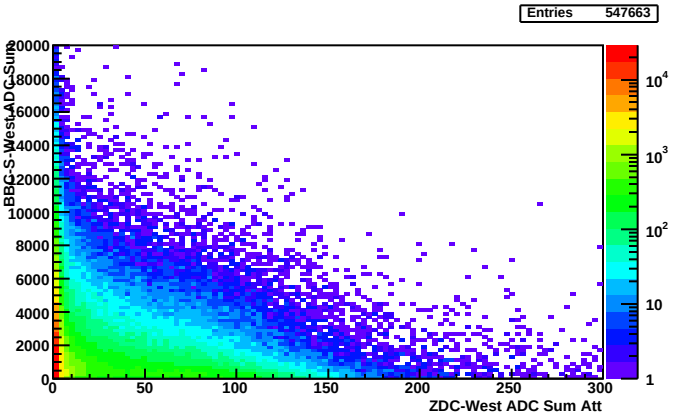

Entries 547663
BBC-L-East ADC Sum
BBC-S-East ADC Sum

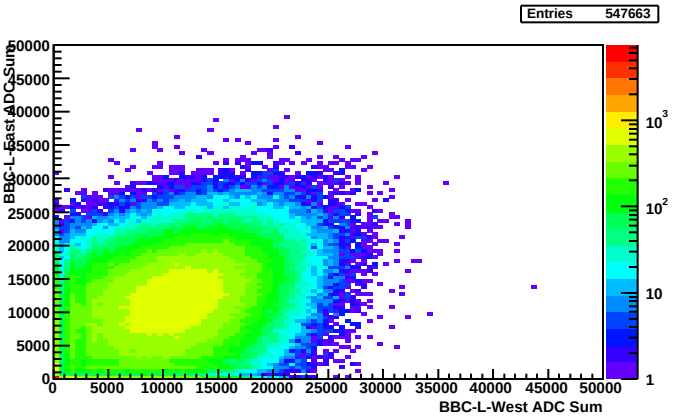

Entries 547663
BBC-L-West ADC Sum
BBC-S-West ADC Sum

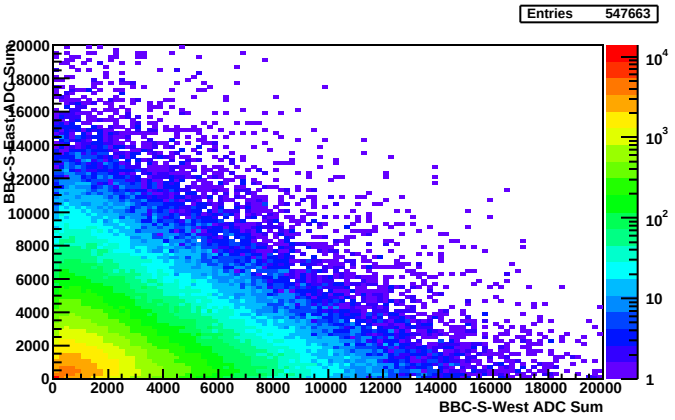

Entries 547663
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

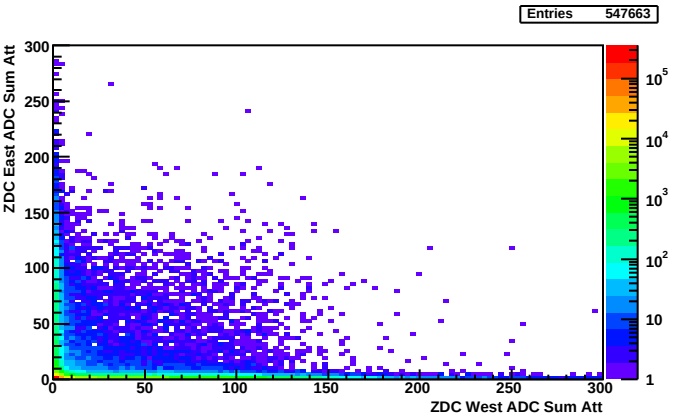

Entries 547663
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

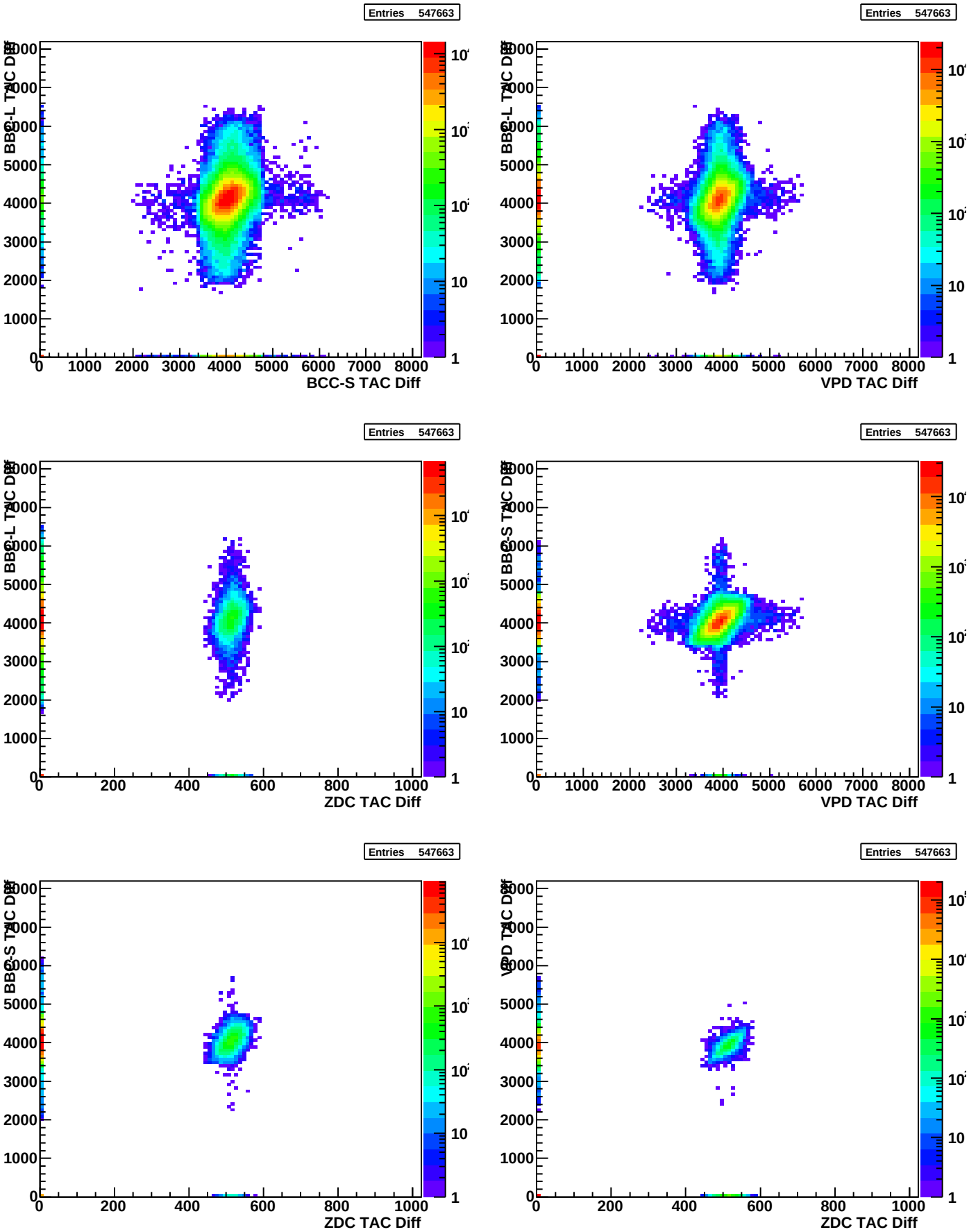

Entries 547663
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

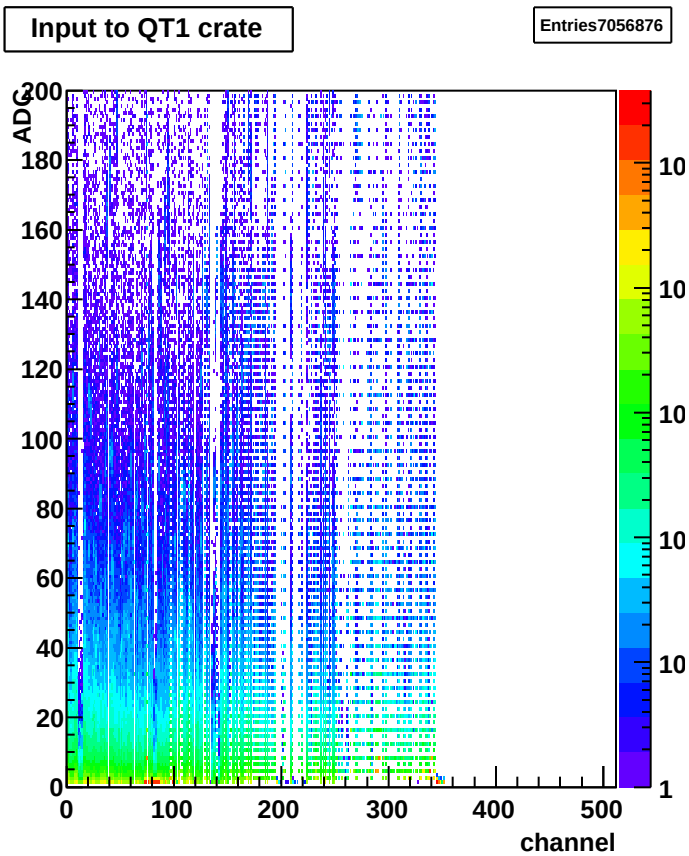

Entries 547663
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

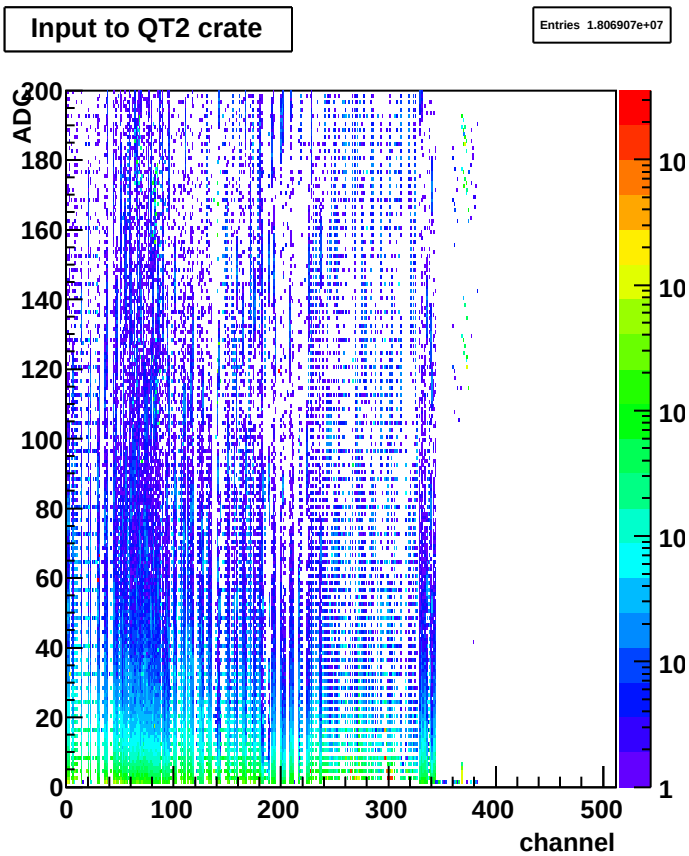

Entries 547663
BBC-L-East ADC Sum
BBC-L-West ADC Sum

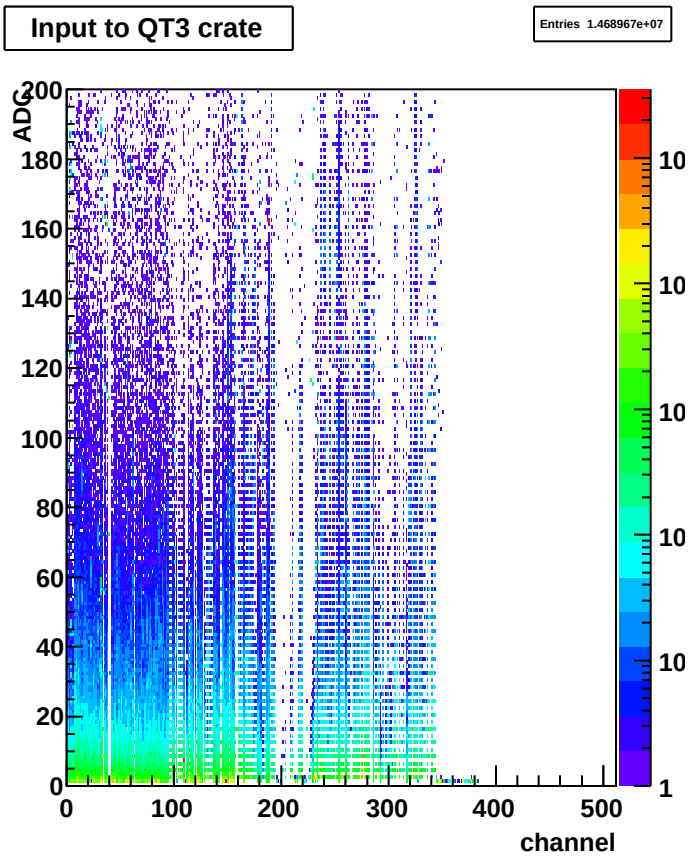

Entries 547663
BBC-S-East ADC Sum
BBC-S-West ADC Sum

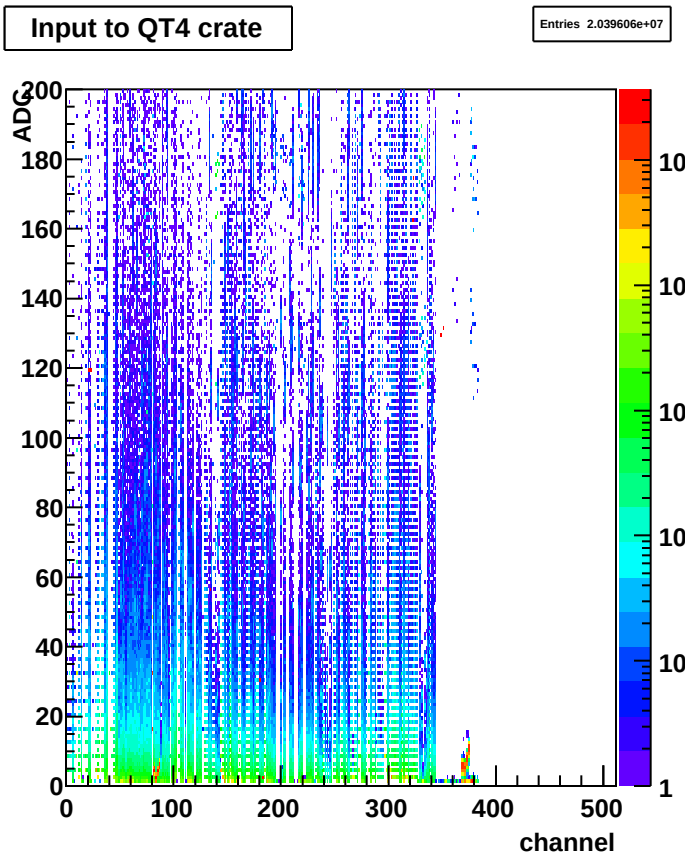

Entries 547663
ZDC East ADC Sum Att
ZDC West ADC Sum Att

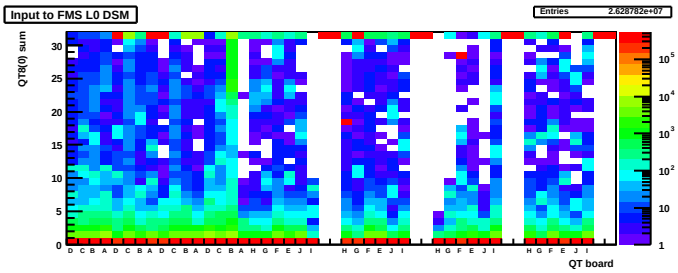

Entries 547663
BBC-L TAC Diff
BCC-S TAC Diff
Entries 547663
BBC-L TAC Diff
VPD TAC Diff
Entries 547663
BBC-L TAC Diff
ZDC TAC Diff
Entries 547663
BBC-S TAC Diff
VPD TAC Diff
Entries 547663
BBC-S TAC Diff
ZDC TAC Diff
Entries 547663
VPD TAC Diff
ZDC TAC Diff

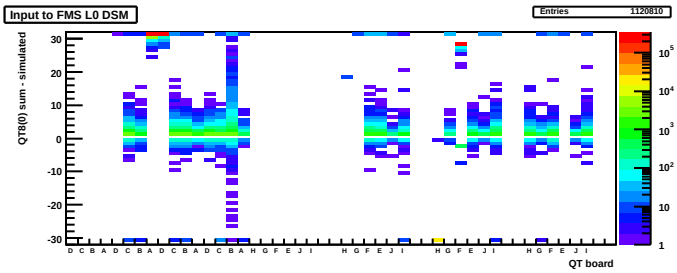

Input to QT1 crate
Entries7056876
ADC
channel

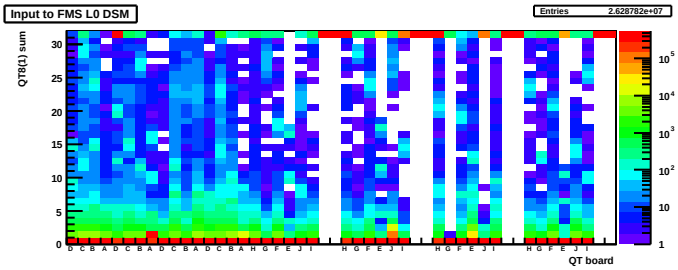

Input to QT2 crate
Entries 1.806907e+07
ADC
channel

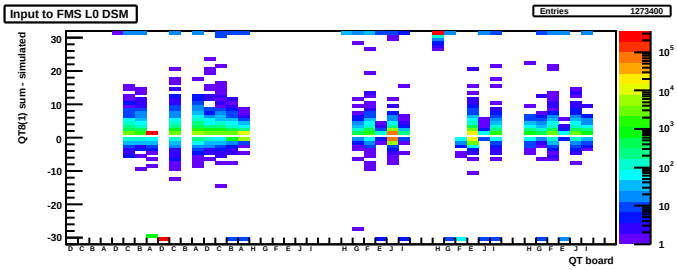

Input to QT3 crate
Entries 1.468967e+07
ADC
channel

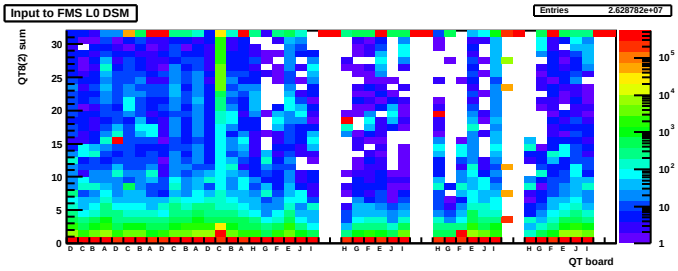

Input to QT4 crate
Entries 2.039606e+07
ADC
channel

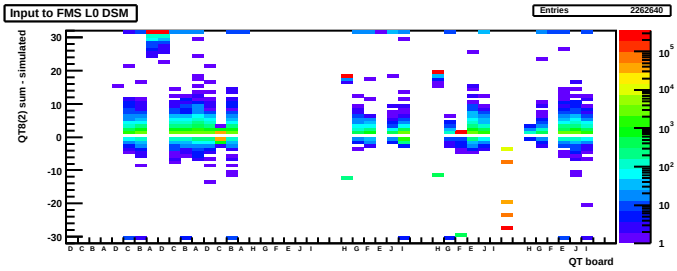

Input to FMS L0 DSM
Entries 2.628782e+07
QT8(0) sum
QT board

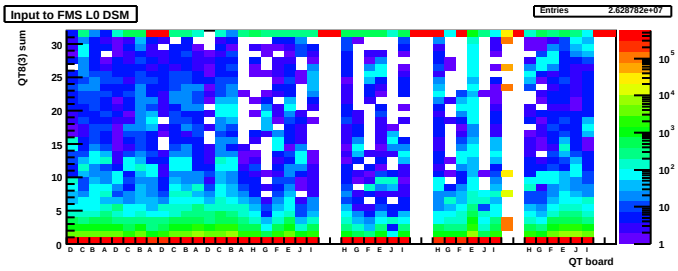

Input to FMS L0 DSM
Entries 1120810
QT8(0) sum - simulated
QT board

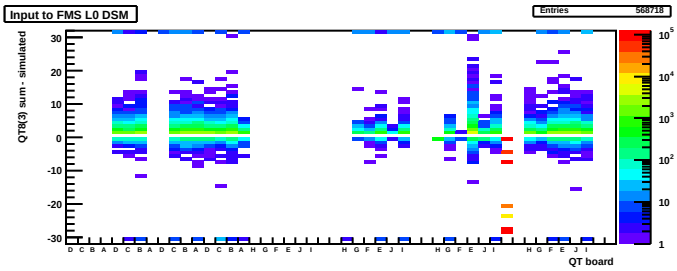

Input to FMS L0 DSM
Entries 2.628782e+07
QT8(1) sum
QT board

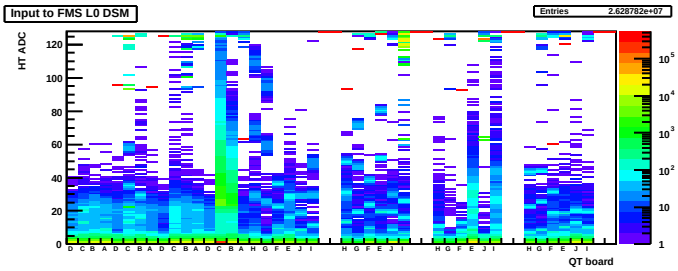

Input to FMS L0 DSM
Entries 1273400
QT8(1) sum - simulated
QT board

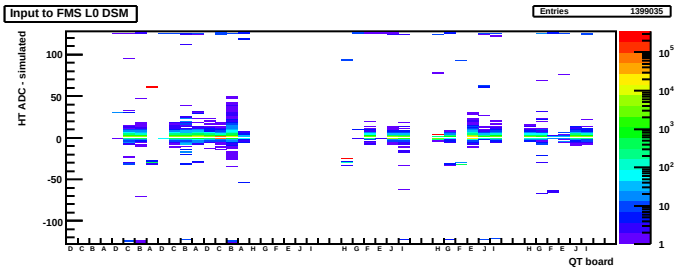

Input to FMS L0 DSM
Entries 2.628782e+07
QT8(2) sum
QT board

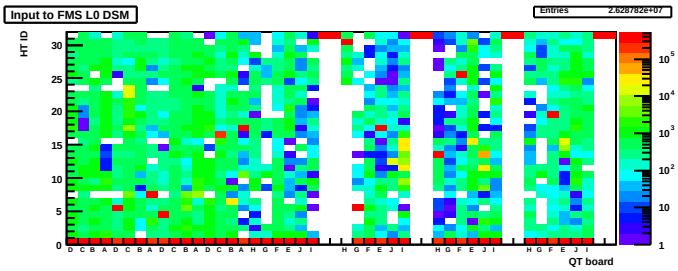

Input to FMS L0 DSM
Entries 2262640
QT8(2) sum - simulated
QT board

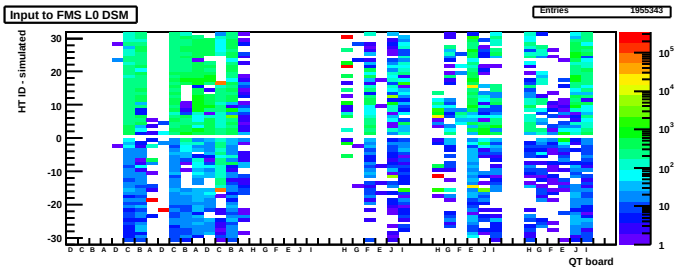

Input to FMS L0 DSM
Entries 2.628782e+07
QT8(3) sum
QT board

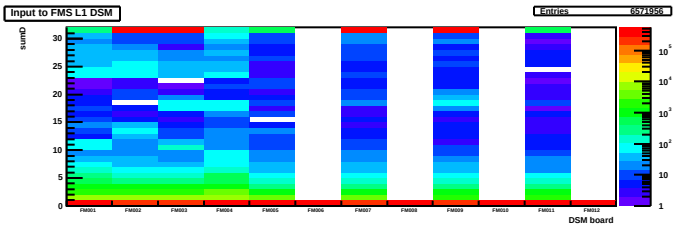

Input to FMS L0 DSM
Entries 568718
QT8(3) sum - simulated
QT board

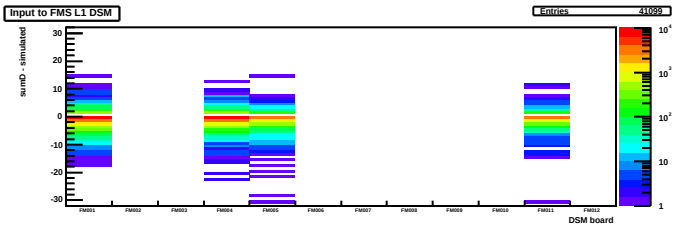

Input to FMS L0 DSM
Entries 2.628782e+07
HT ADC
QT board

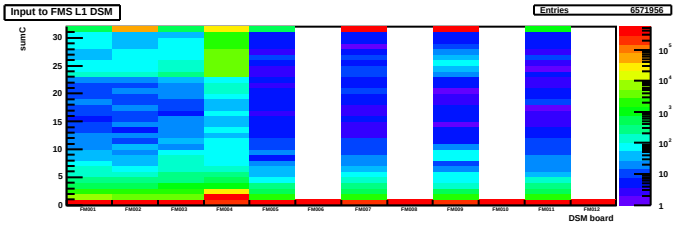

Input to FMS L0 DSM
Entries 1399035
HT ADC - simulated
QT board

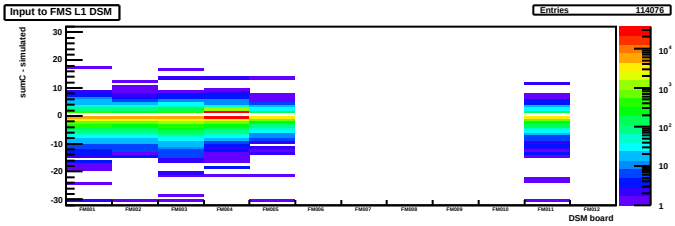

Input to FMS L0 DSM
Entries 2.628782e+07
HT ID
QT board

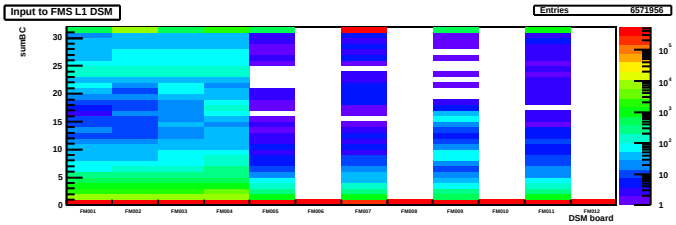

Input to FMS L0 DSM
Entries 1955343
HT ID - simulated
QT board

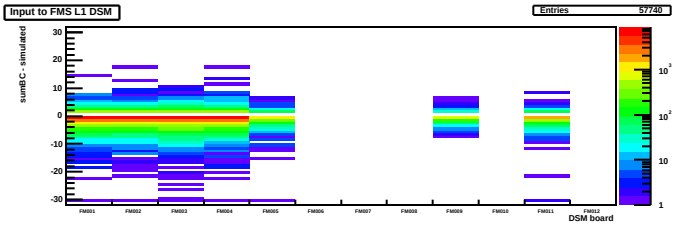

Input to FMS L1 DSM
Entries 6571956
sumD
DSM board

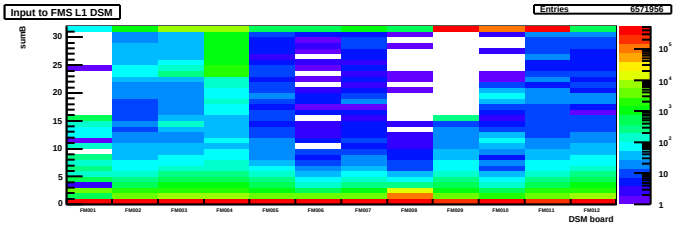

Input to FMS L1 DSM
Entries 41099
sumD - simulated
DSM board

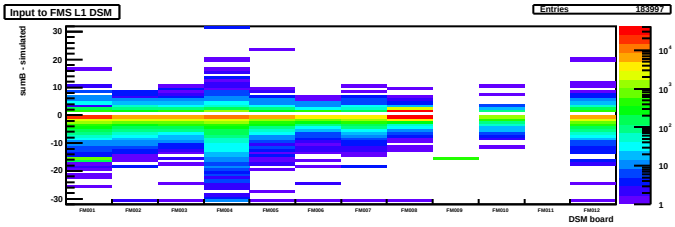

Input to FMS L1 DSM
Entries 6571956
sumC
DSM board

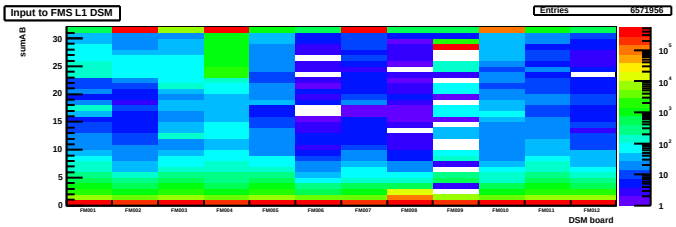

Input to FMS L1 DSM
Entries 114076
sumC - simulated
DSM board

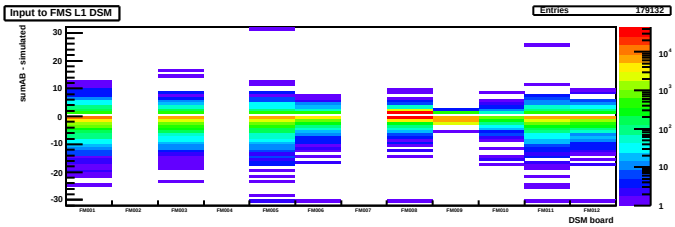

Input to FMS L1 DSM
Entries 6571956
sumBC
DSM board

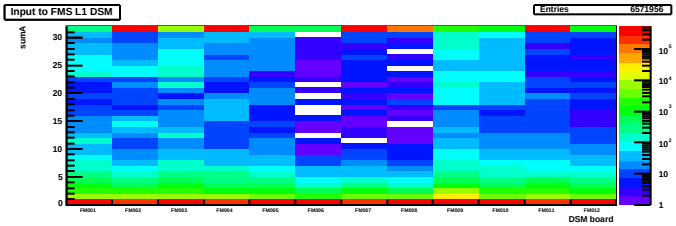

Input to FMS L1 DSM
Entries 57740
sumBC - simulated
DSM board

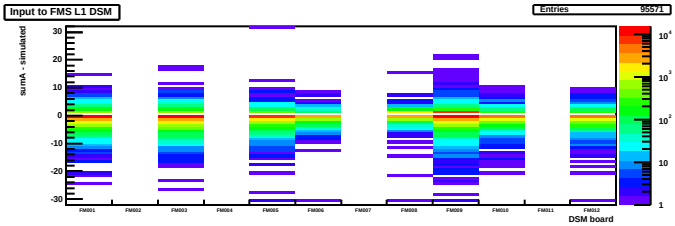

Input to FMS L1 DSM
Entries 6571956
sumB
DSM board

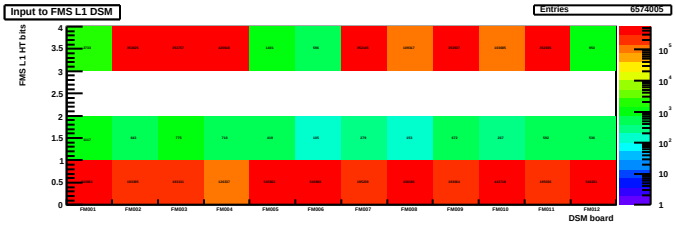

Input to FMS L1 DSM
Entries 183997
sumB - simulated
DSM board

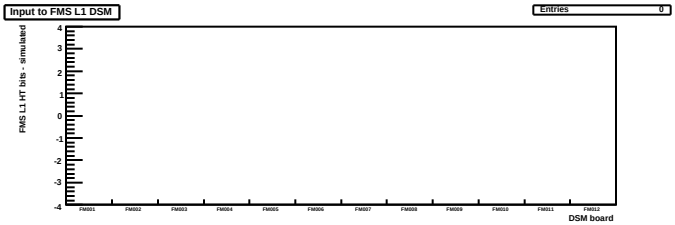

Input to FMS L1 DSM
Entries 6571956
sumAB
DSM board

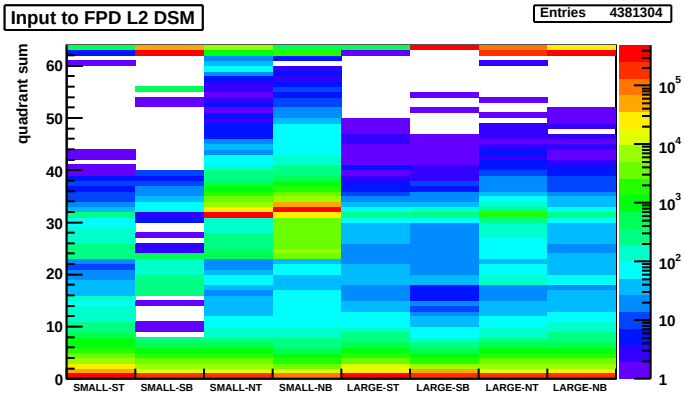

Input to FMS L1 DSM
Entries 179132
sumAB - simulated
DSM board

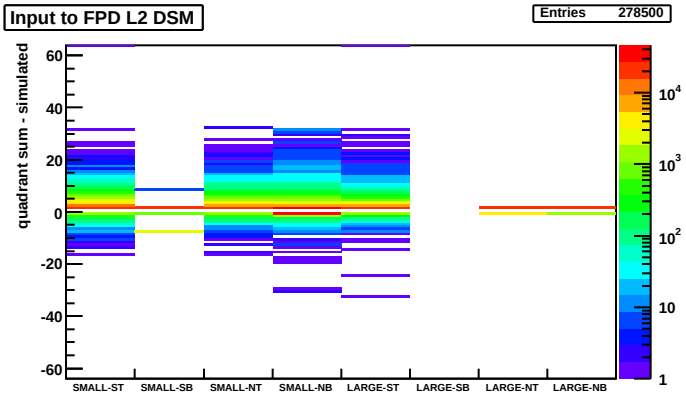

Input to FMS L1 DSM
Entries 6571956
sumA
DSM board

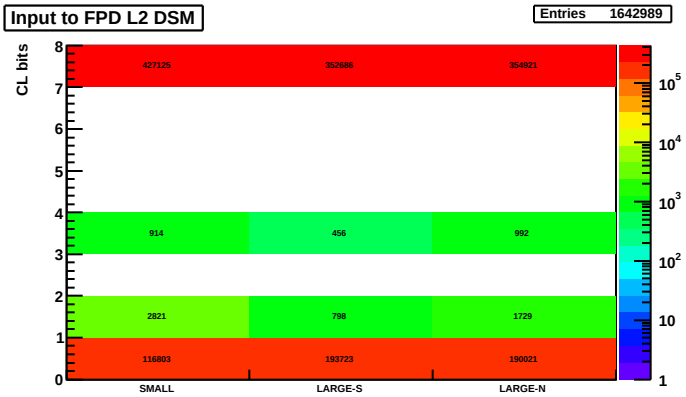

Input to FMS L1 DSM
Entries 95571
sumA - simulated
DSM board

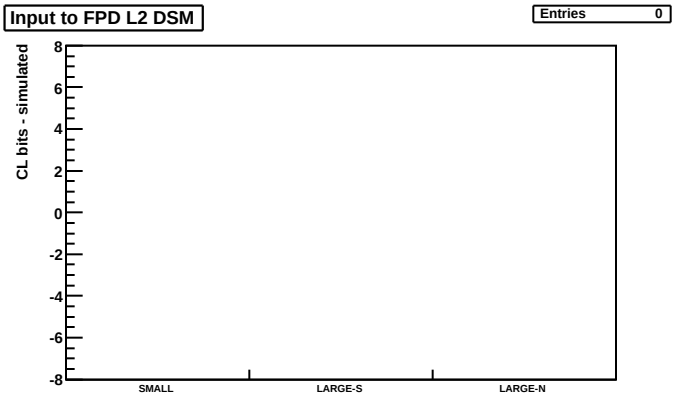

Input to FMS L1 DSM
Entries 6574005
FMS L1 HT bits
DSM board

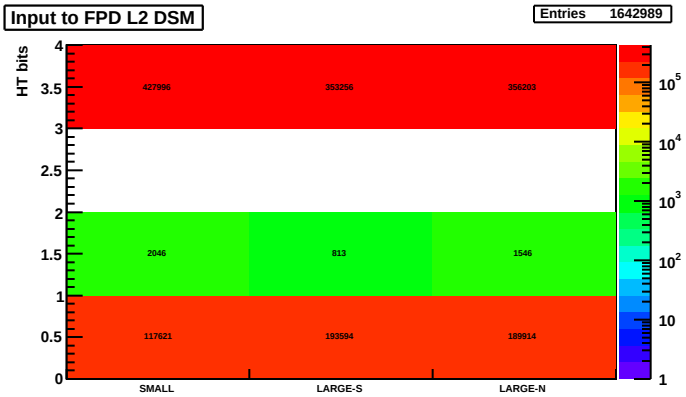

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

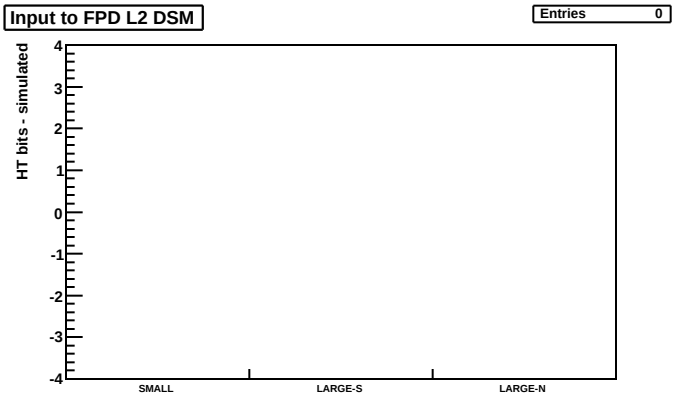

Input to FPD L2 DSM
Entries 4381304
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

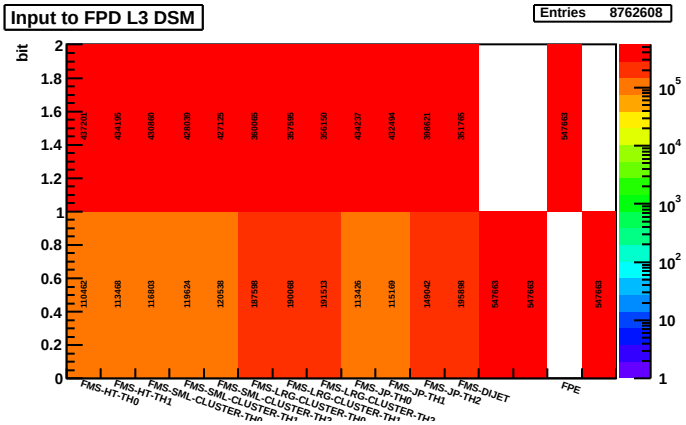

Input to FPD L2 DSM
Entries 278500
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

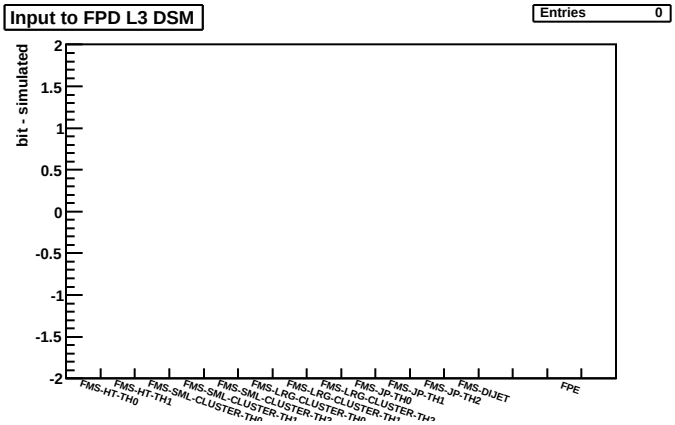

Input to FPD L2 DSM
Entries 1642989
CL bits
427125
352686
354921
914
456
992
2821
798
1729
116803
193723
190021
SMALL
LARGE-S
LARGE-N

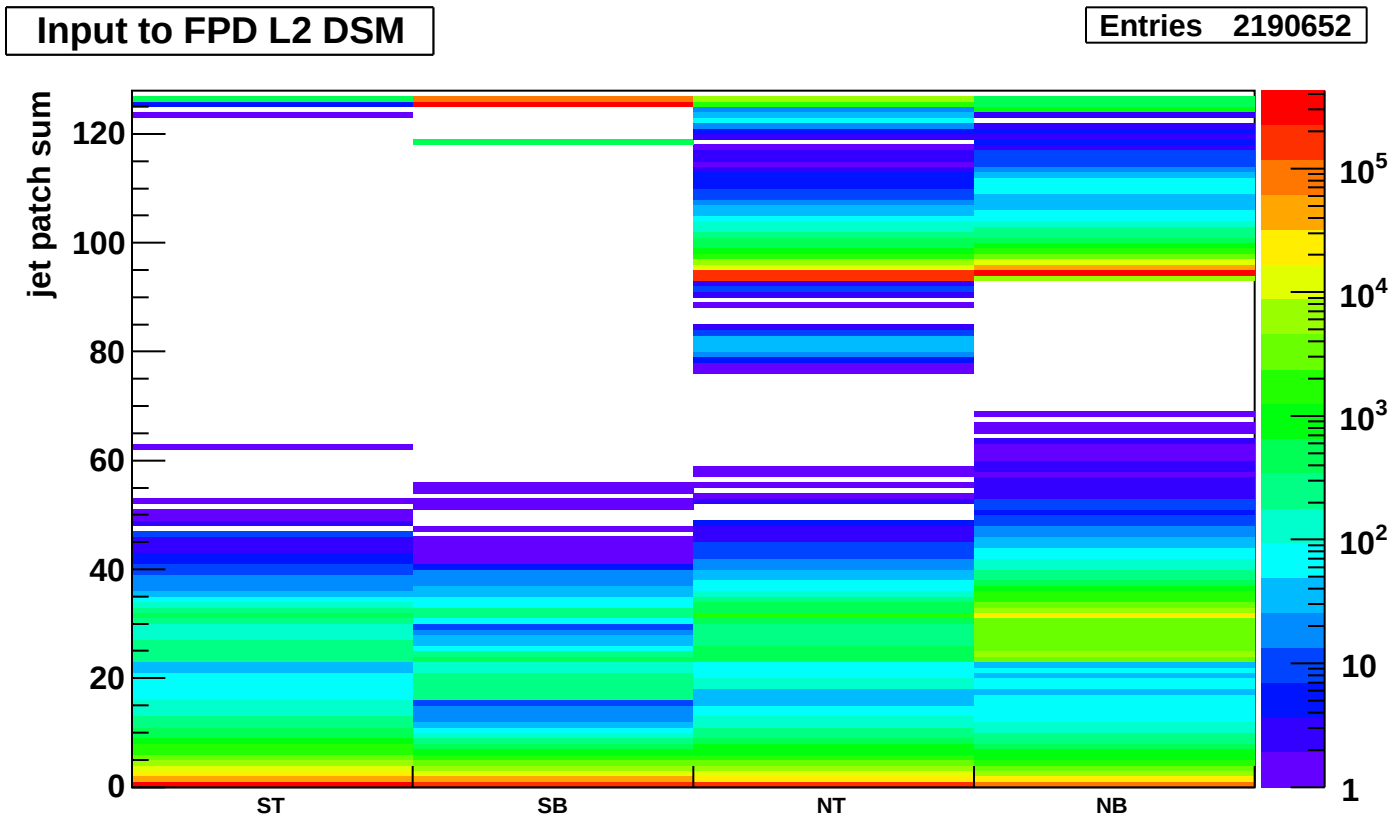

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

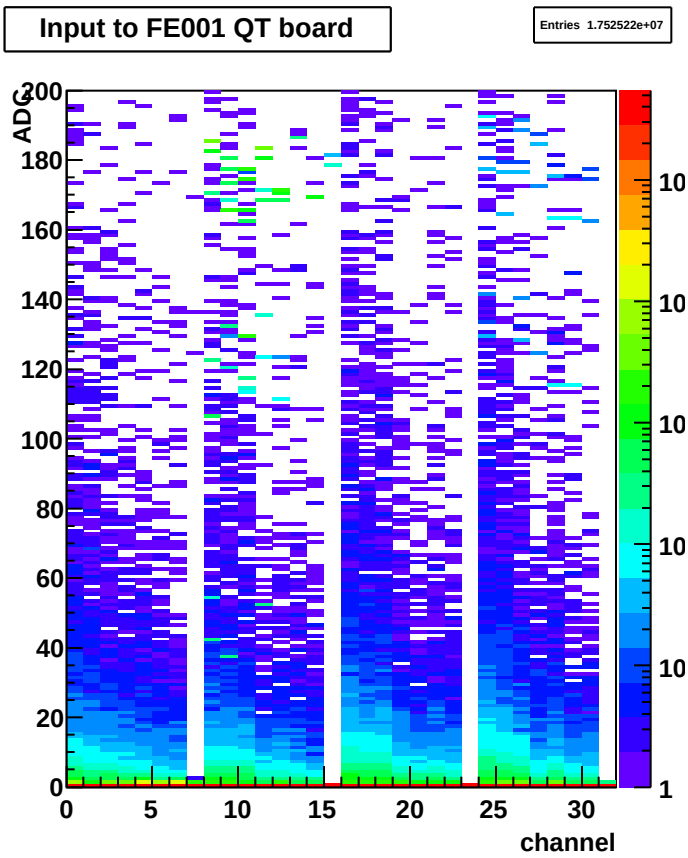

Input to FPD L2 DSM
Entries 1642989
HT bits
427996
353256
356203
2046
813
1546
117621
193594
189914
SMALL
LARGE-S
LARGE-N

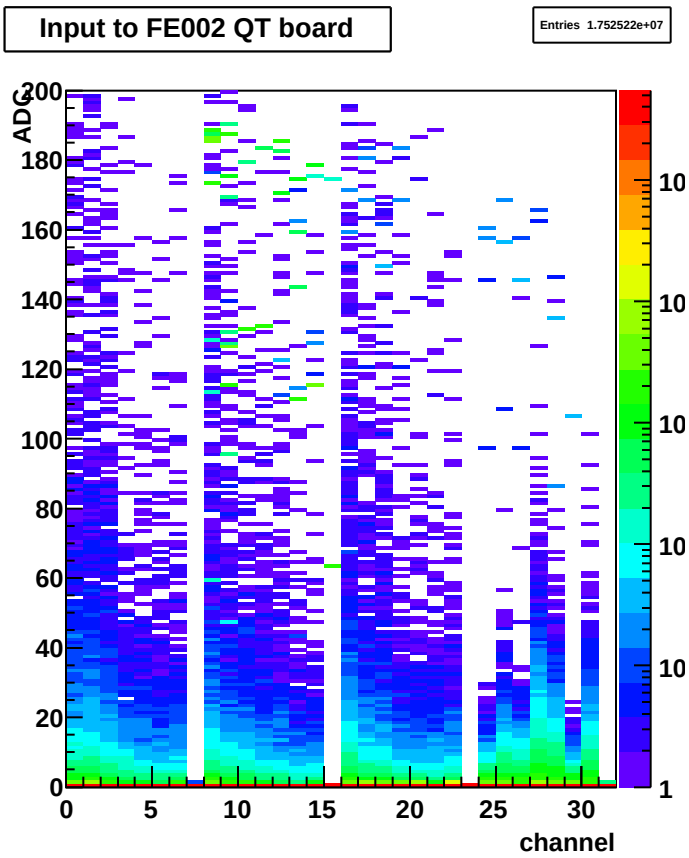

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

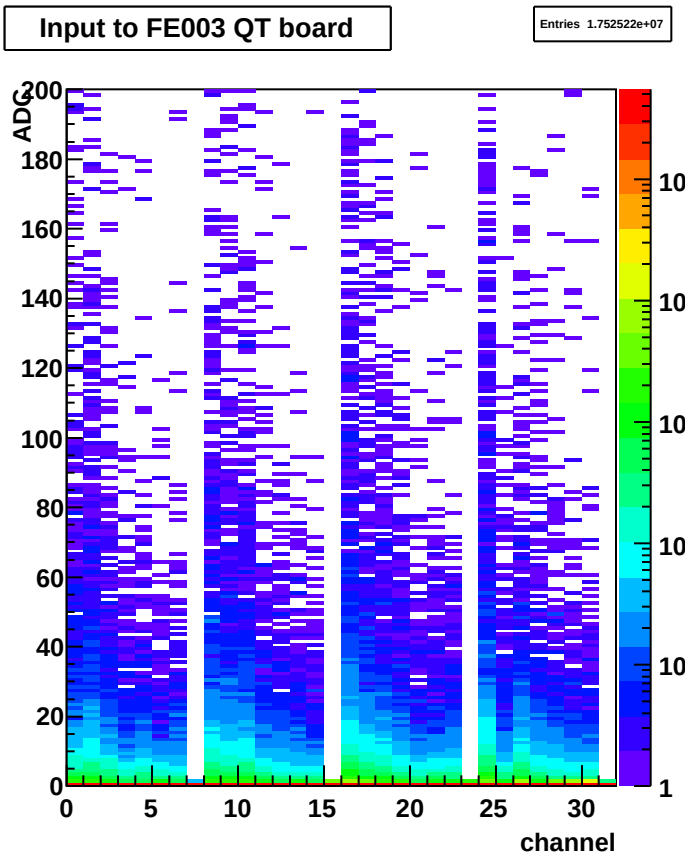

Input to FPD L3 DSM
Entries 8762608
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

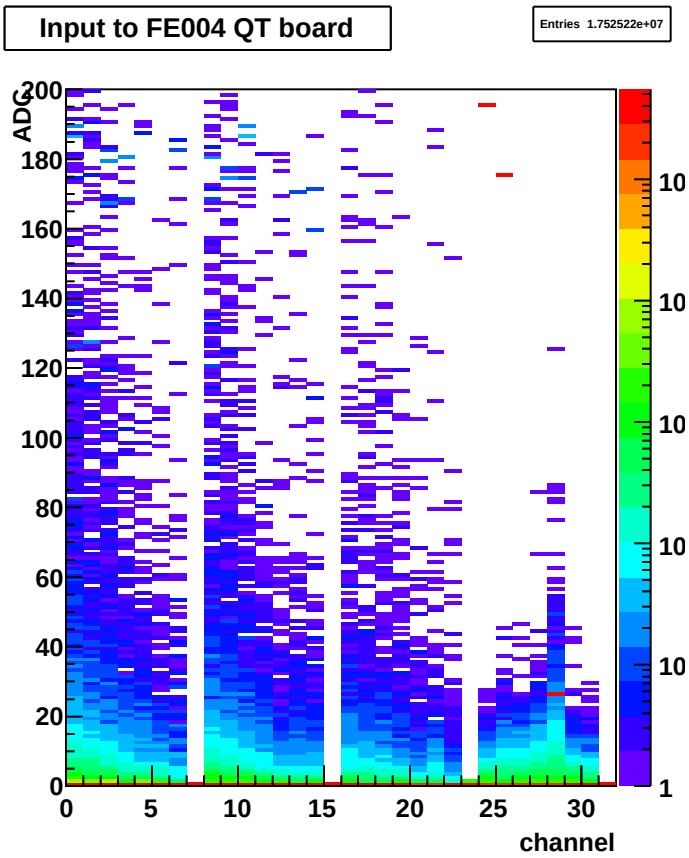

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

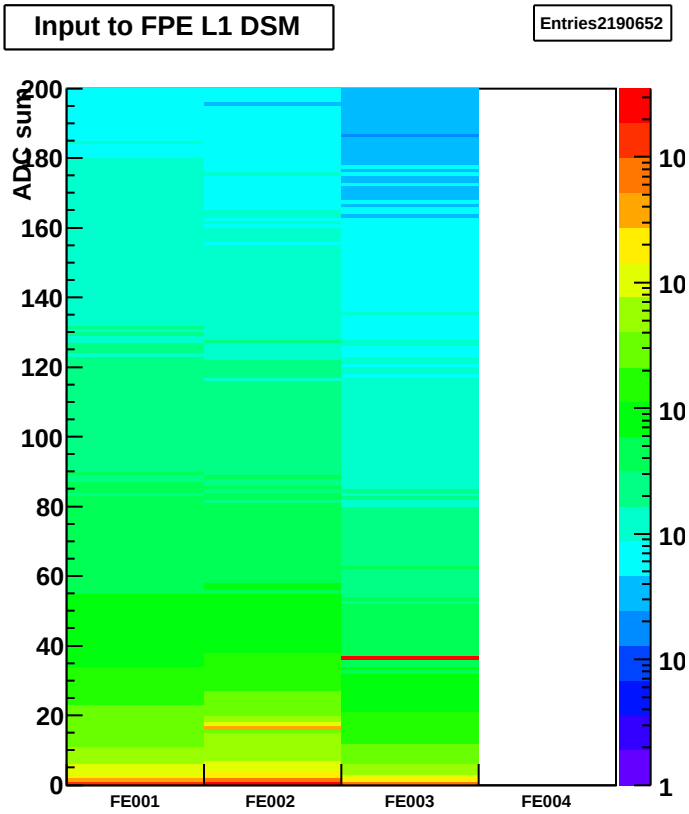

Input to FPD L2 DSM
Entries 2190652
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

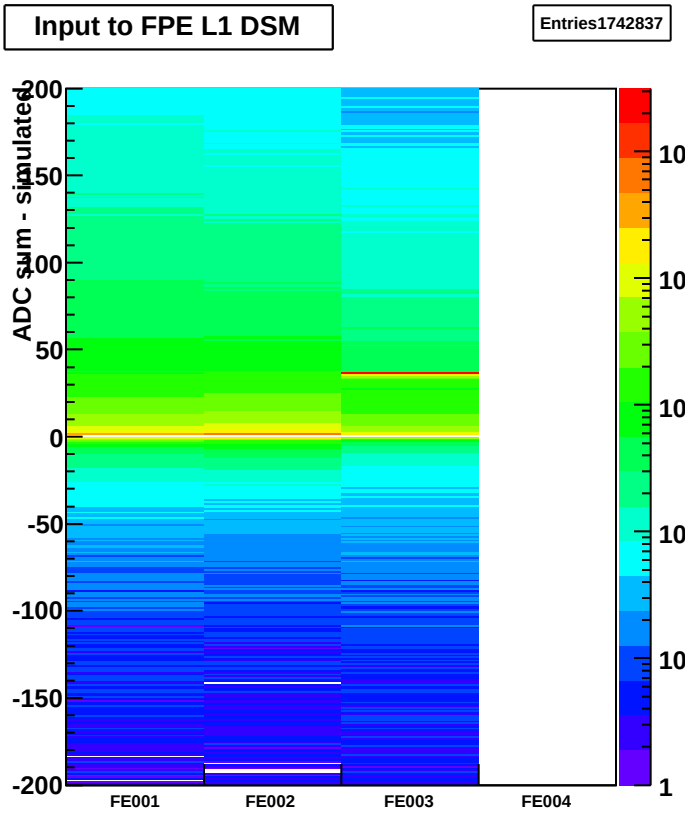

Input to FE001 QT board
Entries 1.752522e+07
ADC
channel

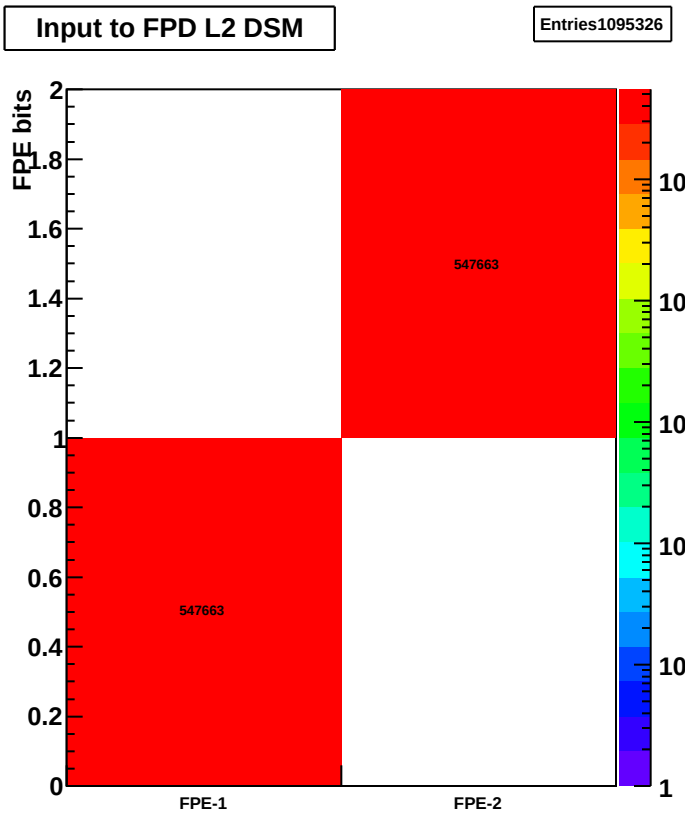

Input to FE002 QT board
Entries 1.752522e+07
ADC
channel

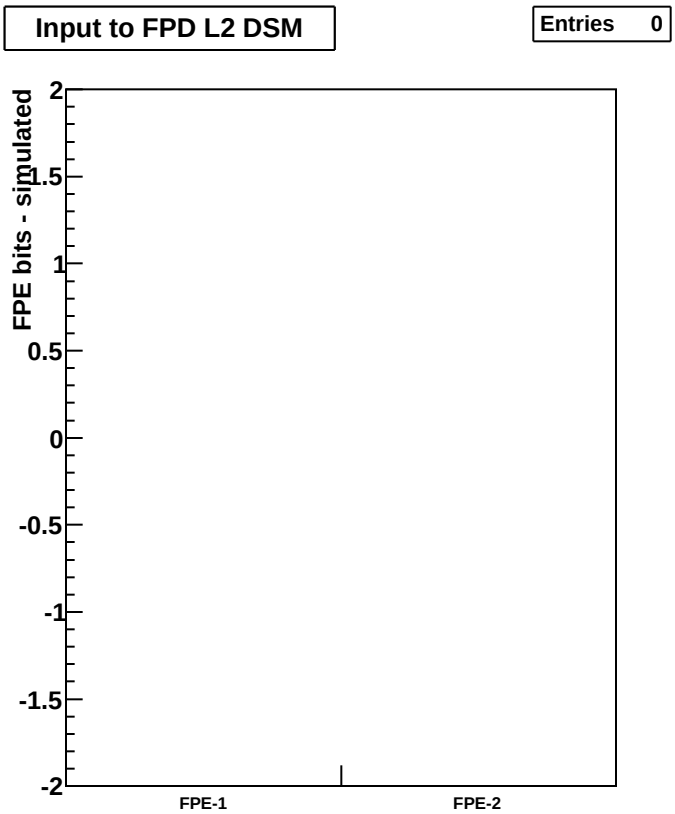

Input to FE003 QT board
Entries 1.752522e+07
ADC
channel


Input to FE004 QT board
Entries 1.752522e+07
ADC
channel


Input to FPE L1 DSM
Entries2190652
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1742837
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1095326
FPE bits
547663
547663
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2