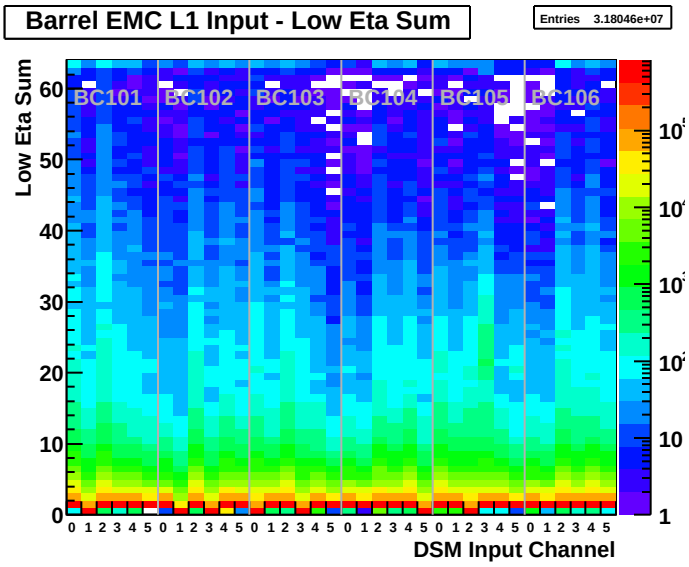
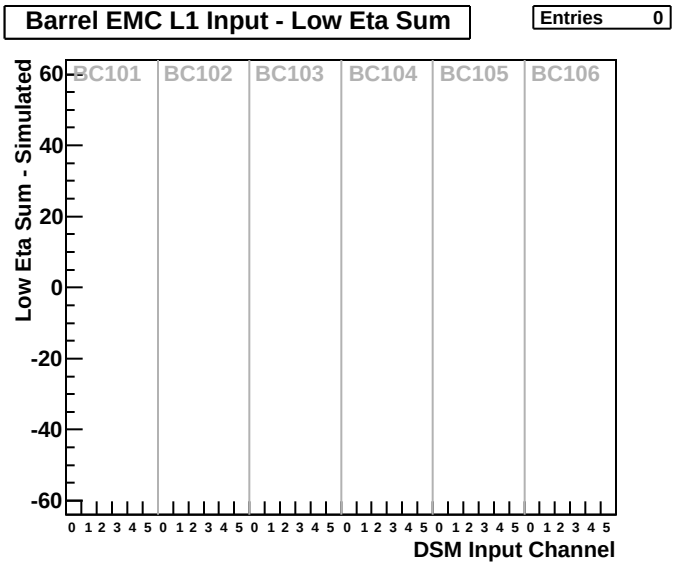
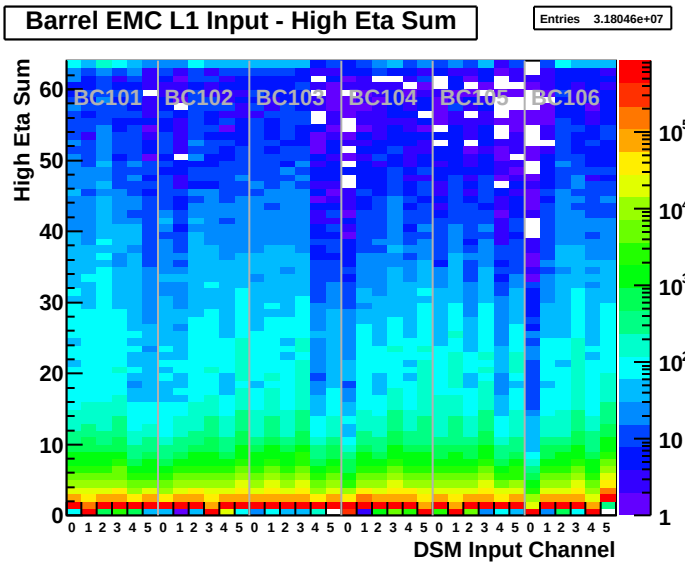
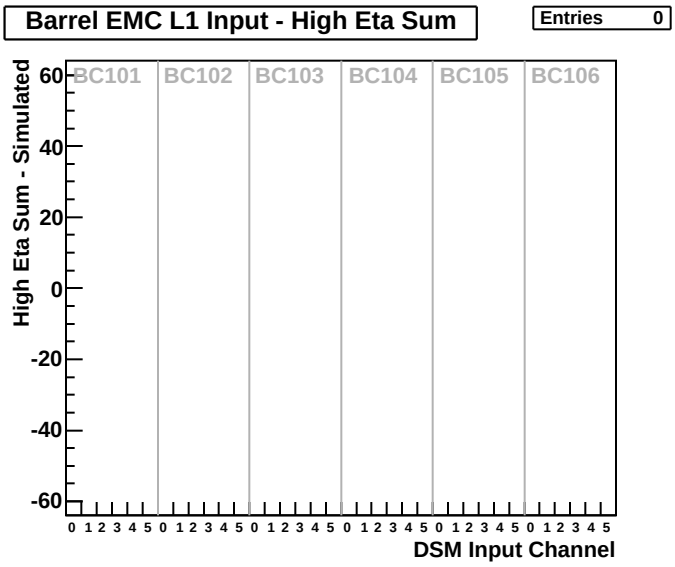
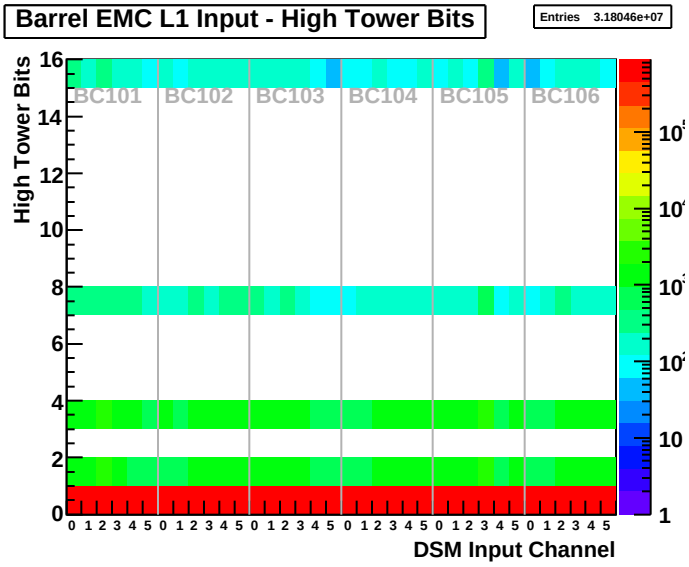
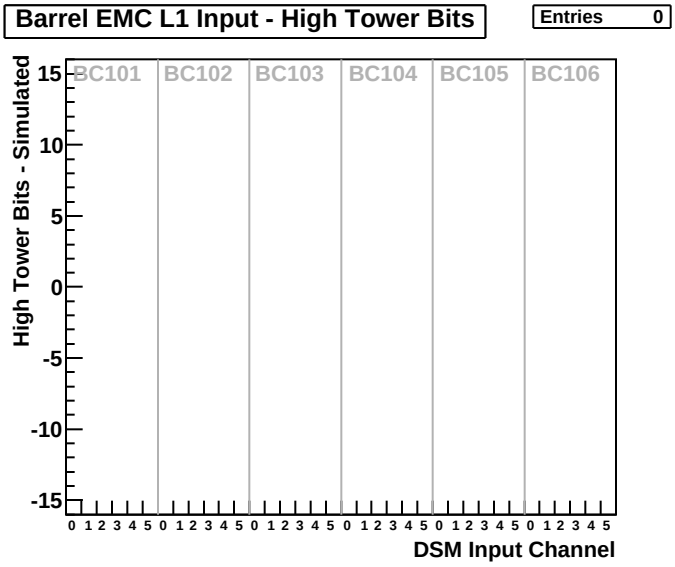

Barrel EMC L1 Input - Low Eta Sum
Entries 3.18046e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

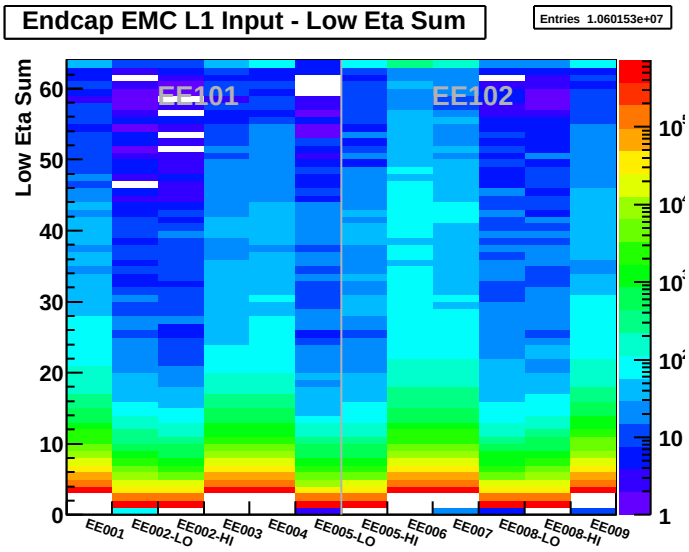

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

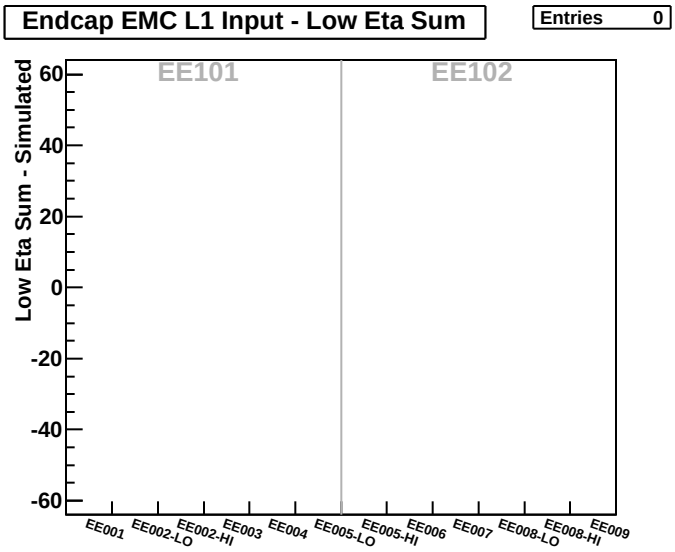

Barrel EMC L1 Input - High Eta Sum
Entries 3.18046e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

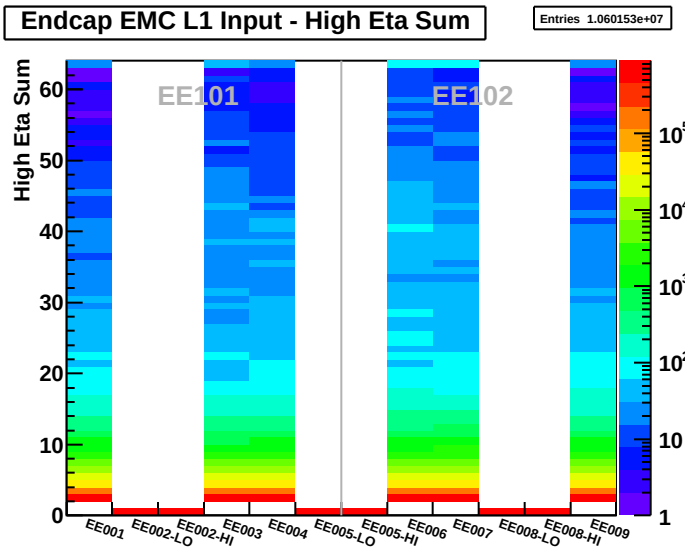

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

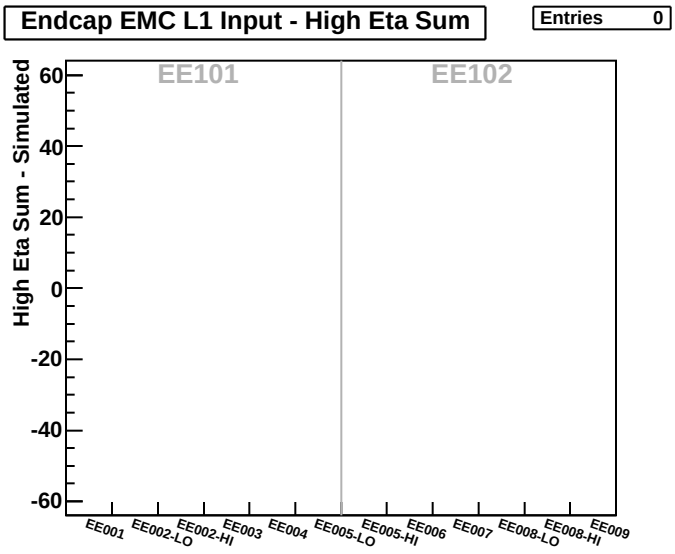

Barrel EMC L1 Input - High Tower Bits
Entries 3.18046e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

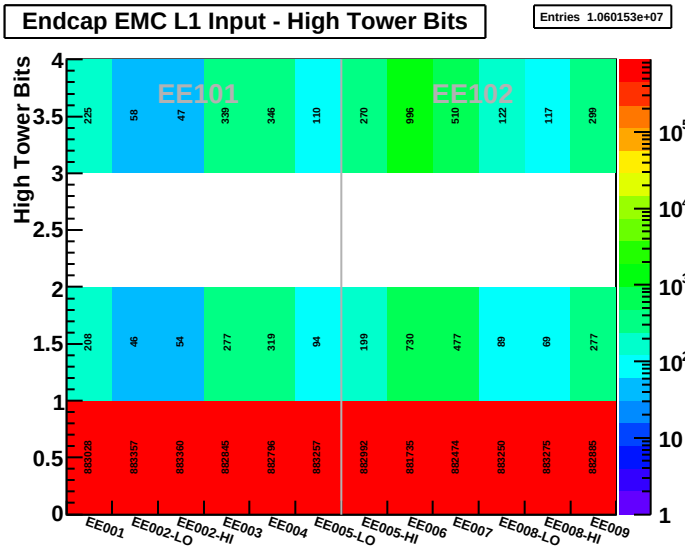

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

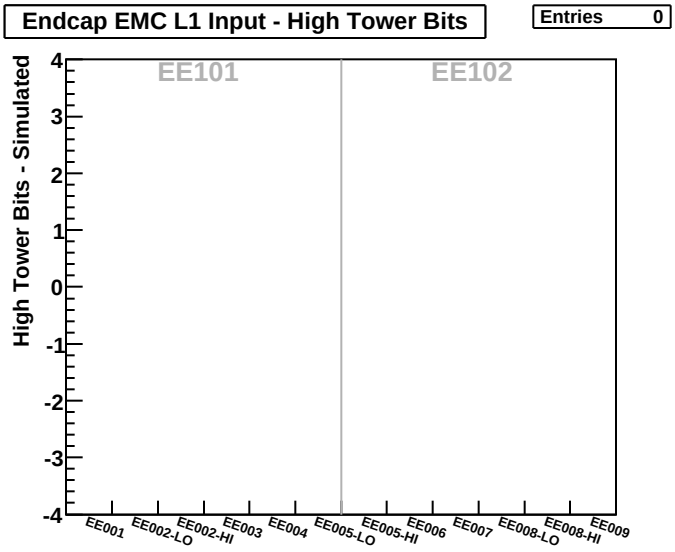

Endcap EMC L1 Input - Low Eta Sum
Entries 1.060153e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

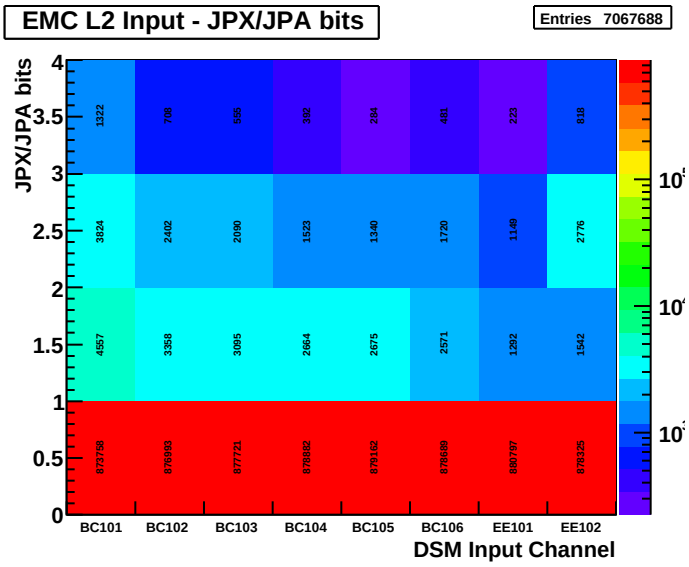

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

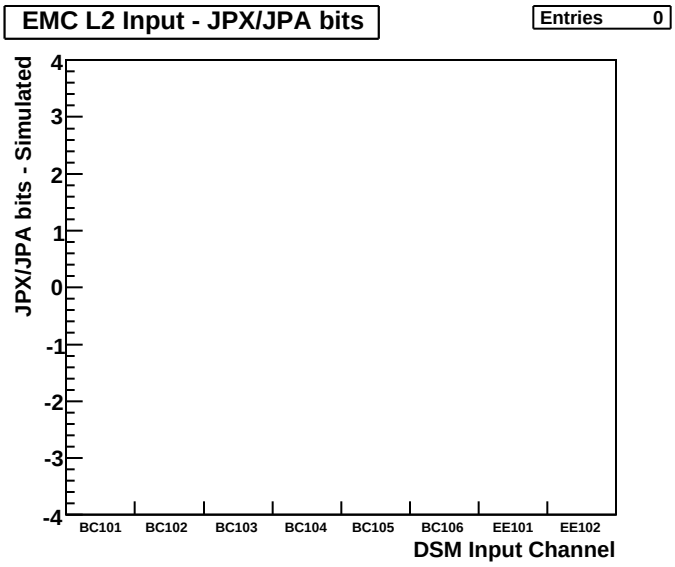

Endcap EMC L1 Input - High Eta Sum
Entries 1.060153e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

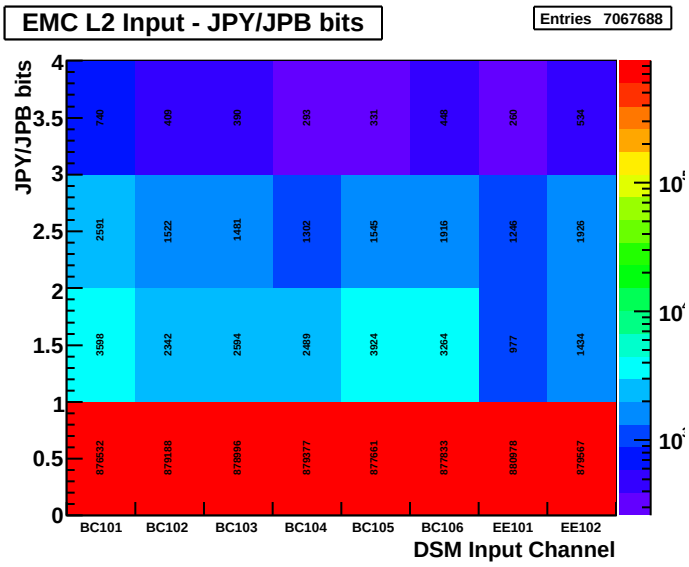

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

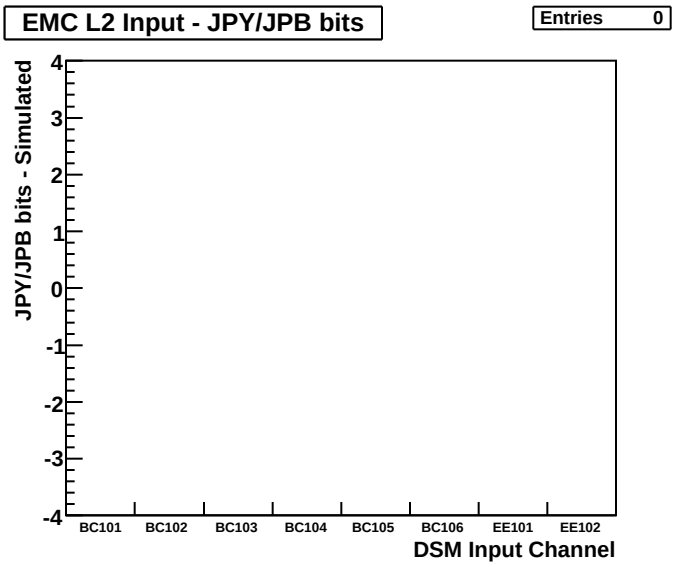

Endcap EMC L1 Input - High Tower Bits
Entries 1.060153e+07
High Tower Bits
EE101
EE102
225 58 47 339 346 110 270 996 510 122 117 299
208 46 54 277 319 94 199 730 477 89 69 277
883028 883357 883360 882845 882796 883257 882992 881795 882474 883250 883275 882885
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

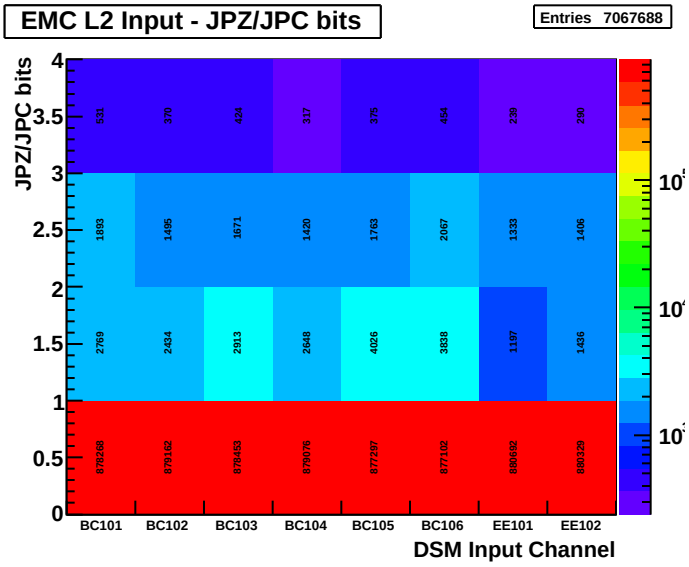

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

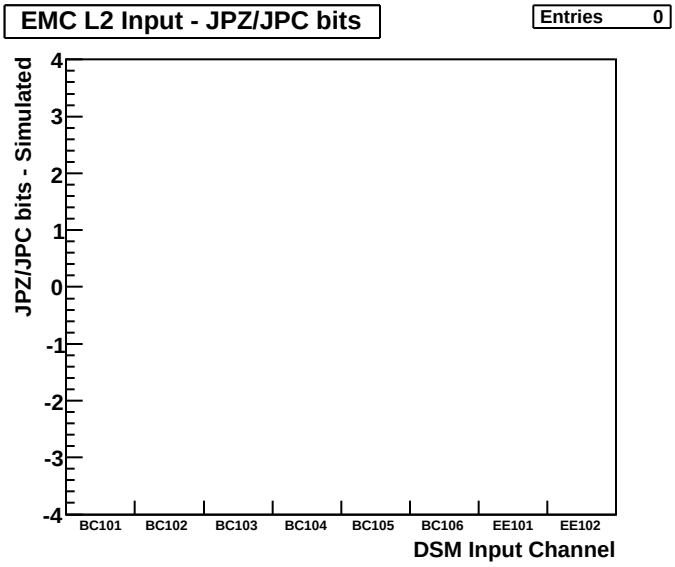

EMC L2 Input - JPX/JPA bits
Entries 7067688
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1322 708 555 392 284 481 223 818
3824 2402 2090 1523 1340 1720 1149 2776
4557 3358 3095 2664 2675 2571 1292 1542
873758 876993 877721 878882 879162 878689 880797 878325

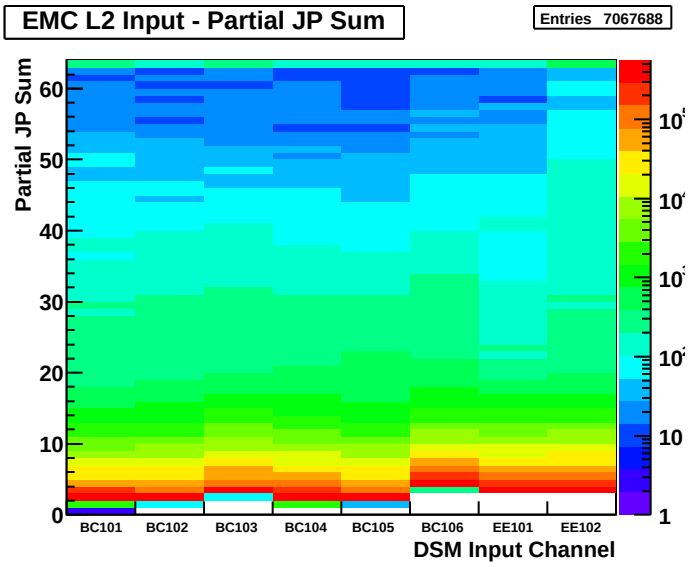

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

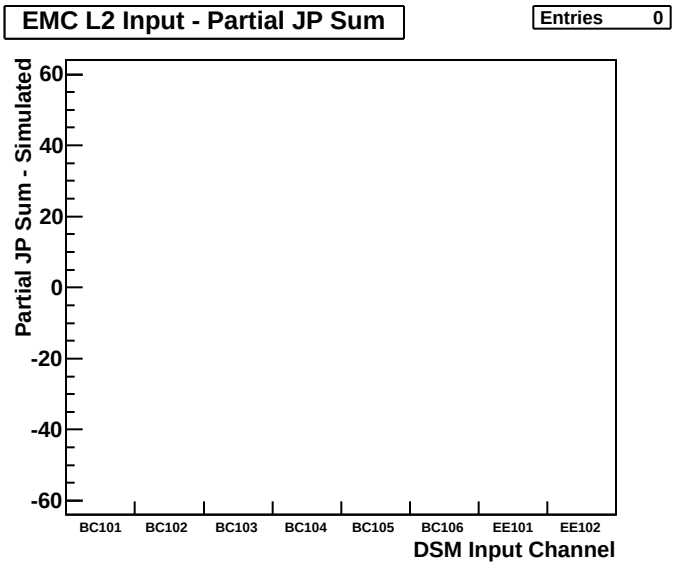

EMC L2 Input - JPY/JPB bits
Entries 7067688
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
746 409 390 293 331 448 260 534
2591 1522 1481 1302 1545 1916 1246 1926
3398 2342 2394 2489 3924 3264 977 1434
876532 878188 878896 879377 877661 877833 880978 875567

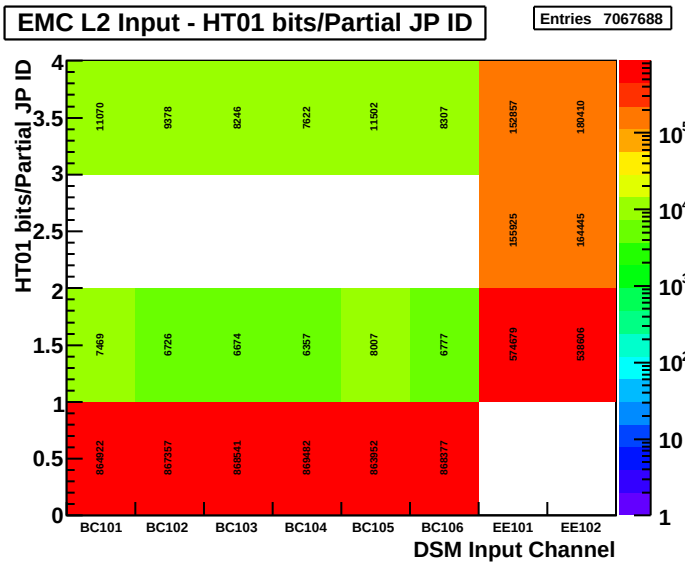

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

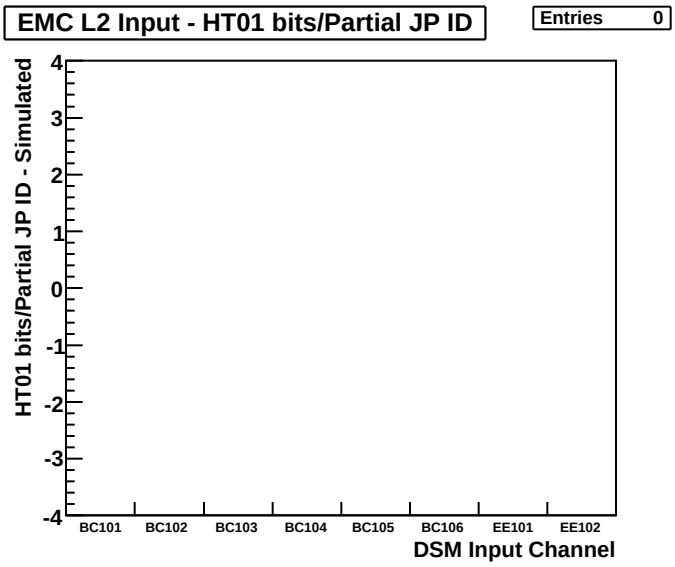

EMC L2 Input - JPZ/JPC bits
Entries 7067688
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
531 370 424 317 375 454 239 290
1893 1495 1671 1420 1763 2067 1333 1406
2769 2434 2913 2648 4026 3838 1197 1436
878268 879162 878453 879076 877297 877102 880592 880329

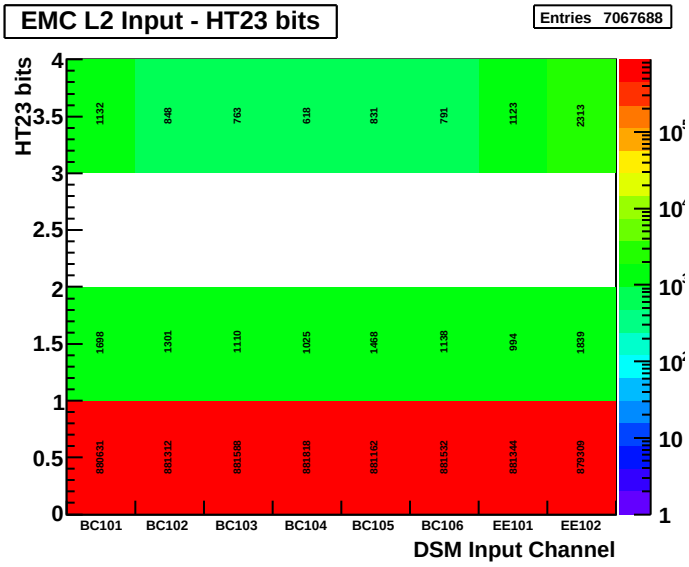

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

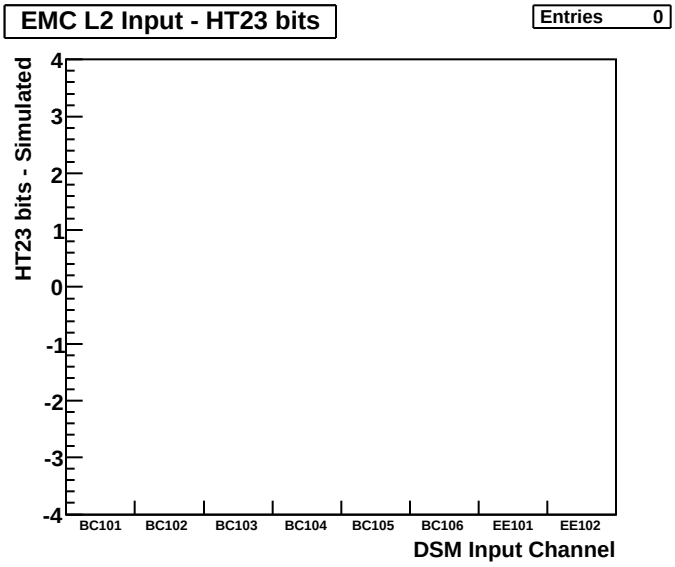

EMC L2 Input - Partial JP Sum
Entries 7067688
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

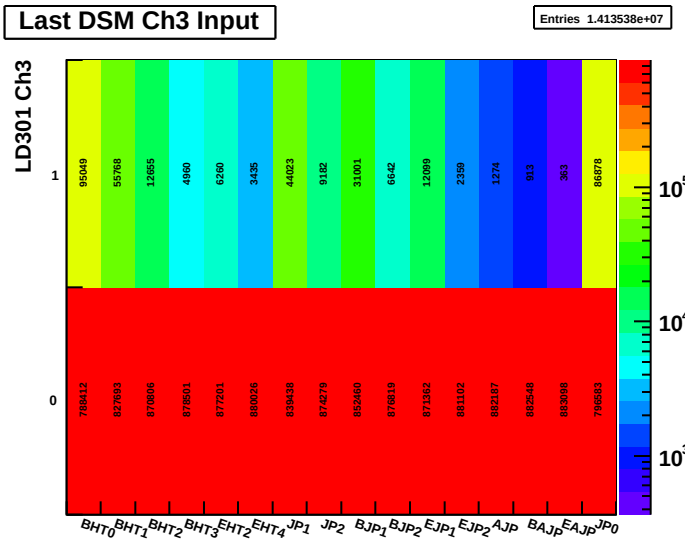

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

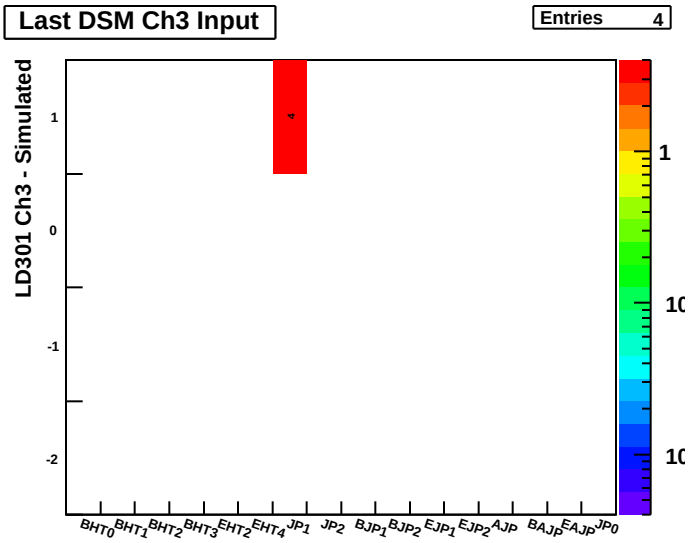

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7067688
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

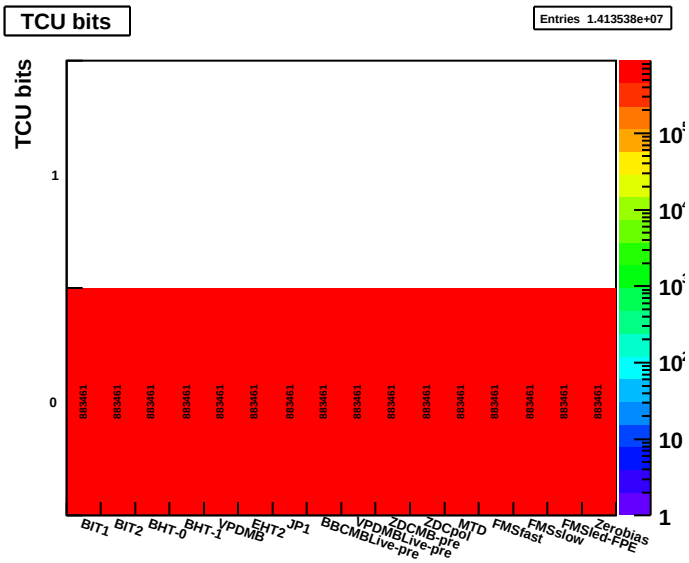

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

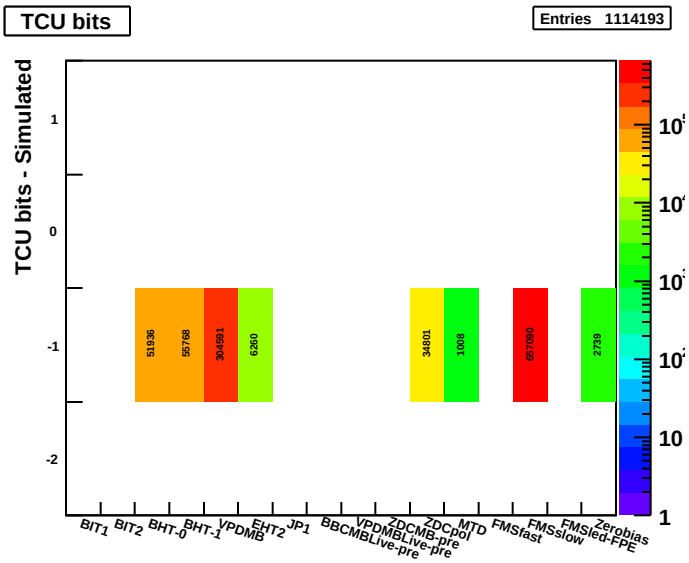

EMC L2 Input - HT23 bits
Entries 7067688
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

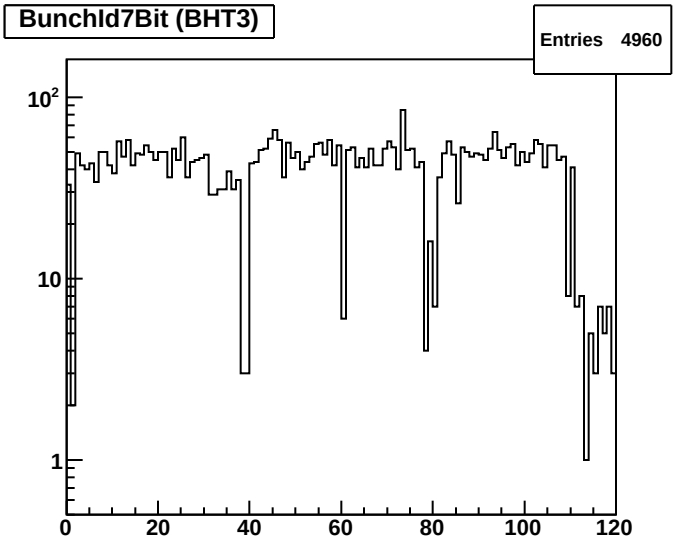

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

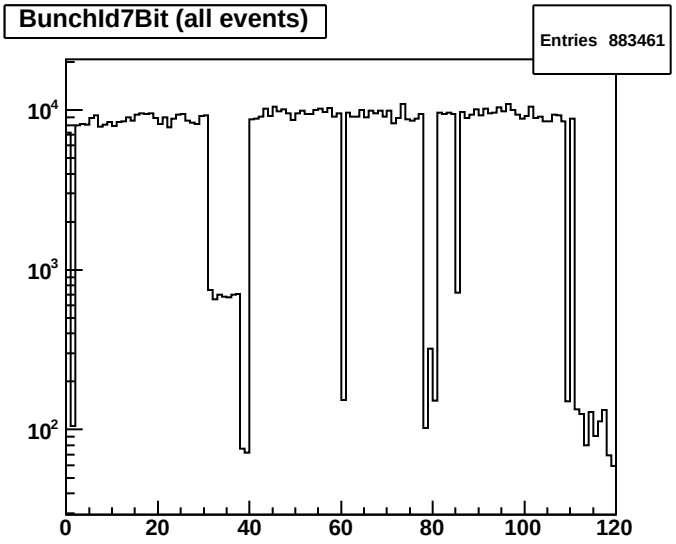

Last DSM Ch3 Input
Entries 1.413538e+07
LD301 Ch3
95049 55768 12655 4960 6260 3435 44023 9182 31001 6642 12099 2339 1274 913 363 86878
788412 827693 870806 876501 877201 880026 839438 874279 852460 876819 871362 881102 882187 882548 883098 796583
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

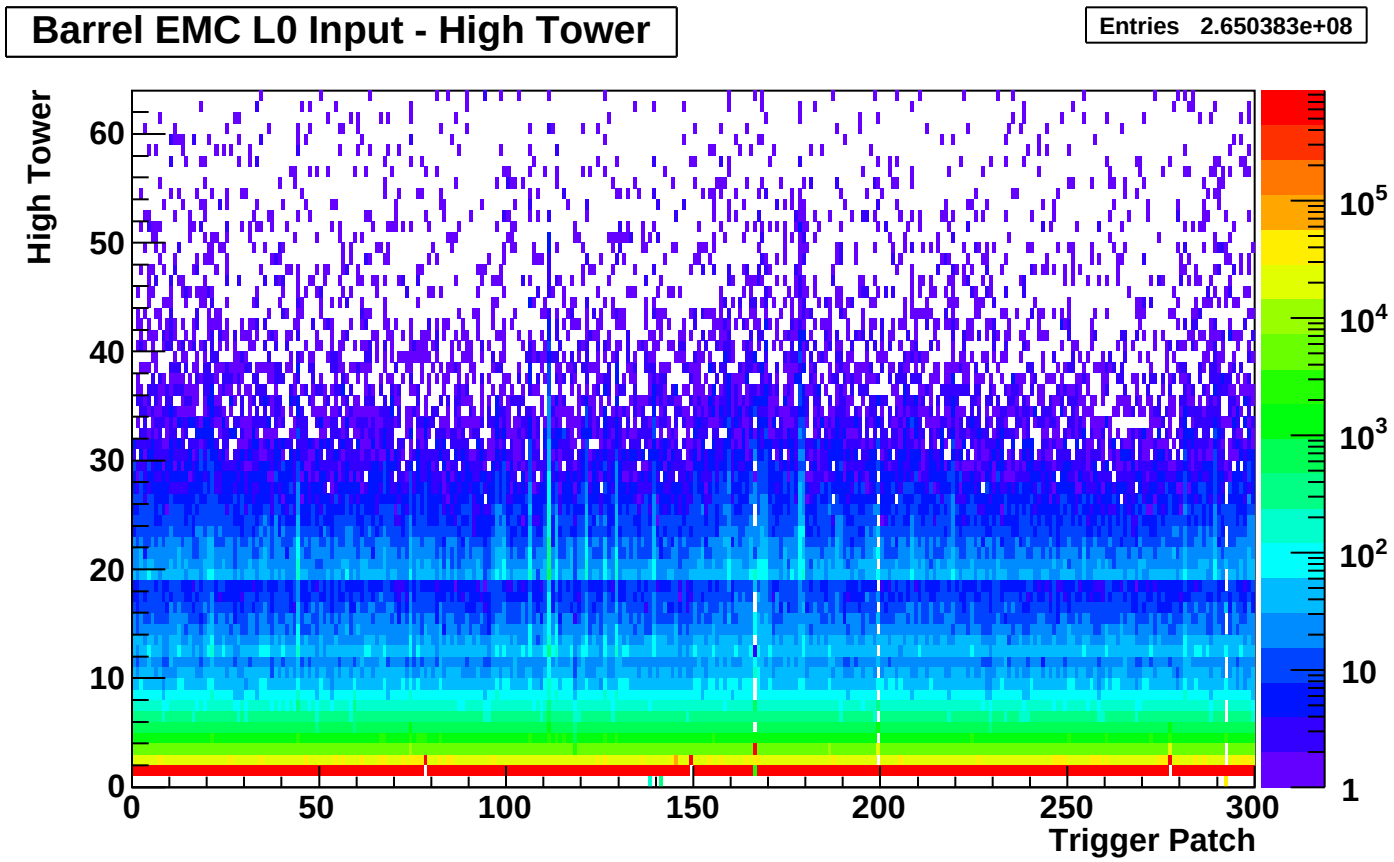

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

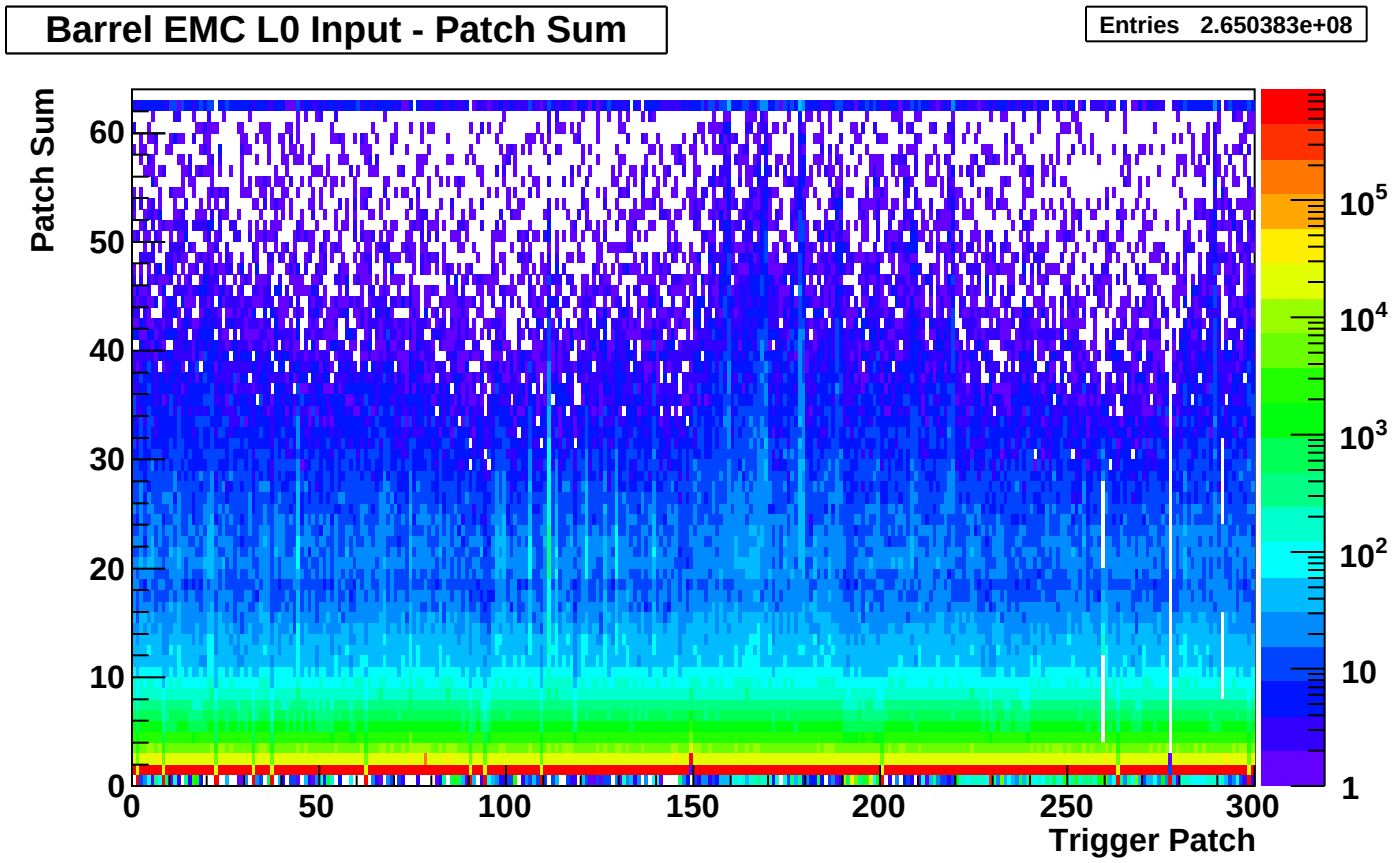

TCU bits
Entries 1.413538e+07
TCU bits
883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461 883461
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

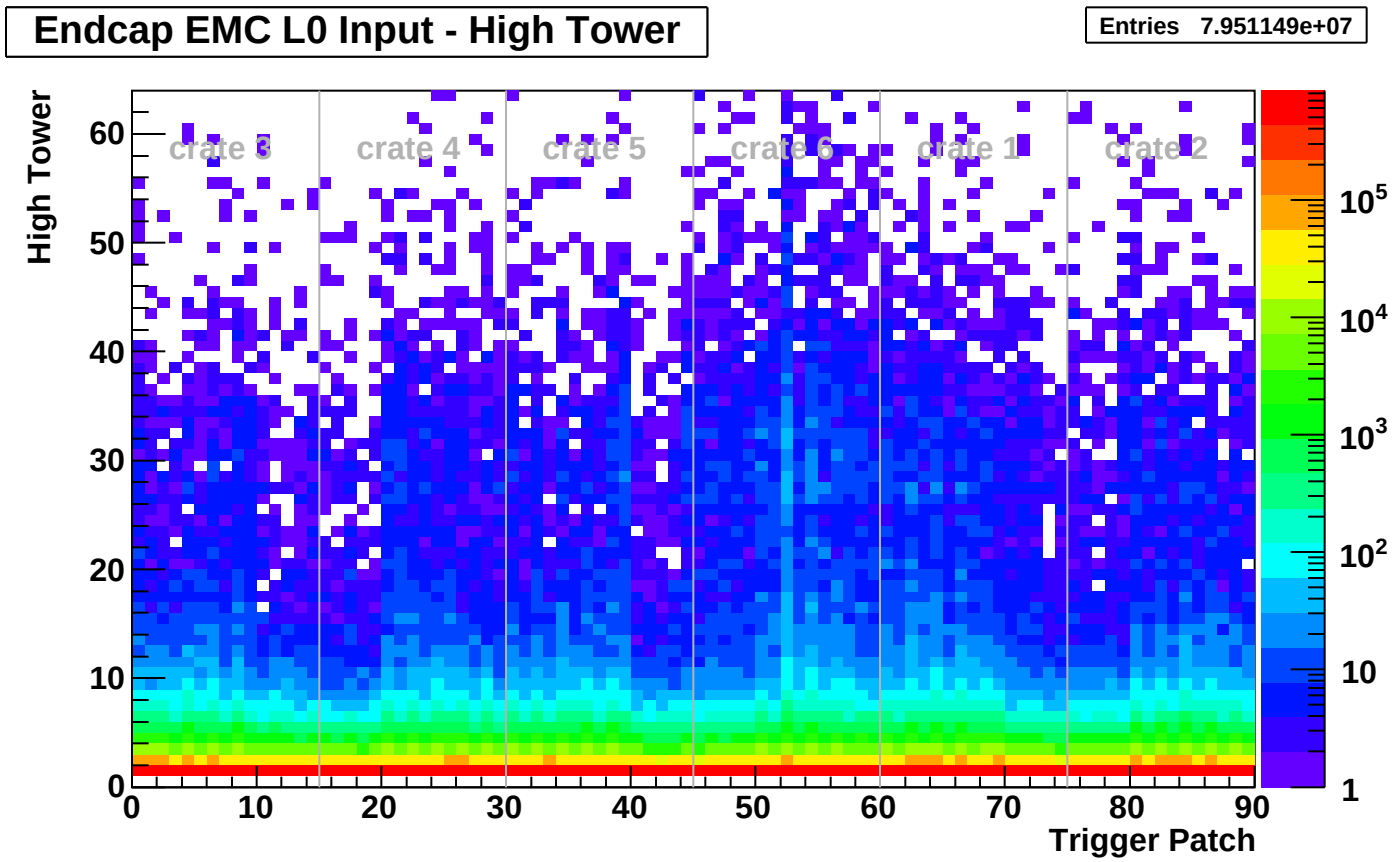

TCU bits
Entries 1114193
TCU bits - Simulated
51936 55768 304591 6260 34801 1008 657090 2739
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

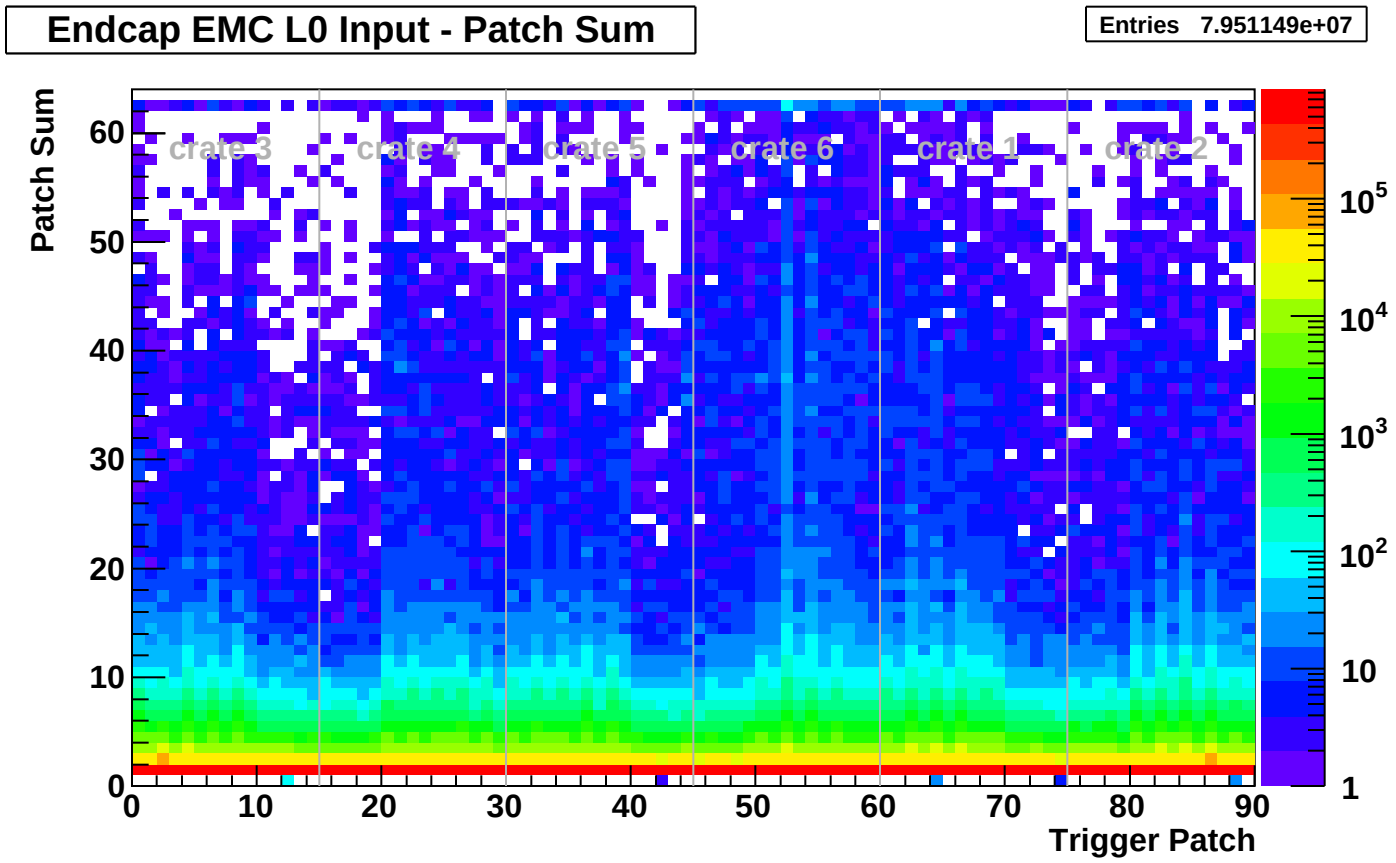

BunchId7Bit (BHT3)
Entries 4960

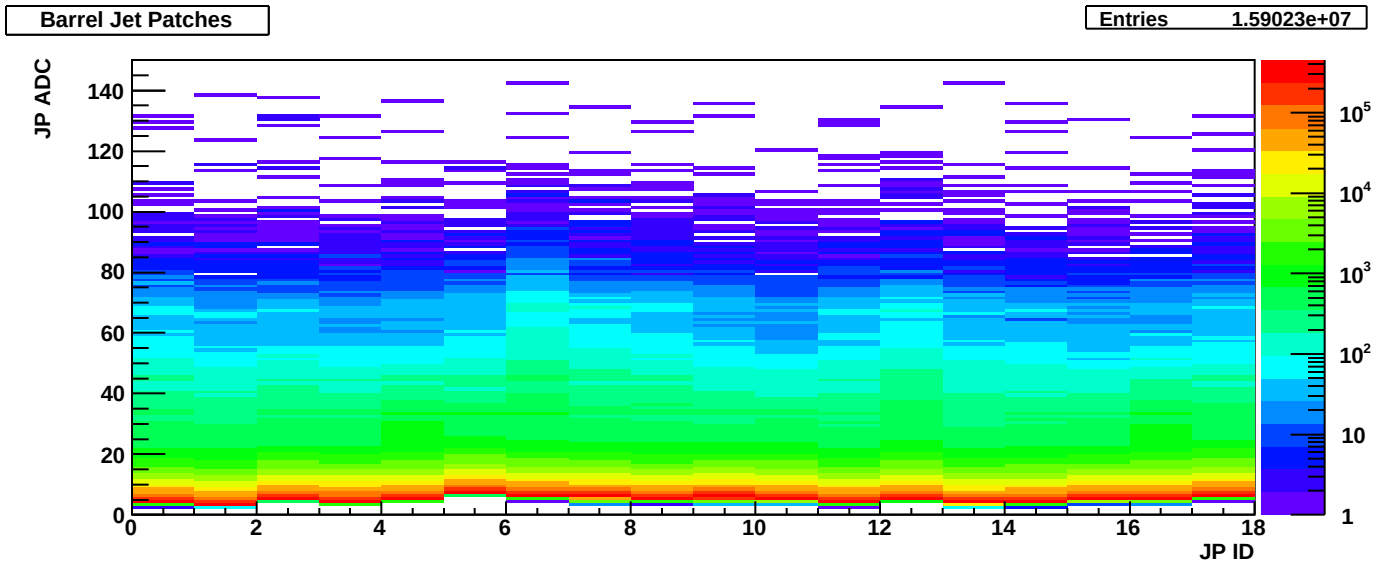

BunchId7Bit (all events)
Entries 883461

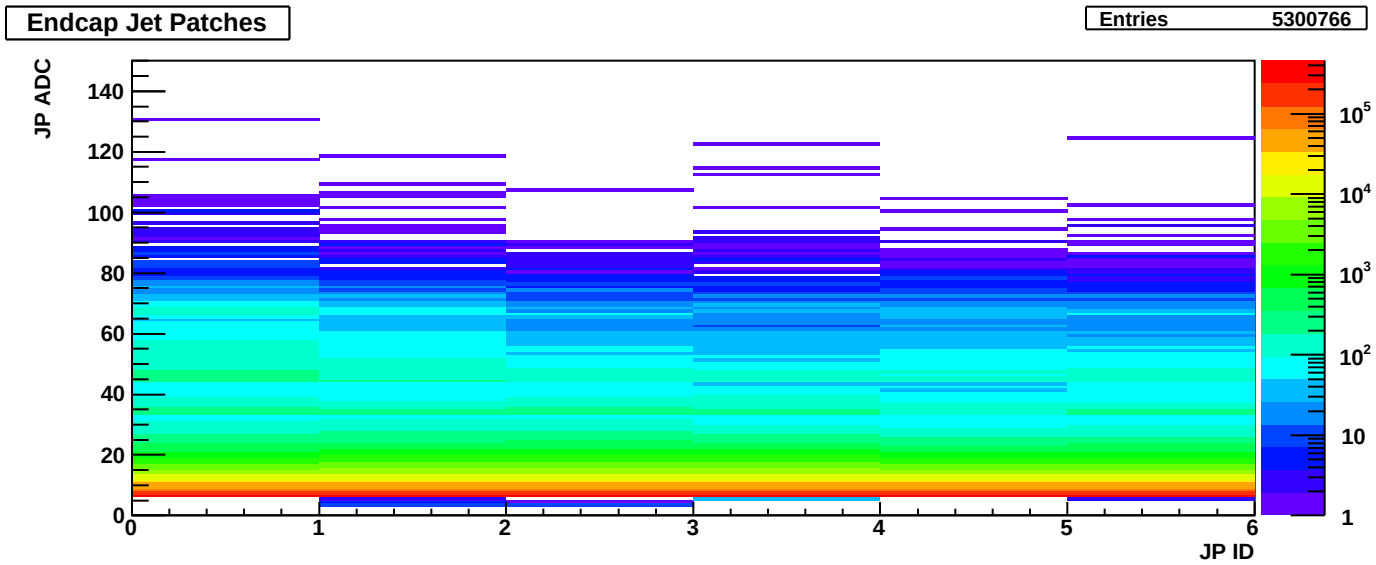

Barrel EMC L0 Input - High Tower
Entries 2.650383e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

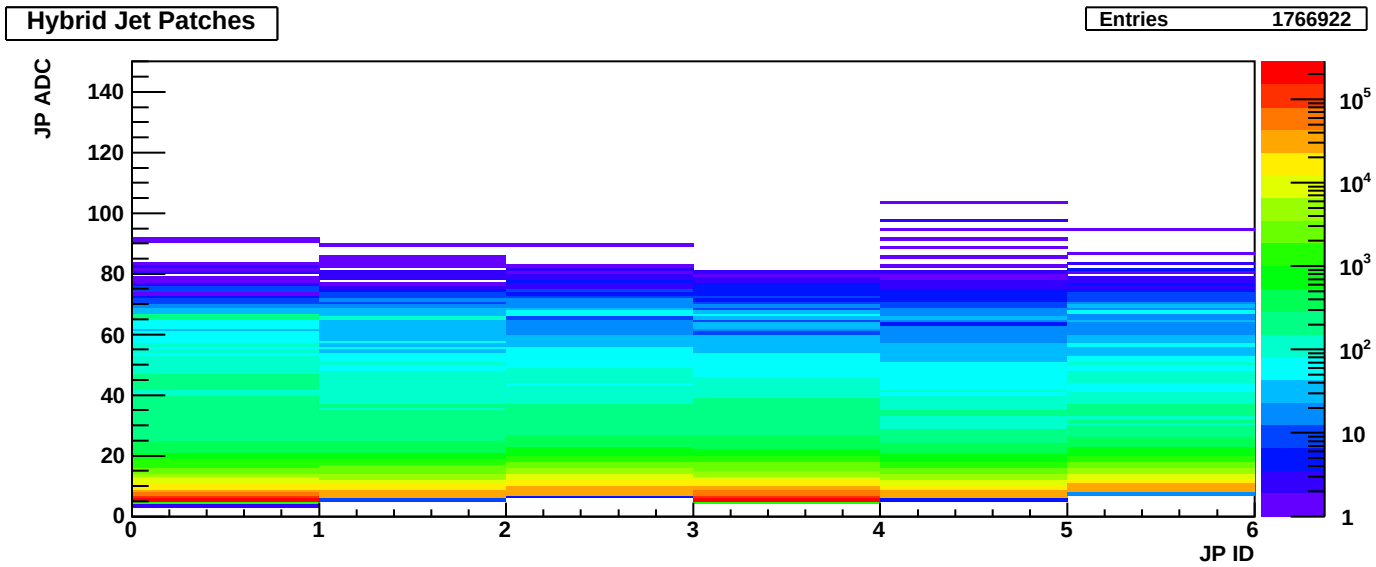

Barrel EMC L0 Input - Patch Sum
Entries 2.650383e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 7.951149e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

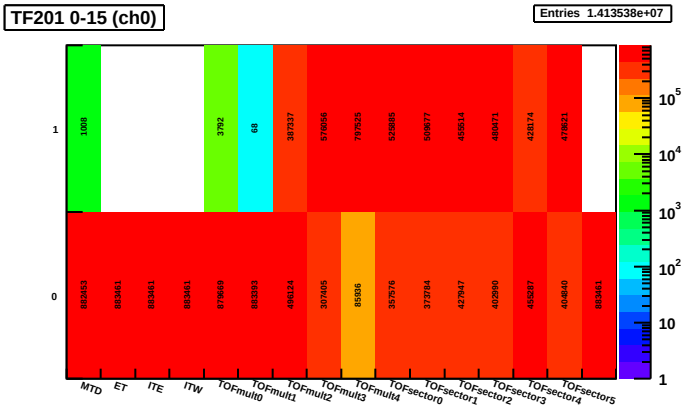

Endcap EMC L0 Input - Patch Sum
Entries 7.951149e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 1.59023e+07
JP ADC
JP ID

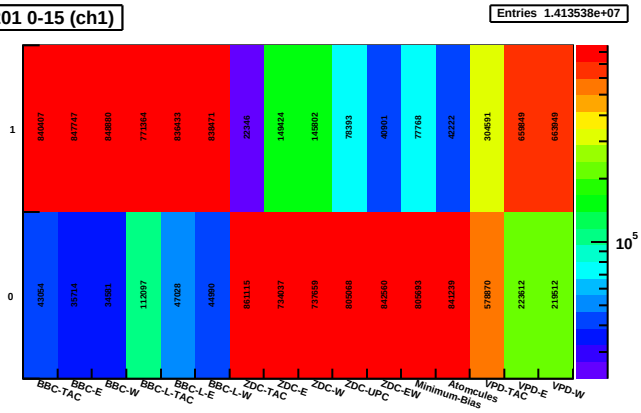

Endcap Jet Patches
Entries 5300766
JP ADC
JP ID


Hybrid Jet Patches
Entries 1766922
JP ADC
JP ID

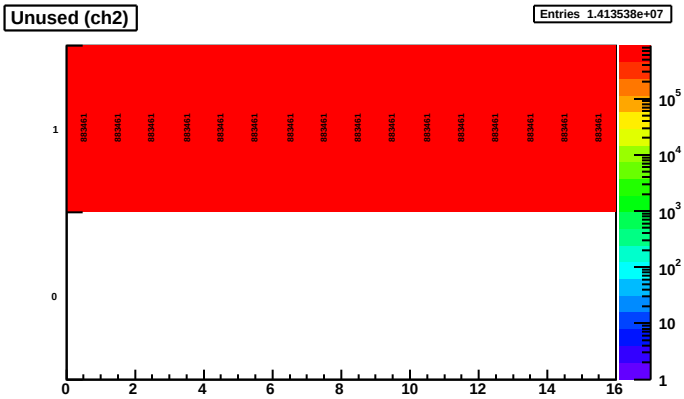
TF201 0-15 (ch0)



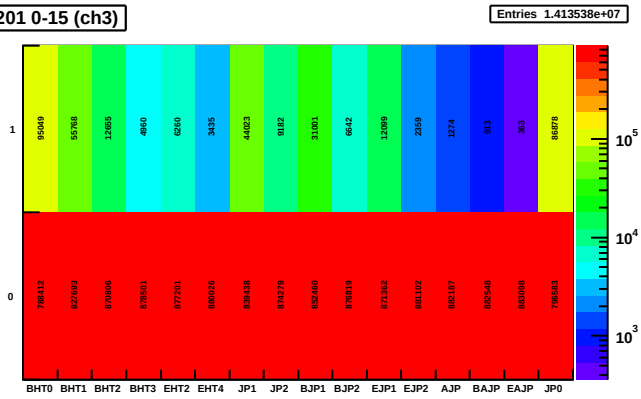
VT201 0-15 (ch1)



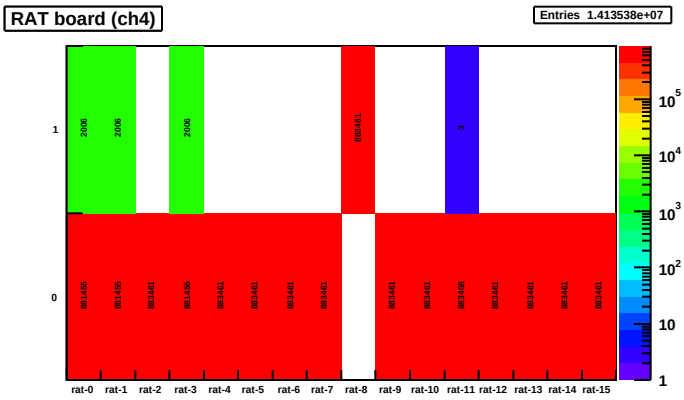
Unused (ch2)



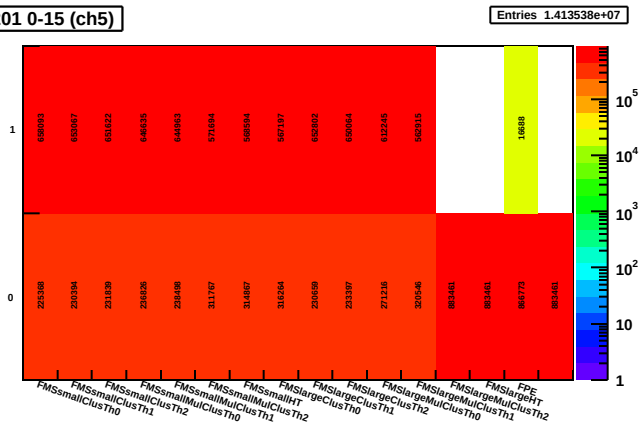
EM201 0-15 (ch3)



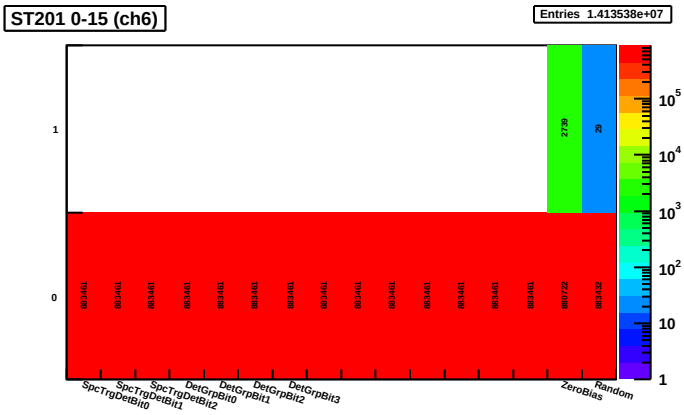
RAT board (ch4)



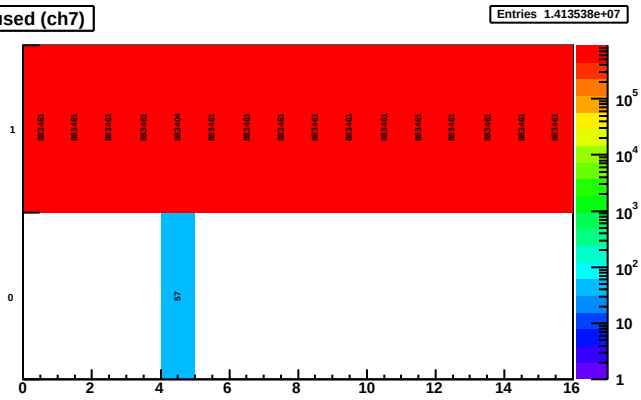
FP201 0-15 (ch5)

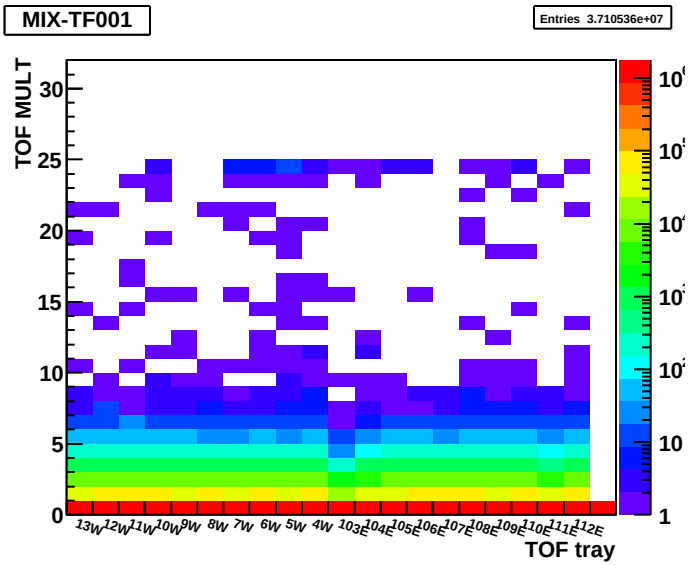


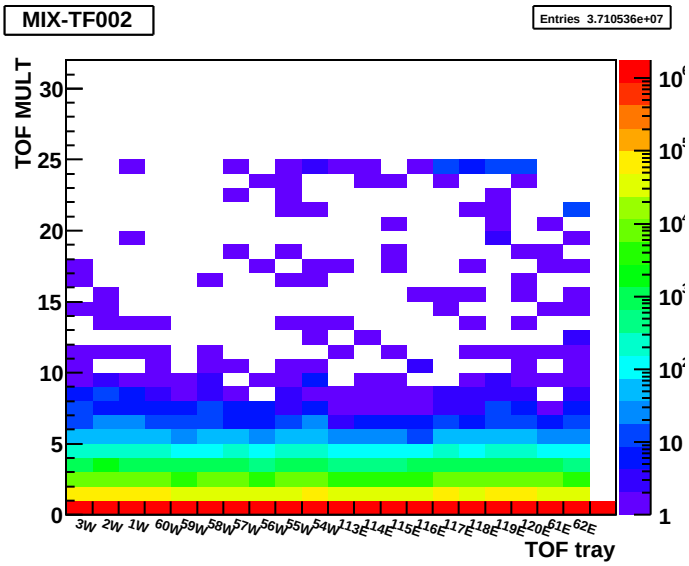
ST201 0-15 (ch6)

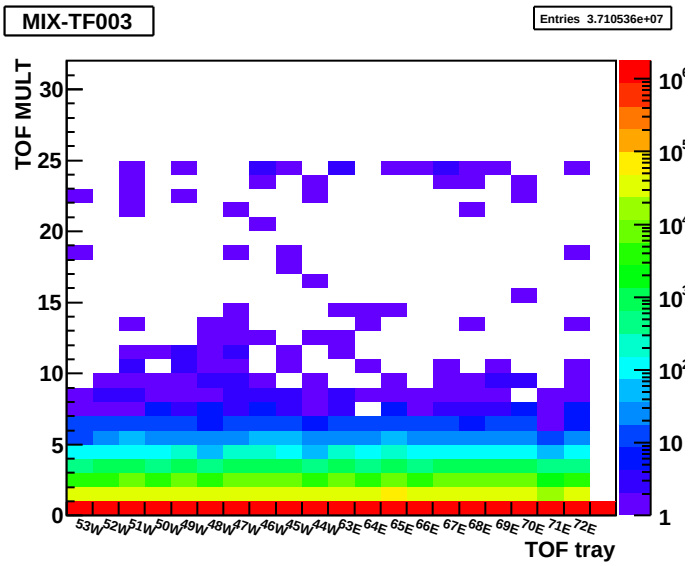


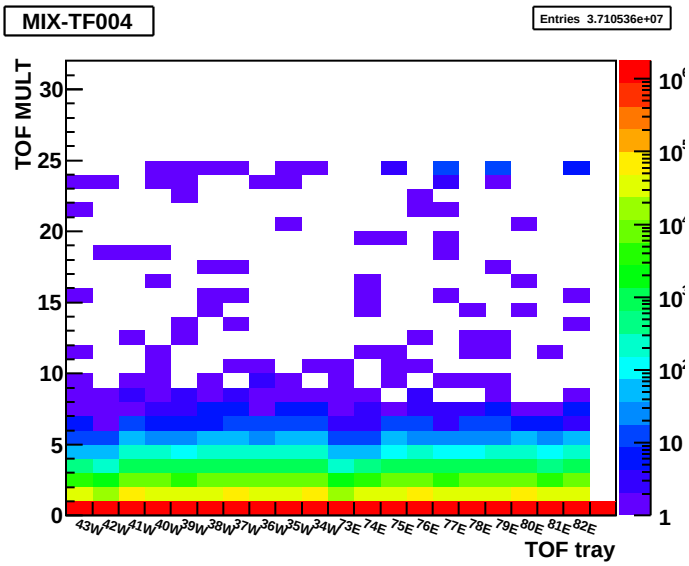
Unused (ch7)

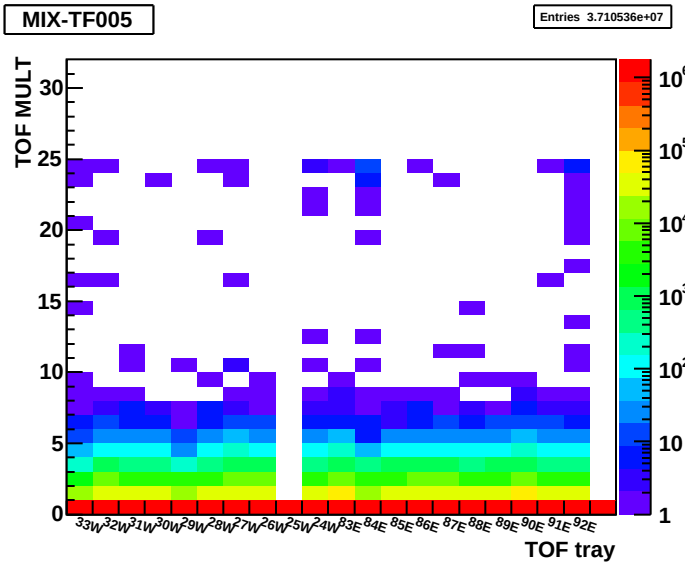


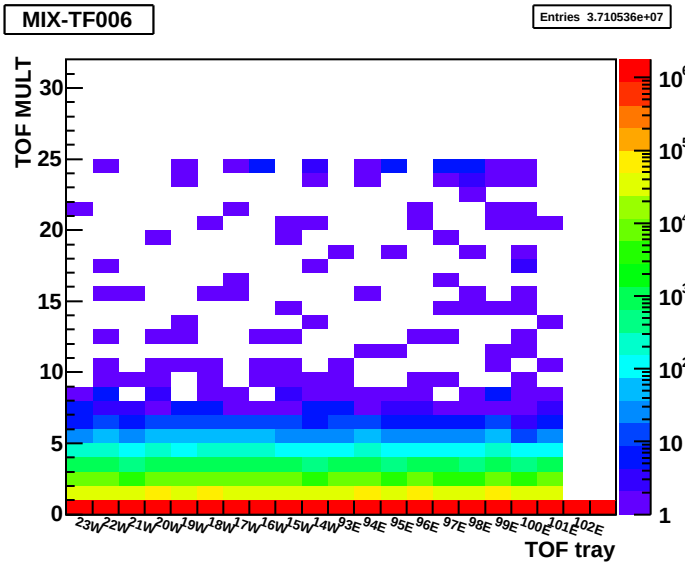

MIX-TF001
Entries 3.710536e+07
TOF MULT
TOF tray

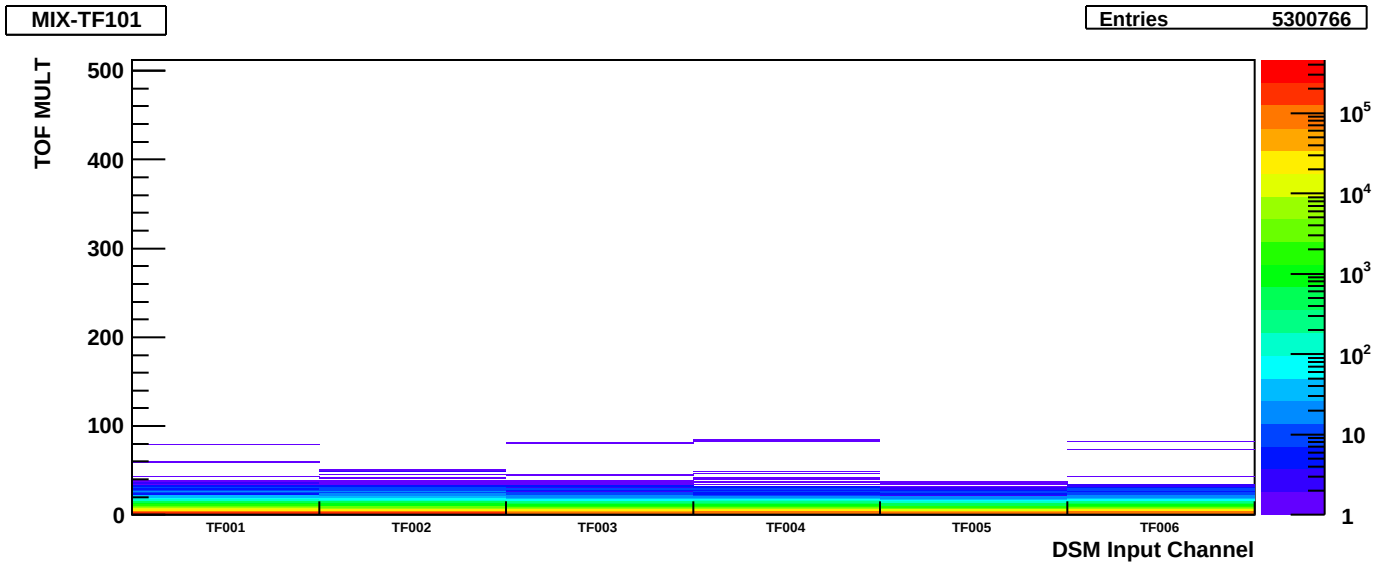

MIX-TF002
Entries 3.710536e+07
TOF MULT
TOF tray

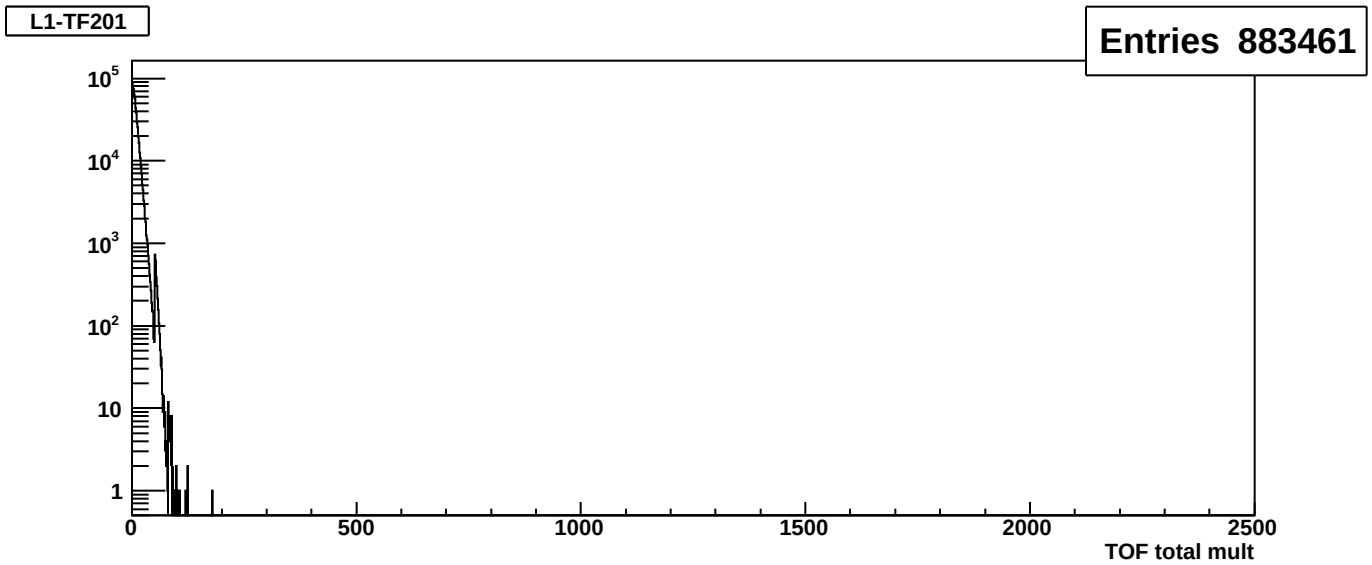

MIX-TF003
Entries 3.710536e+07
TOF MULT
TOF tray

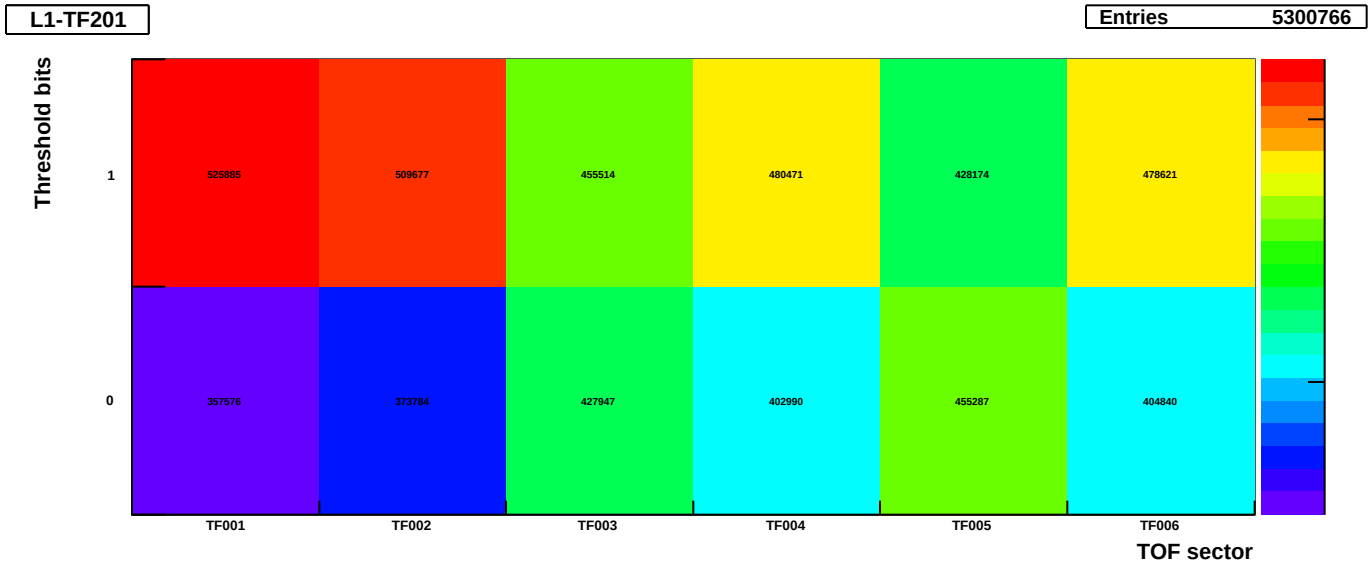

MIX-TF004
Entries 3.710536e+07
TOF MULT
TOF tray

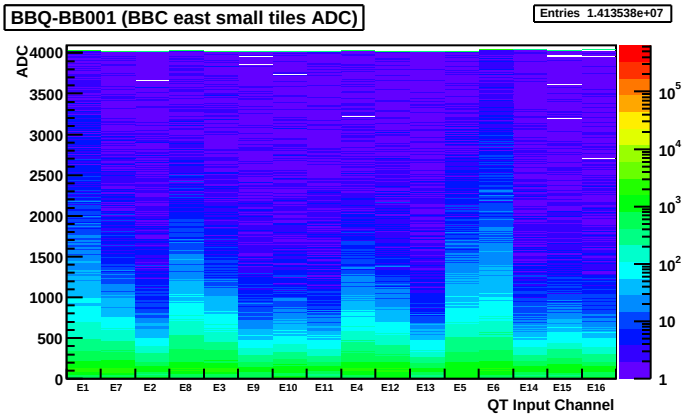

MIX-TF005
Entries 3.710536e+07
TOF MULT
TOF tray

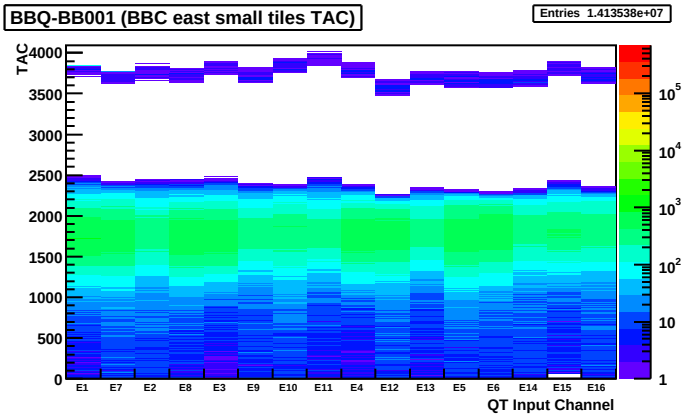

MIX-TF006
Entries 3.710536e+07
TOF MULT
TOF tray

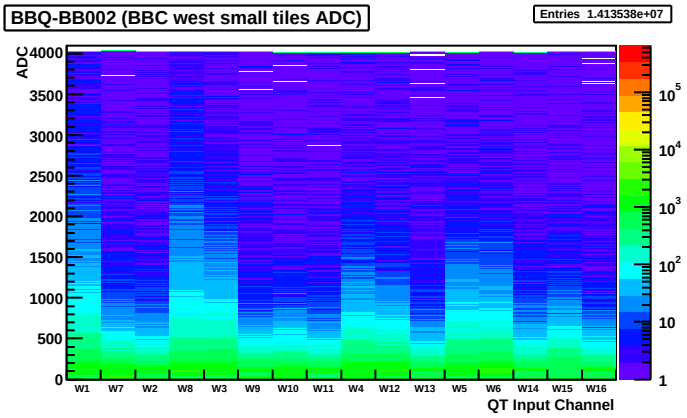

MIX-TF101
Entries 5300766
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

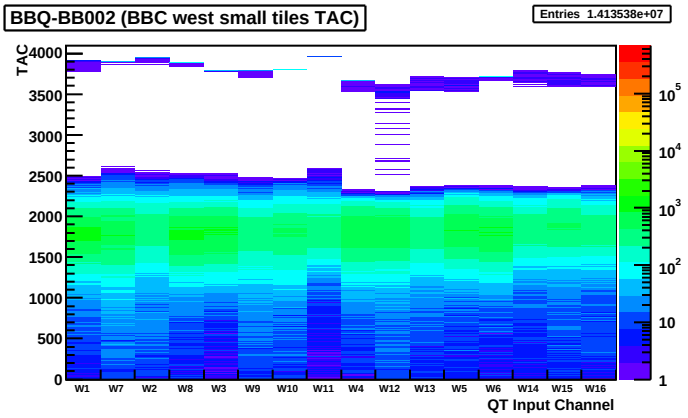

L1-TF201
Entries 883461
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

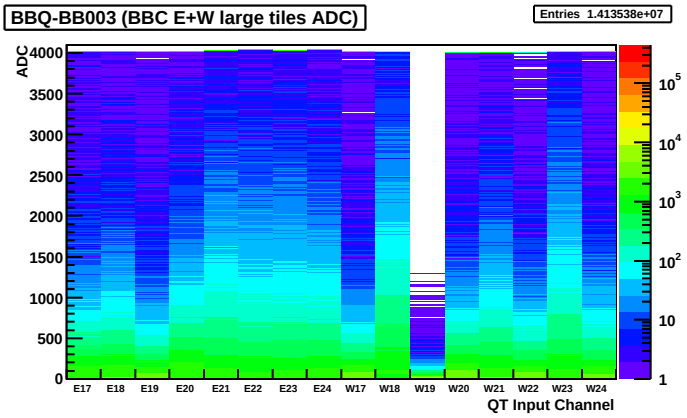

L1-TF201
Entries 5300766
Threshold bits
1
0
525885
509677
455514
480471
428174
478621
357576
373784
427947
402990
455287
404840
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

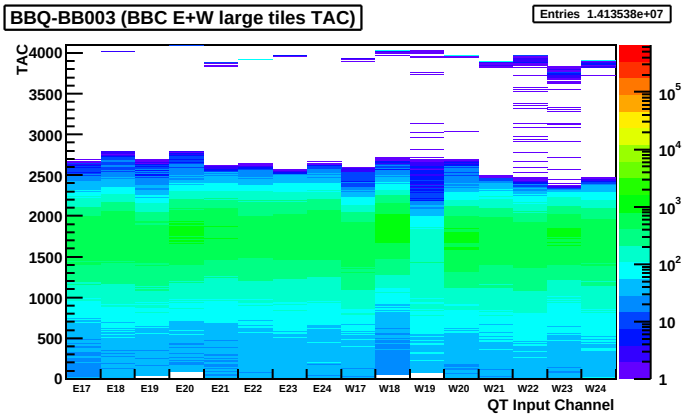

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.413538e+07
ADC
QT Input Channel

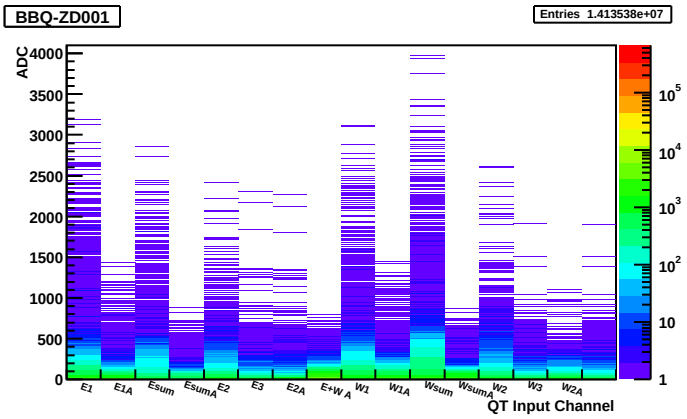

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.413538e+07
TAC
QT Input Channel

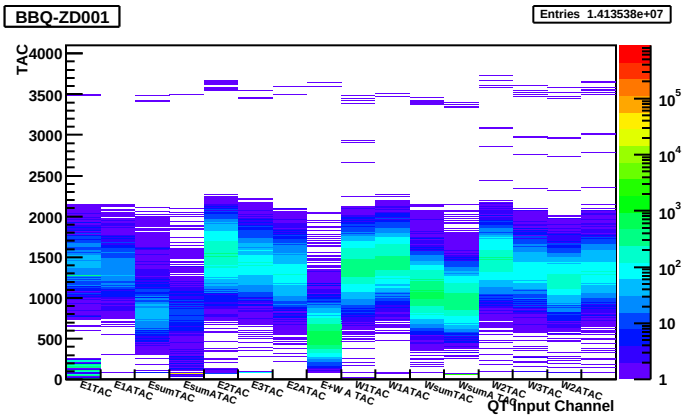

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.413538e+07
ADC
QT Input Channel

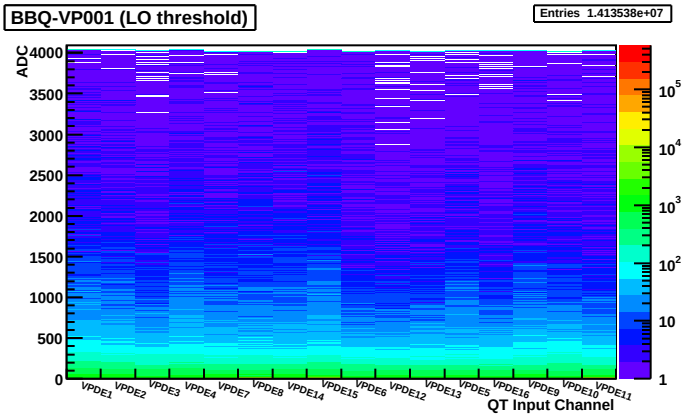

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.413538e+07
TAC
QT Input Channel

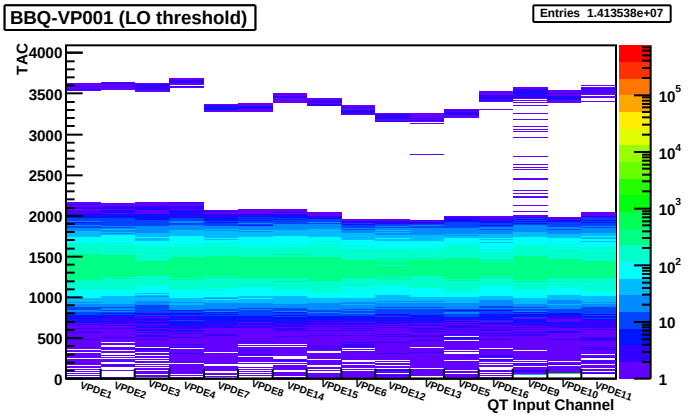

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.413538e+07
ADC
QT Input Channel

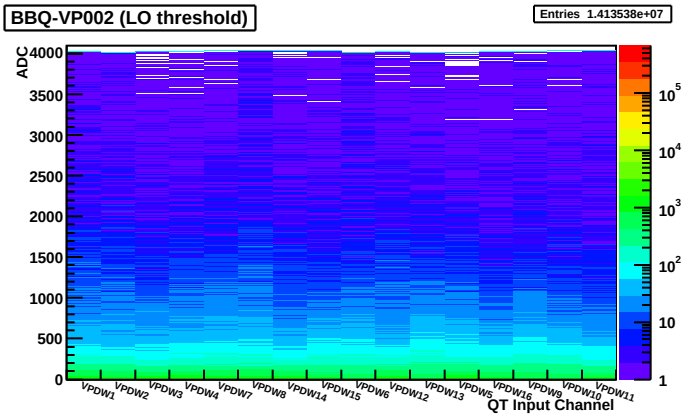

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.413538e+07
TAC
QT Input Channel

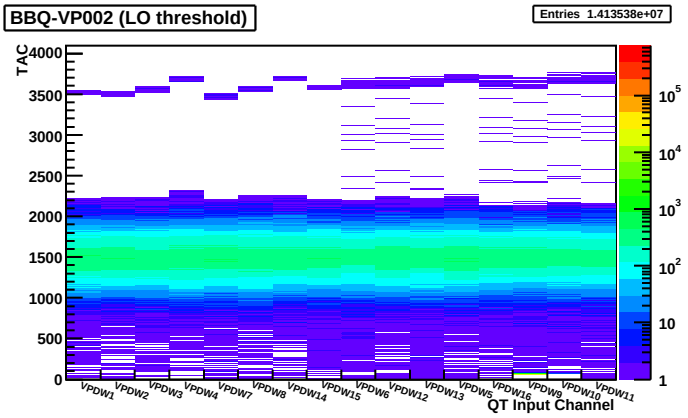

BBQ-ZD001
Entries 1.413538e+07
ADC
QT Input Channel

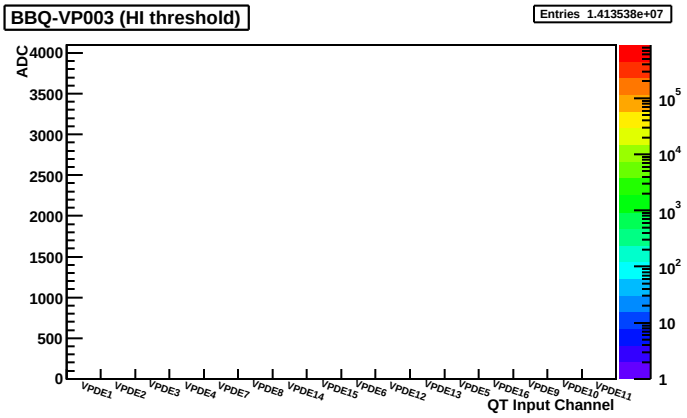

BBQ-ZD001
Entries 1.413538e+07
TAC
QT Input Channel

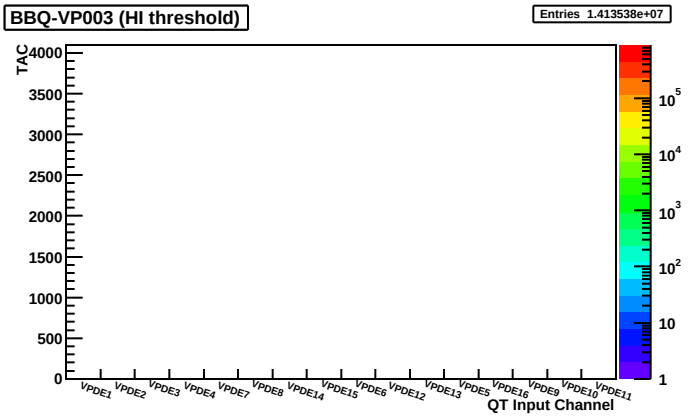

BBQ-VP001 (LO threshold)
Entries 1.413538e+07
ADC
QT Input Channel

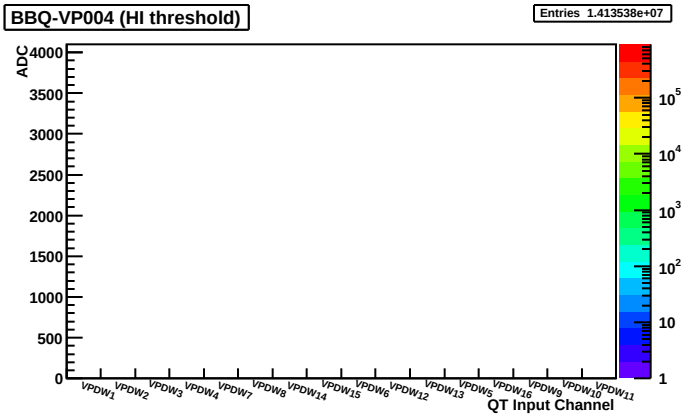

BBQ-VP001 (LO threshold)
Entries 1.413538e+07
TAC
QT Input Channel

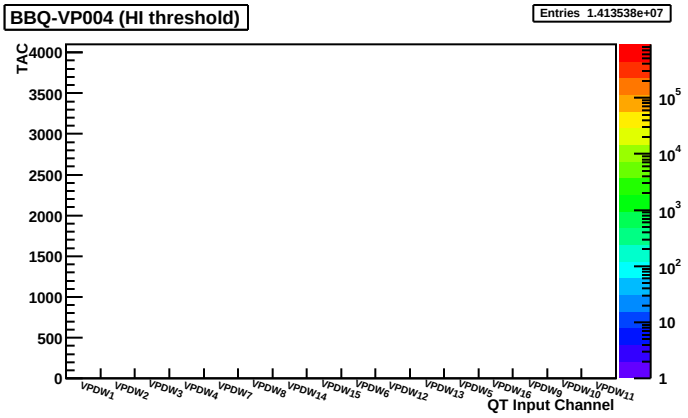

BBQ-VP002 (LO threshold)
Entries 1.413538e+07
ADC
QT Input Channel

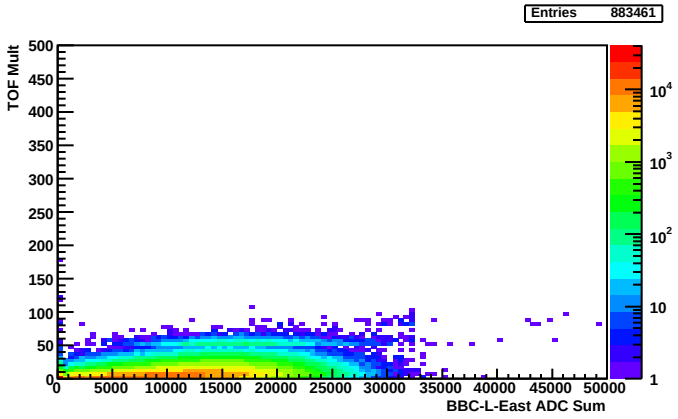

BBQ-VP002 (LO threshold)
Entries 1.413538e+07
TAC
QT Input Channel

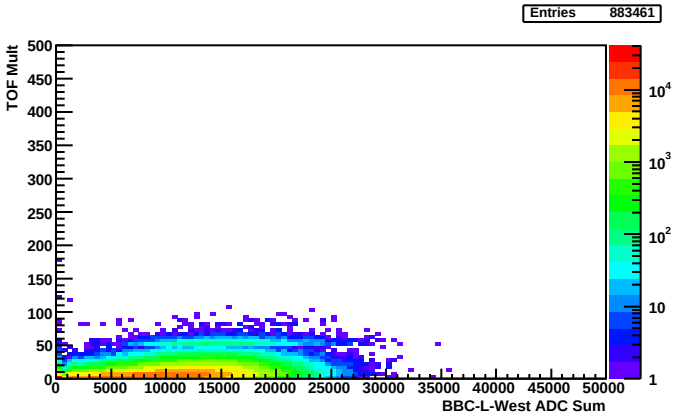

BBQ-VP003 (HI threshold)
Entries 1.413538e+07
ADC
QT Input Channel

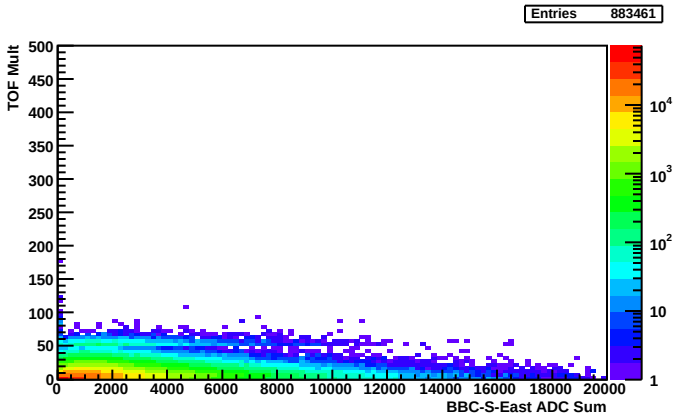

BBQ-VP003 (HI threshold)
Entries 1.413538e+07
TAC
QT Input Channel

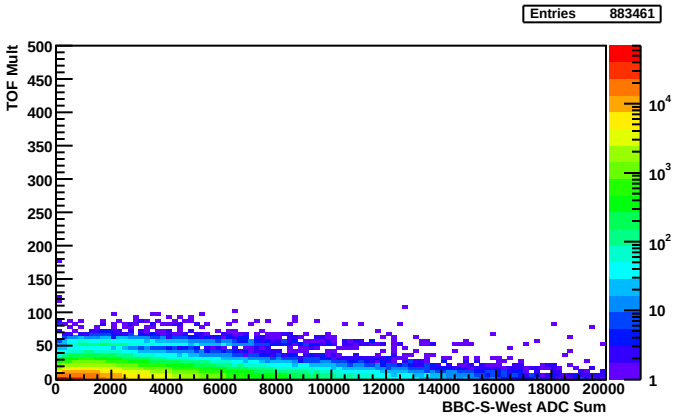

BBQ-VP004 (HI threshold)
Entries 1.413538e+07
ADC
QT Input Channel

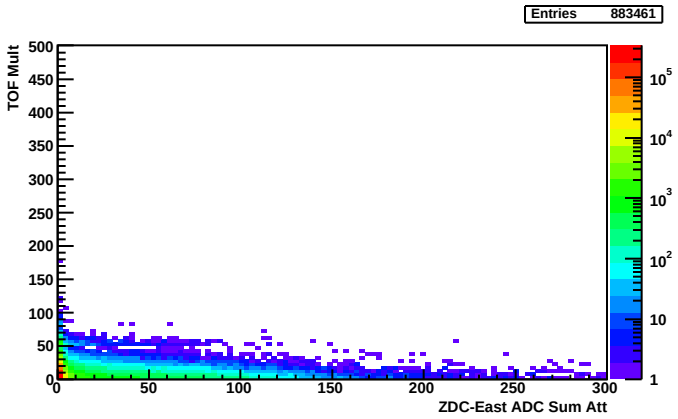

BBQ-VP004 (HI threshold)
Entries 1.413538e+07
TAC
QT Input Channel

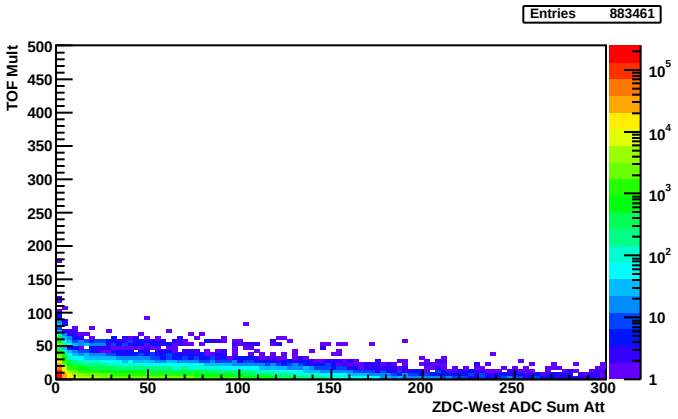

Entries 883461
TOF Mult
BBC-L-East ADC Sum

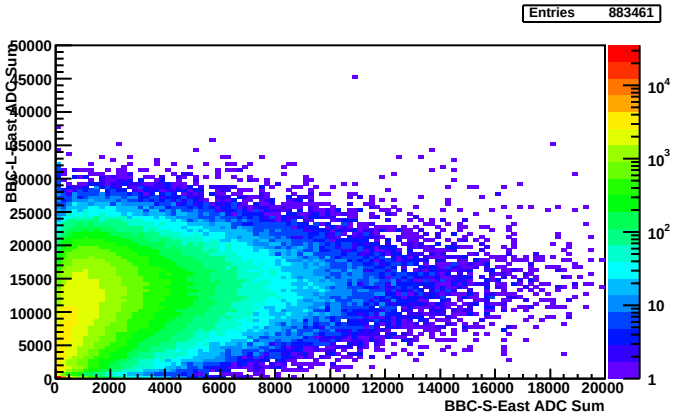

Entries 883461
TOF Mult
BBC-L-West ADC Sum

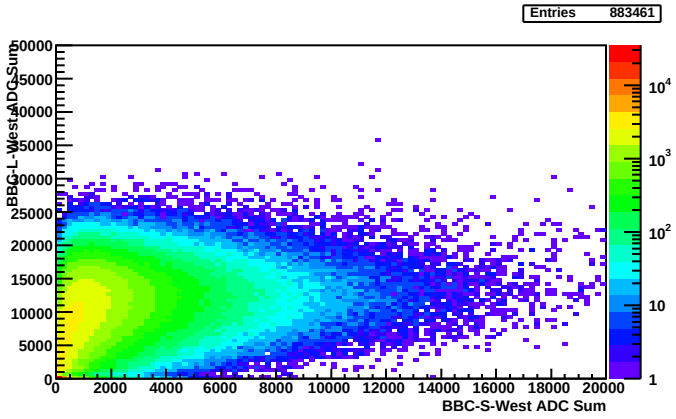

Entries 883461
TOF Mult
BBC-S-East ADC Sum

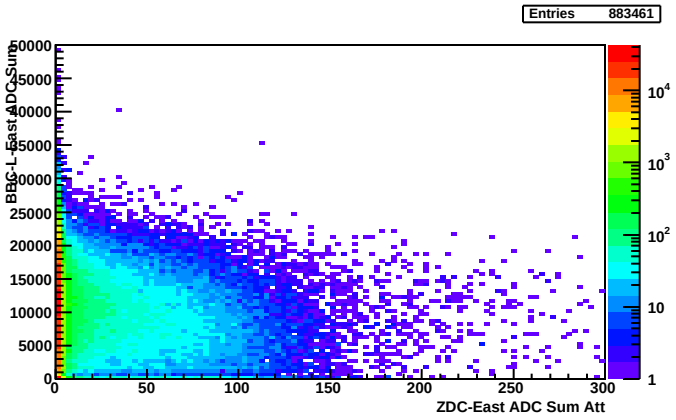

Entries 883461
TOF Mult
BBC-S-West ADC Sum

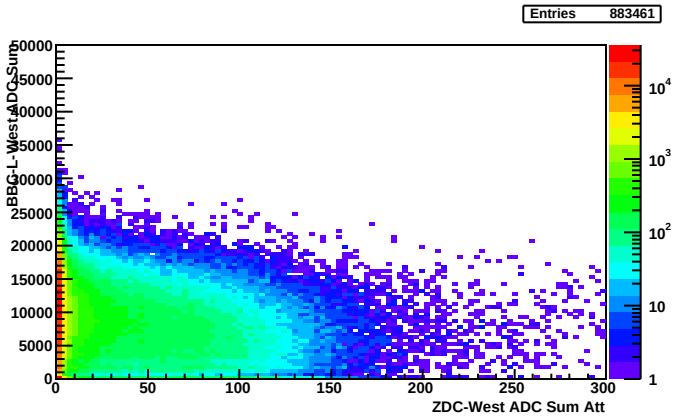

Entries 883461
TOF Mult
ZDC-East ADC Sum Att

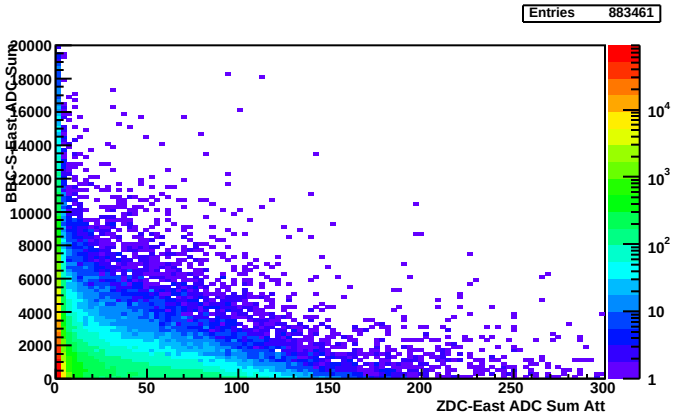

Entries 883461
TOF Mult
ZDC-West ADC Sum Att

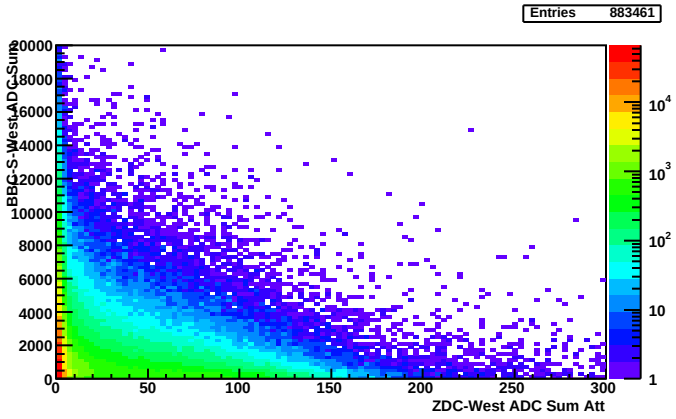

Entries 883461
BBC-L-East ADC Sum
BBC-S-East ADC Sum

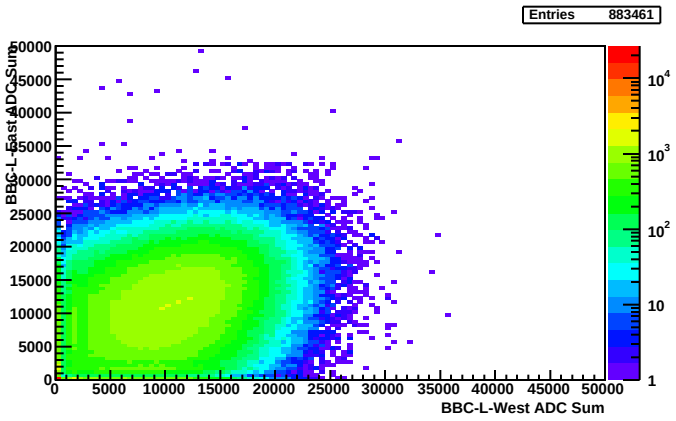

Entries 883461
BBC-L-West ADC Sum
BBC-S-West ADC Sum

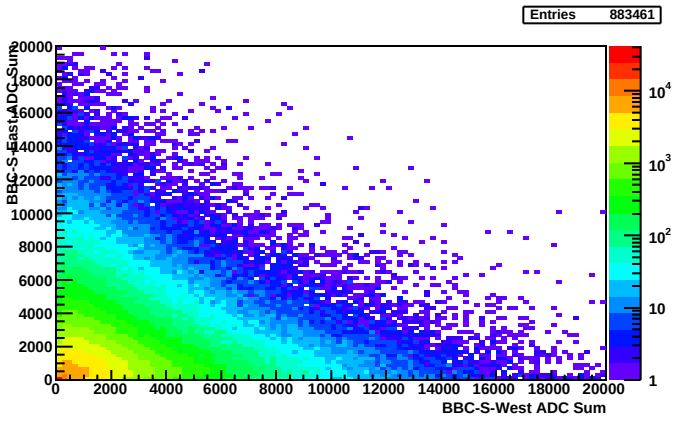

Entries 883461
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

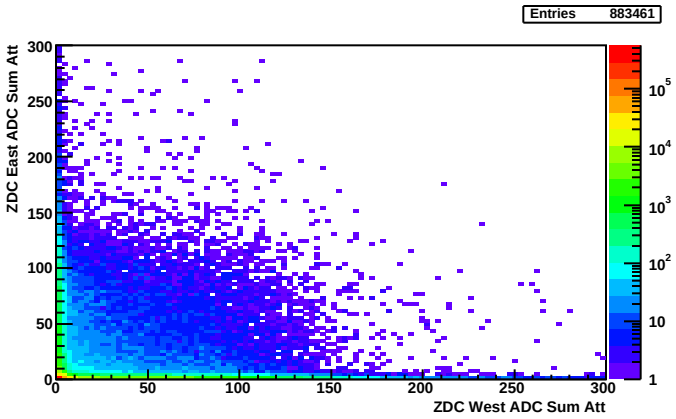

Entries 883461
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

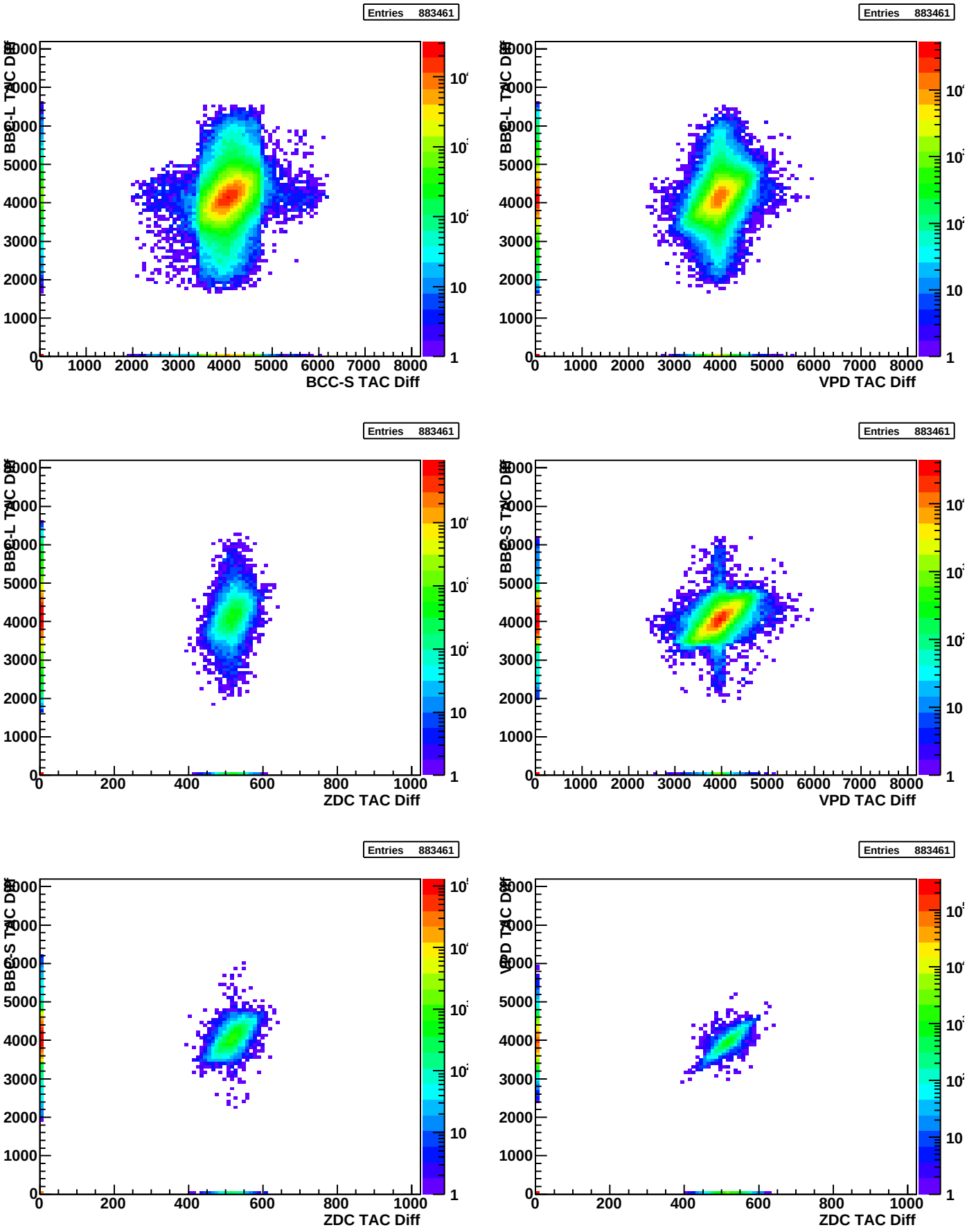

Entries 883461
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

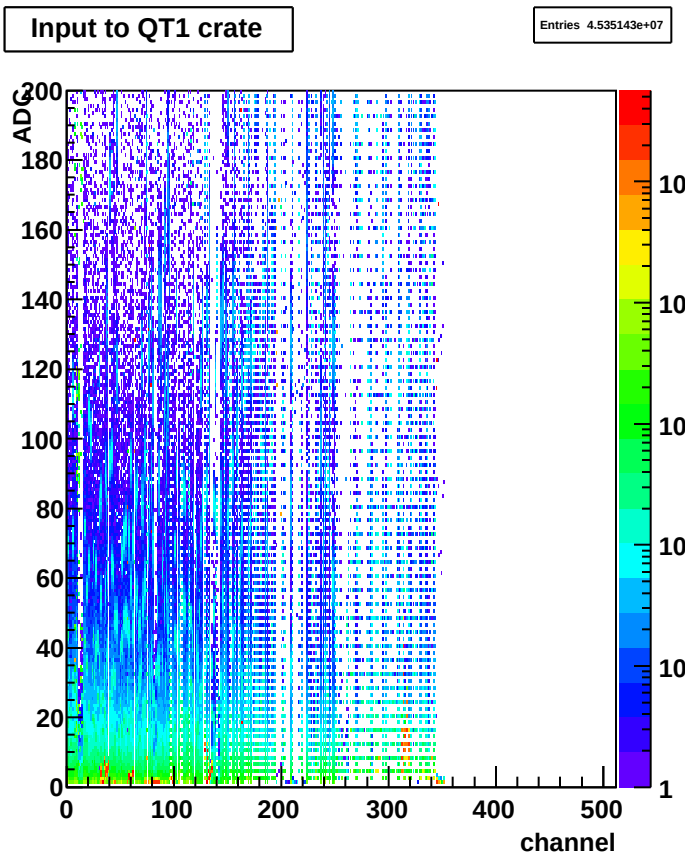

Entries 883461
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

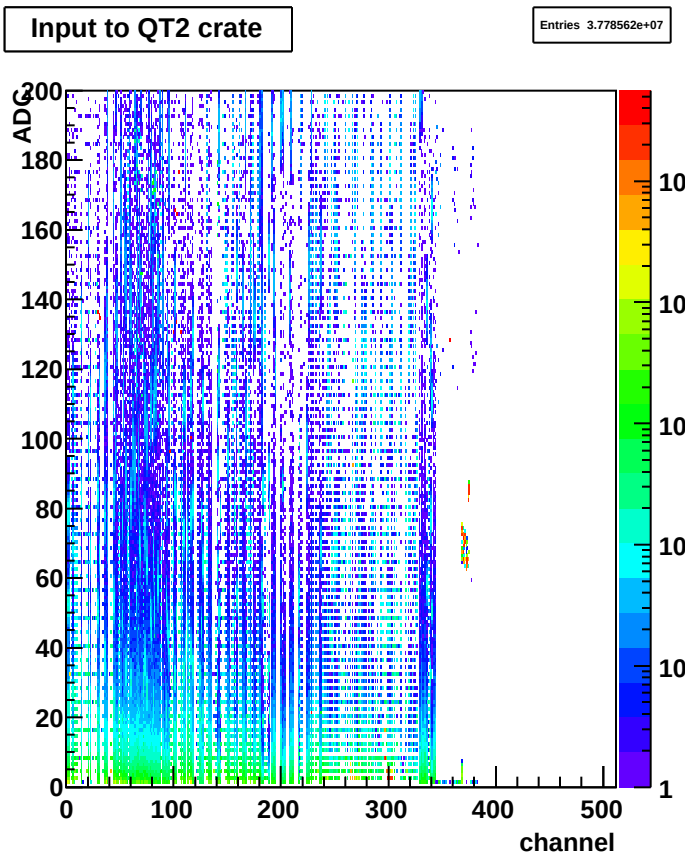

Entries 883461
BBC-L-East ADC Sum
BBC-L-West ADC Sum

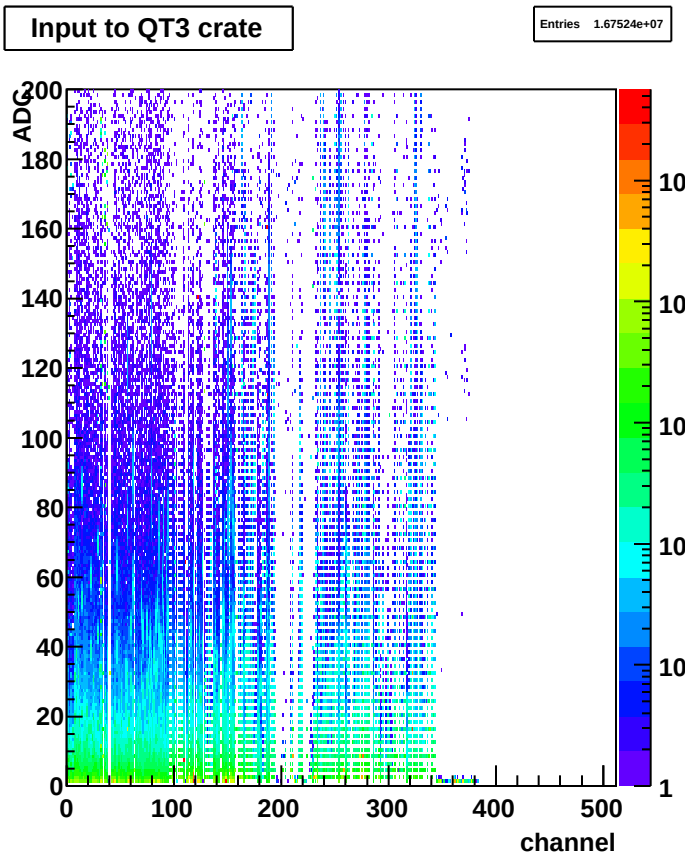

Entries 883461
BBC-S-East ADC Sum
BBC-S-West ADC Sum

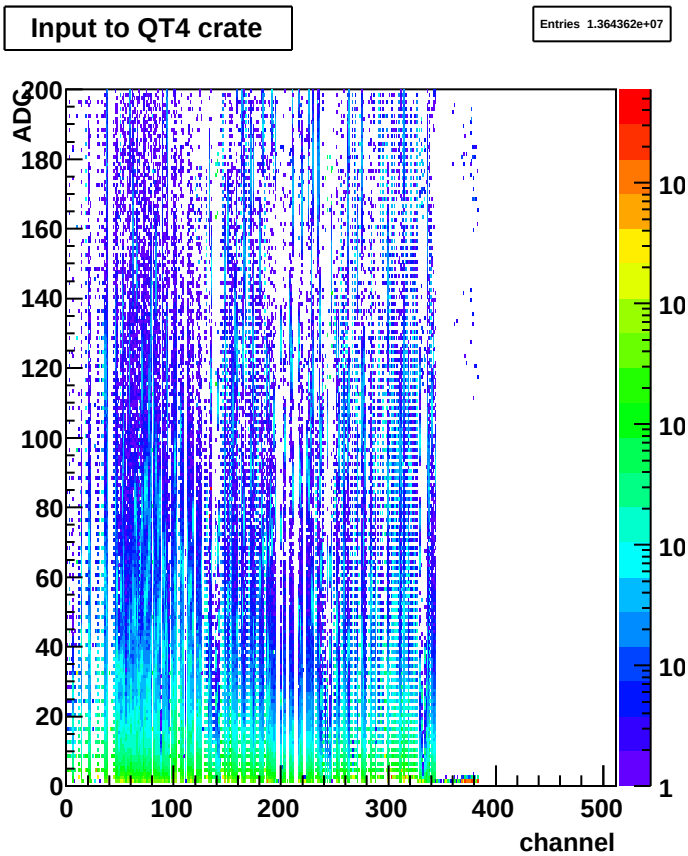

Entries 883461
ZDC East ADC Sum Att
ZDC West ADC Sum Att

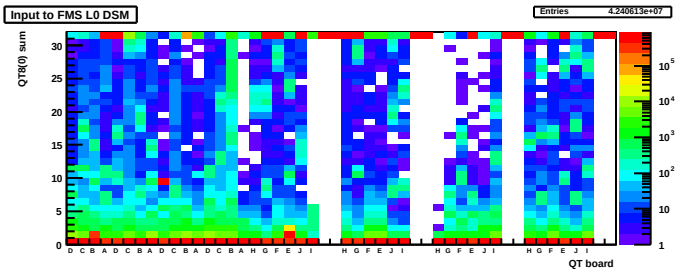

Entries 883461
BBC-L TAC Diff
BCC-S TAC Diff
Entries 883461
BBC-L TAC Diff
VPD TAC Diff
Entries 883461
BBC-L TAC Diff
ZDC TAC Diff
Entries 883461
BBC-S TAC Diff
VPD TAC Diff
Entries 883461
BBC-S TAC Diff
ZDC TAC Diff
Entries 883461
VPD TAC Diff
ZDC TAC Diff

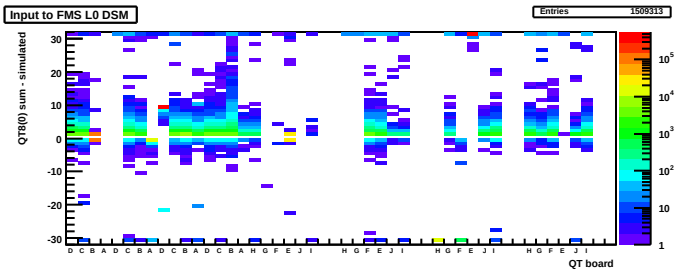

Input to QT1 crate
Entries 4.535143e+07
ADC
channel

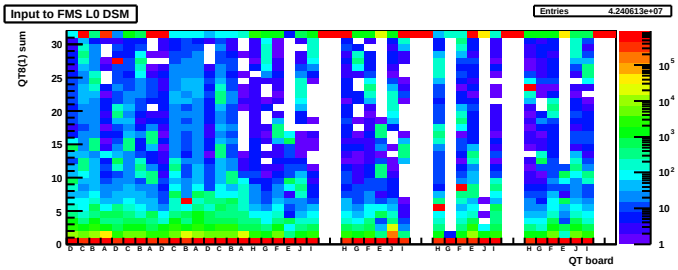

Input to QT2 crate
Entries 3.778562e+07
ADC
channel

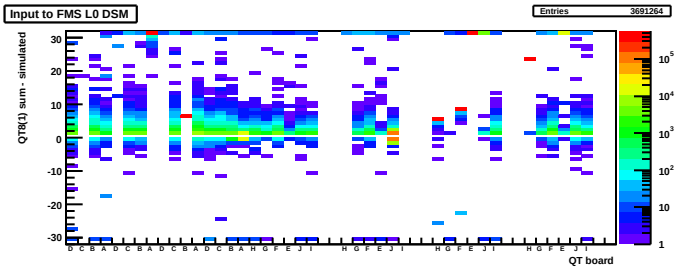

Input to QT3 crate
Entries 1.67524e+07
ADC
channel

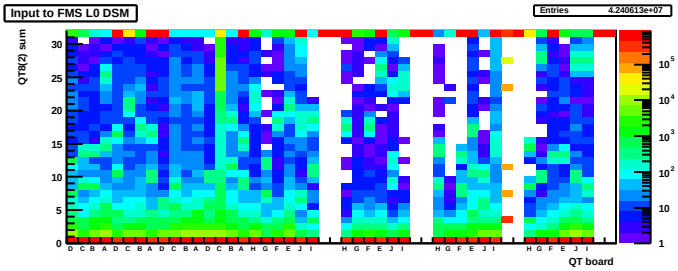

Input to QT4 crate
Entries 1.364362e+07
ADC
channel

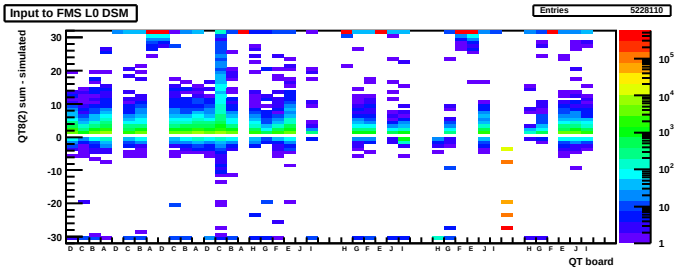

Input to FMS L0 DSM
Entries 4.240613e+07
QT8(0) sum
QT board

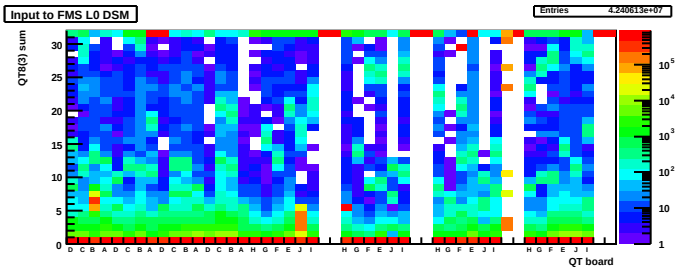

Input to FMS L0 DSM
Entries 1509313
QT8(0) sum - simulated
QT board

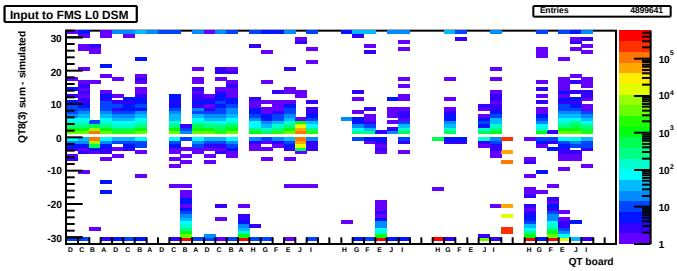

Input to FMS L0 DSM
Entries 4.240613e+07
QT8(1) sum
QT board

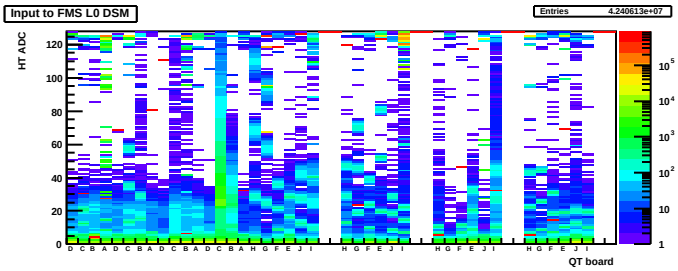

Input to FMS L0 DSM
Entries 3691264
QT8(1) sum - simulated
QT board

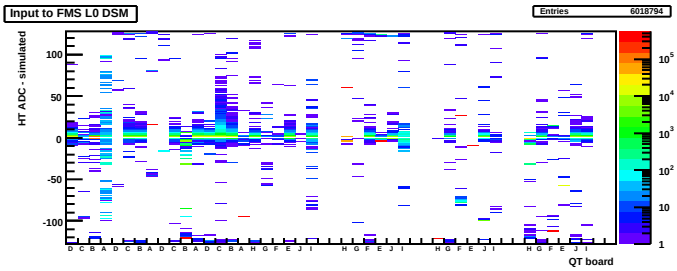

Input to FMS L0 DSM
Entries 4.240613e+07
QT8(2) sum
QT board

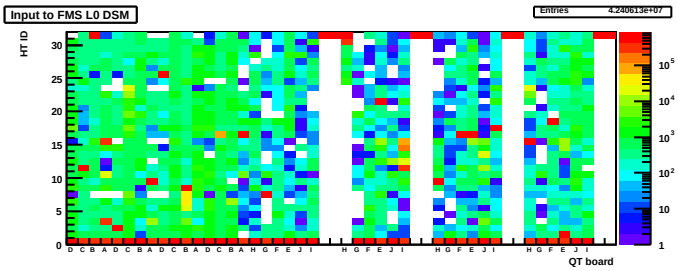

Input to FMS L0 DSM
Entries 5228110
QT8(2) sum - simulated
QT board

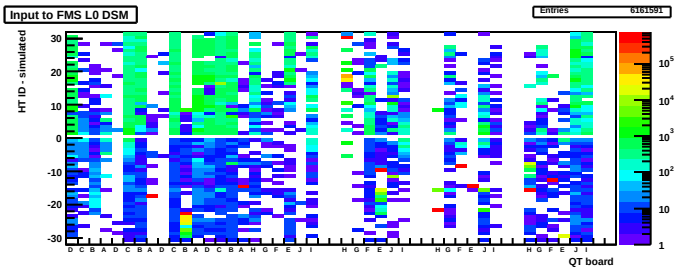

Input to FMS L0 DSM
Entries 4.240613e+07
QT8(3) sum
QT board

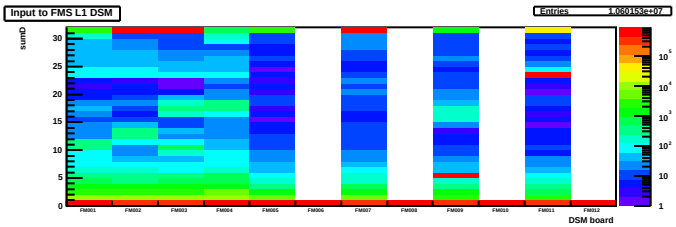

Input to FMS L0 DSM
Entries 4899641
QT8(3) sum - simulated
QT board

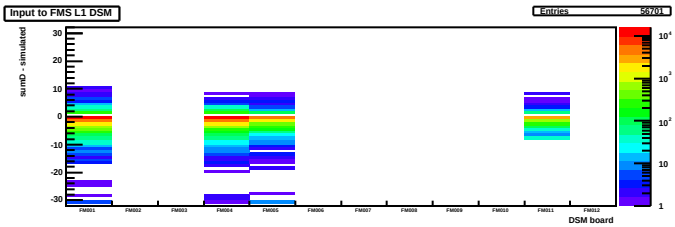

Input to FMS L0 DSM
Entries 4.240613e+07
HT ADC
QT board

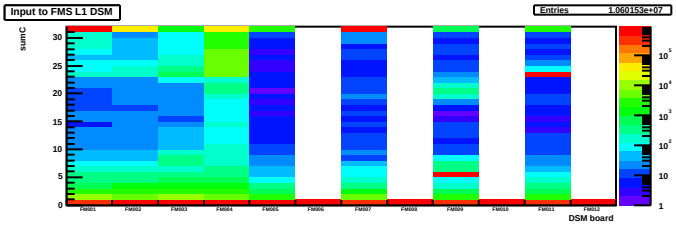

Input to FMS L0 DSM
Entries 6018794
HT ADC - simulated
QT board

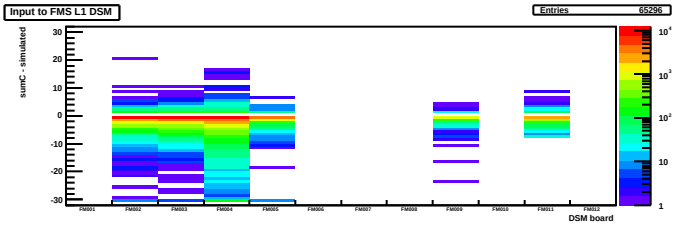

Input to FMS L0 DSM
Entries 4.240613e+07
HT ID
QT board

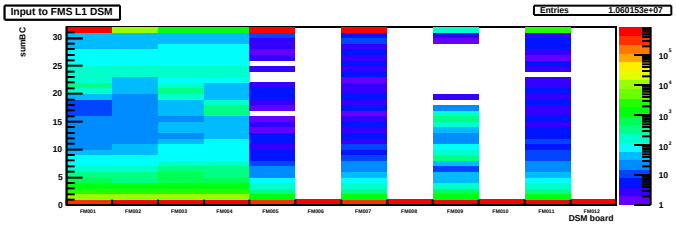

Input to FMS L0 DSM
Entries 6161591
HT ID - simulated
QT board

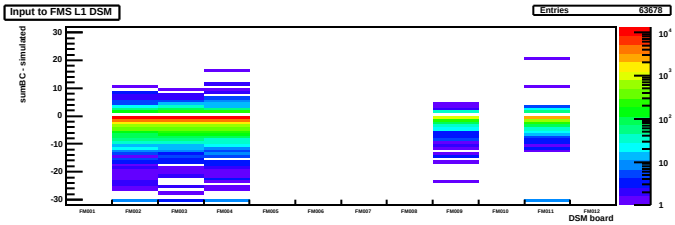

Input to FMS L1 DSM
Entries 1.060153e+07
sumD
DSM board

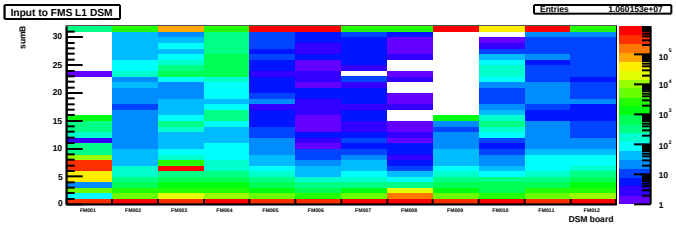

Input to FMS L1 DSM
Entries 56701
sumD - simulated
DSM board

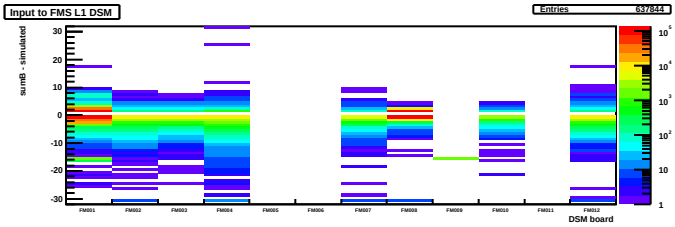

Input to FMS L1 DSM
Entries 1.060153e+07
sumC
DSM board

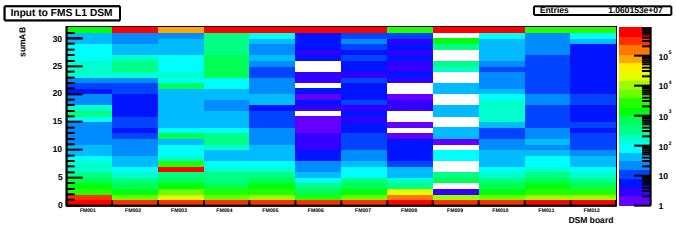

Input to FMS L1 DSM
Entries 65296
sumC - simulated
DSM board

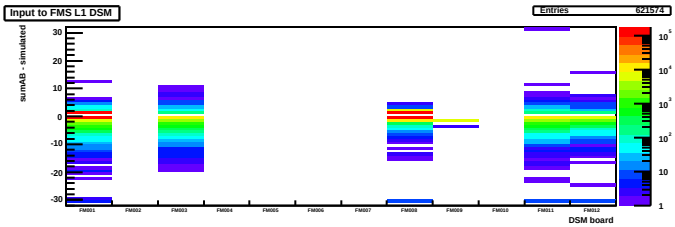

Input to FMS L1 DSM
Entries 1.060153e+07
sumBC
DSM board

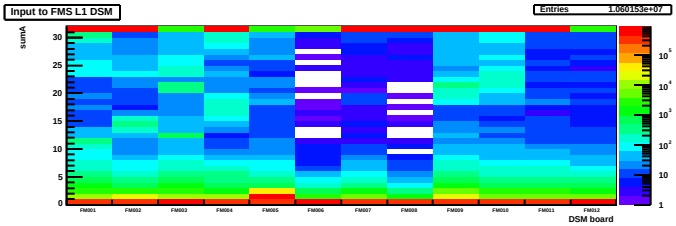

Input to FMS L1 DSM
Entries 63678
sumBC - simulated
DSM board

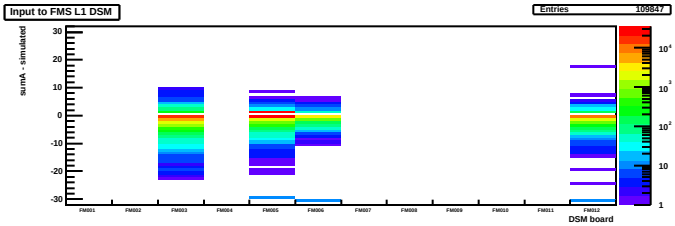

Input to FMS L1 DSM
Entries 1.060153e+07
sumB
DSM board

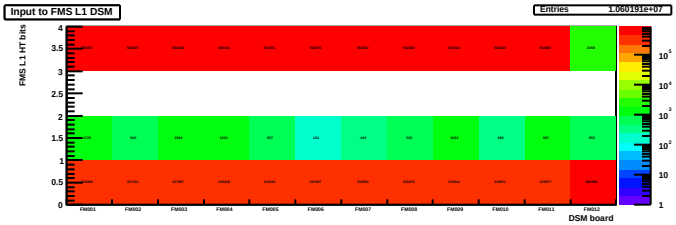

Input to FMS L1 DSM
Entries 637844
sumB - simulated
DSM board

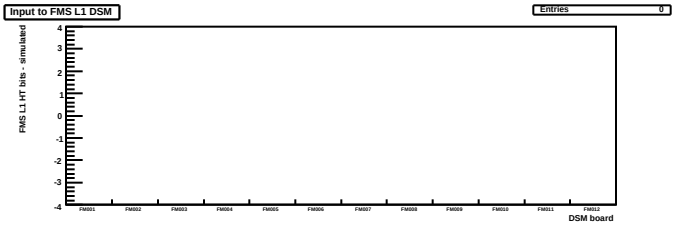

Input to FMS L1 DSM
Entries 1.060153e+07
sumAB
DSM board

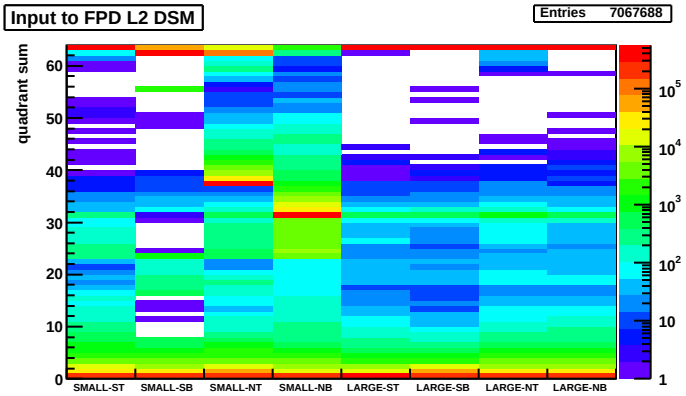

Input to FMS L1 DSM
Entries 621574
sumAB - simulated
DSM board

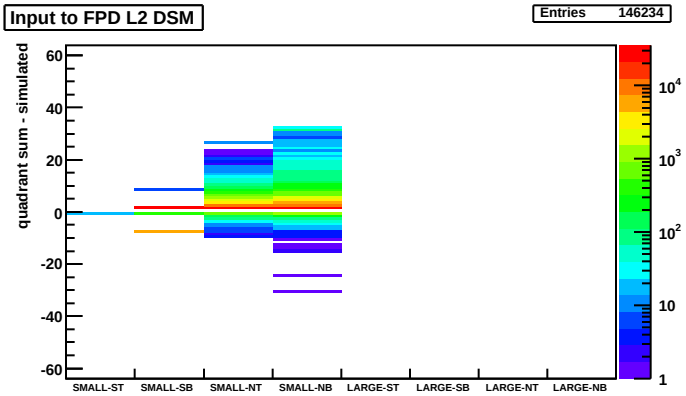

Input to FMS L1 DSM
Entries 1.060153e+07
sumA
DSM board

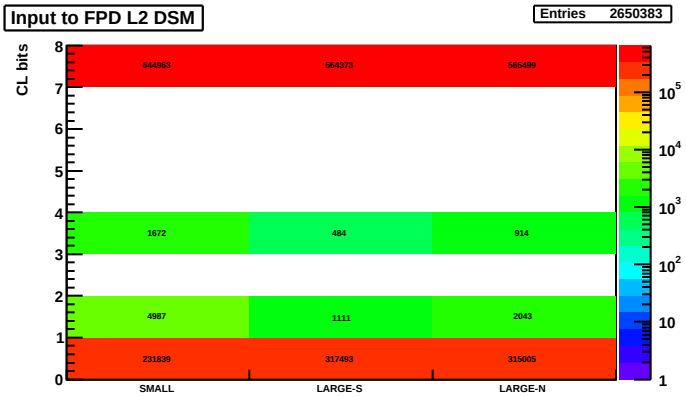

Input to FMS L1 DSM
Entries 109847
sumA - simulated
DSM board

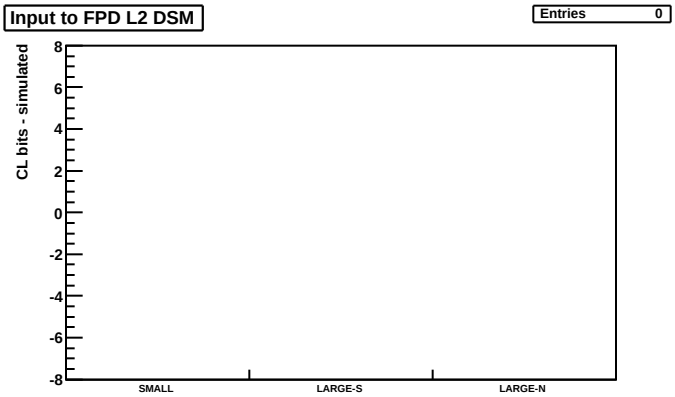

Input to FMS L1 DSM
Entries 1.060191e+07
FMS L1 HT bits
DSM board

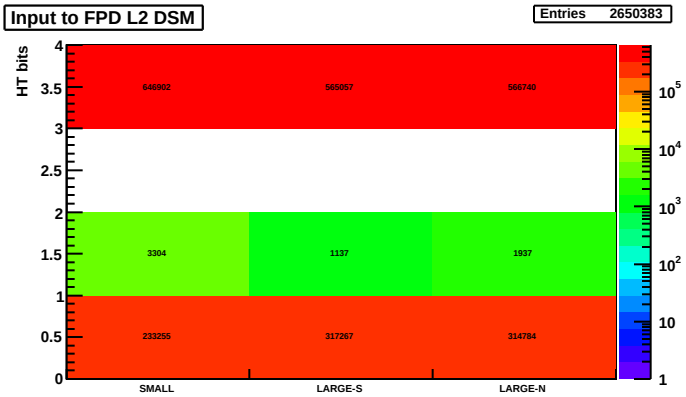

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

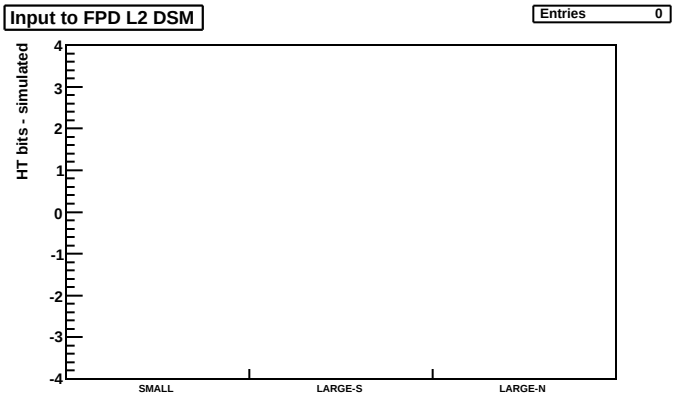

Input to FPD L2 DSM
Entries 7067688
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

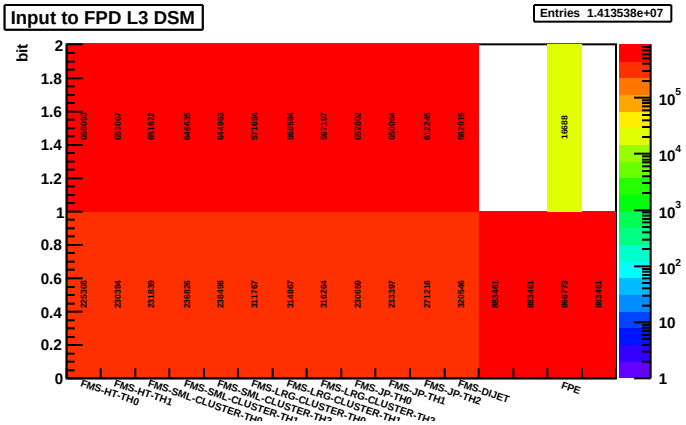

Input to FPD L2 DSM
Entries 146234
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

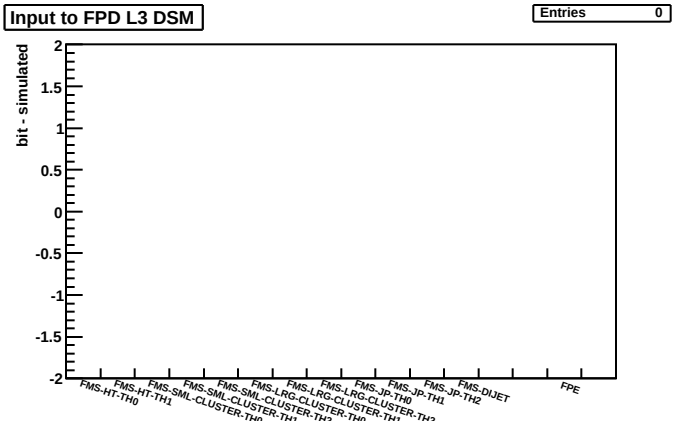

Input to FPD L2 DSM
Entries 2650383
CL bits
644963
564373
565499
1672
484
914
4987
1111
2043
231839
317493
315005
SMALL
LARGE-S
LARGE-N

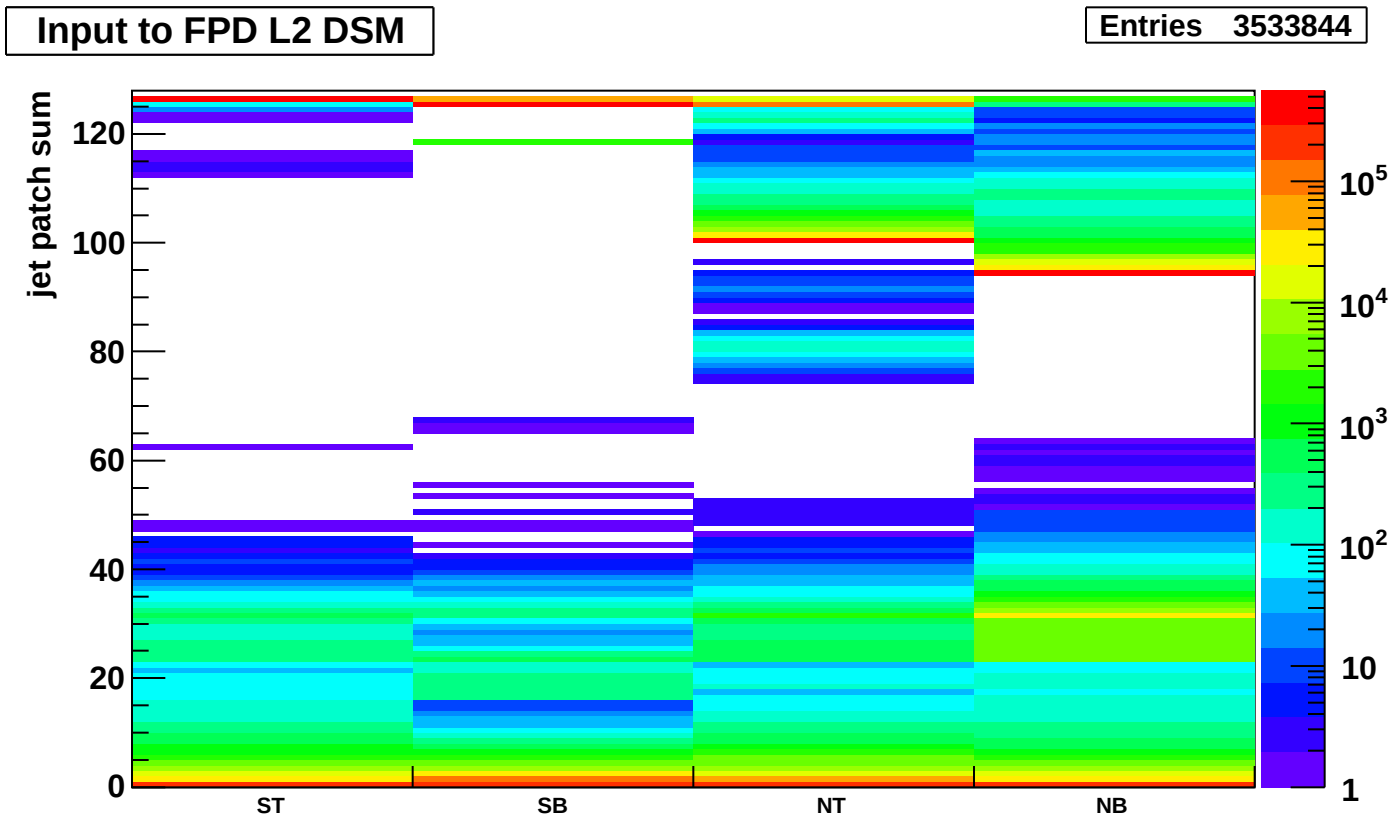

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

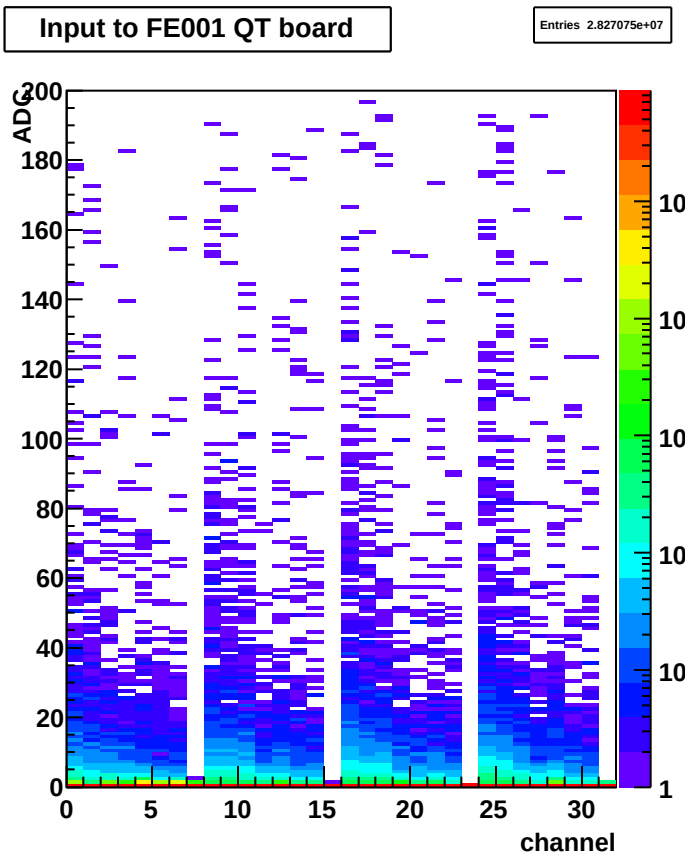

Input to FPD L2 DSM
Entries 2650383
HT bits
646902
565057
566740
3304
1137
1937
233255
317267
314784
SMALL
LARGE-S
LARGE-N

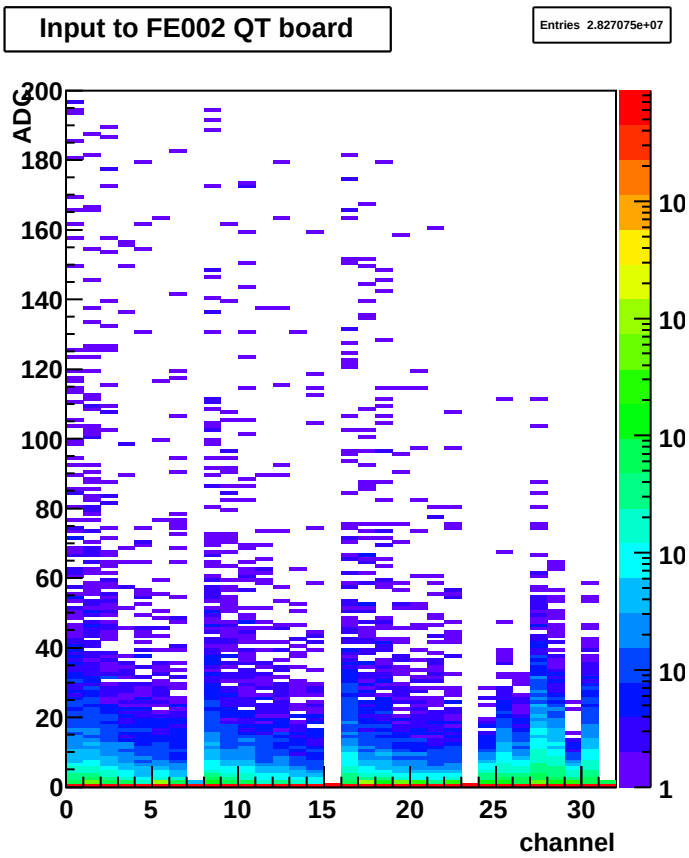

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

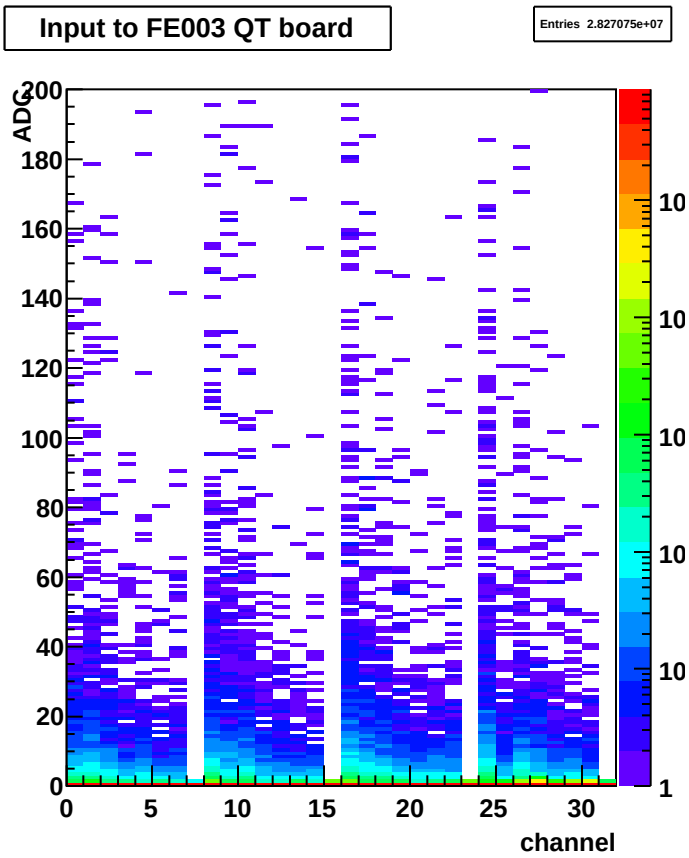

Input to FPD L3 DSM
Entries 1.413538e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

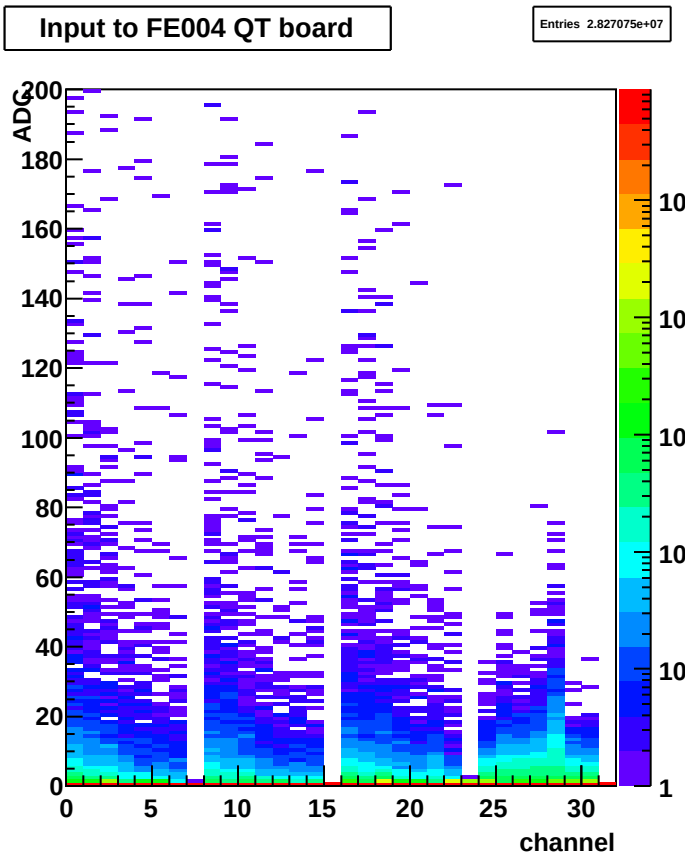

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

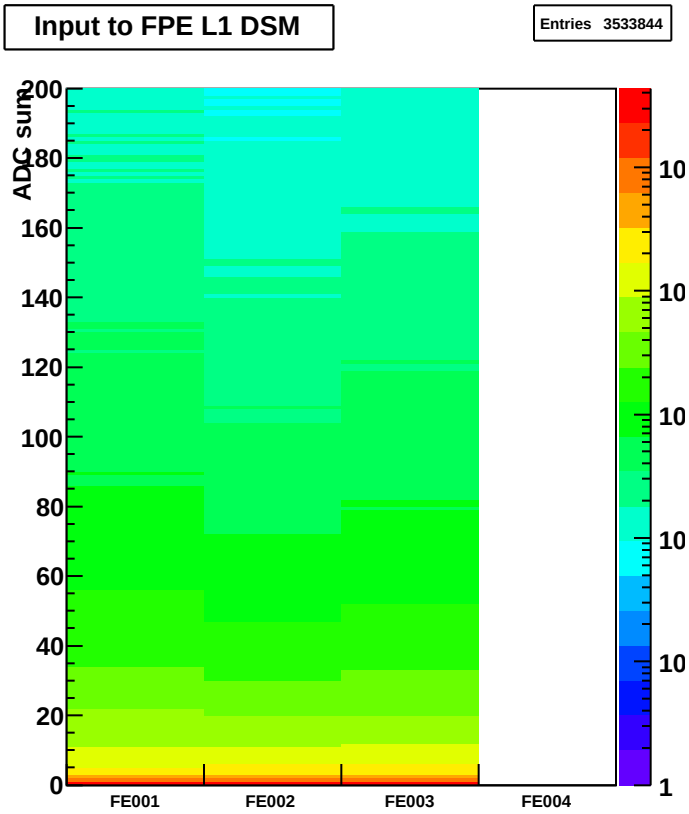

Input to FPD L2 DSM
Entries 3533844
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

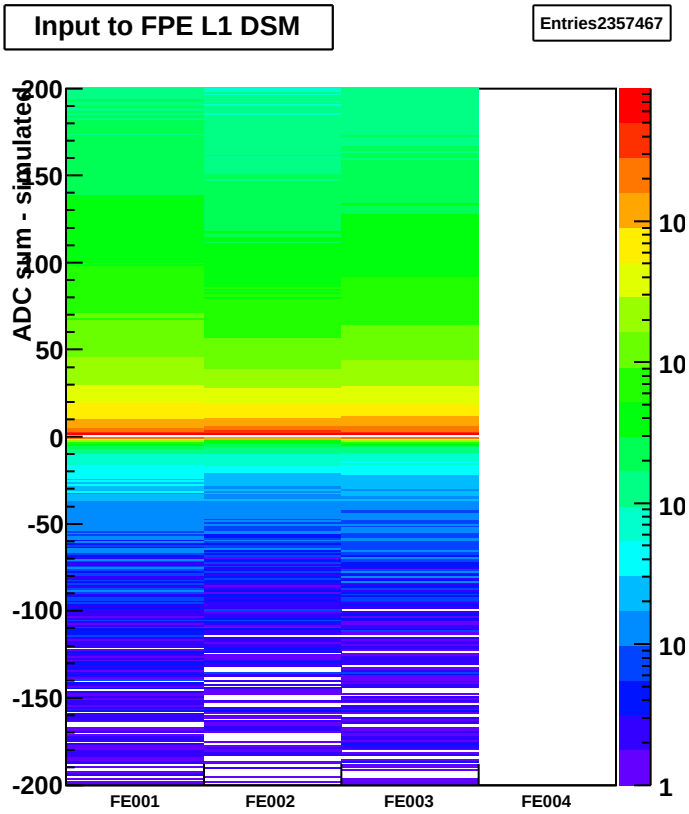

Input to FE001 QT board
Entries 2.827075e+07
ADC
channel

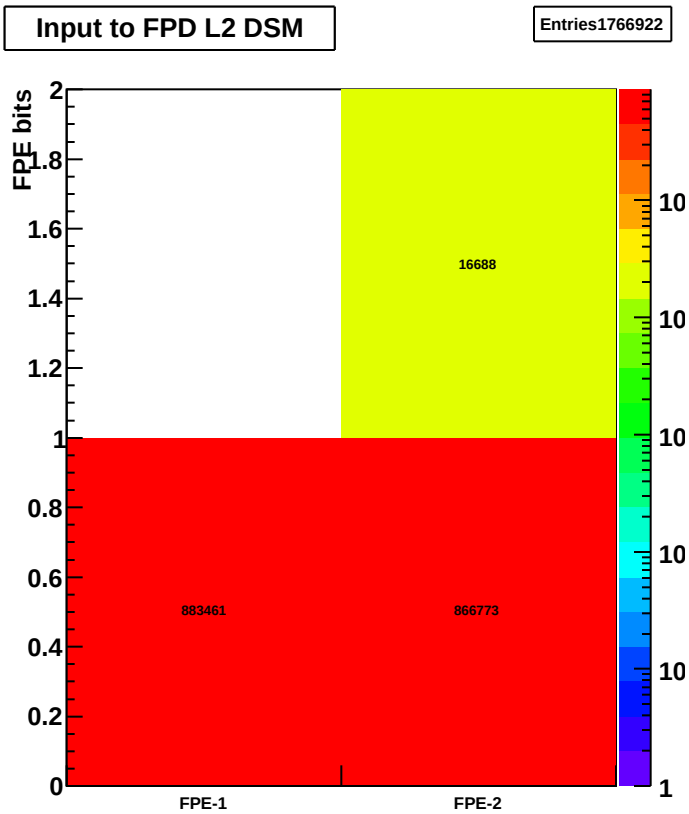

Input to FE002 QT board
Entries 2.827075e+07
ADC
channel

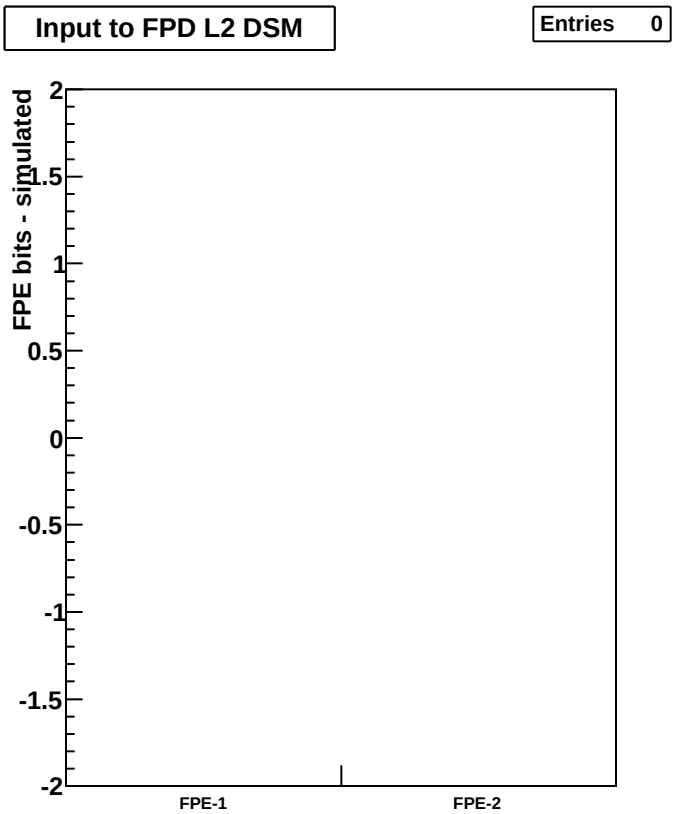

Input to FE003 QT board
Entries 2.827075e+07
ADC
channel


Input to FE004 QT board
Entries 2.827075e+07
ADC
channel


Input to FPE L1 DSM
Entries 3533844
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries2357467
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1766922
FPE bits
16688
883461
866773
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2