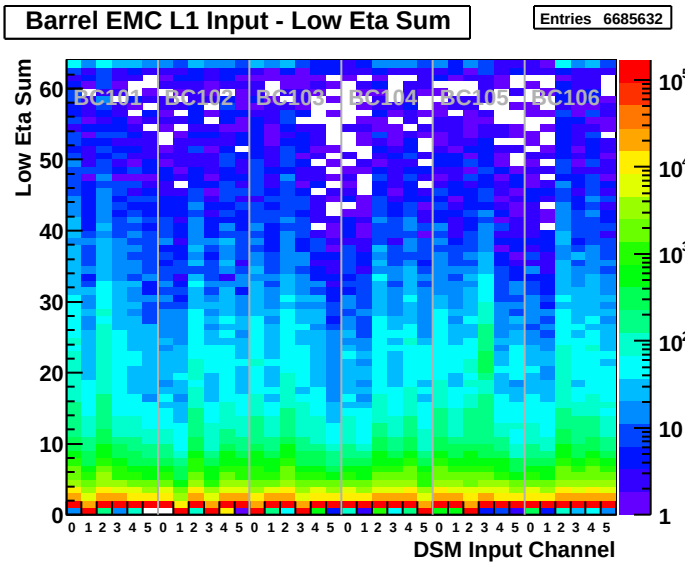
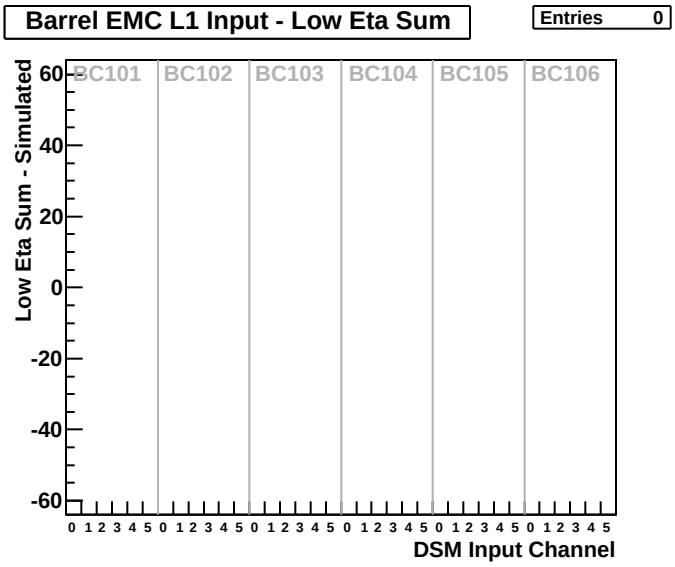
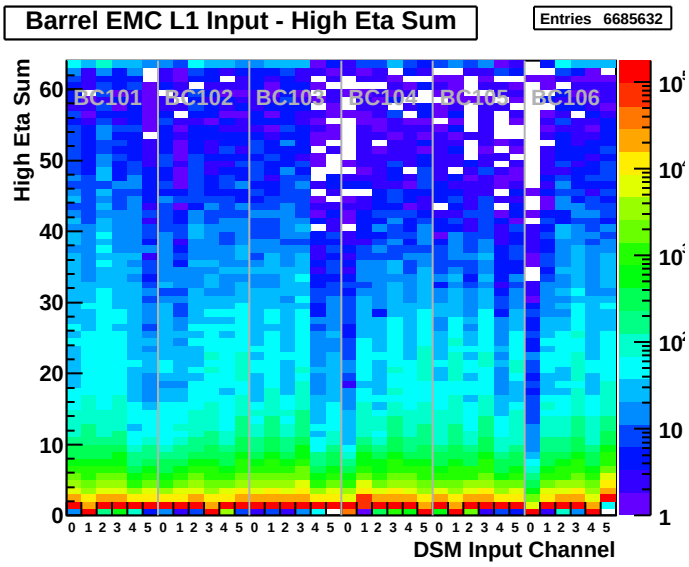
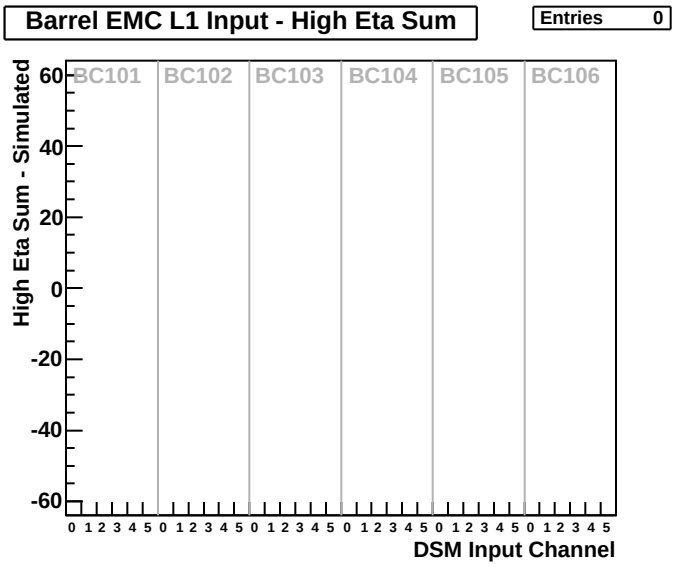
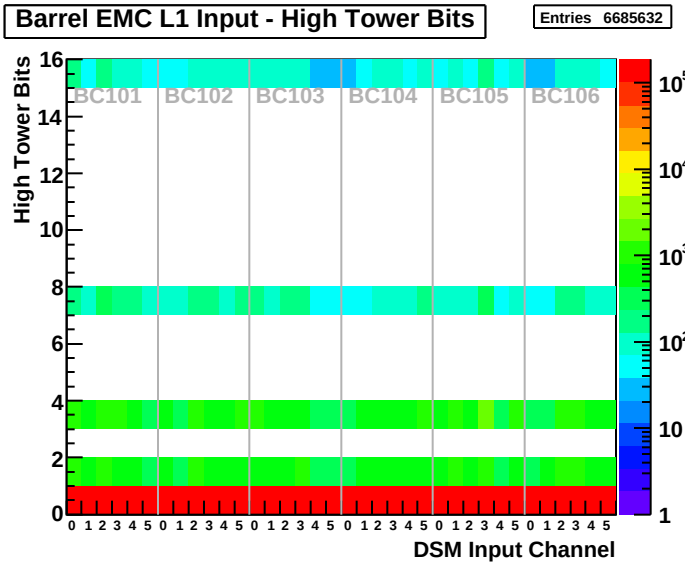
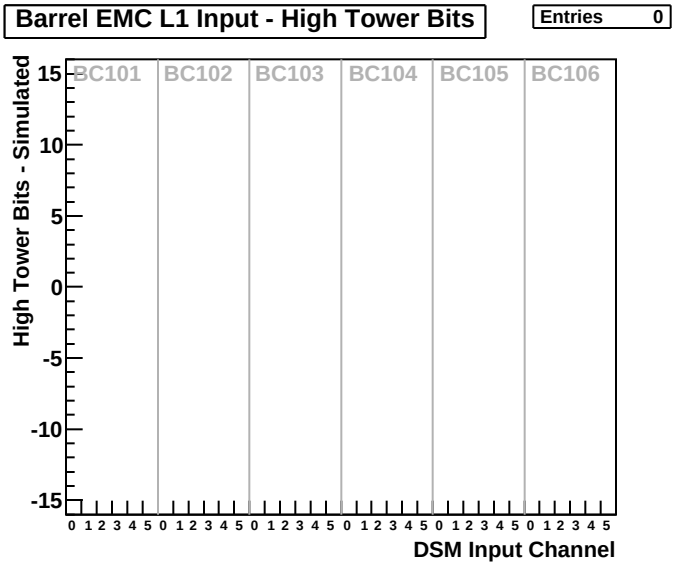

Barrel EMC L1 Input - Low Eta Sum
Entries 6685632
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

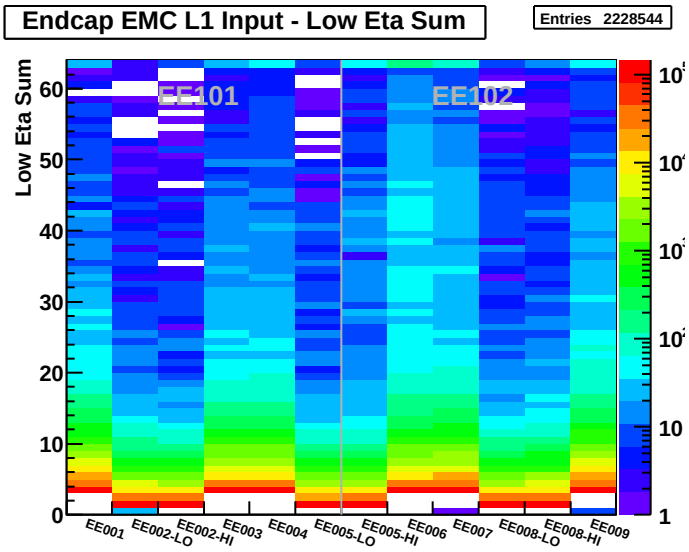

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

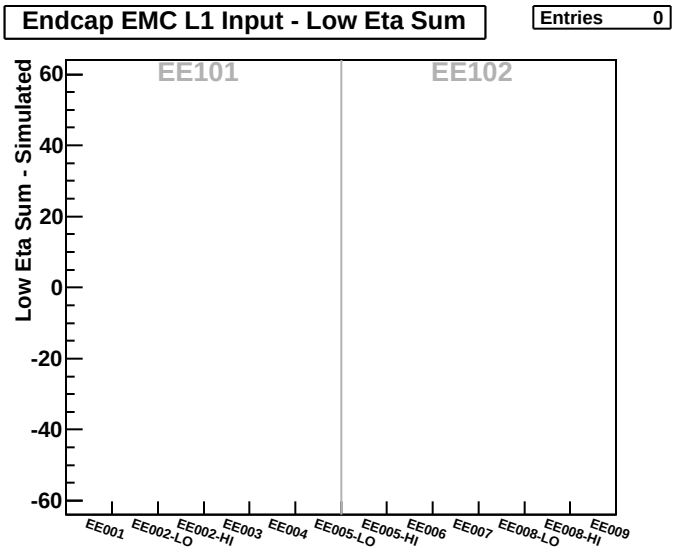

Barrel EMC L1 Input - High Eta Sum
Entries 6685632
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

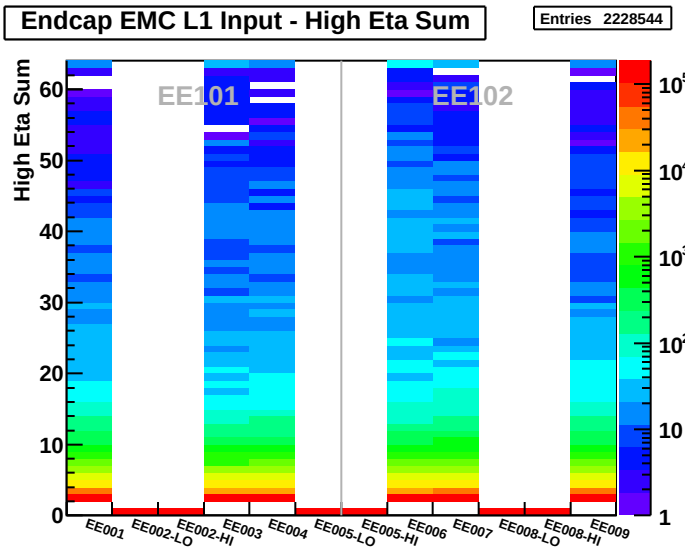

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

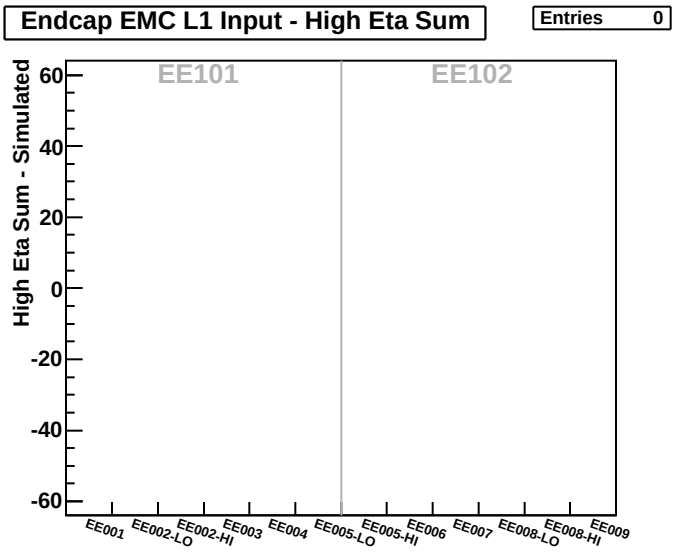

Barrel EMC L1 Input - High Tower Bits
Entries 6685632
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

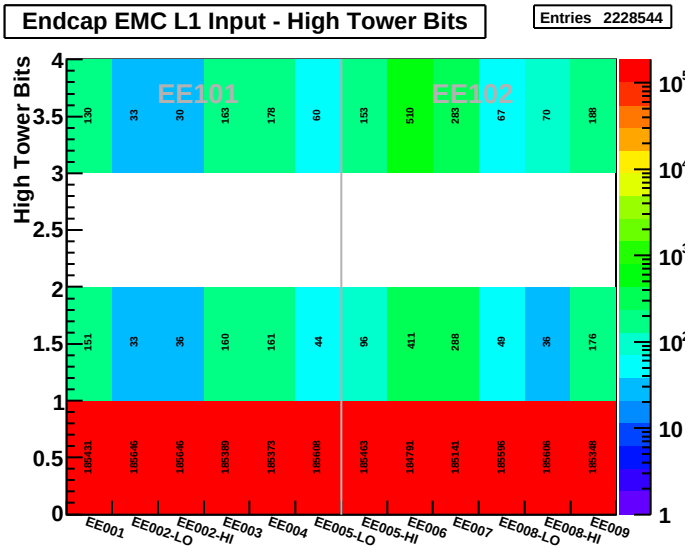

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

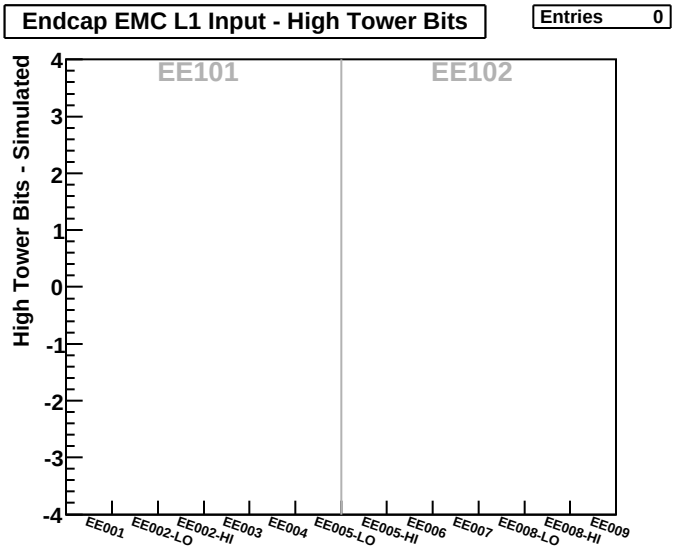

Endcap EMC L1 Input - Low Eta Sum
Entries 2228544
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

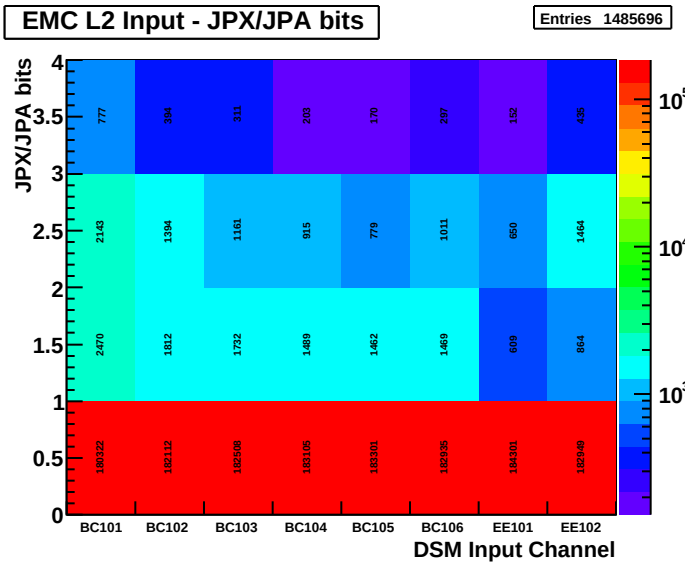

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

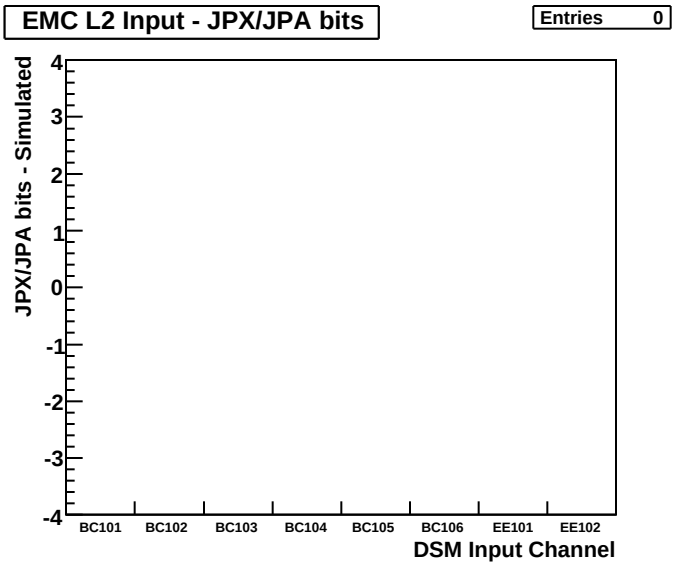

Endcap EMC L1 Input - High Eta Sum
Entries 2228544
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

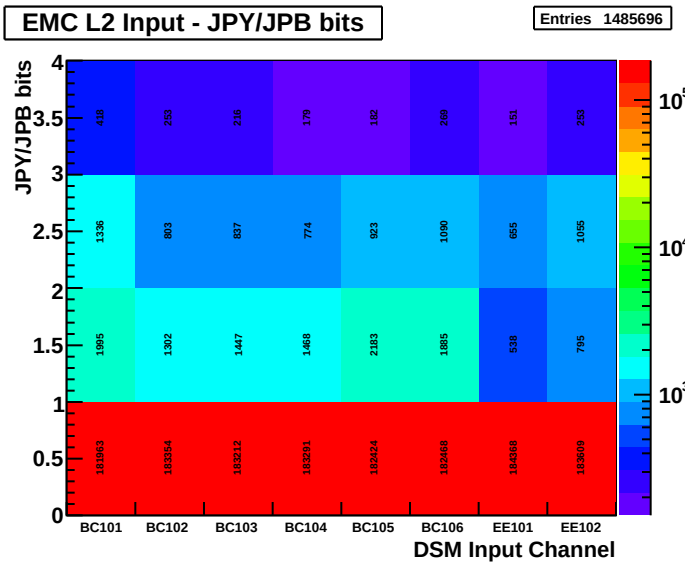

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

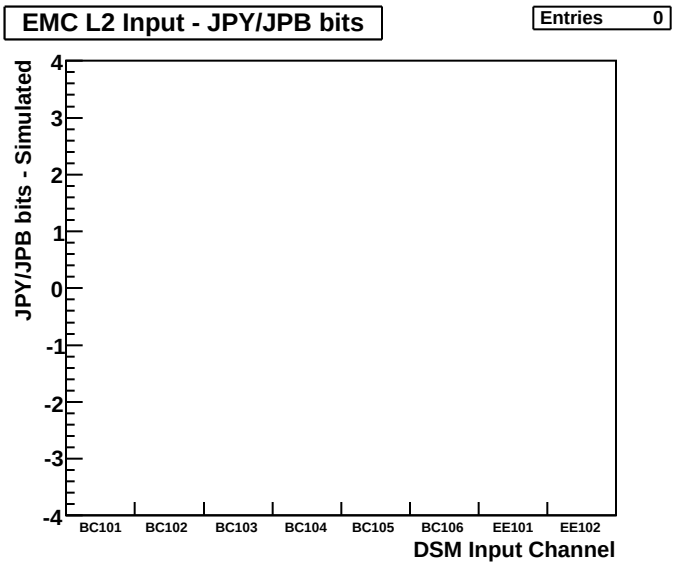

Endcap EMC L1 Input - High Tower Bits
Entries 2228544
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

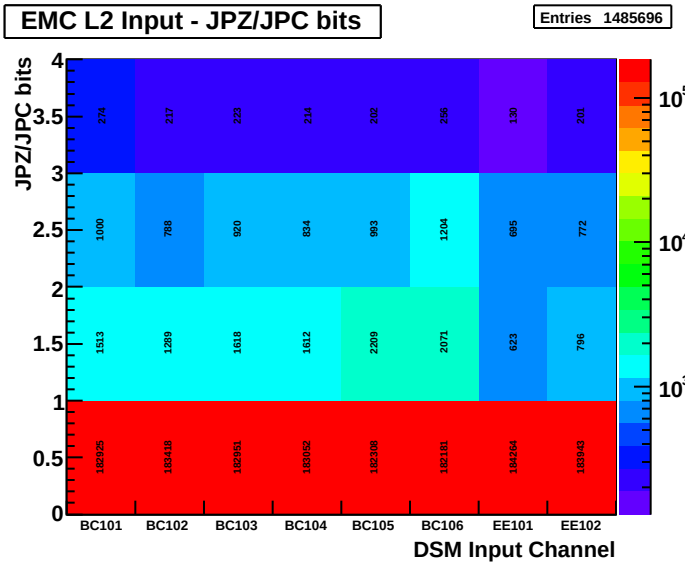

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

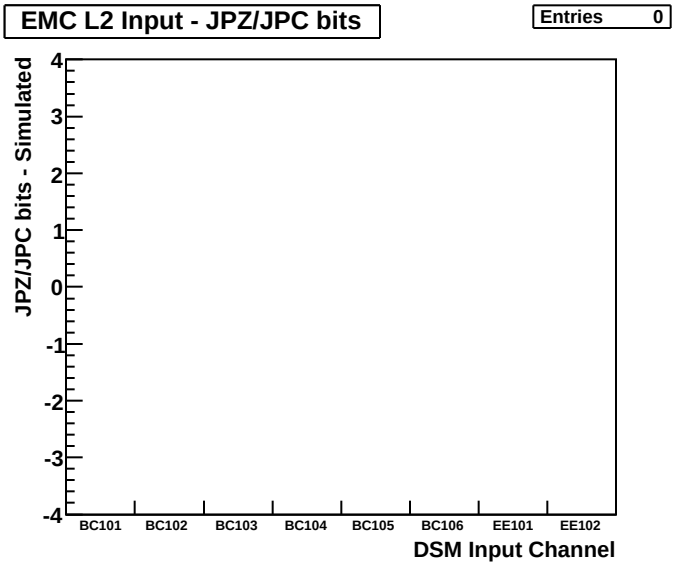

EMC L2 Input - JPX/JPA bits
Entries 1485696
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
777 394 311 203 170 297 152 435
2143 1394 1161 915 779 1011 650 1464
2470 1812 1732 1489 1462 1469 609 864
180322 182112 182508 183105 183301 182935 184301 182940

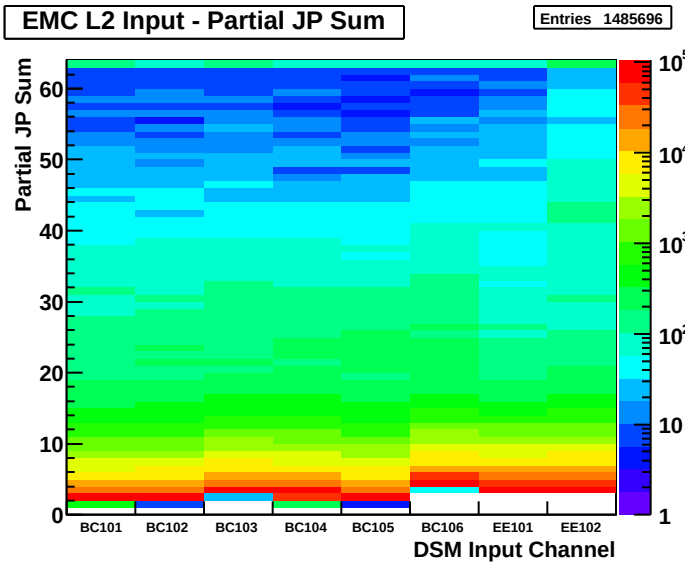

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

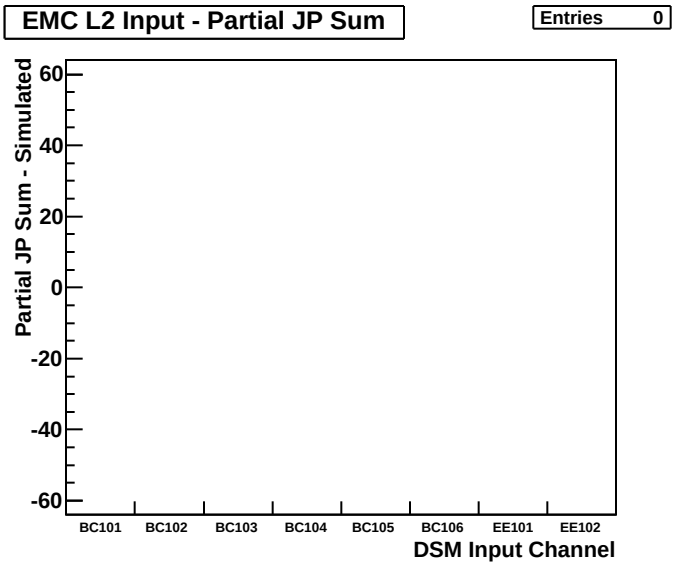

EMC L2 Input - JPY/JPB bits
Entries 1485696
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
416 263 216 170 182 269 151 235
1336 803 837 774 923 1090 655 1055
1995 1302 1447 1468 2183 1885 538 795
181963 182354 182212 182291 182424 182468 184368 183609

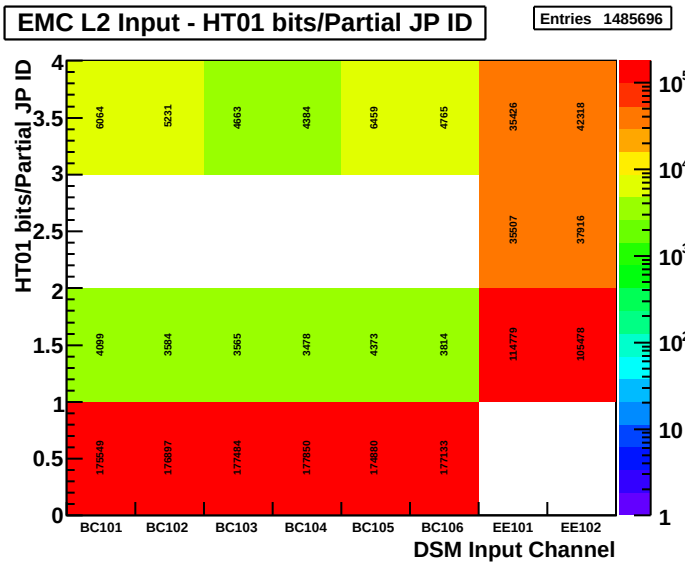

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

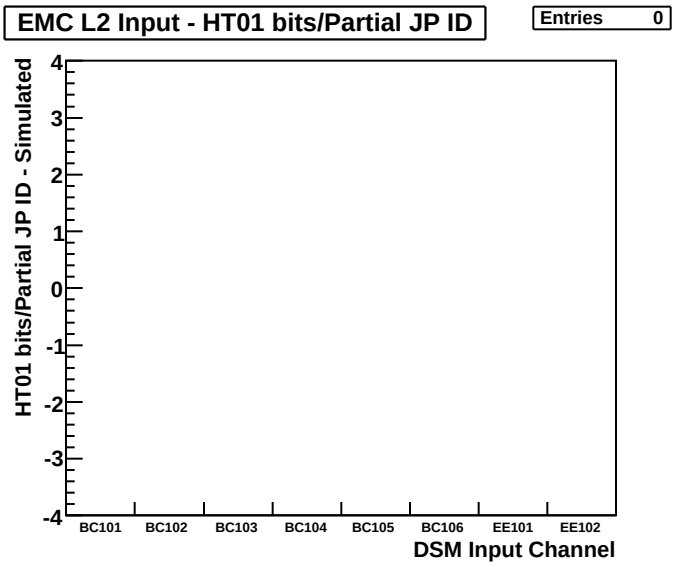

EMC L2 Input - JPZ/JPC bits
Entries 1485696
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
274 217 223 214 202 256 130 201
1000 788 920 834 993 1204 695 772
1513 1289 1618 1612 2209 2071 623 796
182925 183418 182951 183052 182308 182181 184264 183943

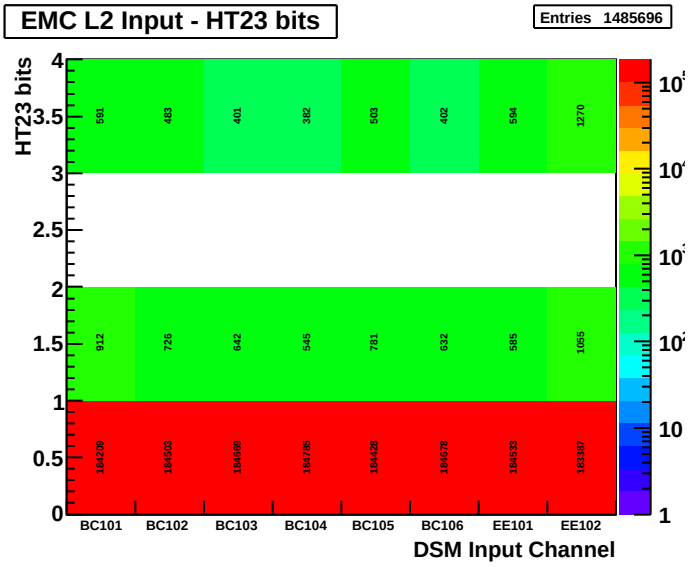

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

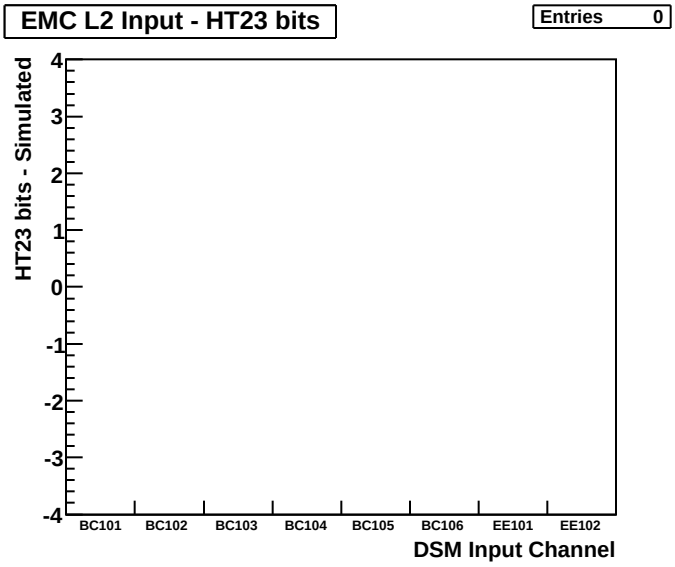

EMC L2 Input - Partial JP Sum
Entries 1485696
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

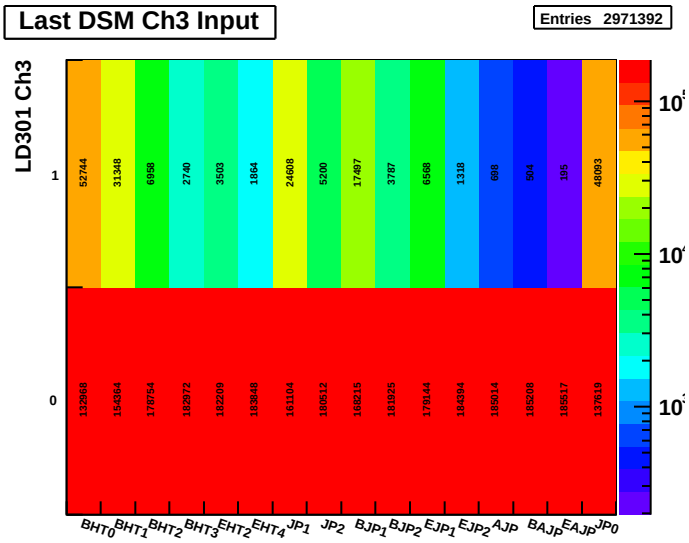

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

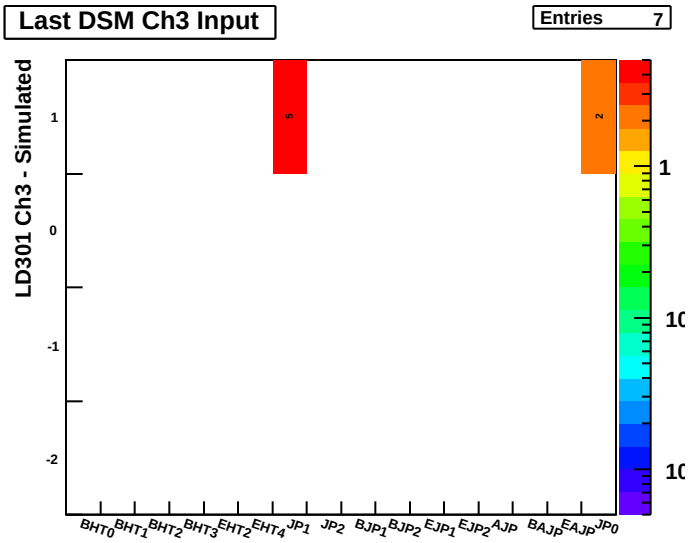

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1485696
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

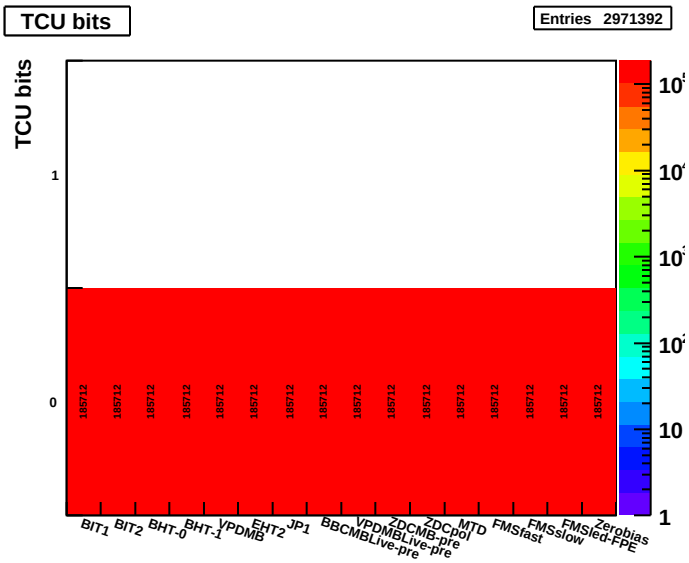

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

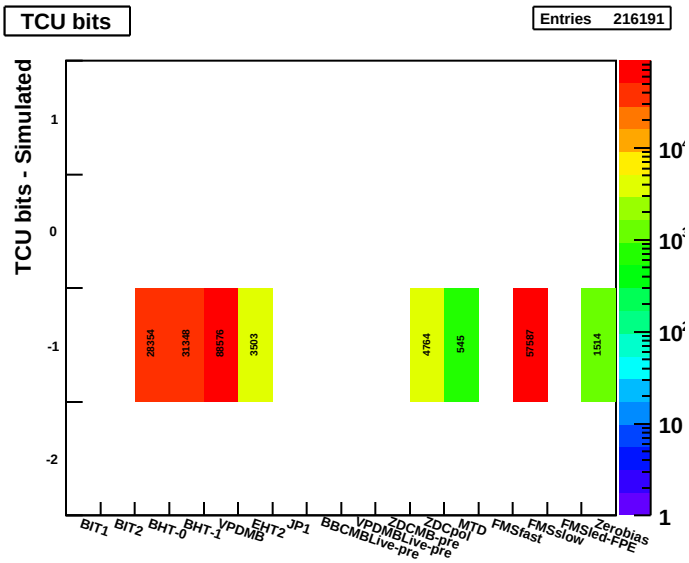

EMC L2 Input - HT23 bits
Entries 1485696
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

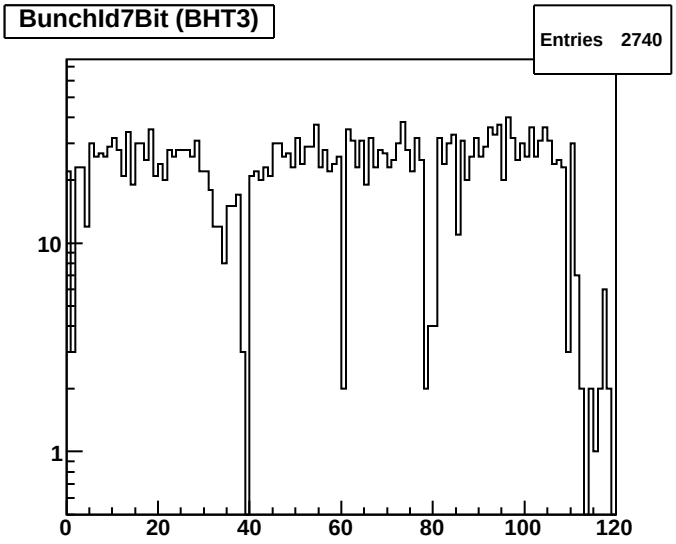

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

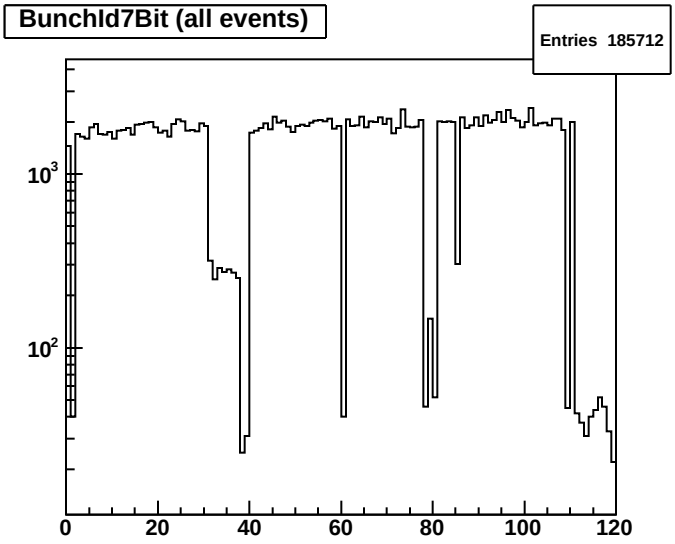

Last DSM Ch3 Input
Entries 2971392
LD301 Ch3
52744 31348 6958 2740 3503 1864 24608 5200 17497 3787 6568 1318 698 504 195 48093
132968 154364 178754 182972 182209 183848 161104 180512 168215 181925 179144 184394 185014 185208 185517 137619
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

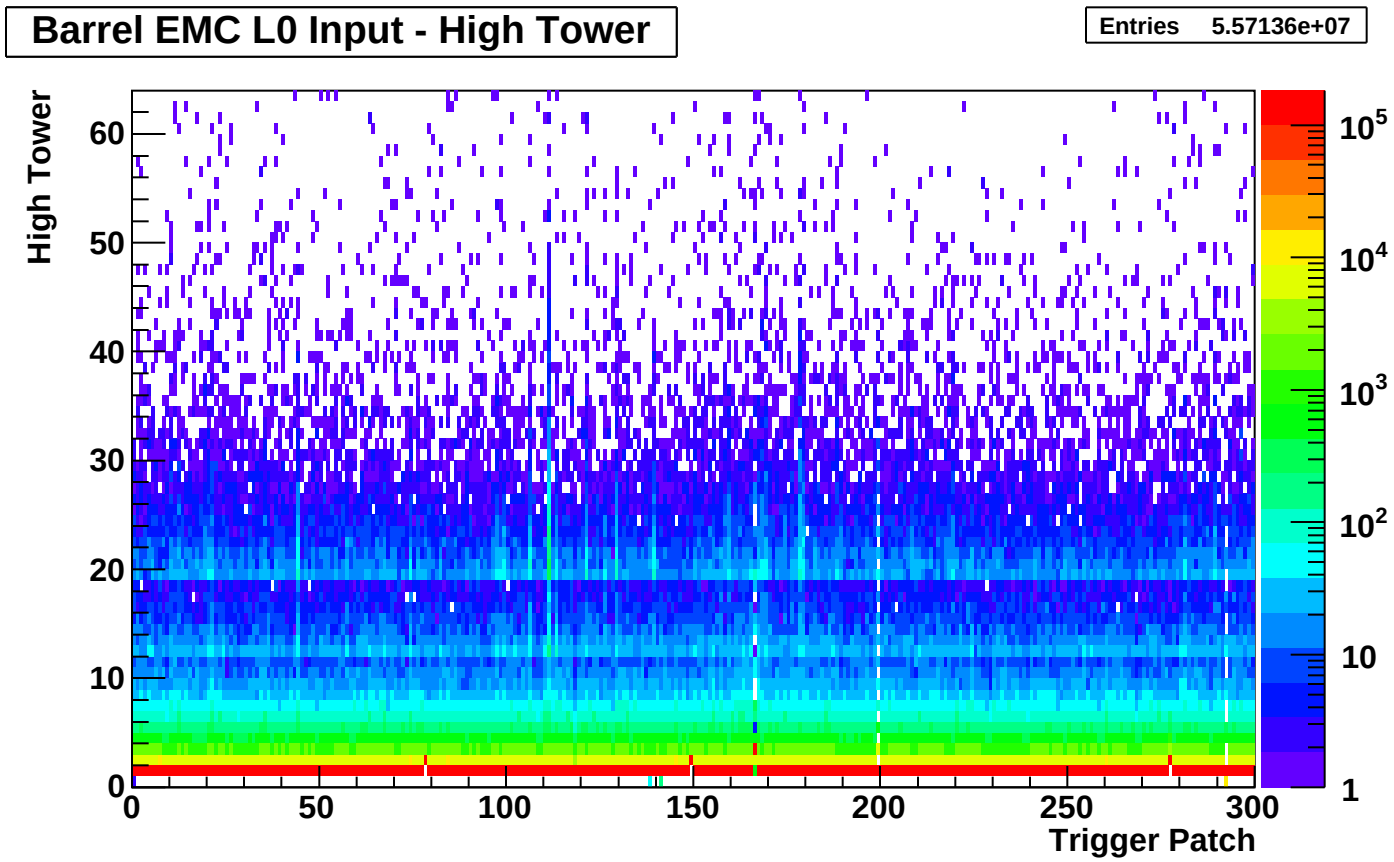

Last DSM Ch3 Input
Entries 7
LD301 Ch3 - Simulated
5
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

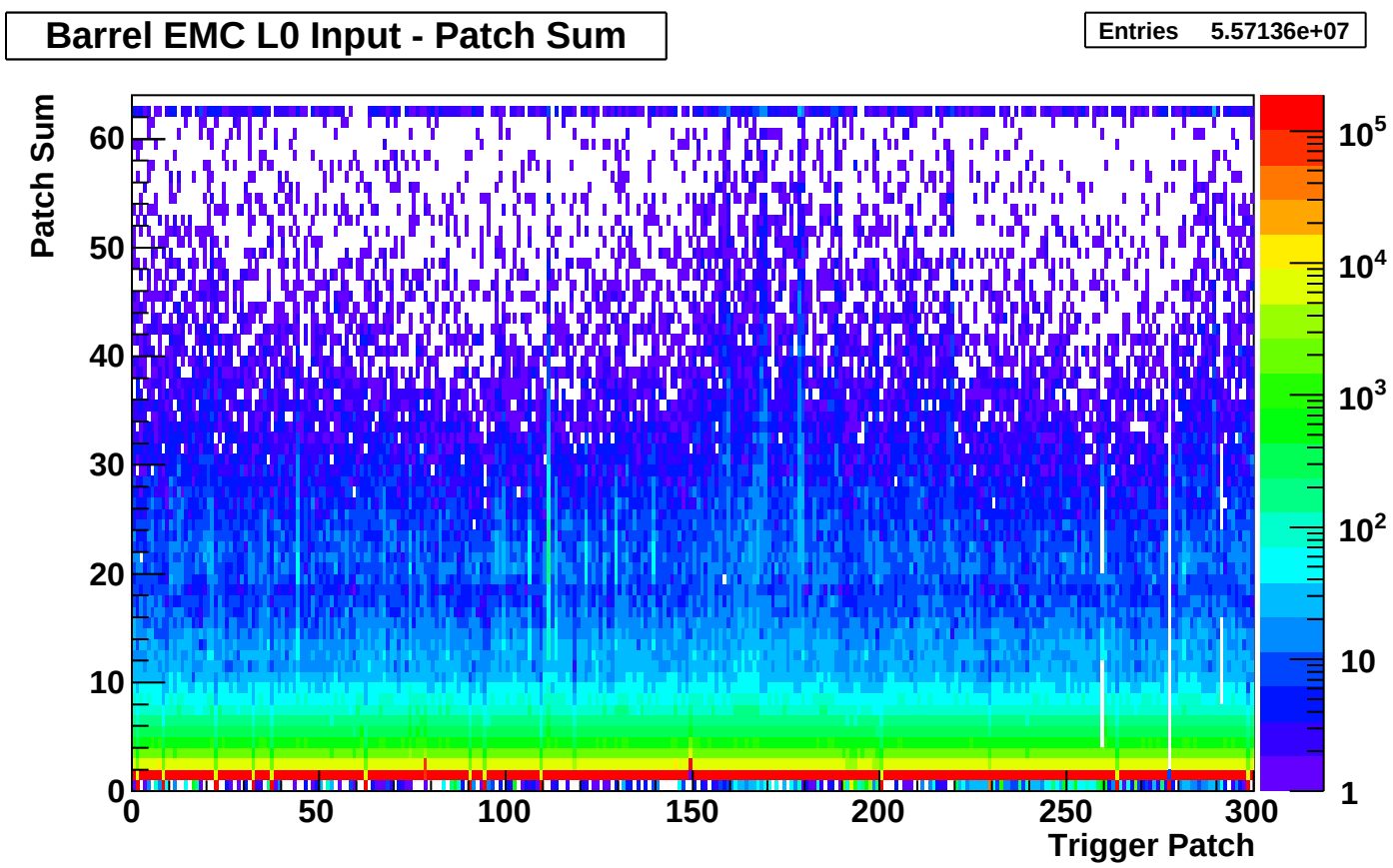

TCU bits
Entries 2971392
TCU bits
185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712 185712
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

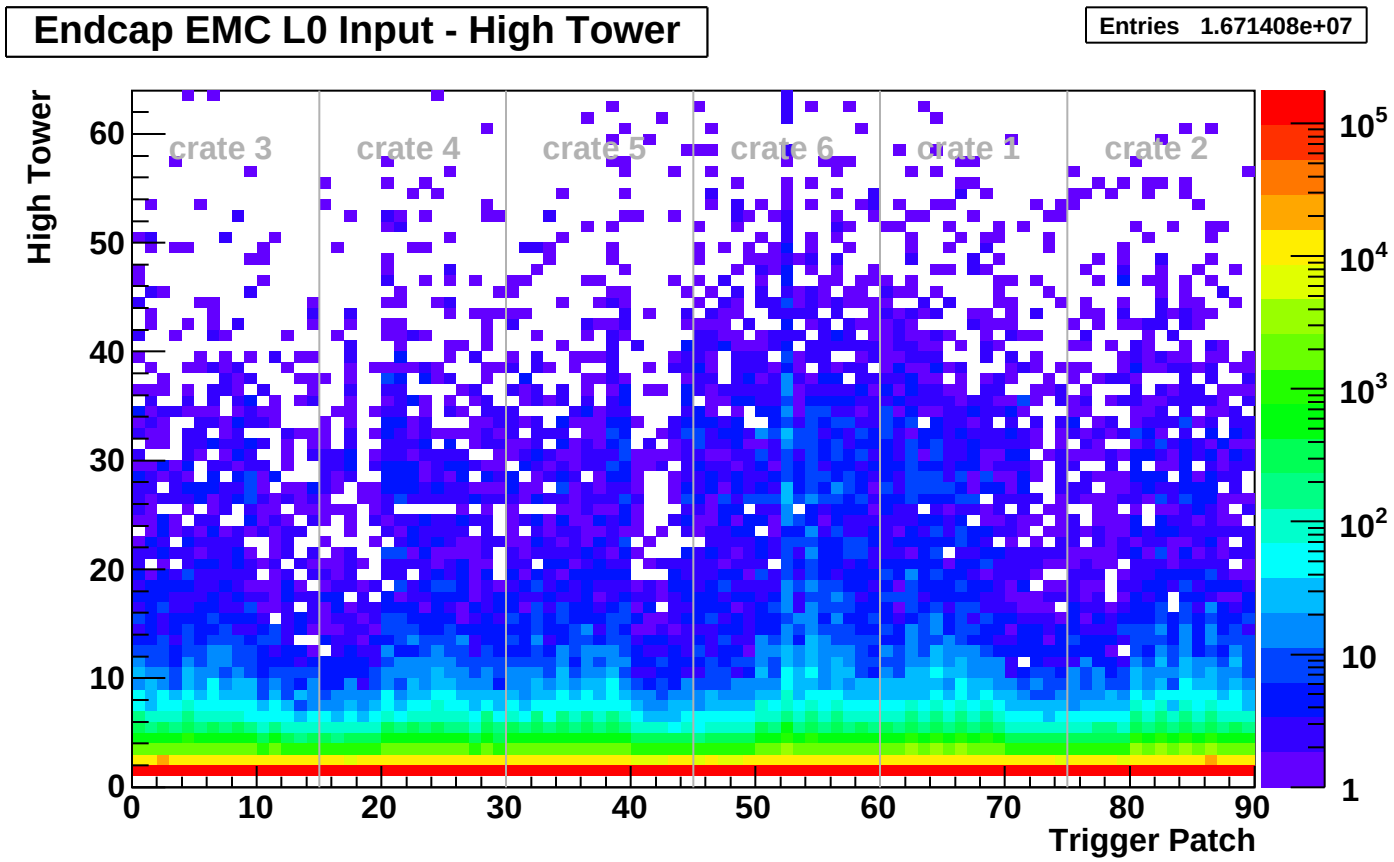

TCU bits
Entries 216191
TCU bits - Simulated
28354 31348 88576 3503 4764 545 57587 1514
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

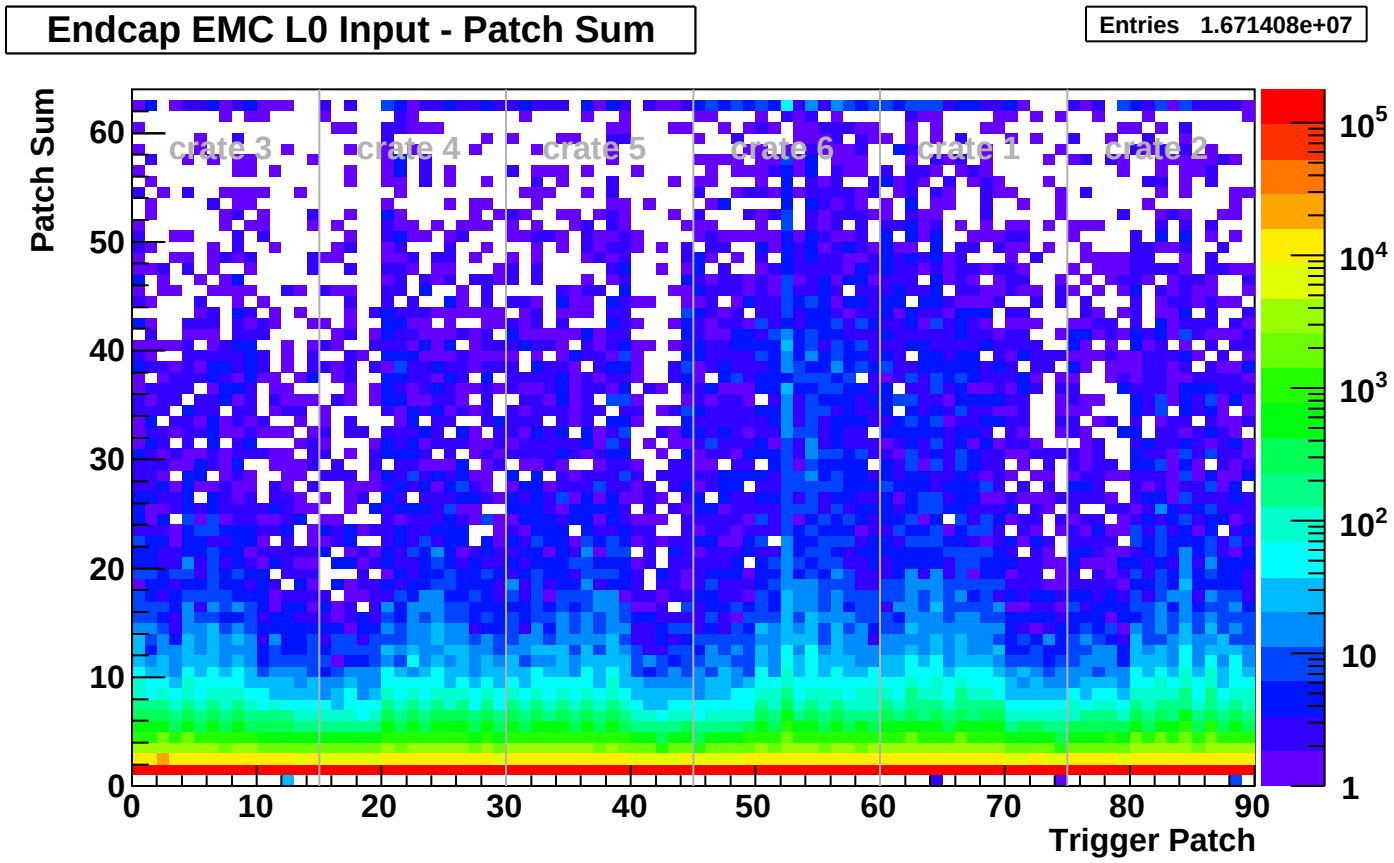

BunchId7Bit (BHT3)
Entries 2740

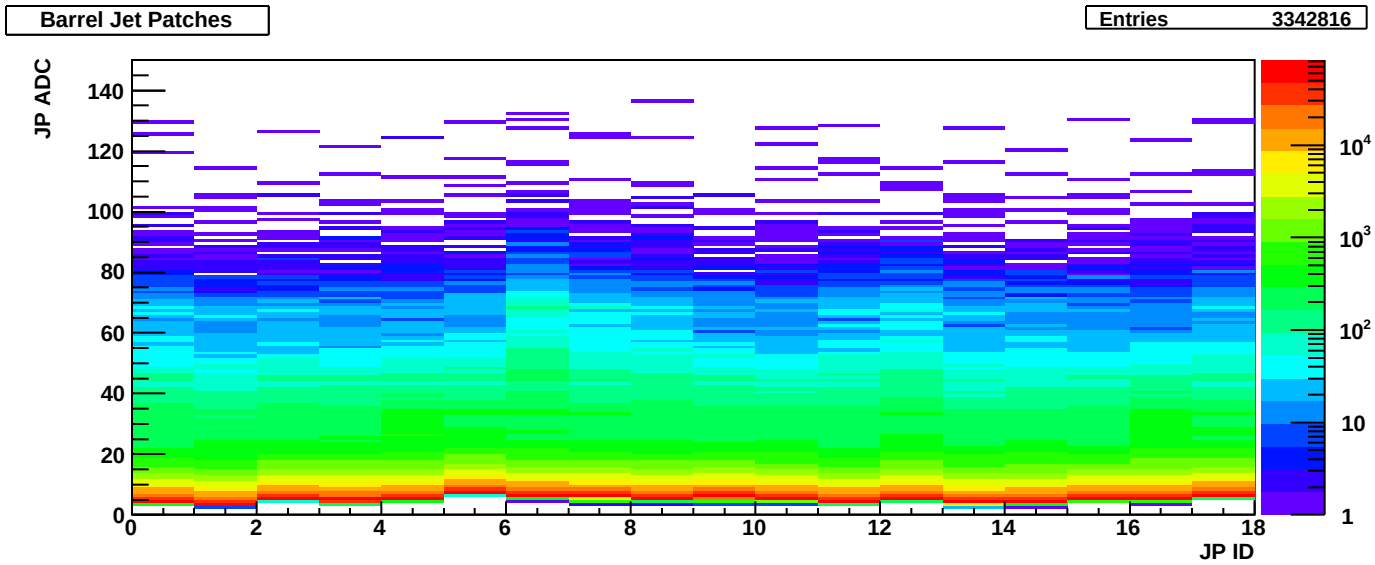

BunchId7Bit (all events)
Entries 185712

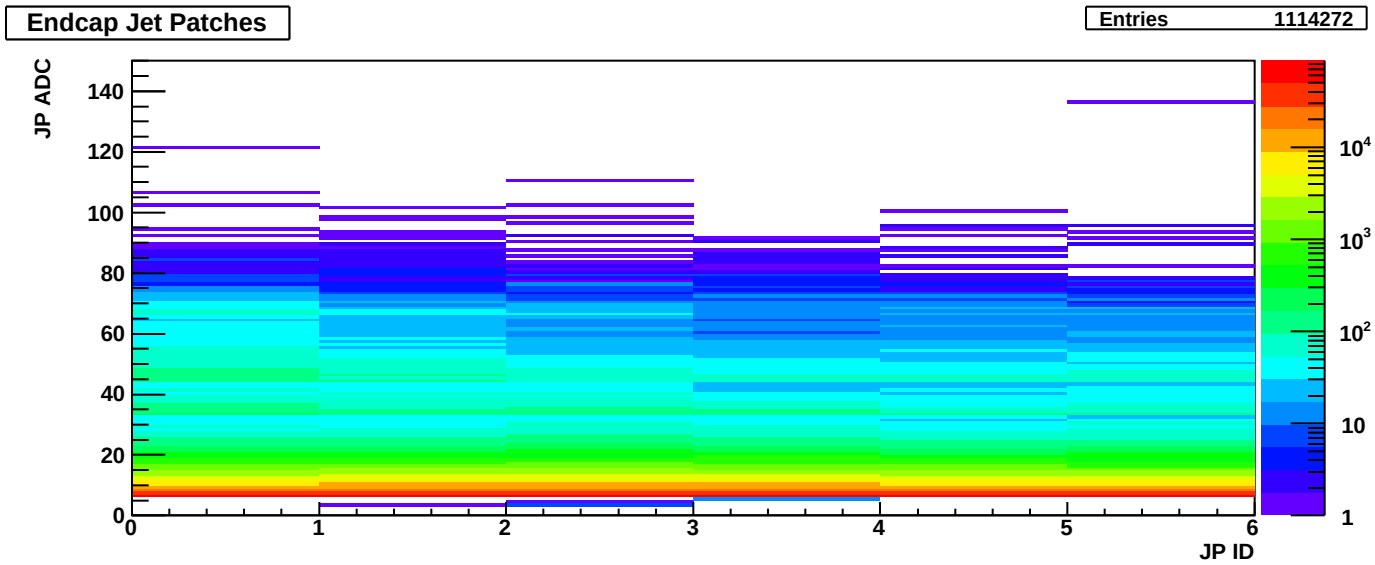

Barrel EMC L0 Input - High Tower
Entries 5.57136e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

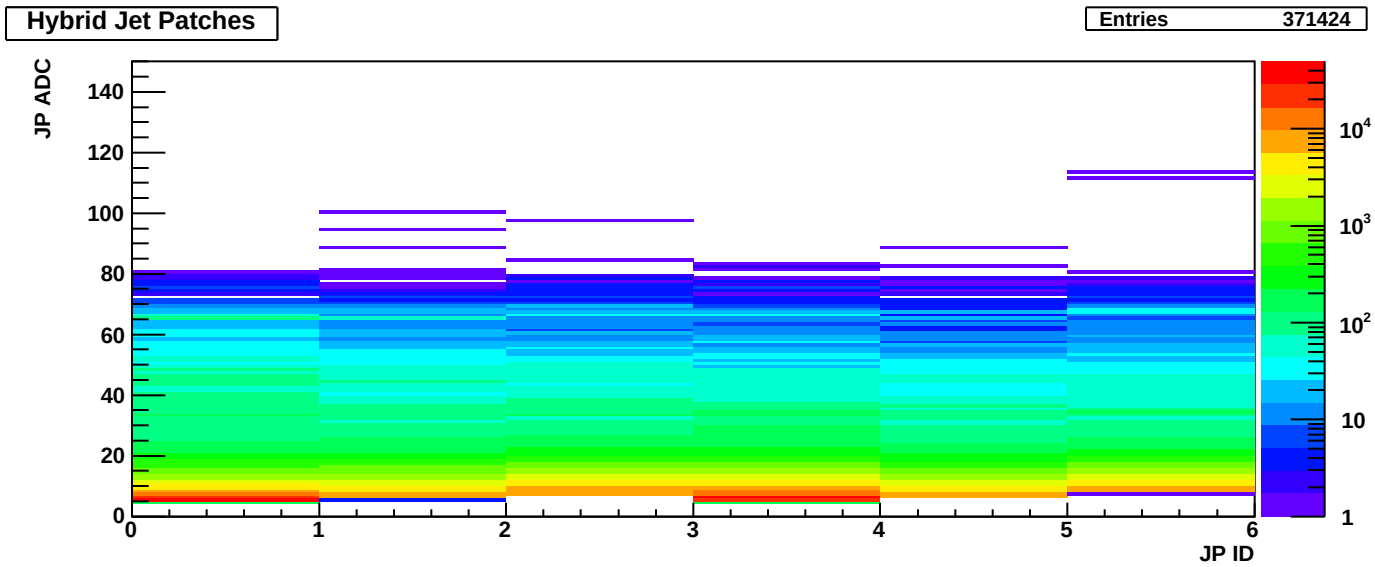

Barrel EMC L0 Input - Patch Sum
Entries 5.57136e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

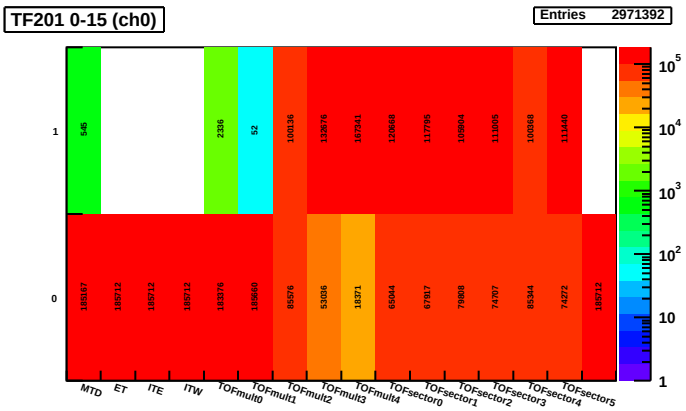

Endcap EMC L0 Input - High Tower
Entries 1.671408e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

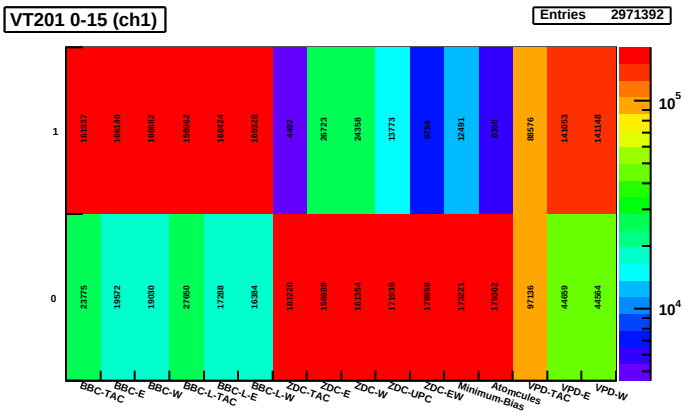

Endcap EMC L0 Input - Patch Sum
Entries 1.671408e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

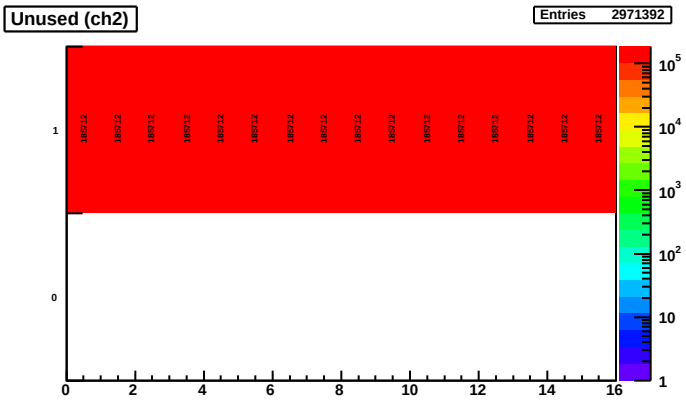

Barrel Jet Patches
Entries 3342816
JP ADC
JP ID

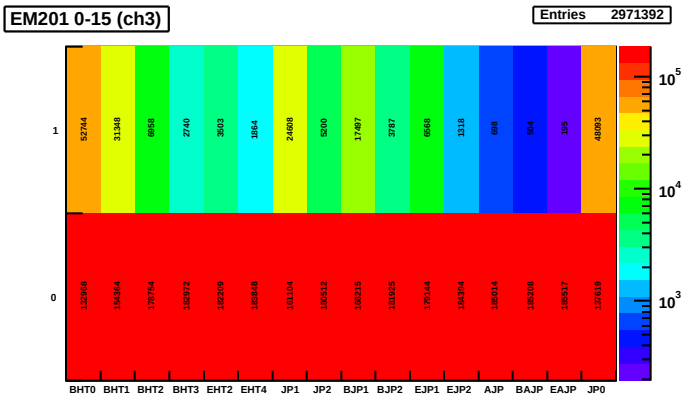

Endcap Jet Patches
Entries 1114272
JP ADC
JP ID

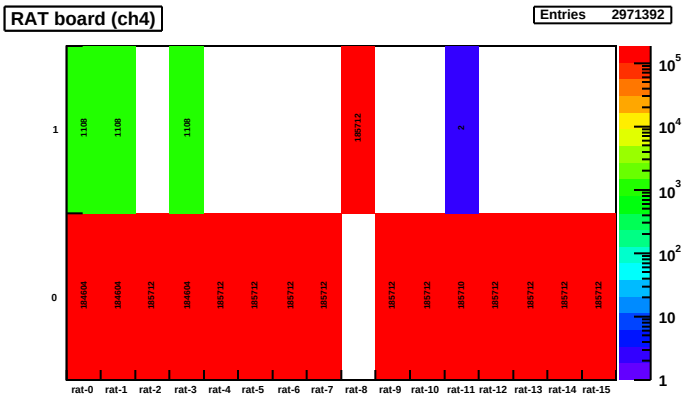

Hybrid Jet Patches
Entries 371424
JP ADC
JP ID

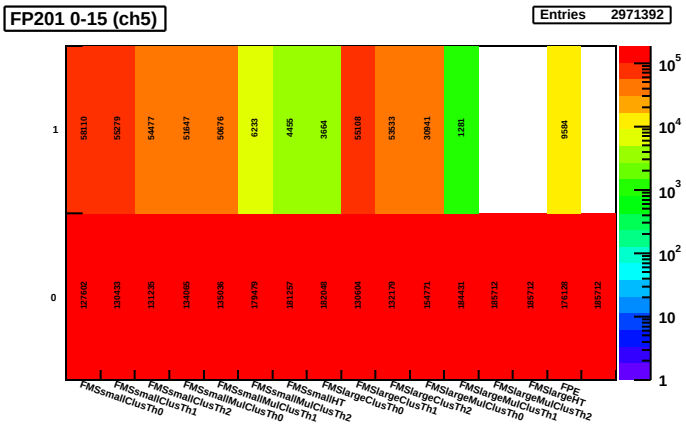

TF201 0-15 (ch0)
Entries 2971392
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

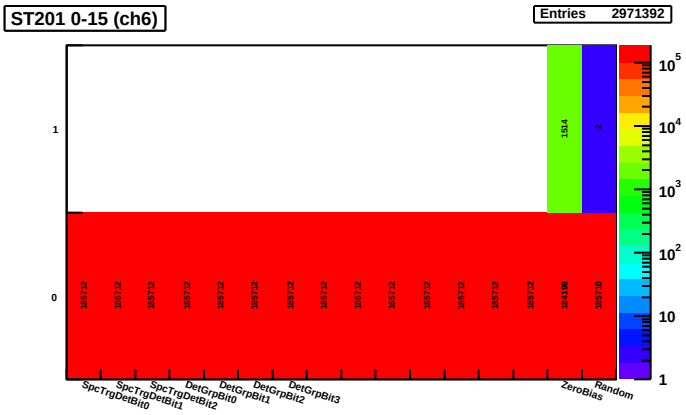

VT201 0-15 (ch1)
Entries 2971392
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

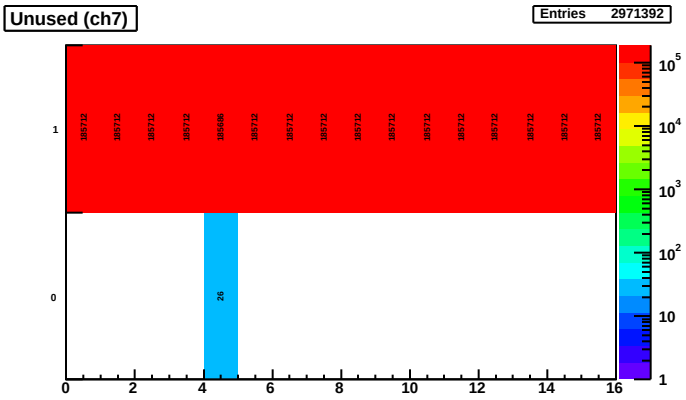

Unused (ch2)
Entries 2971392

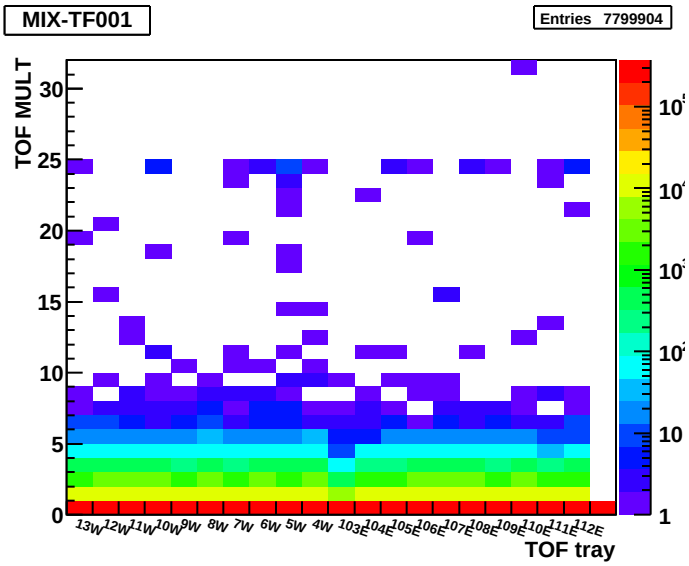

EM201 0-15 (ch3)
Entries 2971392
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

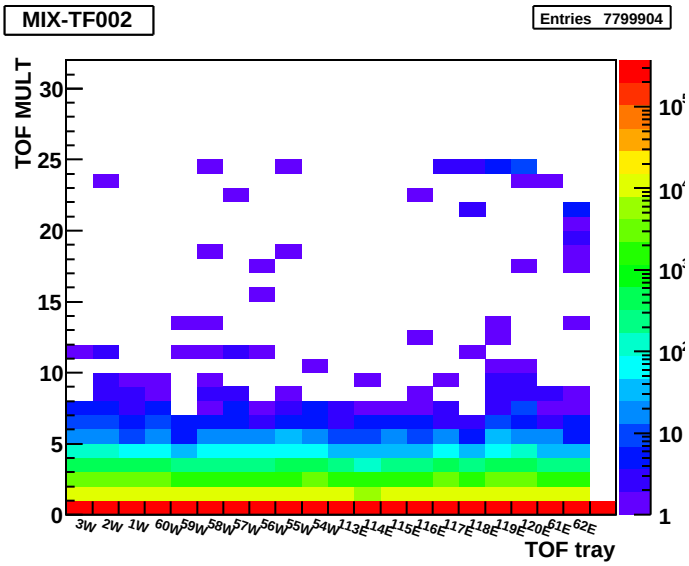

RAT board (ch4)
Entries 2971392
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

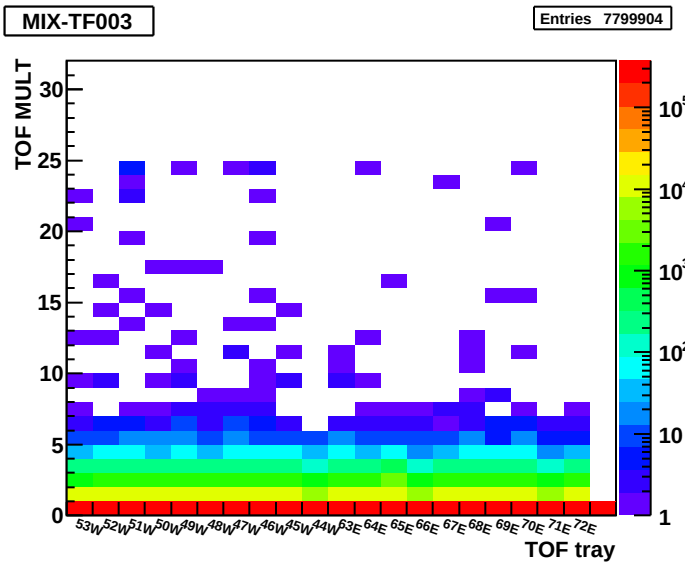

FP201 0-15 (ch5)
Entries 2971392
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

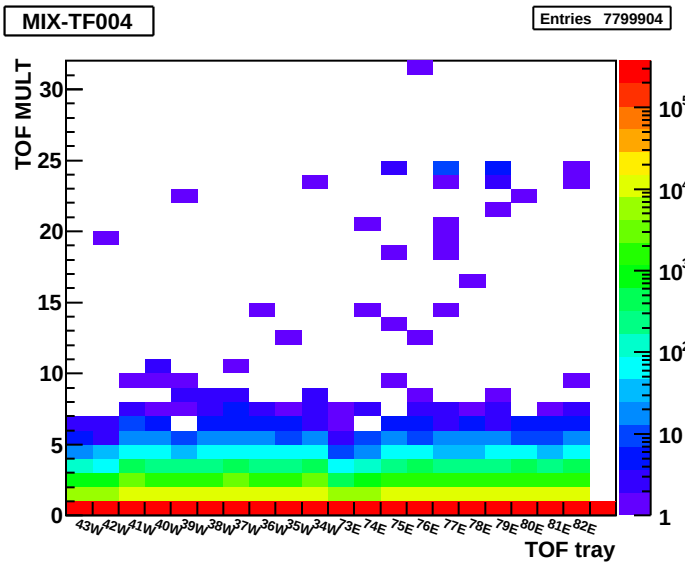

ST201 0-15 (ch6)
Entries 2971392
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

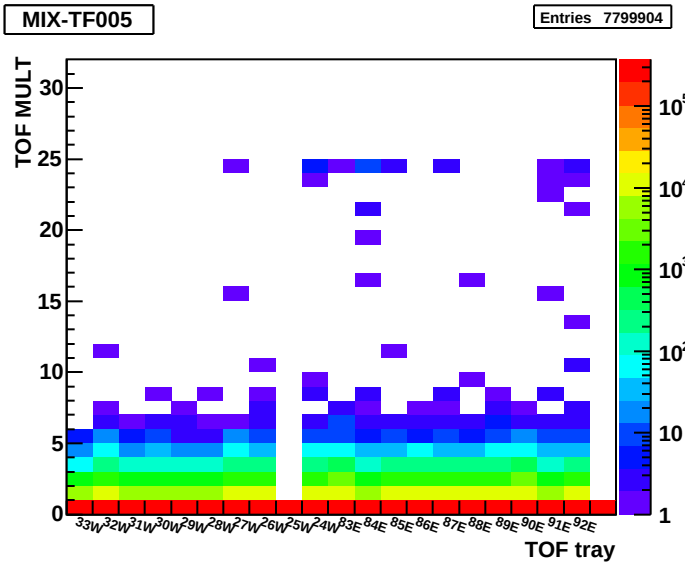

Unused (ch7)
Entries 2971392

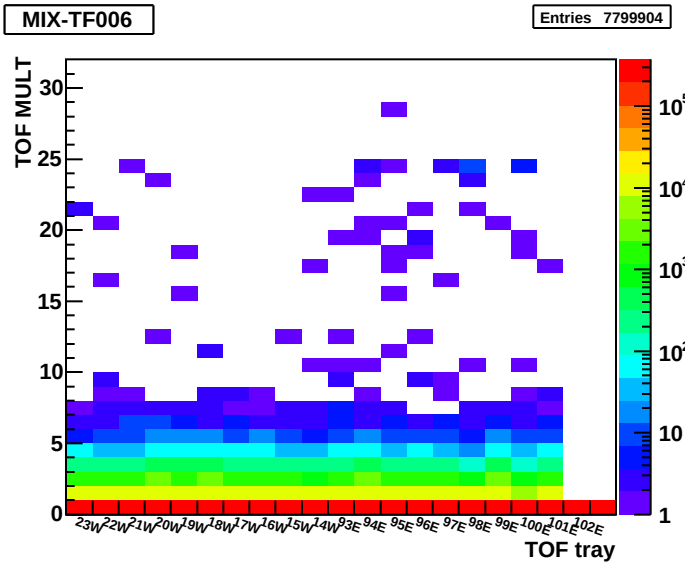

MIX-TF001
Entries 7799904
TOF MULT
TOF tray

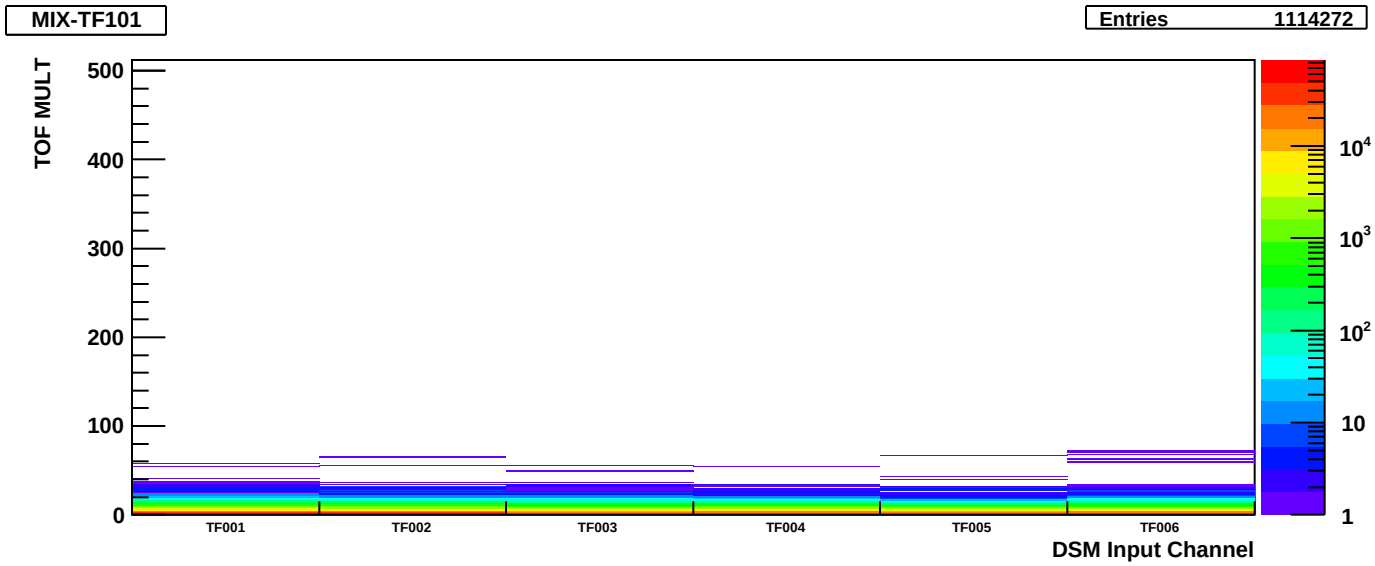

MIX-TF002
Entries 7799904
TOF MULT
TOF tray

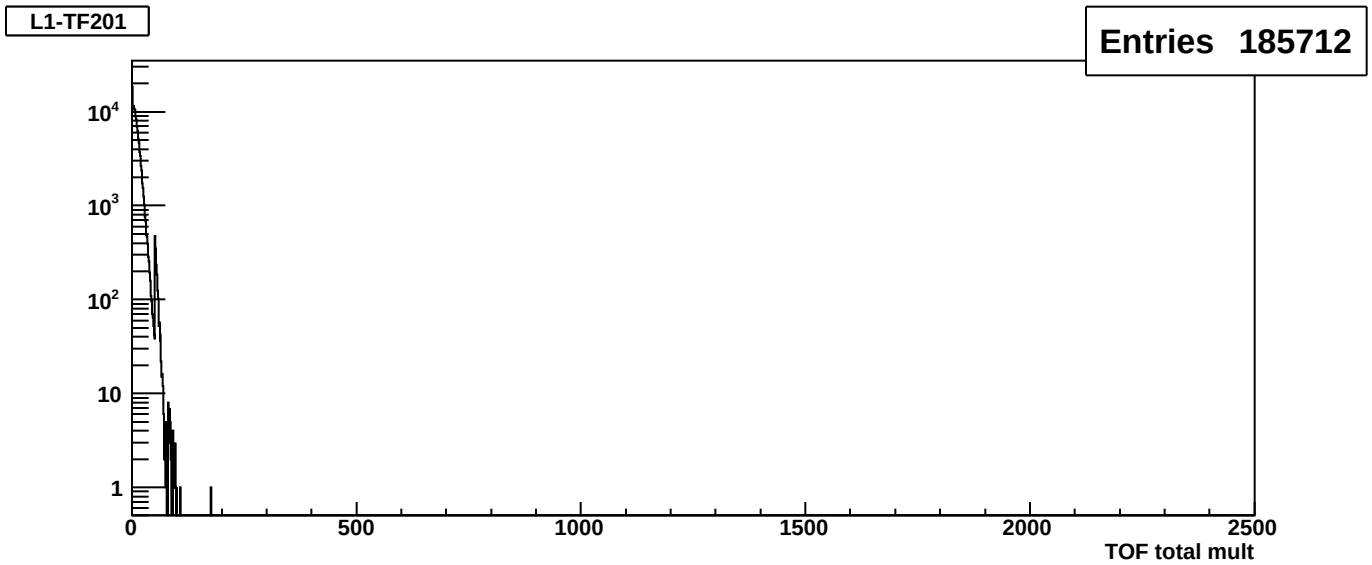

MIX-TF003
Entries 7799904
TOF MULT
TOF tray

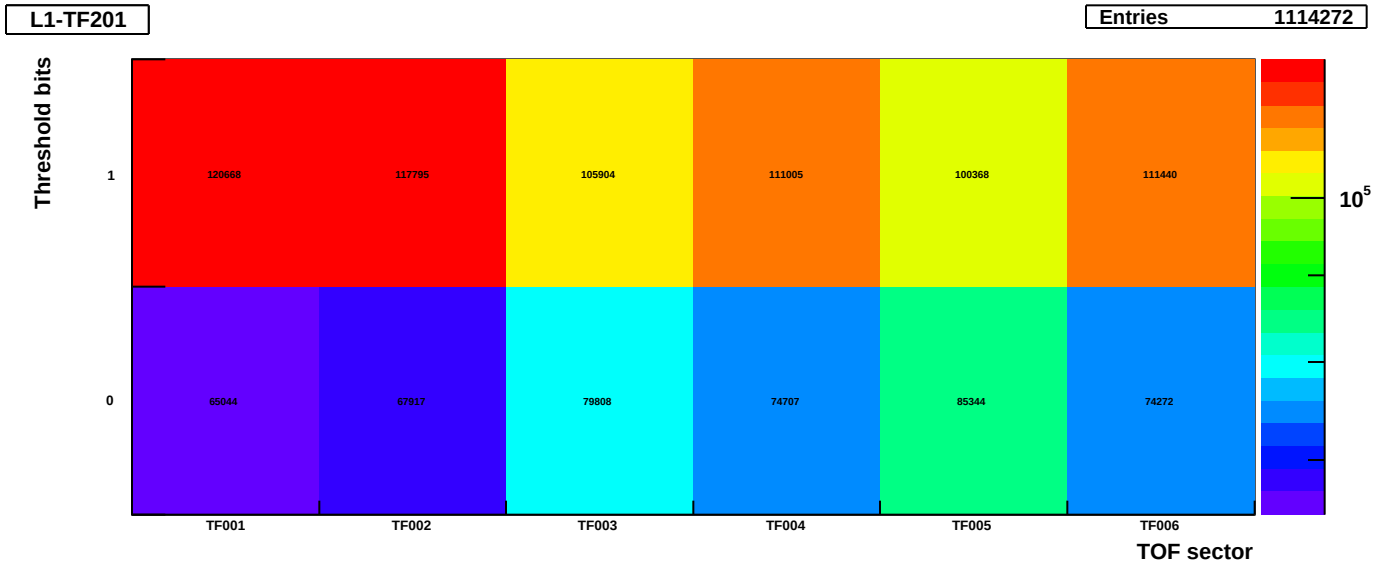

MIX-TF004
Entries 7799904
TOF MULT
TOF tray

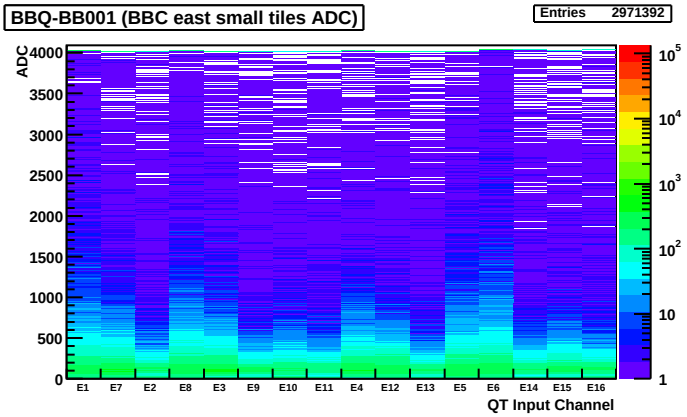

MIX-TF005
Entries 7799904
TOF MULT
TOF tray

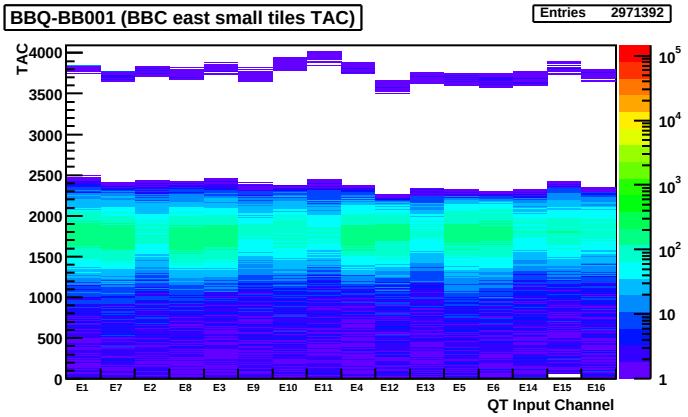

MIX-TF006
Entries 7799904
TOF MULT
TOF tray

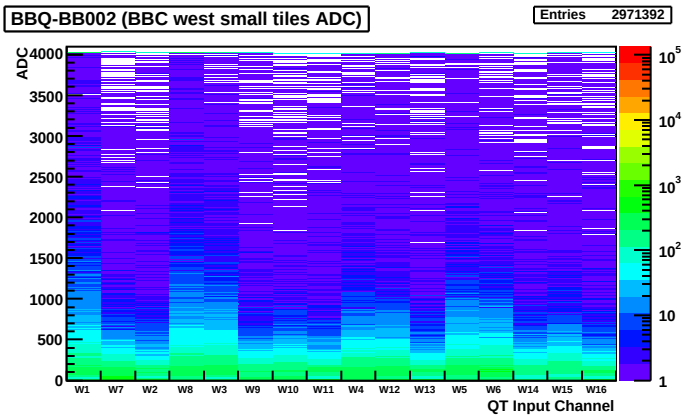

MIX-TF101
Entries 1114272
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

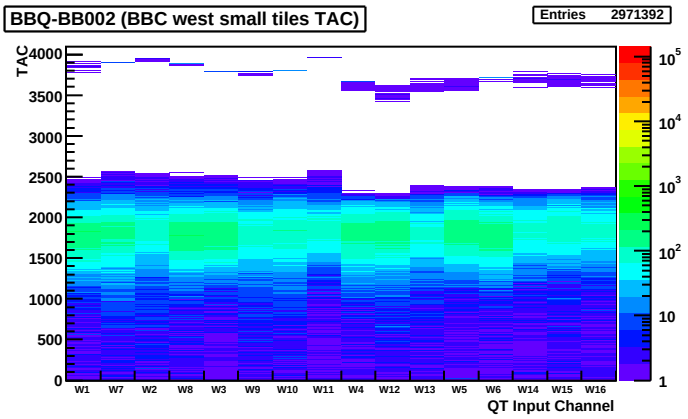

L1-TF201
Entries 185712
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

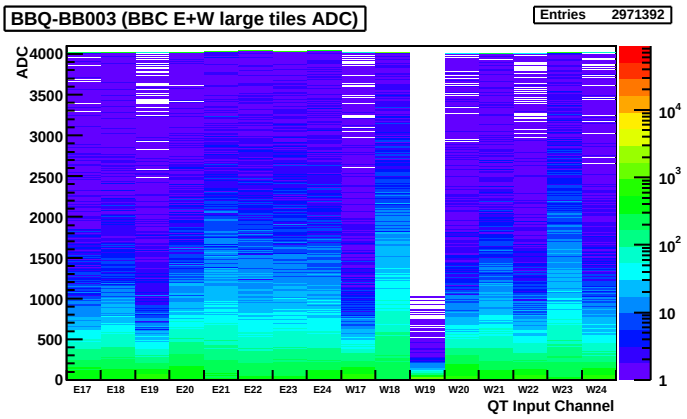

L1-TF201
Entries 1114272
Threshold bits
1
0
120668
117795
105904
111005
100368
111440
65044
67917
79808
74707
85344
74272
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

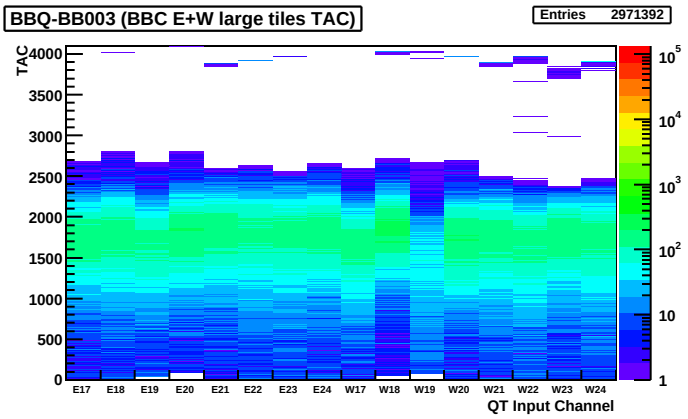

BBQ-BB001 (BBC east small tiles ADC)
Entries 2971392
ADC
QT Input Channel

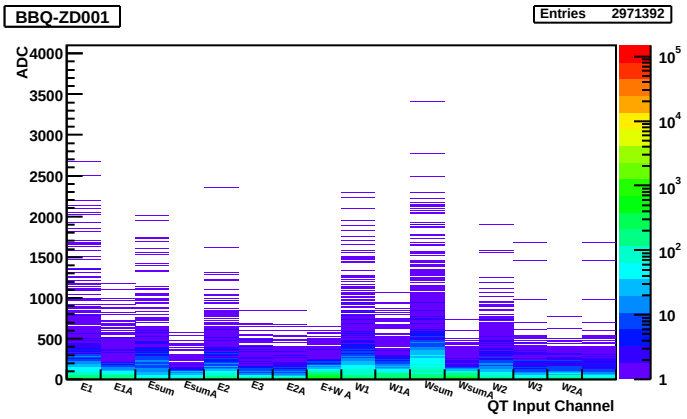

BBQ-BB001 (BBC east small tiles TAC)
Entries 2971392
TAC
QT Input Channel

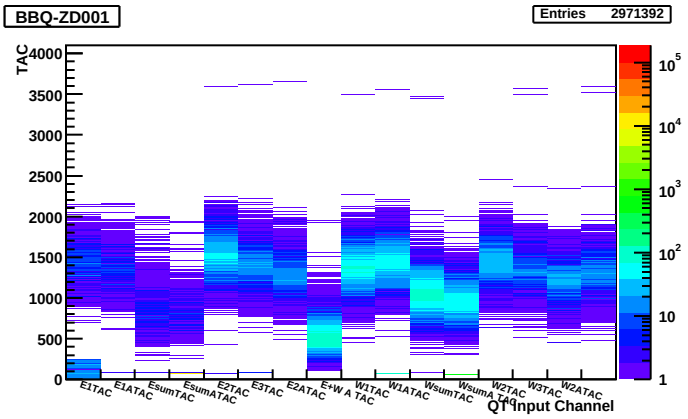

BBQ-BB002 (BBC west small tiles ADC)
Entries 2971392
ADC
QT Input Channel

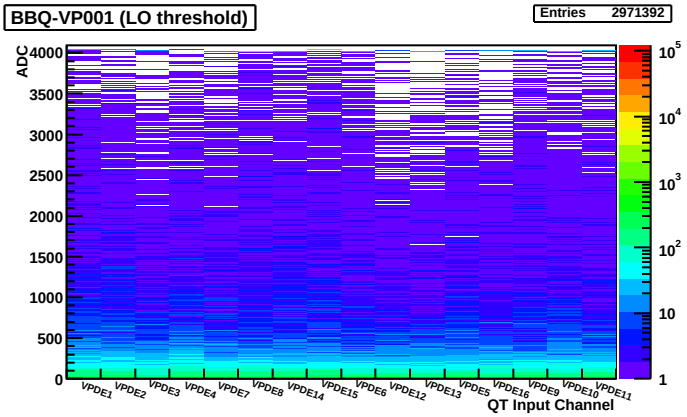

BBQ-BB002 (BBC west small tiles TAC)
Entries 2971392
TAC
QT Input Channel

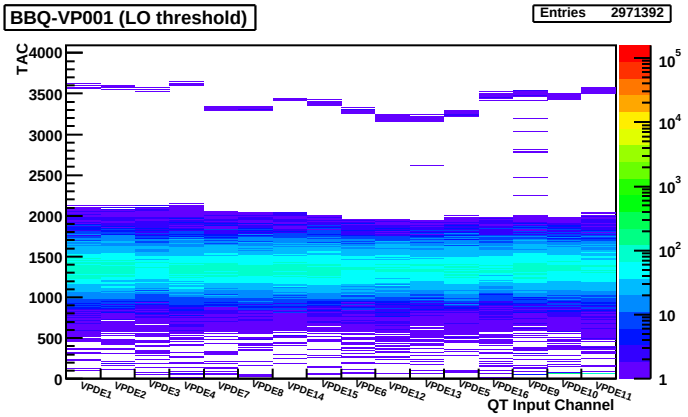

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2971392
ADC
QT Input Channel

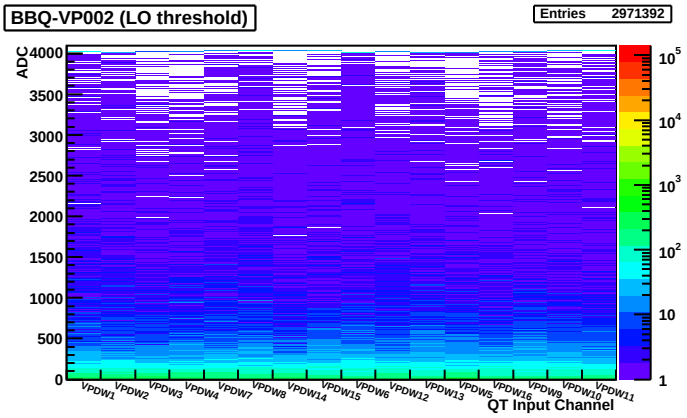

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2971392
TAC
QT Input Channel

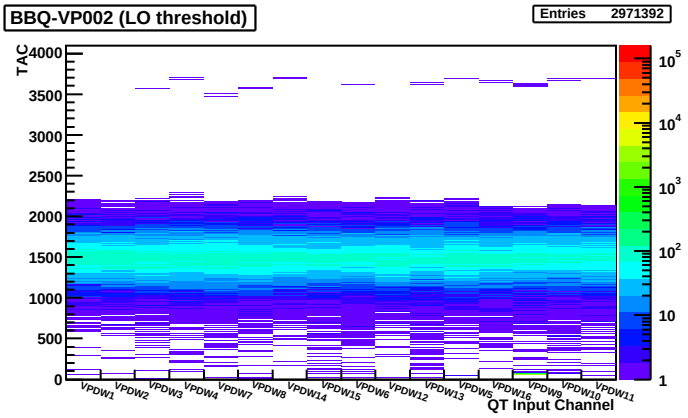

BBQ-ZD001
Entries 2971392
ADC
QT Input Channel

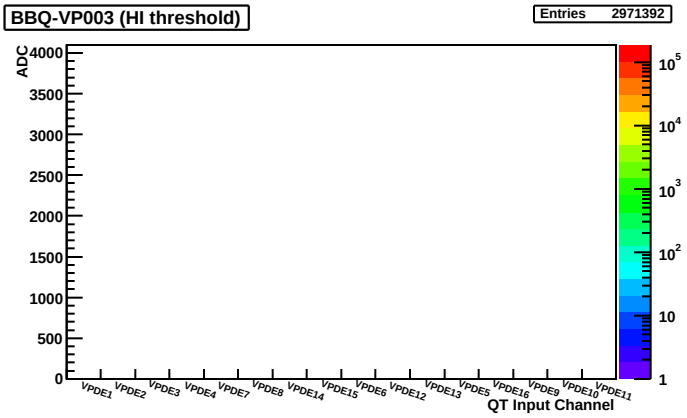

BBQ-ZD001
Entries 2971392
TAC
QT Input Channel

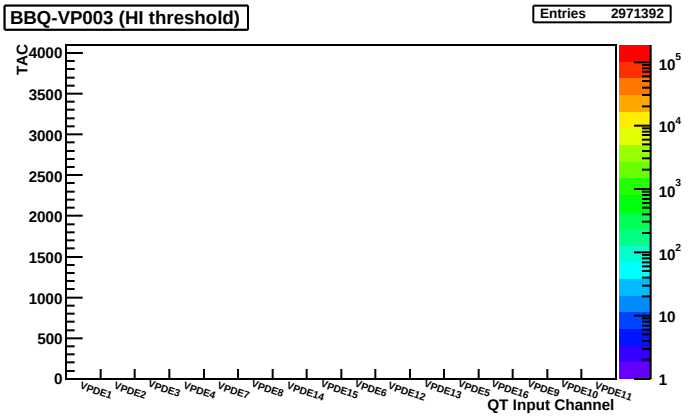

BBQ-VP001 (LO threshold)
Entries 2971392
ADC
QT Input Channel

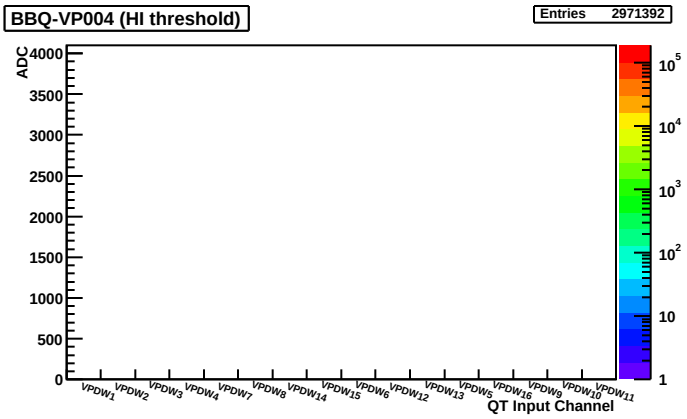

BBQ-VP001 (LO threshold)
Entries 2971392
TAC
QT Input Channel

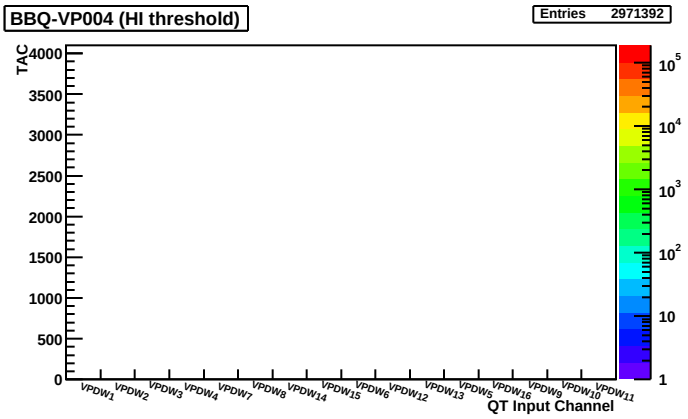

BBQ-VP002 (LO threshold)
Entries 2971392
ADC
QT Input Channel

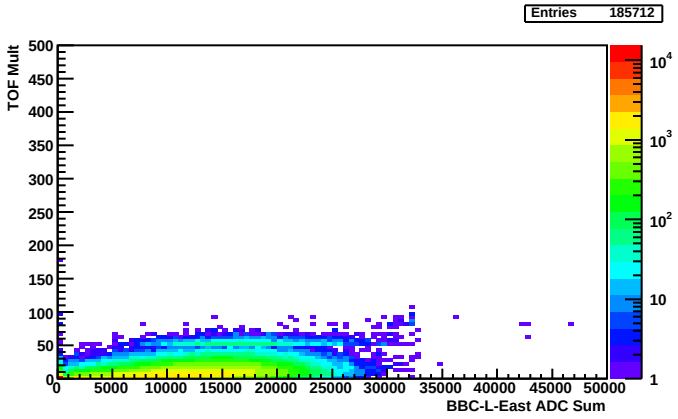

BBQ-VP002 (LO threshold)
Entries 2971392
TAC
QT Input Channel

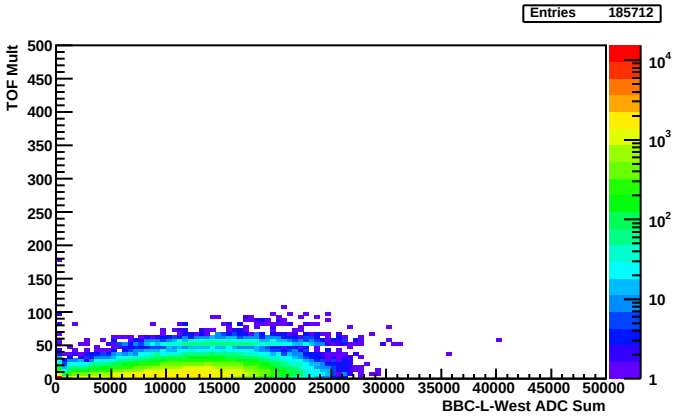

BBQ-VP003 (HI threshold)
Entries 2971392
ADC
QT Input Channel

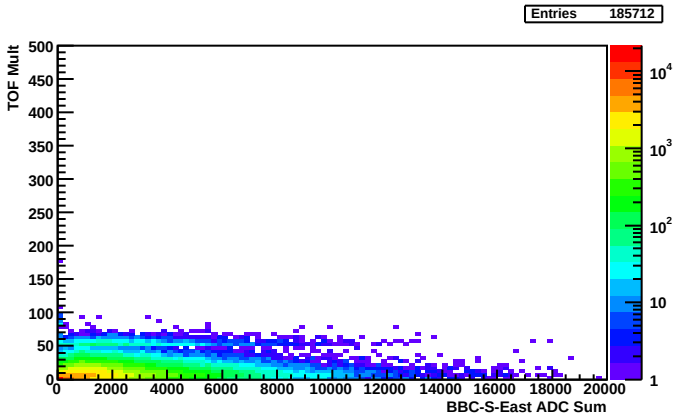

BBQ-VP003 (HI threshold)
Entries 2971392
TAC
QT Input Channel

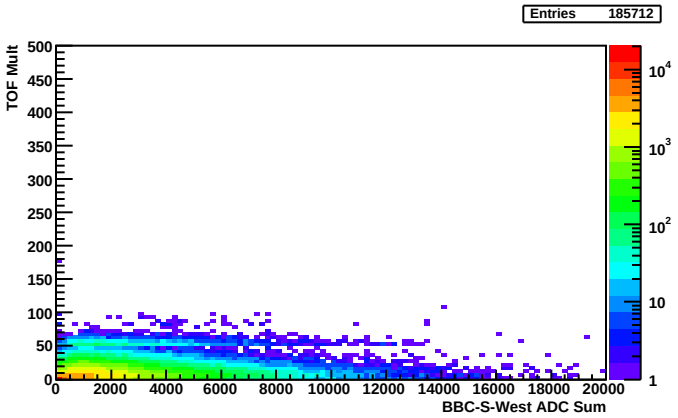

BBQ-VP004 (HI threshold)
Entries 2971392
ADC
QT Input Channel

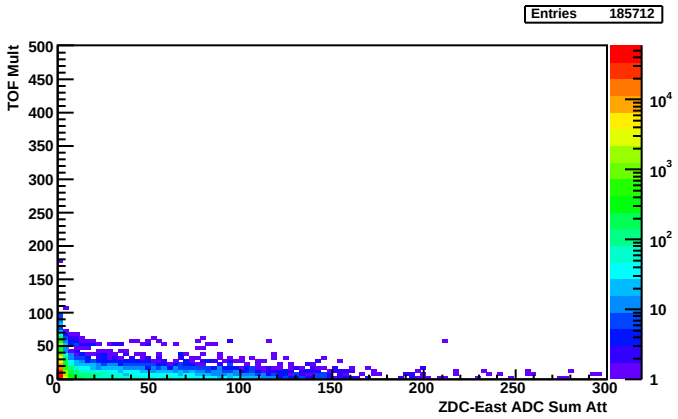

BBQ-VP004 (HI threshold)
Entries 2971392
TAC
QT Input Channel

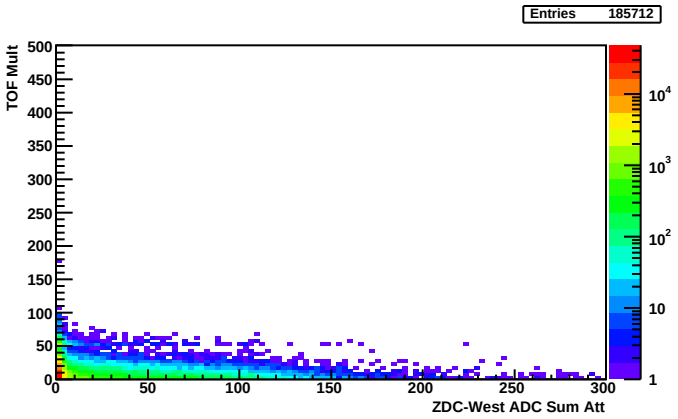

Entries 185712
TOF Mult
BBC-L-East ADC Sum

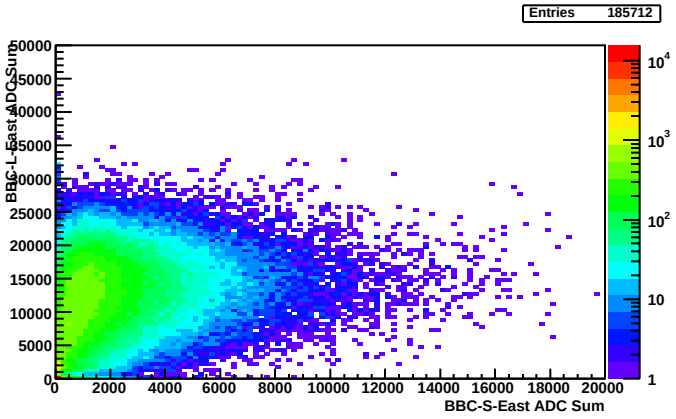

Entries 185712
TOF Mult
BBC-L-West ADC Sum

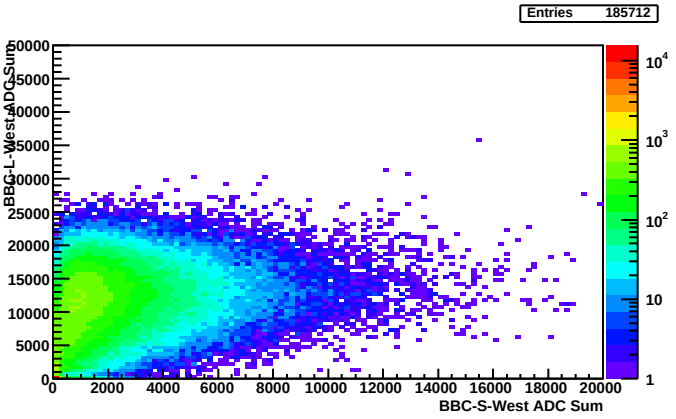

Entries 185712
TOF Mult
BBC-S-East ADC Sum

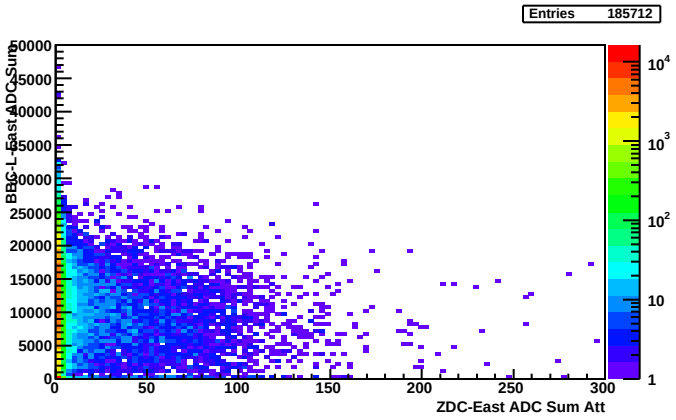

Entries 185712
TOF Mult
BBC-S-West ADC Sum

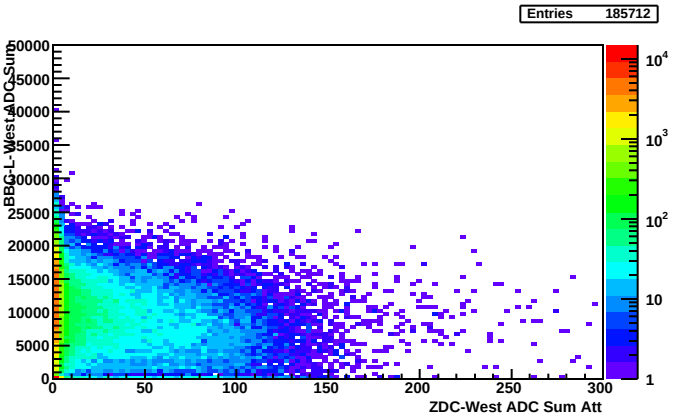

Entries 185712
TOF Mult
ZDC-East ADC Sum Att

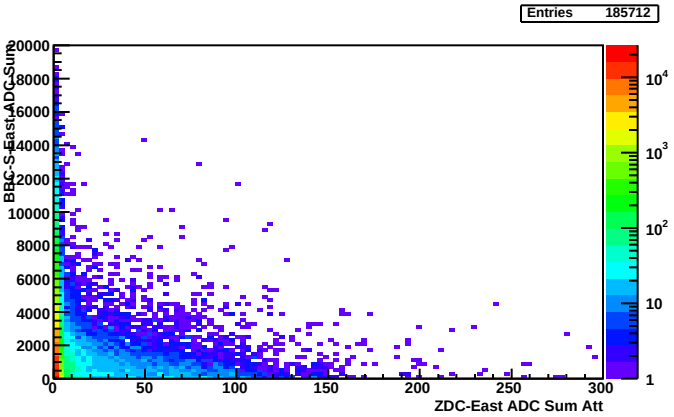

Entries 185712
TOF Mult
ZDC-West ADC Sum Att

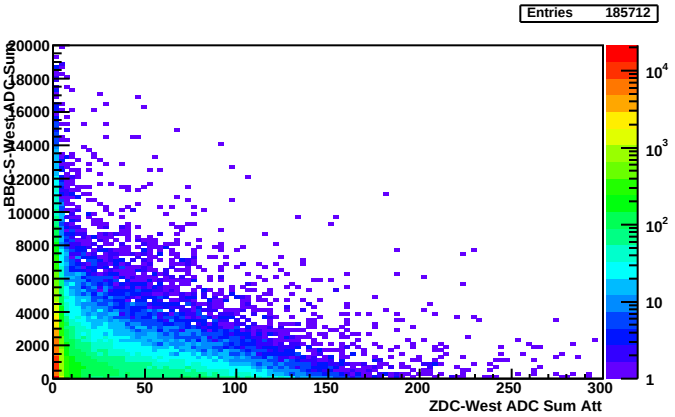

Entries 185712
BBC-L-East ADC Sum
BBC-S-East ADC Sum

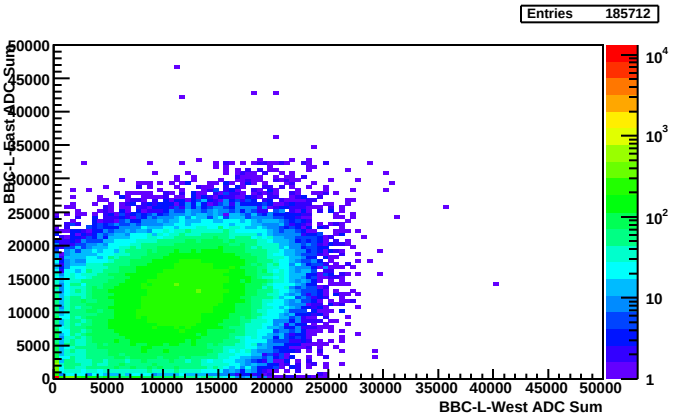

Entries 185712
BBC-L-West ADC Sum
BBC-S-West ADC Sum

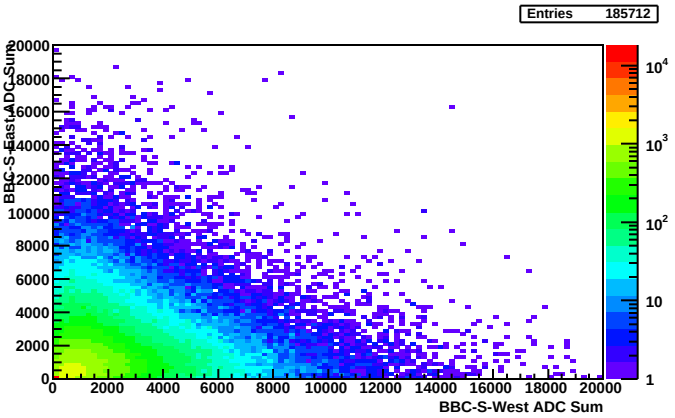

Entries 185712
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

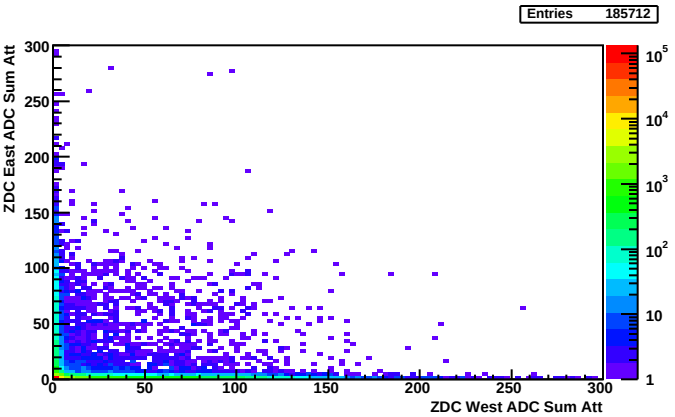

Entries 185712
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

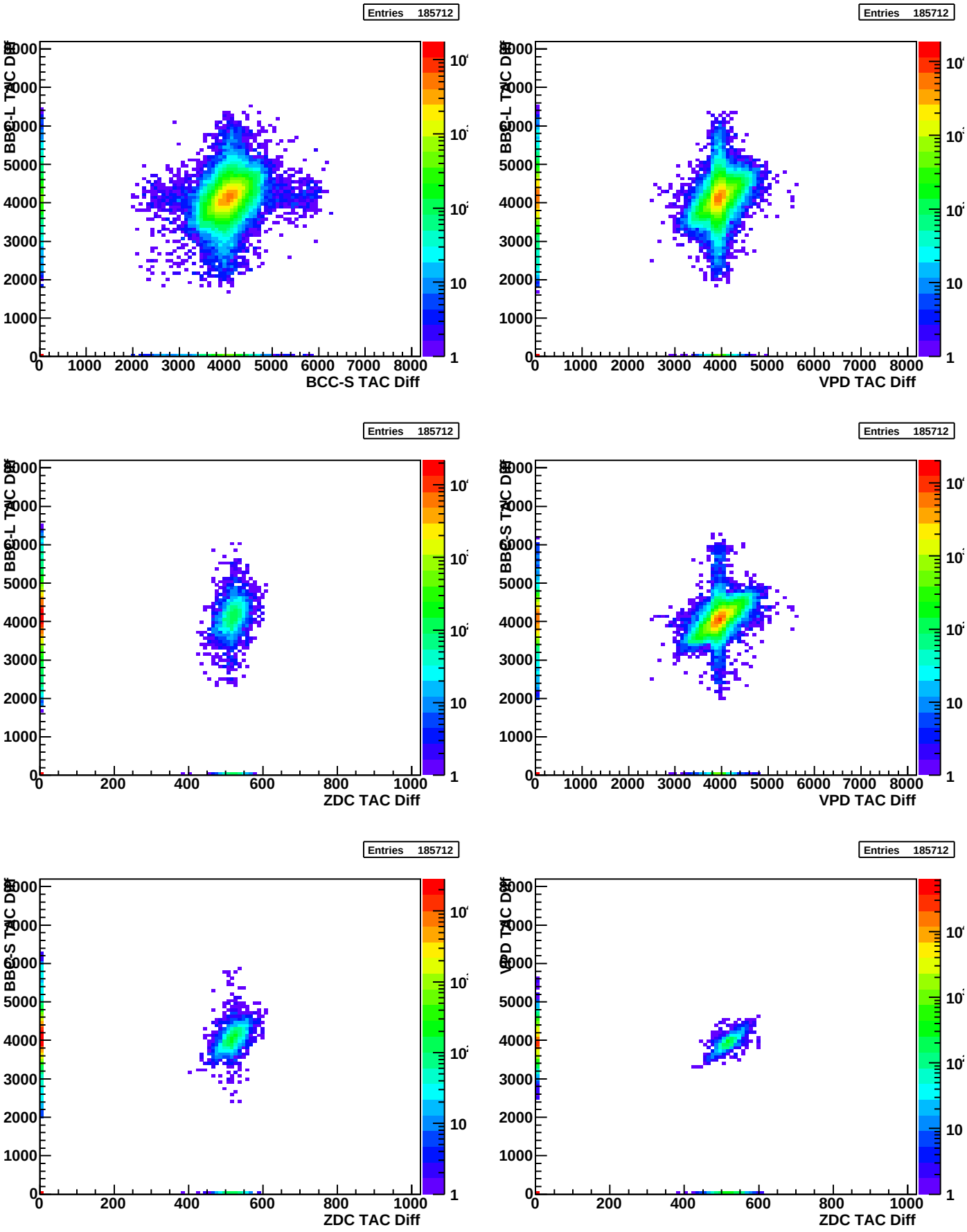

Entries 185712
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

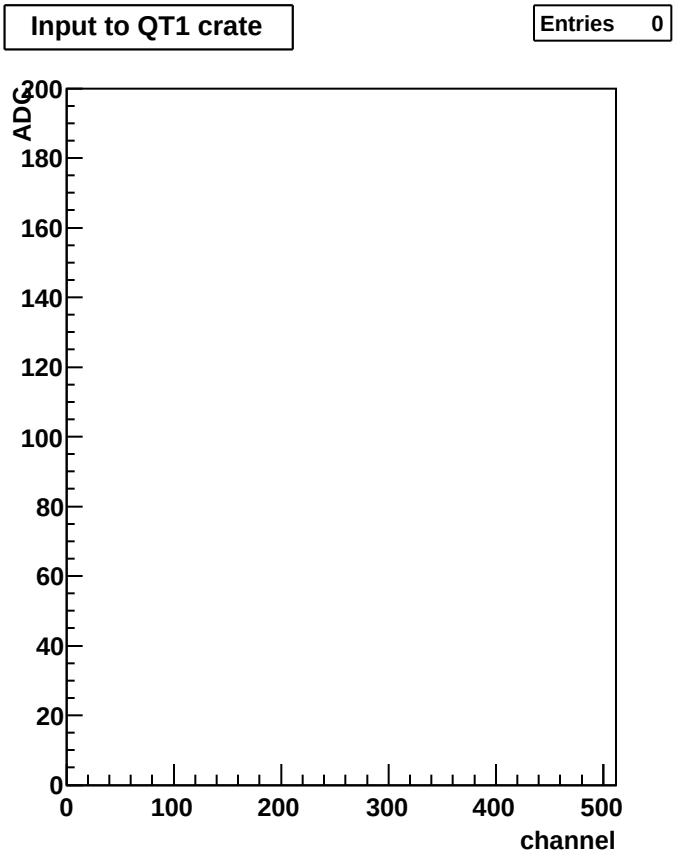

Entries 185712
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

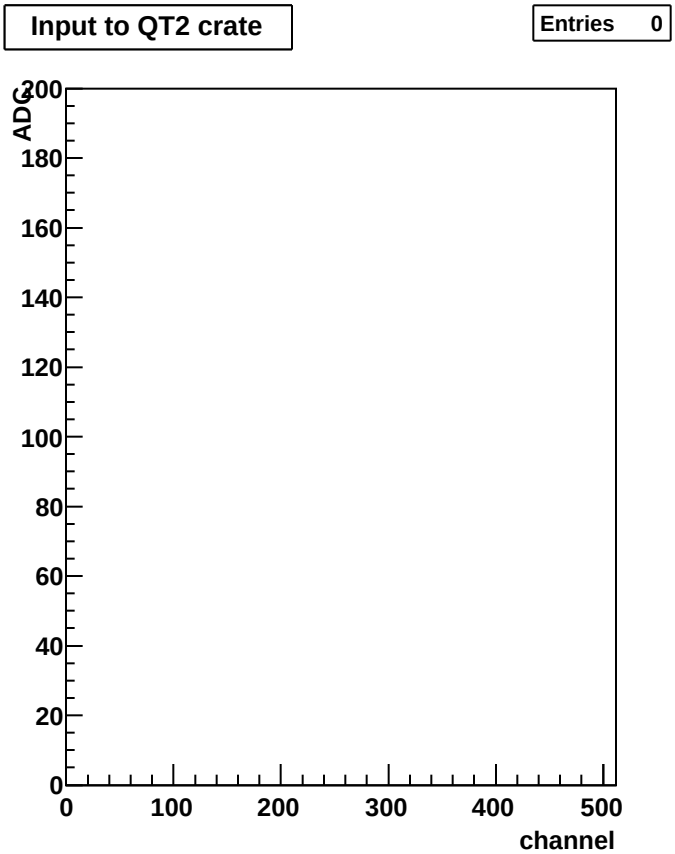

Entries 185712
BBC-L-East ADC Sum
BBC-L-West ADC Sum

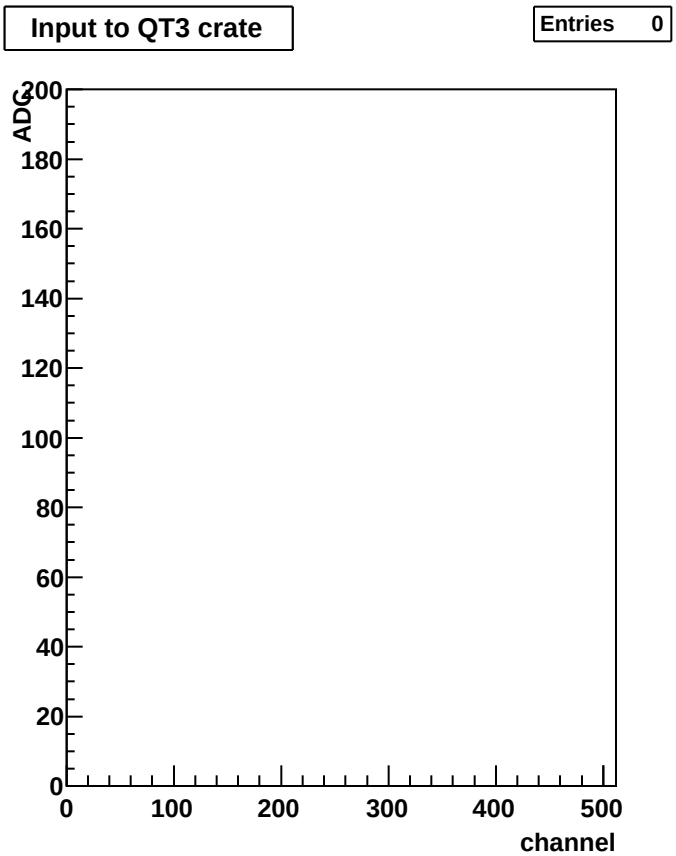

Entries 185712
BBC-S-East ADC Sum
BBC-S-West ADC Sum

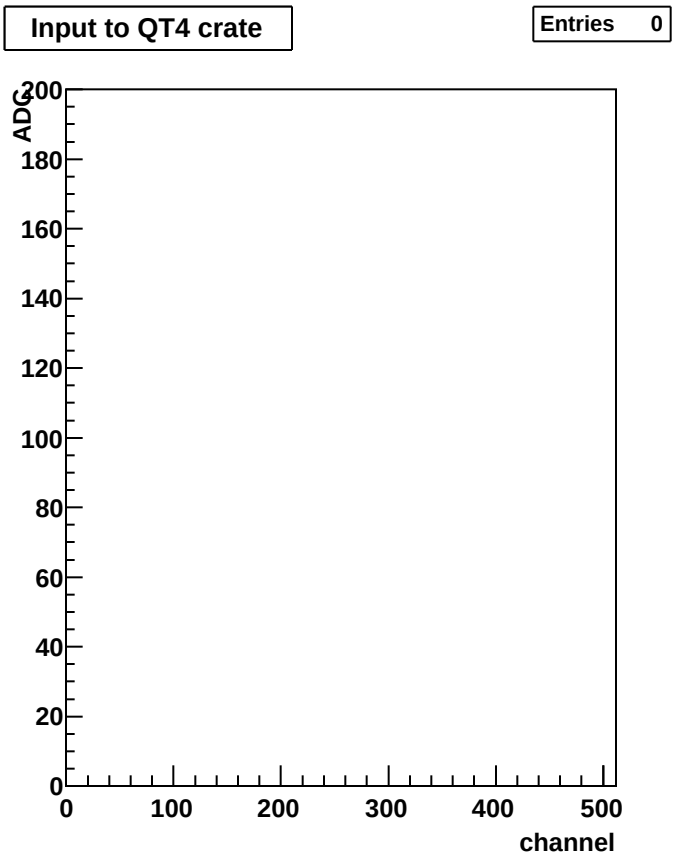

Entries 185712
ZDC East ADC Sum Att
ZDC West ADC Sum Att

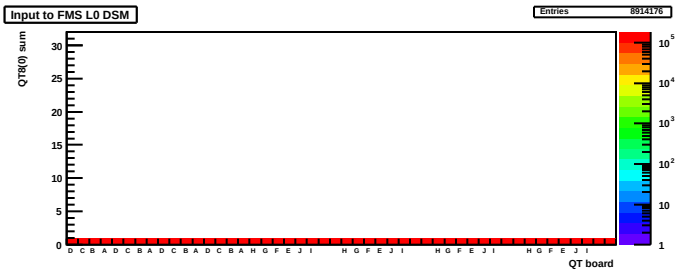

Entries 185712
BBC-L TAC Diff
BCC-S TAC Diff
Entries 185712
BBC-L TAC Diff
VPD TAC Diff
Entries 185712
BBC-L TAC Diff
ZDC TAC Diff
Entries 185712
BBC-S TAC Diff
VPD TAC Diff
Entries 185712
BBC-S TAC Diff
ZDC TAC Diff
Entries 185712
VPD TAC Diff
ZDC TAC Diff

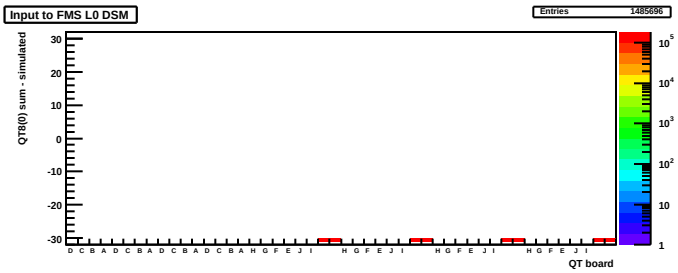

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

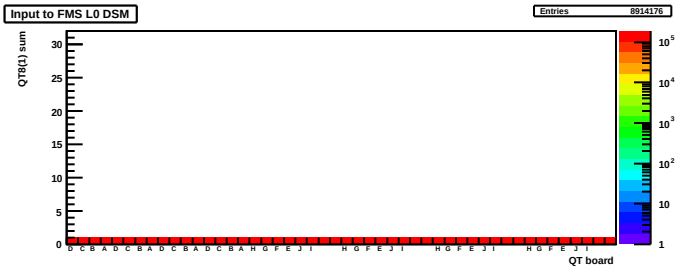

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

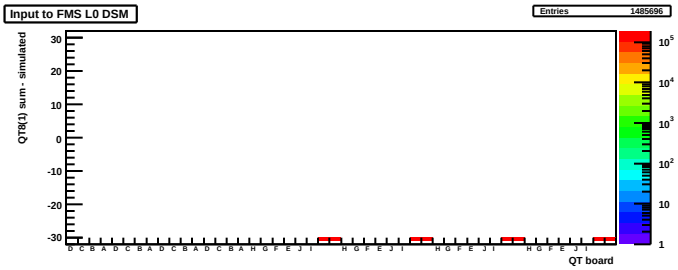

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

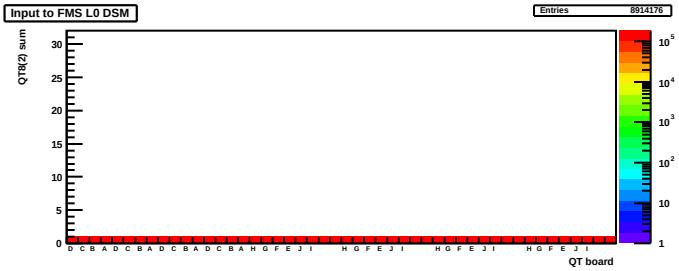

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

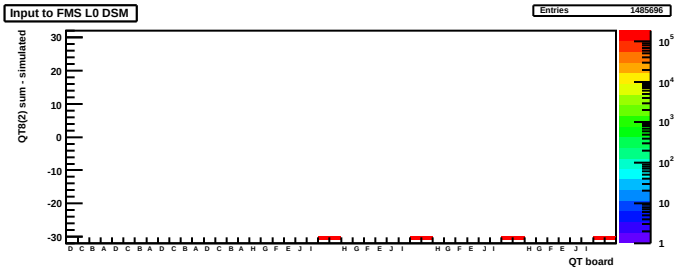

Input to FMS L0 DSM
Entries 8914176
QT8(0) sum
QT board

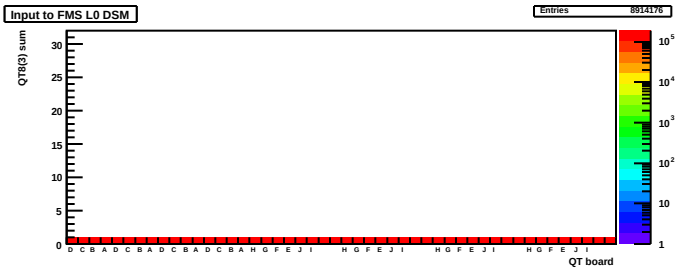

Input to FMS L0 DSM
Entries 1485696
QT8(0) sum - simulated
QT board

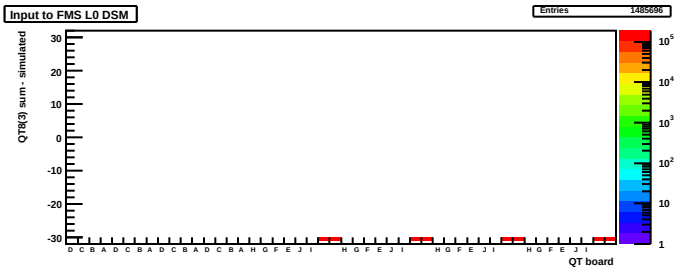

Input to FMS L0 DSM
Entries 8914176
QT8(1) sum
QT board

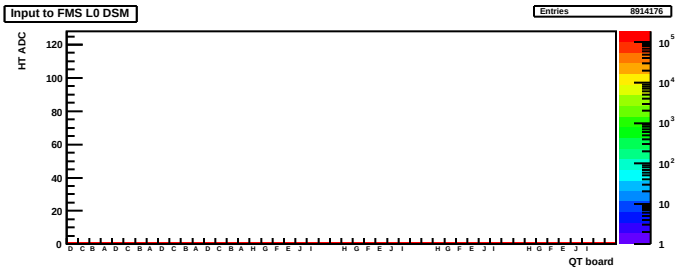

Input to FMS L0 DSM
Entries 1485696
QT8(1) sum - simulated
QT board

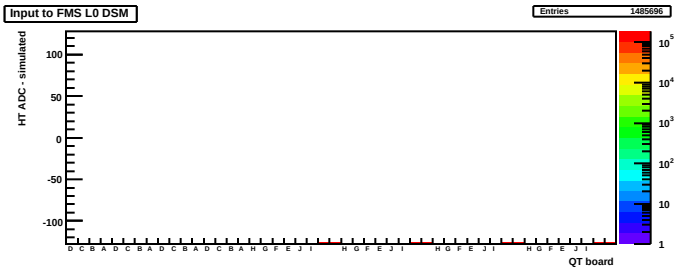

Input to FMS L0 DSM
Entries 8914176
QT8(2) sum
QT board

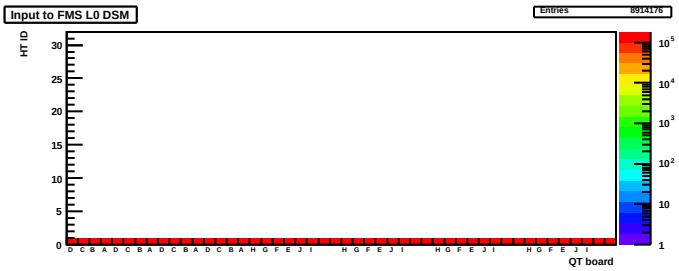

Input to FMS L0 DSM
Entries 1485696
QT8(2) sum - simulated
QT board

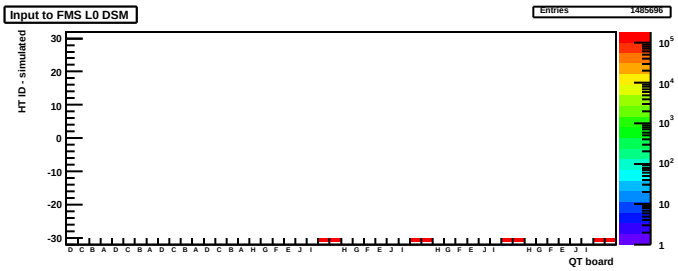

Input to FMS L0 DSM
Entries 8914176
QT8(3) sum
QT board

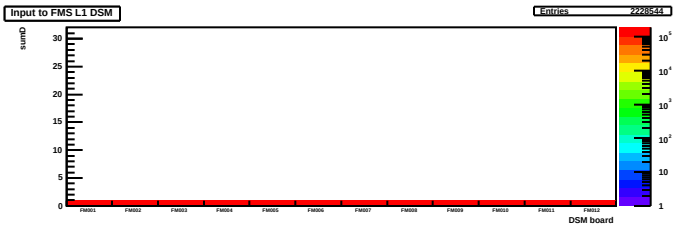

Input to FMS L0 DSM
Entries 1485696
QT8(3) sum - simulated
QT board

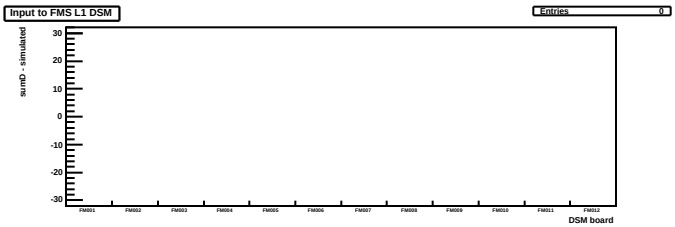

Input to FMS L0 DSM
Entries 8914176
HT ADC
QT board

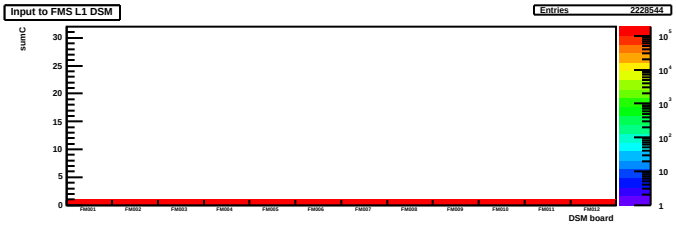

Input to FMS L0 DSM
Entries 1485696
HT ADC - simulated
QT board

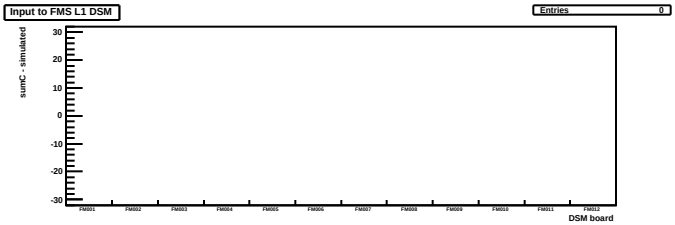

Input to FMS L0 DSM
Entries 8914176
HT ID
QT board

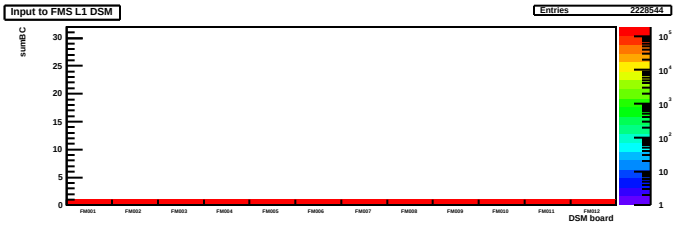

Input to FMS L0 DSM
Entries 1485696
HT ID - simulated
QT board

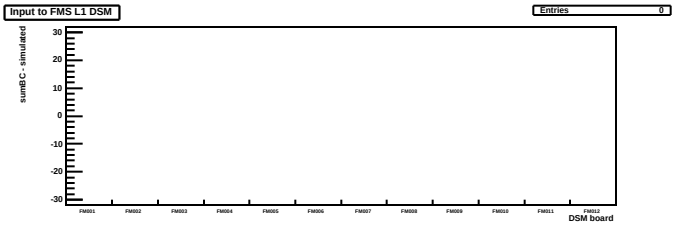

Input to FMS L1 DSM
Entries 2228544
sumD
DSM board

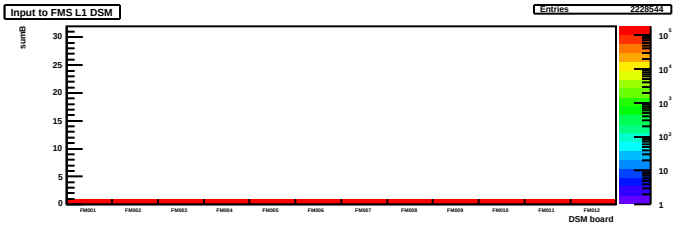

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

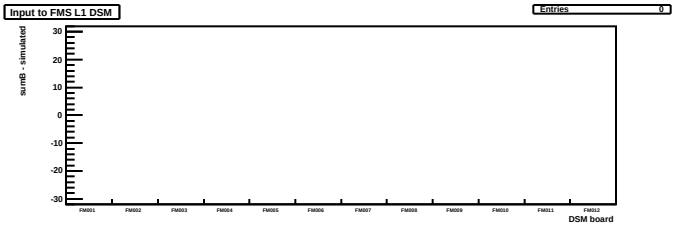

Input to FMS L1 DSM
Entries 2228544
sumC
DSM board

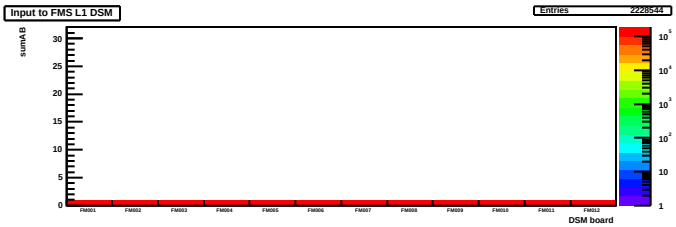

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

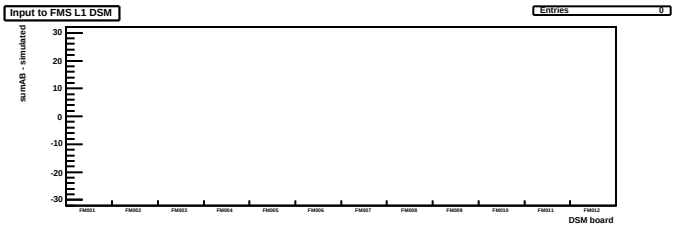

Input to FMS L1 DSM
Entries 2228544
sumBC
DSM board

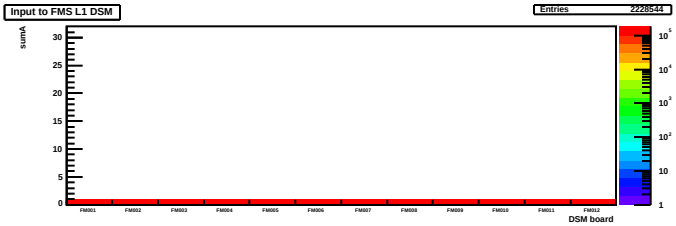

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

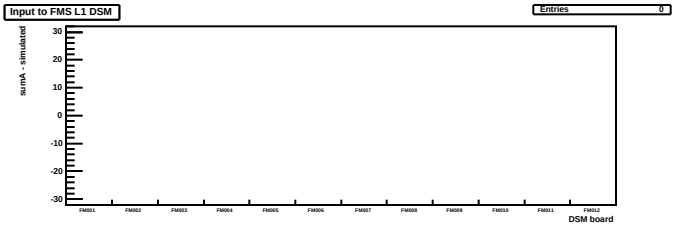

Input to FMS L1 DSM
Entries 2228544
sumB
DSM board

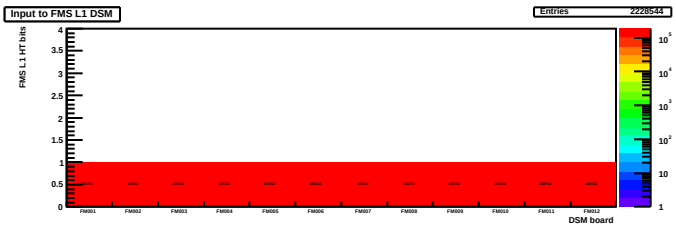

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

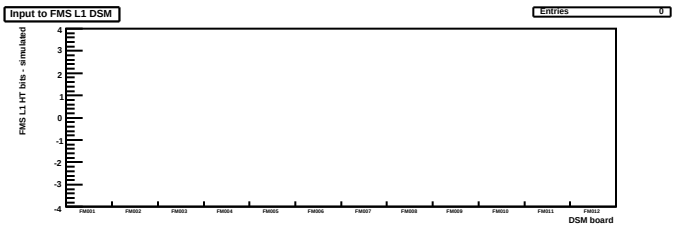

Input to FMS L1 DSM
Entries 2228544
sumAB
DSM board

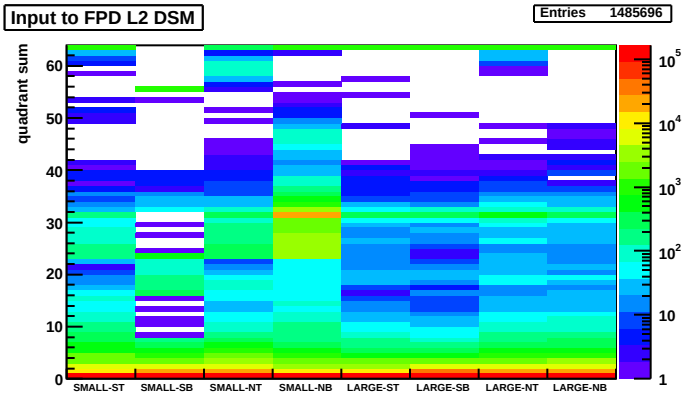

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

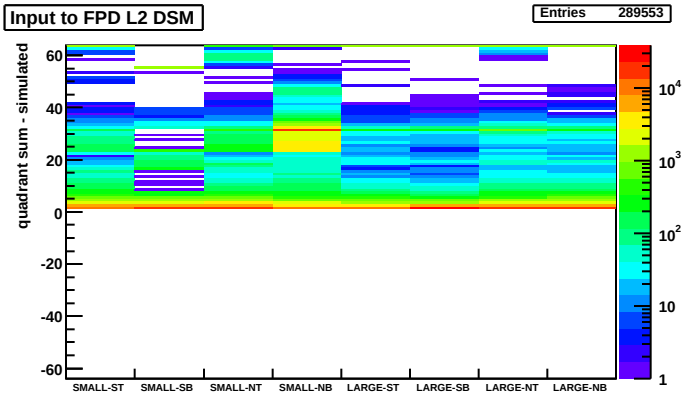

Input to FMS L1 DSM
Entries 2228544
sumA
DSM board

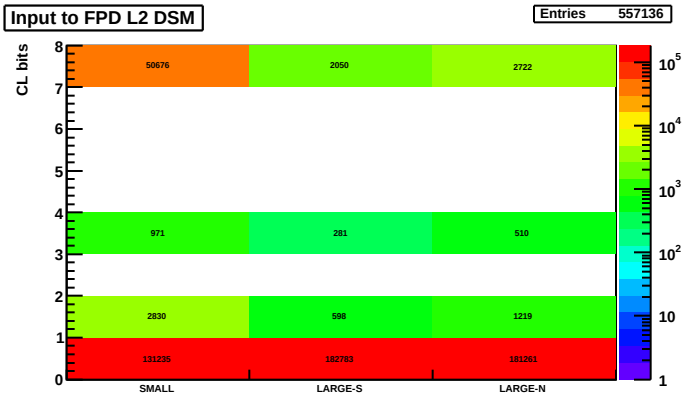

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

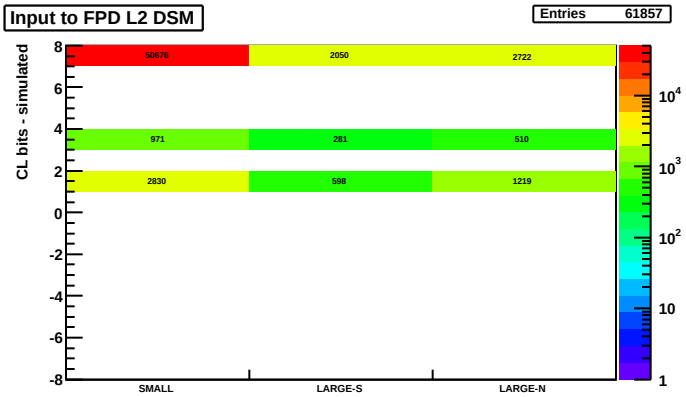

Input to FMS L1 DSM
Entries 2228544
FMS L1 HT bits
DSM board

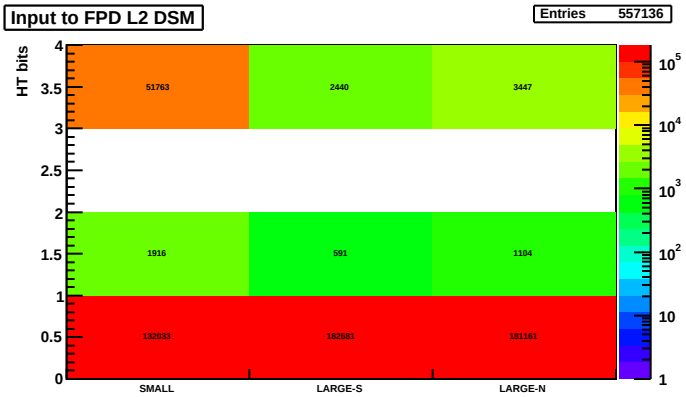

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

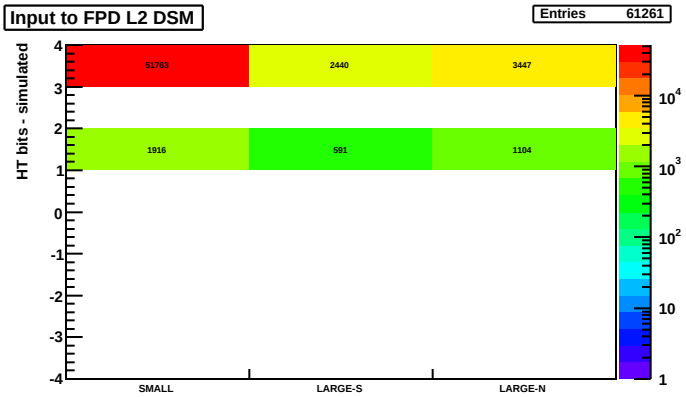

Input to FPD L2 DSM
Entries 1485696
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

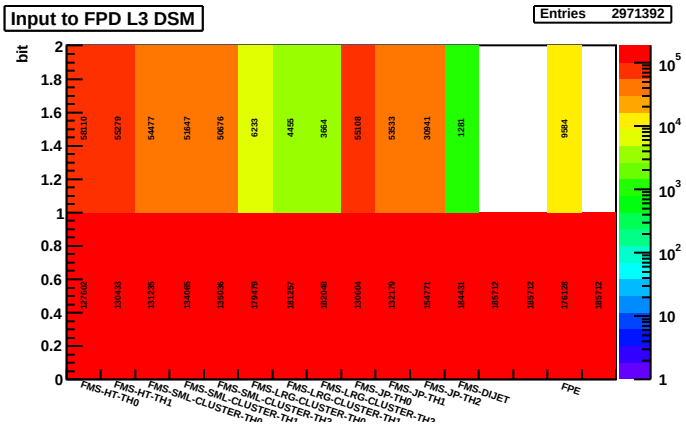

Input to FPD L2 DSM
Entries 289553
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

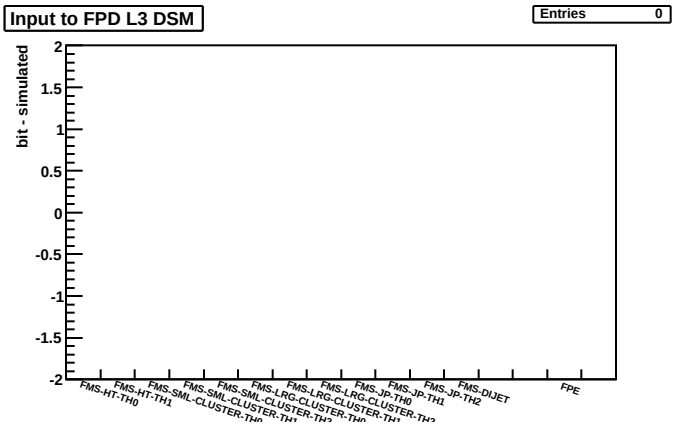

Input to FPD L2 DSM
Entries 557136
CL bits
50676
2050
2722
971
281
510
2830
598
1219
131235
182783
181261
SMALL
LARGE-S
LARGE-N

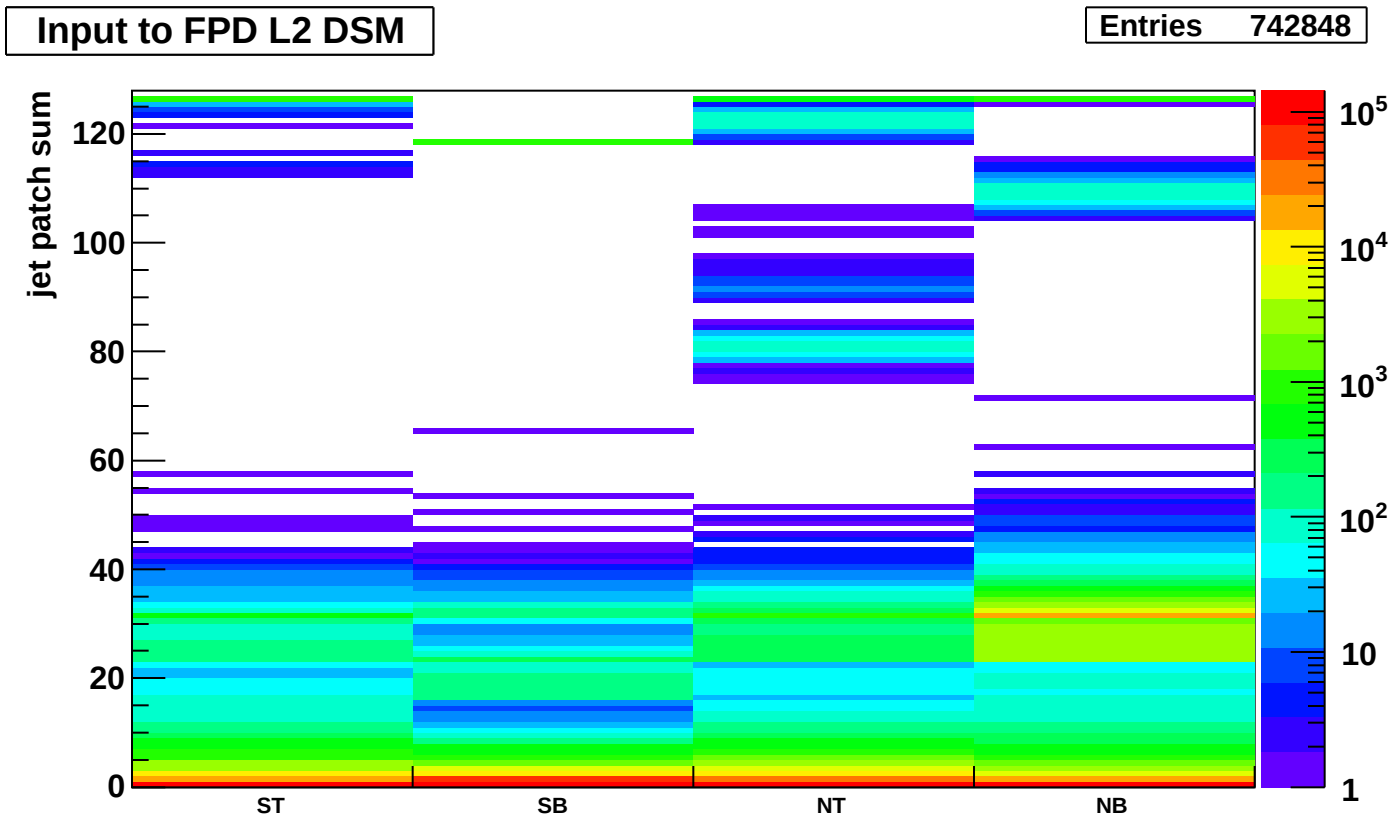

Input to FPD L2 DSM
Entries 61857
CL bits - simulated
50676
2050
2722
971
281
510
2830
598
1219
SMALL
LARGE-S
LARGE-N

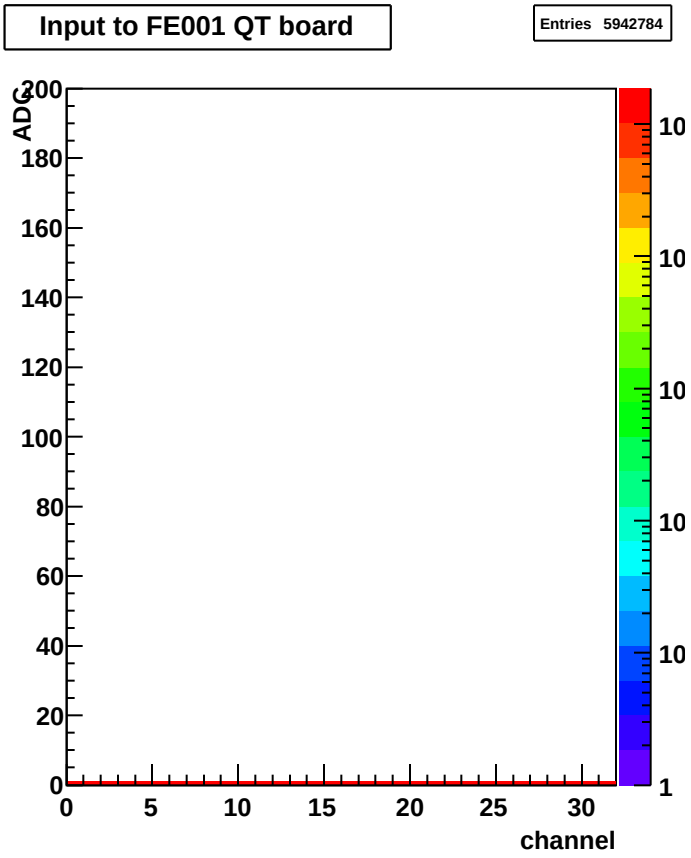

Input to FPD L2 DSM
Entries 557136
HT bits
51763
2440
3447
1916
591
1104
132033
182681
181161
SMALL
LARGE-S
LARGE-N

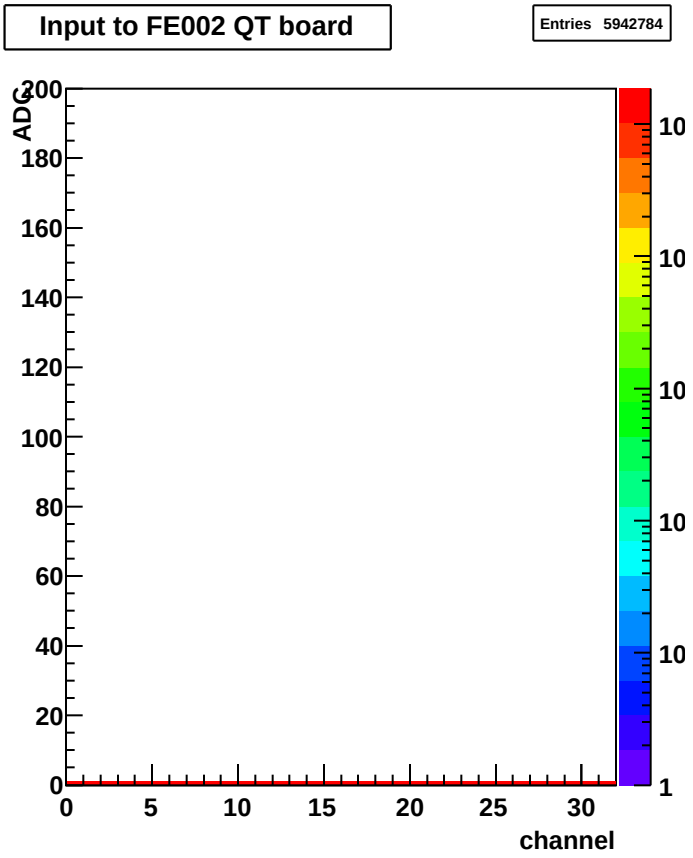

Input to FPD L2 DSM
Entries 61261
HT bits - simulated
51763
2440
3447
1916
591
1104
SMALL
LARGE-S
LARGE-N

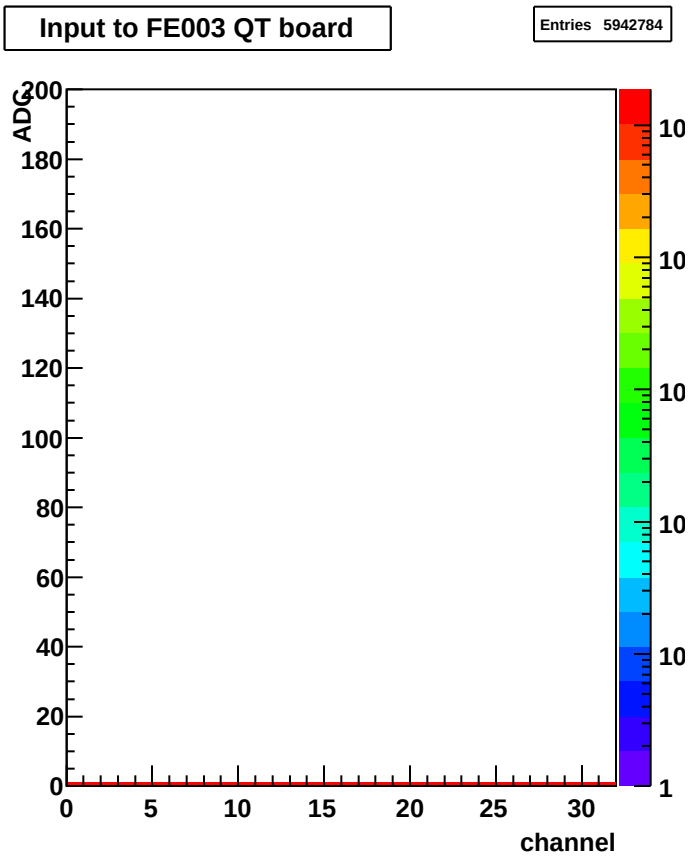

Input to FPD L3 DSM
Entries 2971392
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

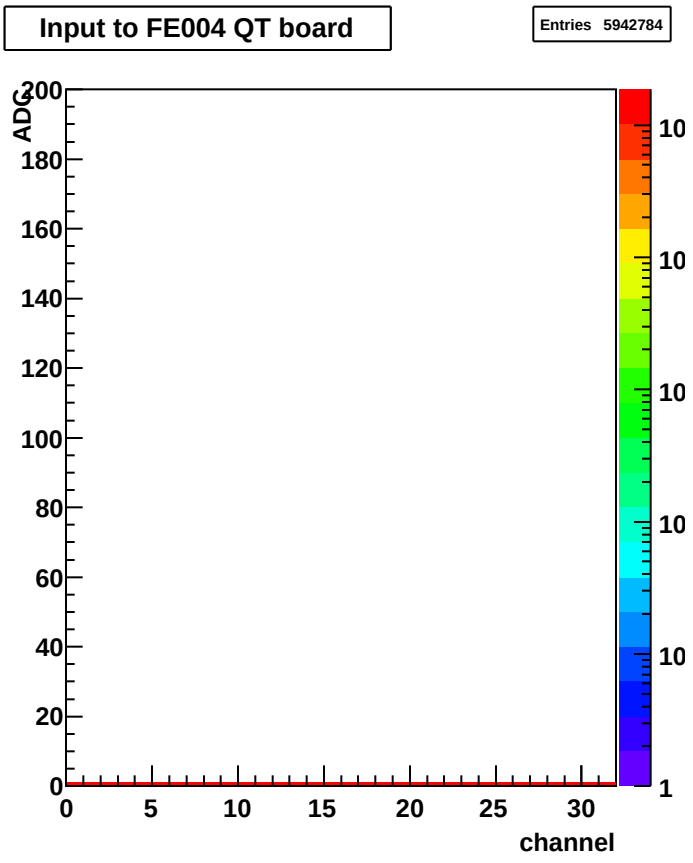

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

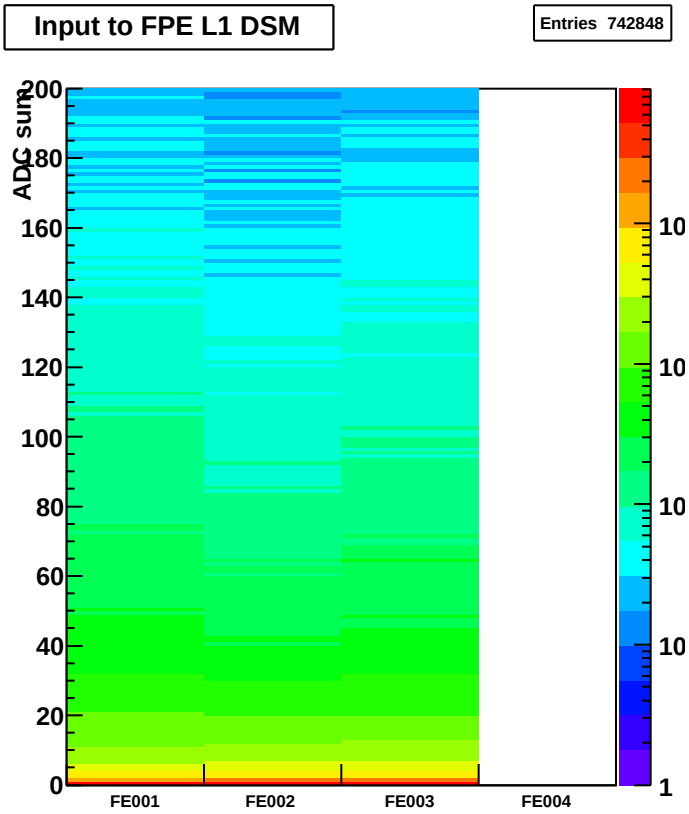

Input to FPD L2 DSM
Entries 742848
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

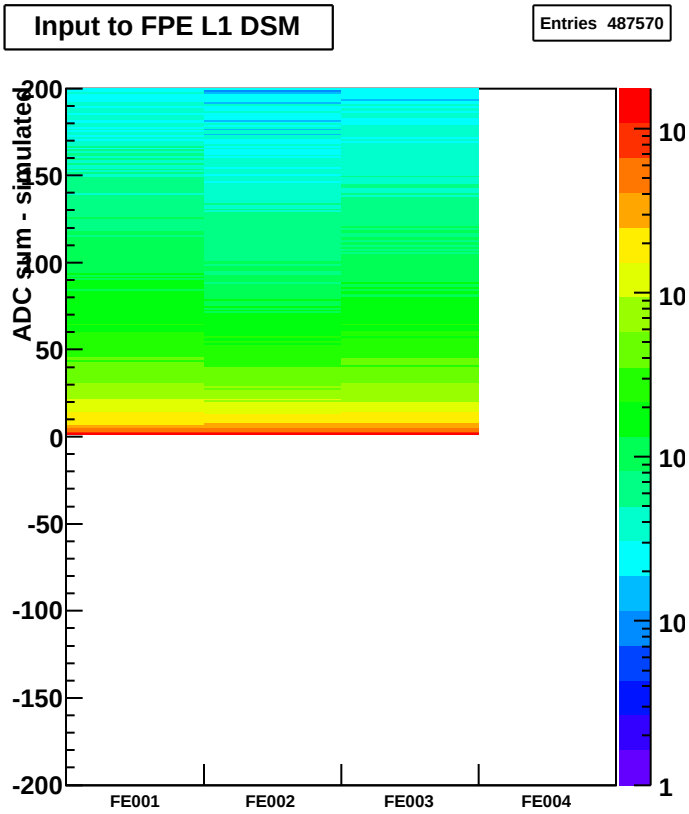

Input to FE001 QT board
Entries 5942784
ADC
channel

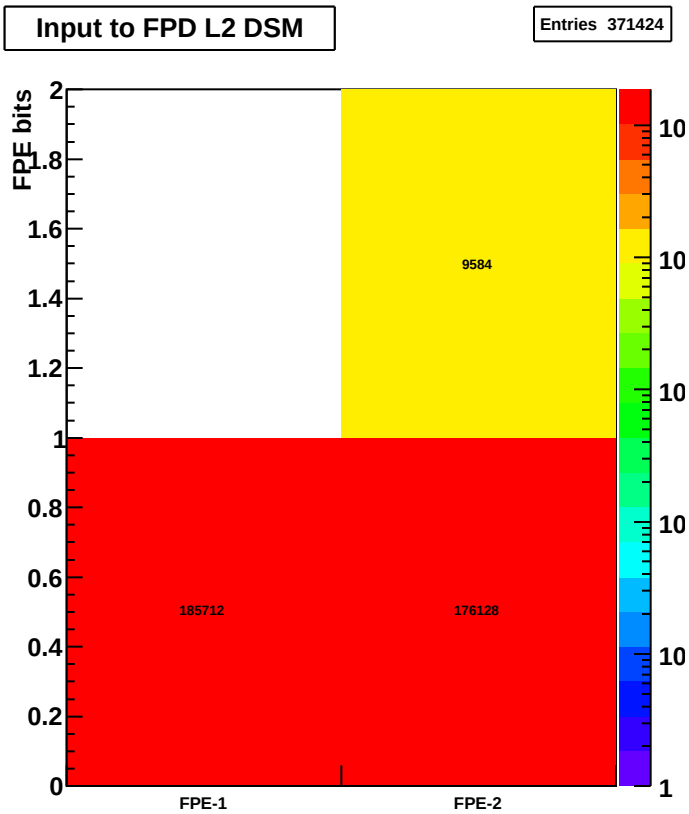

Input to FE002 QT board
Entries 5942784
ADC
channel

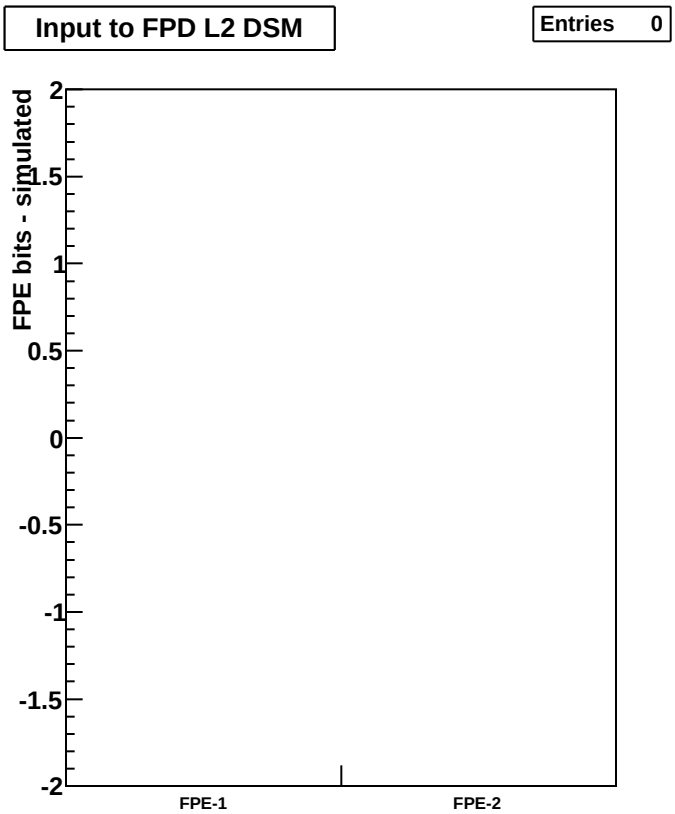

Input to FE003 QT board
Entries 5942784
ADC
channel


Input to FE004 QT board
Entries 5942784
ADC
channel


Input to FPE L1 DSM
Entries 742848
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 487570
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 371424
FPE bits
9584
185712
176128
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2