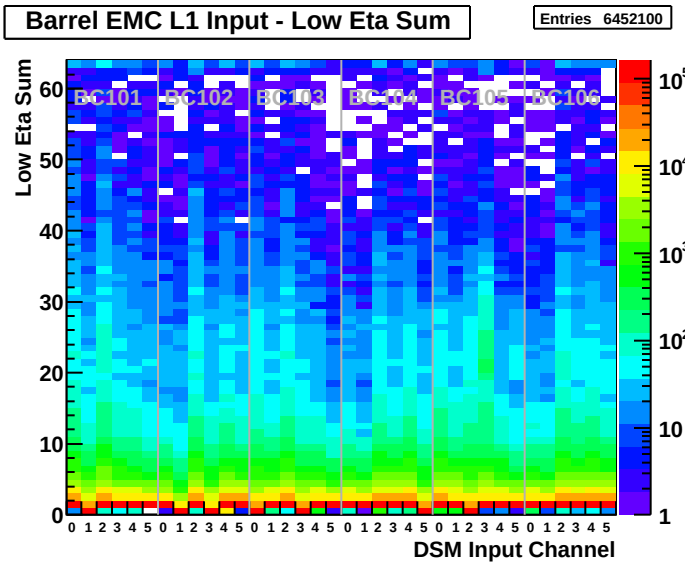
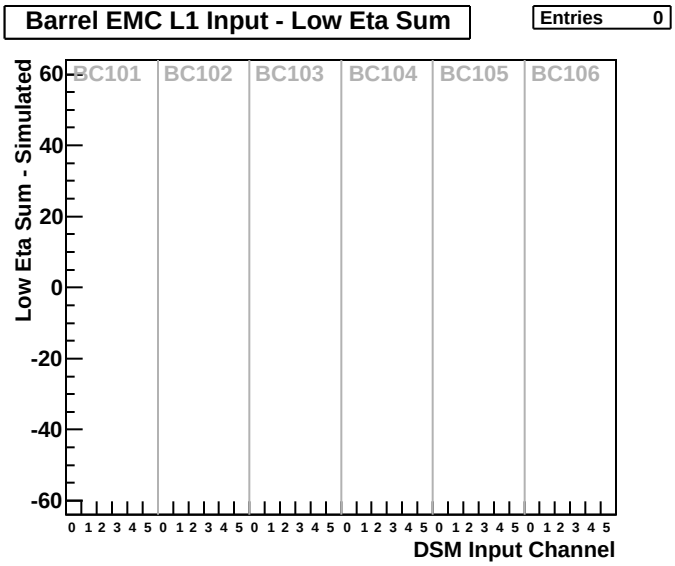
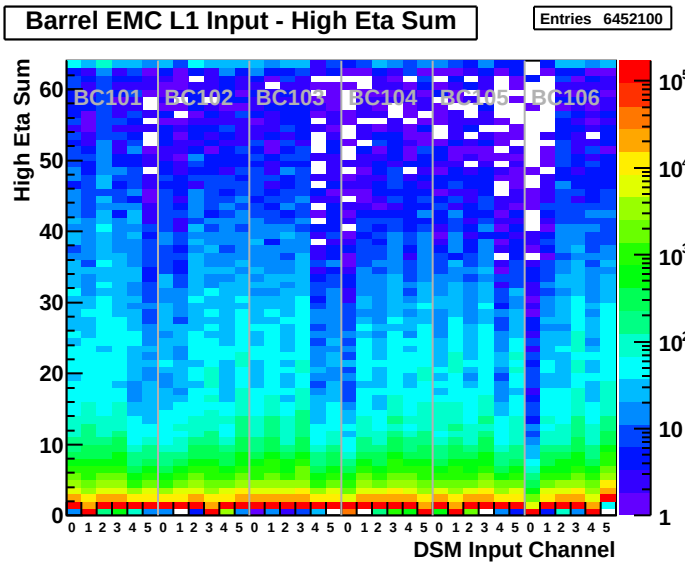
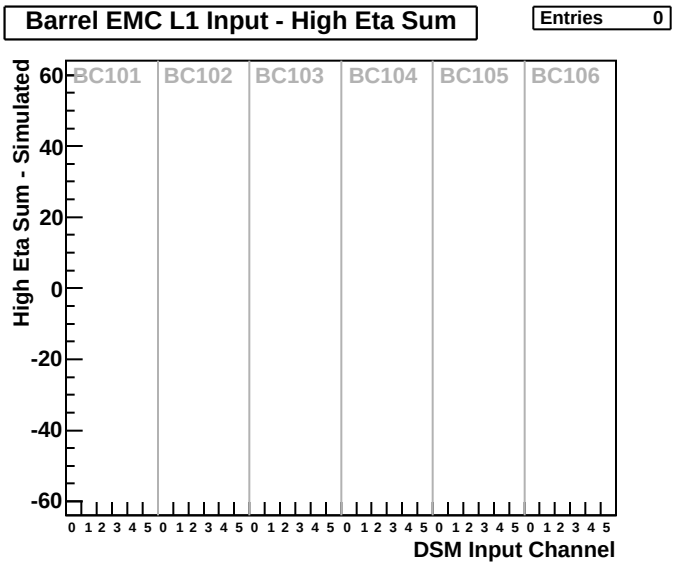
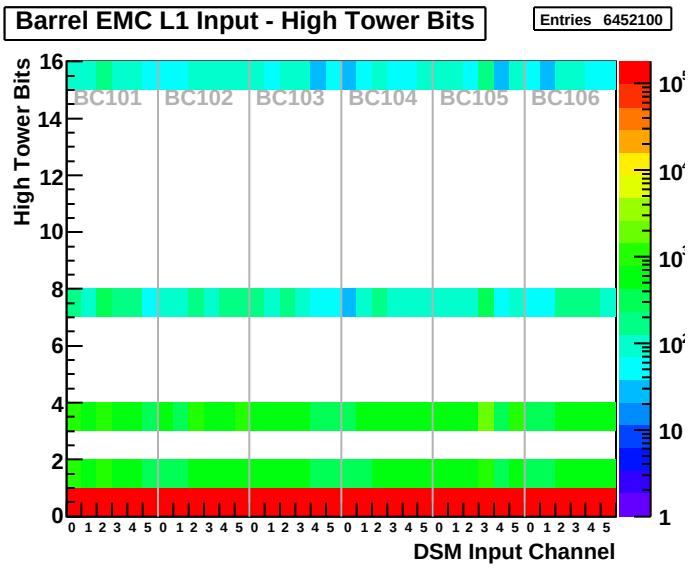
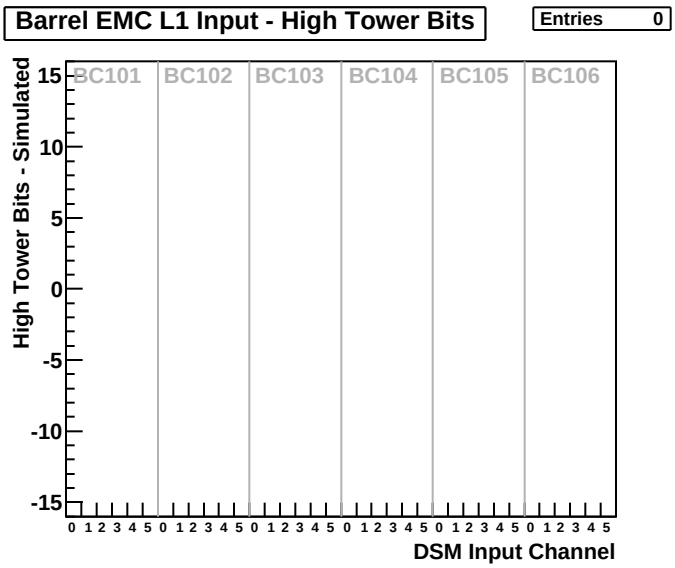

Barrel EMC L1 Input - Low Eta Sum
Entries 6452100
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

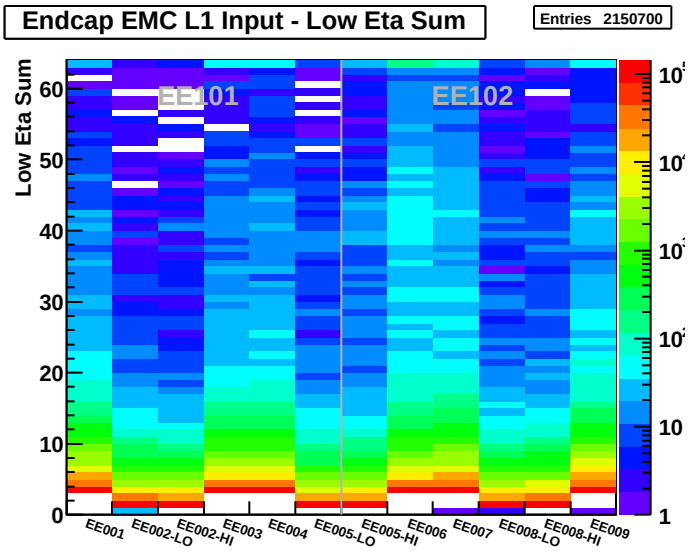

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

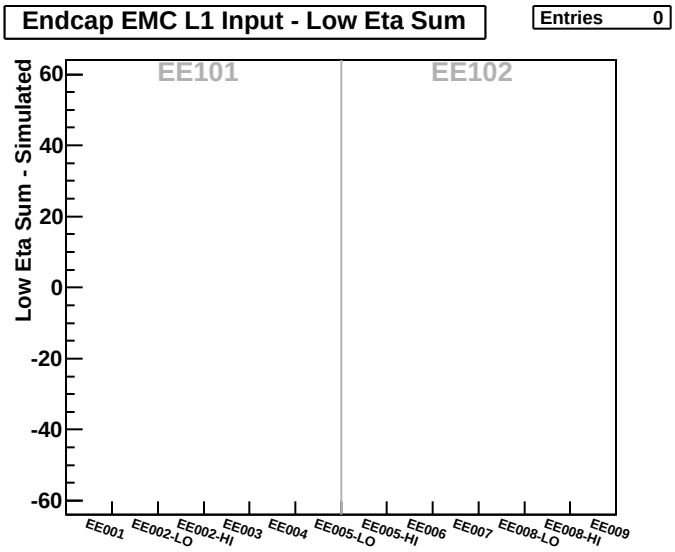

Barrel EMC L1 Input - High Eta Sum
Entries 6452100
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

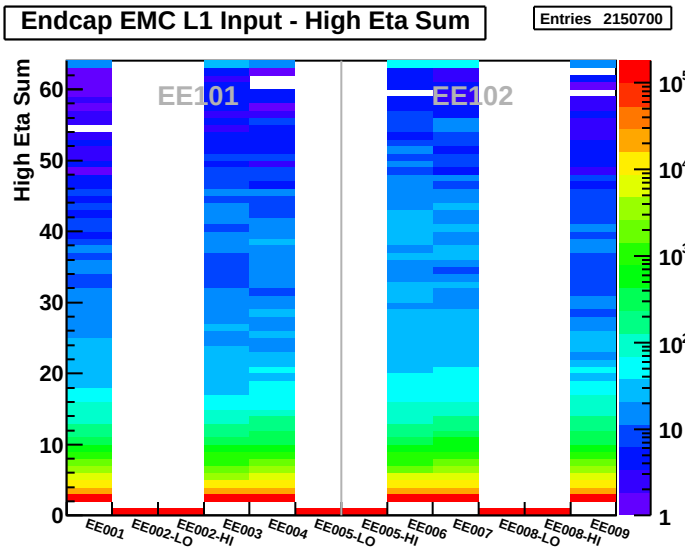

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

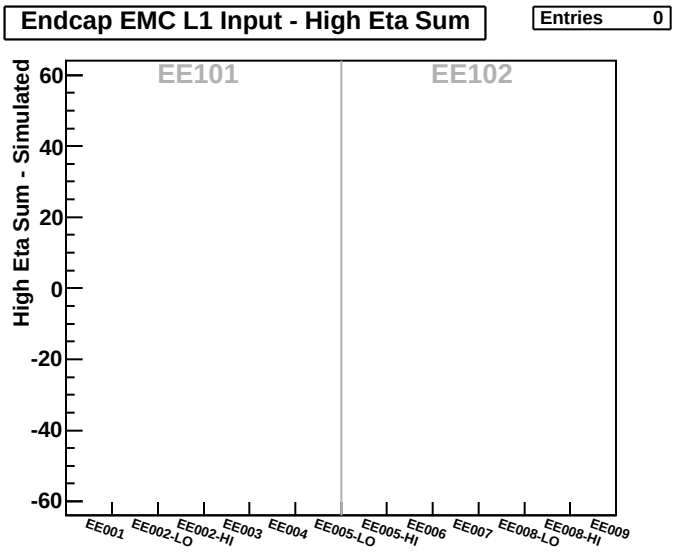

Barrel EMC L1 Input - High Tower Bits
Entries 6452100
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

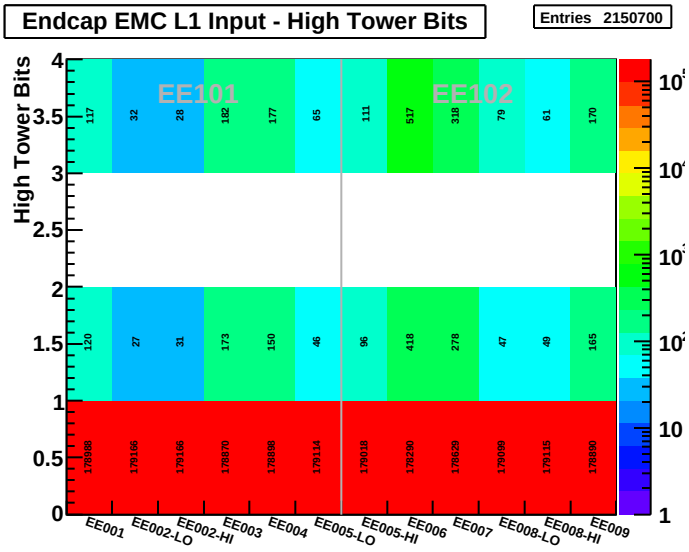

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

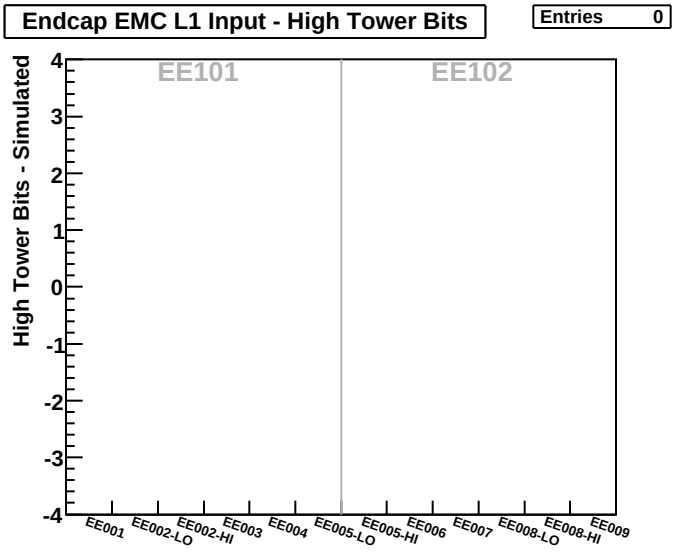

Endcap EMC L1 Input - Low Eta Sum
Entries 2150700
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

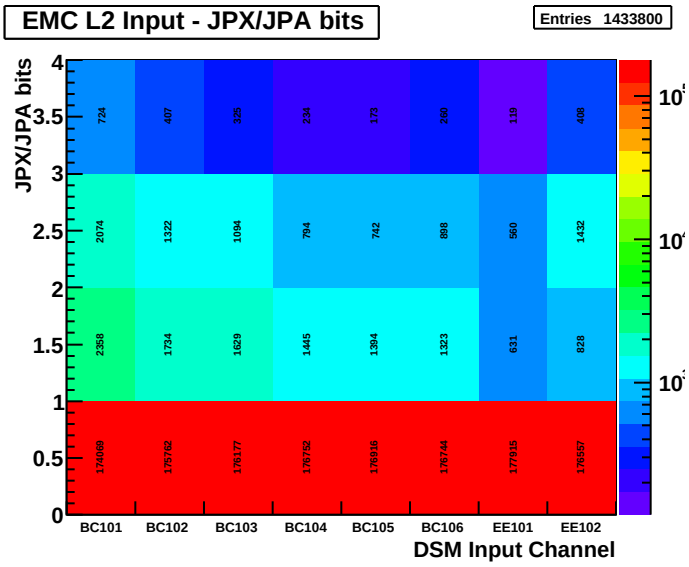

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

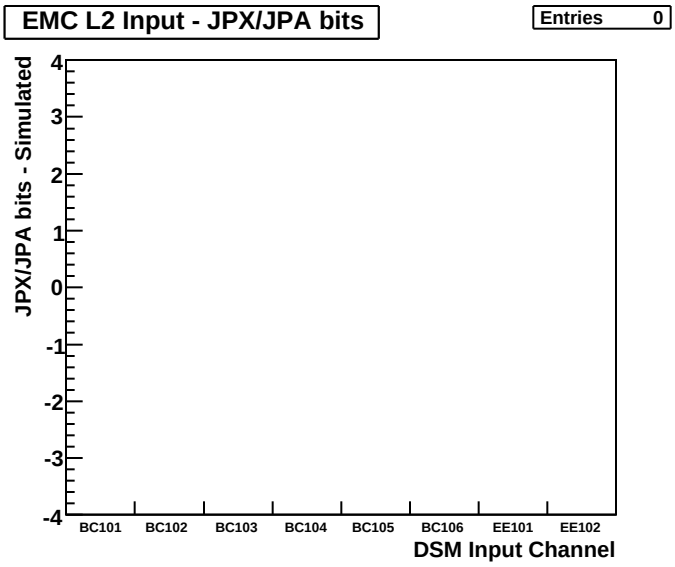

Endcap EMC L1 Input - High Eta Sum
Entries 2150700
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

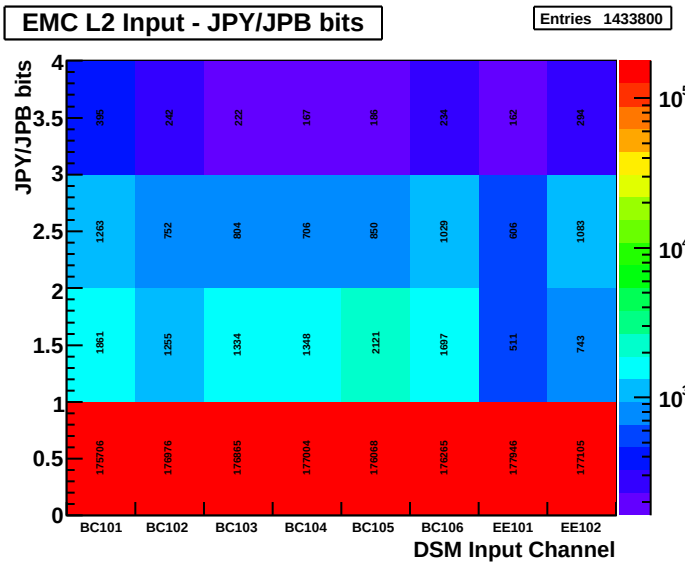

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

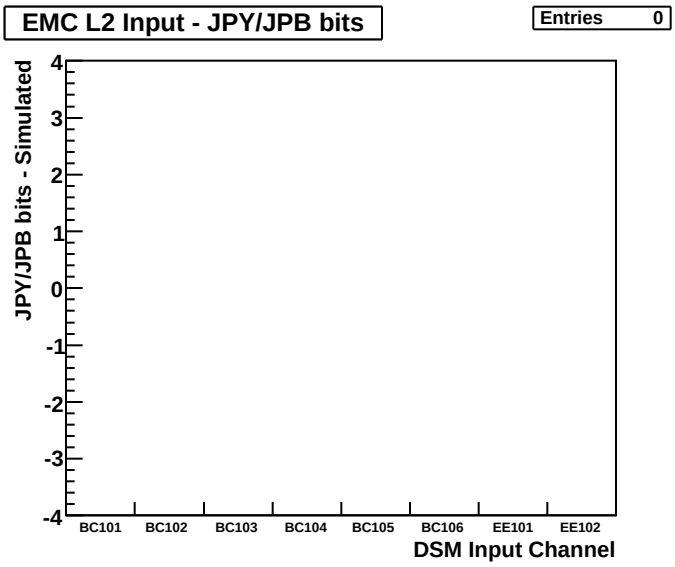

Endcap EMC L1 Input - High Tower Bits
Entries 2150700
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

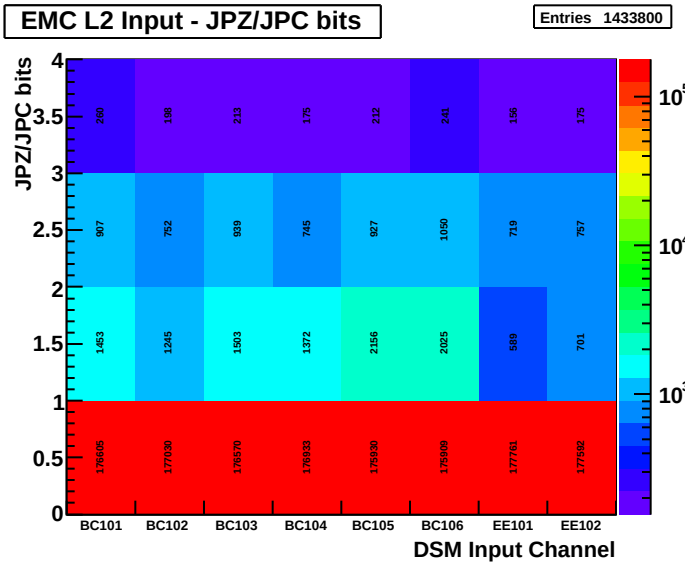

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

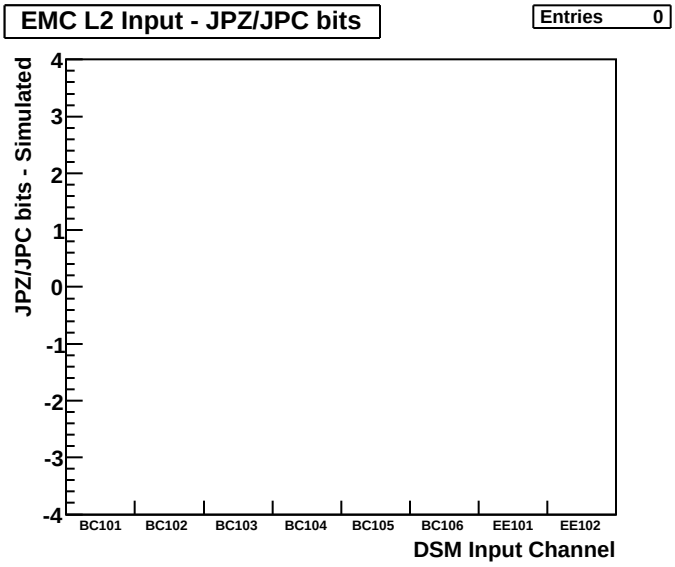

EMC L2 Input - JPX/JPA bits
Entries 1433800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
724 407 325 234 173 260 119 408
2074 1322 1094 794 742 898 560 1432
2358 1734 1629 1445 1394 1323 631 828
174069 175762 176177 176752 176916 176744 177915 176557

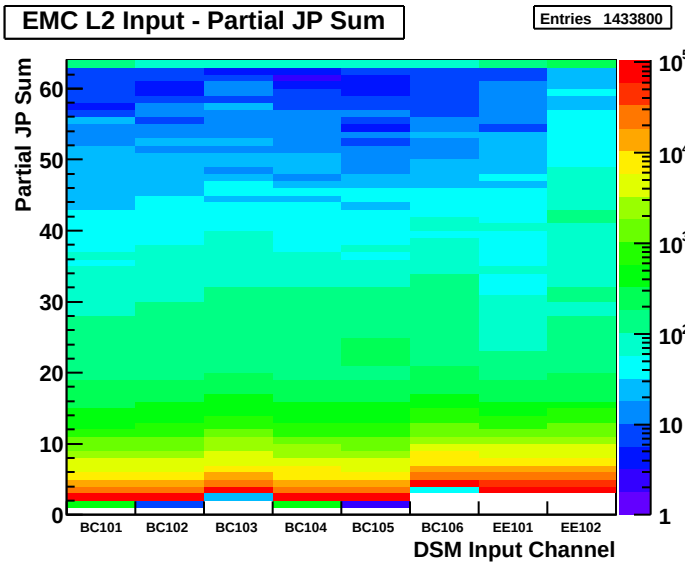

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

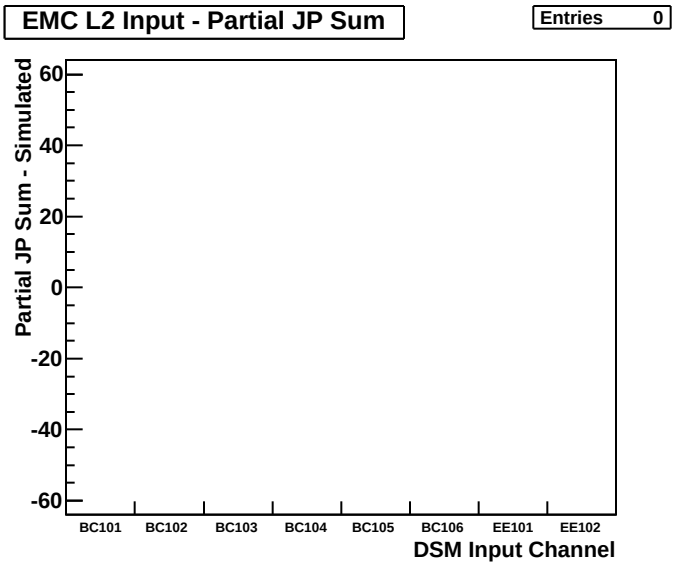

EMC L2 Input - JPY/JPB bits
Entries 1433800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
355 242 222 167 186 234 162 294
1263 752 804 706 850 1029 606 1083
1861 1255 1334 1348 2121 1697 511 743
175706 176876 176865 177004 176068 176265 177946 177105

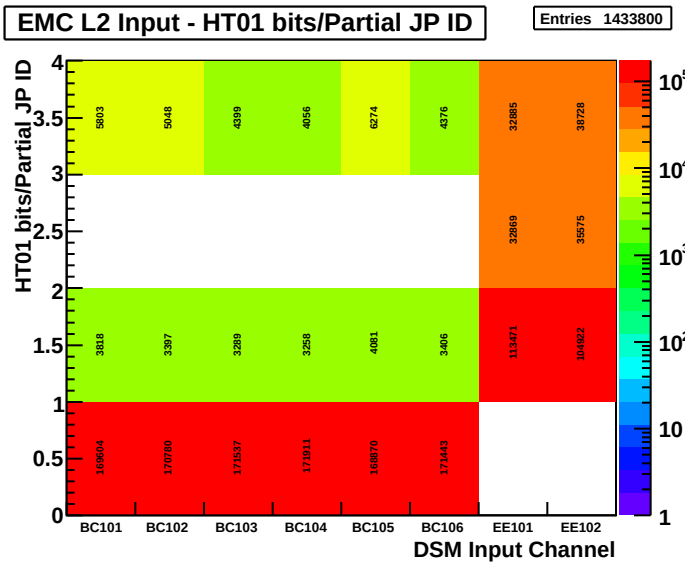

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

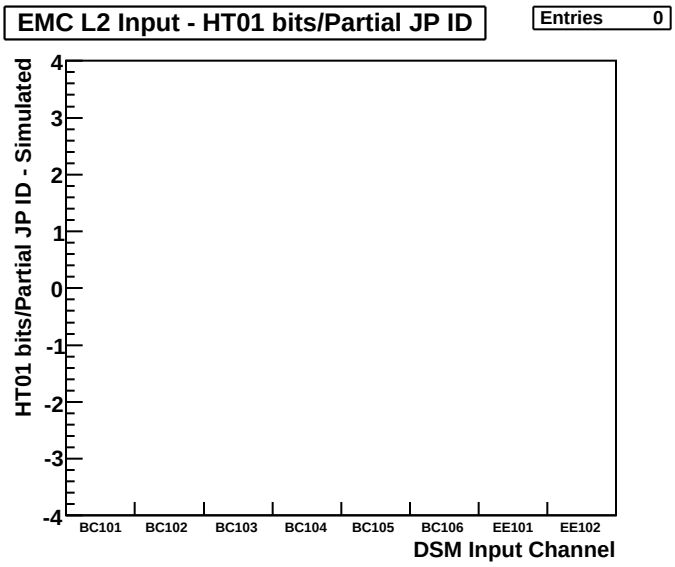

EMC L2 Input - JPZ/JPC bits
Entries 1433800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
260 198 213 175 212 241 156 175
907 752 939 745 927 1050 719 757
1463 1245 1503 1372 2156 2025 589 701
176505 177030 176570 176933 175930 175909 177761 177592

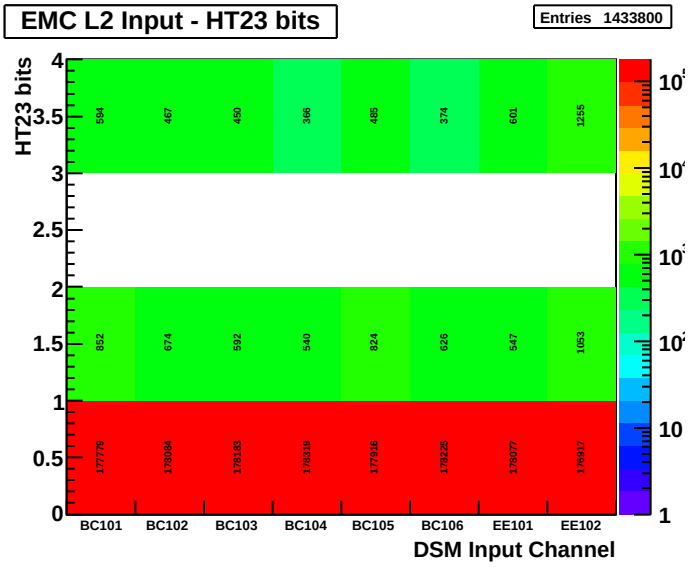

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

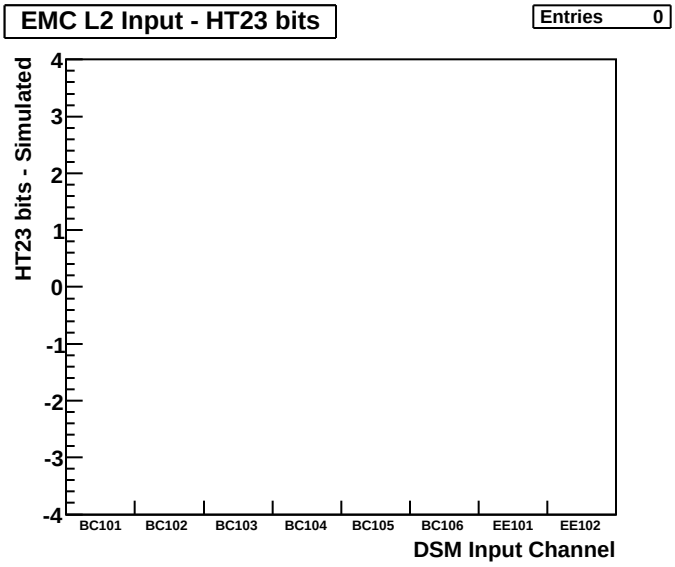

EMC L2 Input - Partial JP Sum
Entries 1433800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

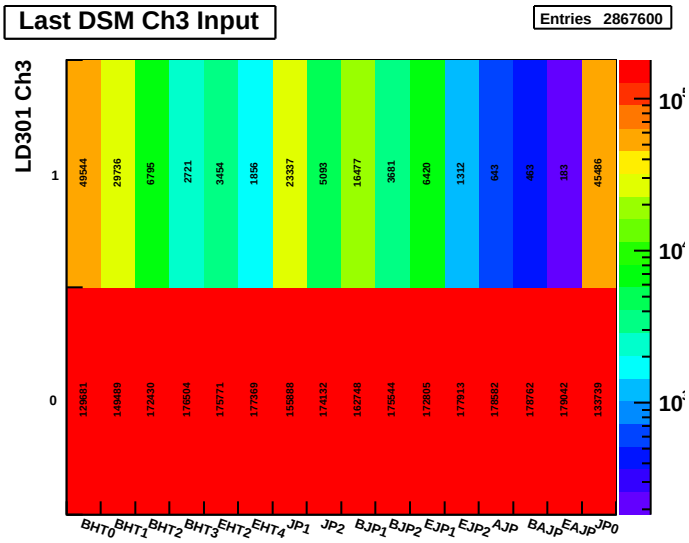

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

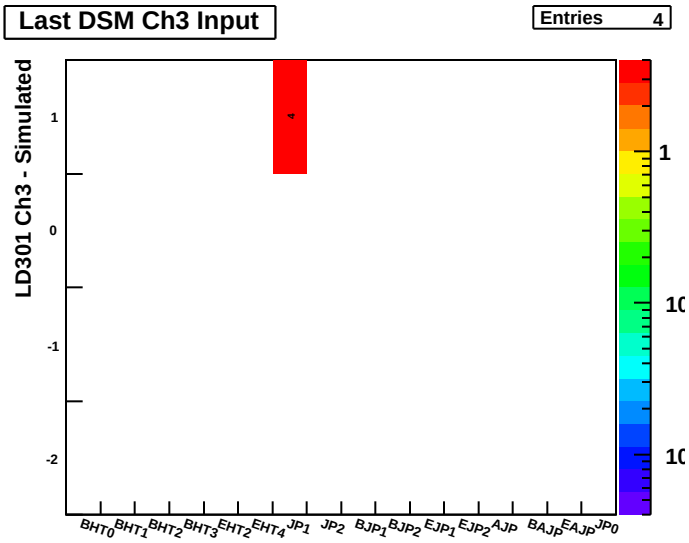

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1433800
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

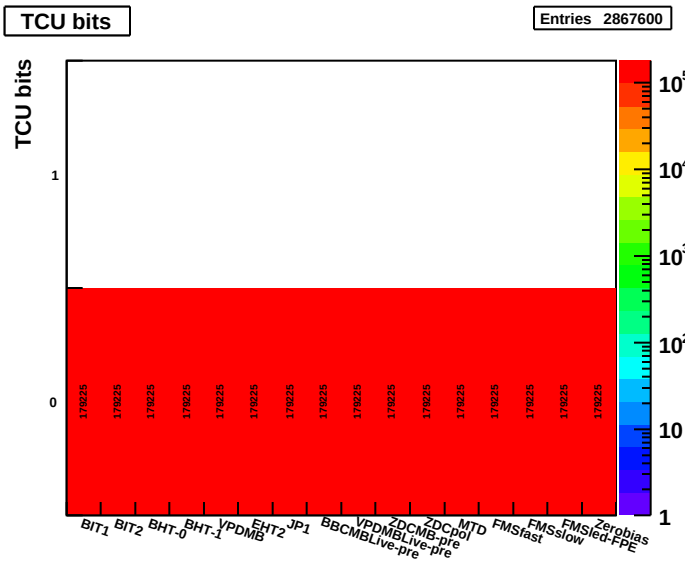

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

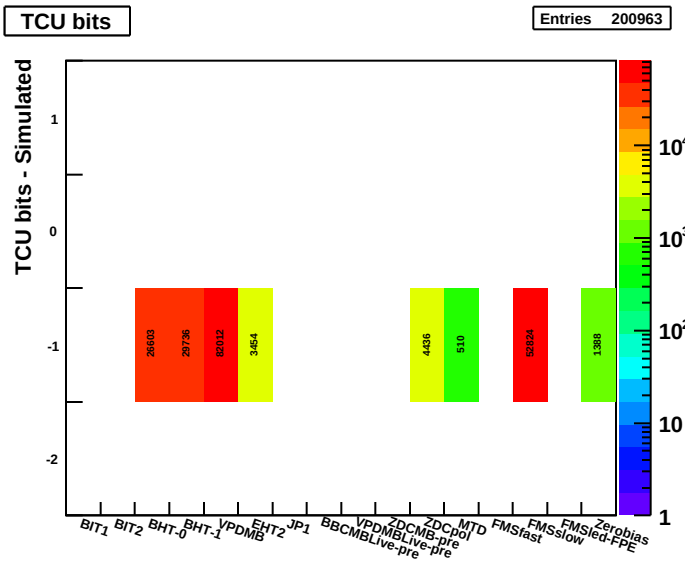

EMC L2 Input - HT23 bits
Entries 1433800
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

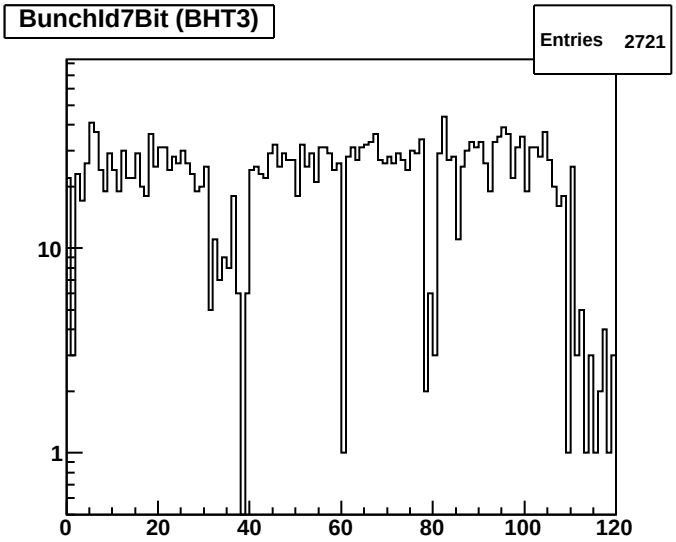

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

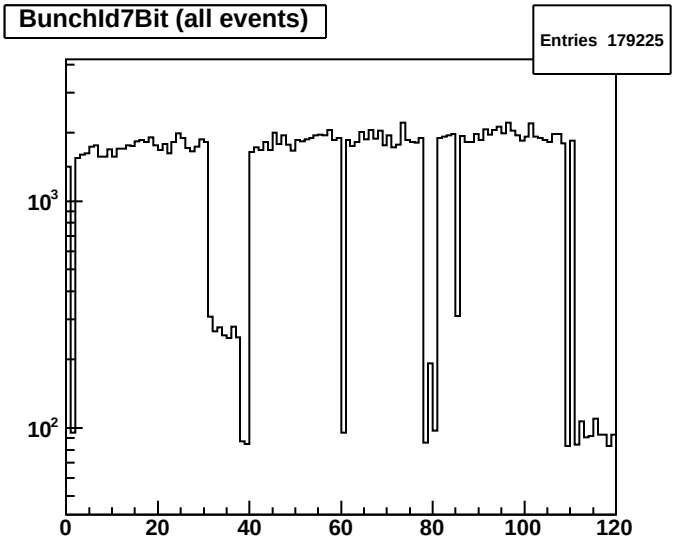

Last DSM Ch3 Input
Entries 2867600
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

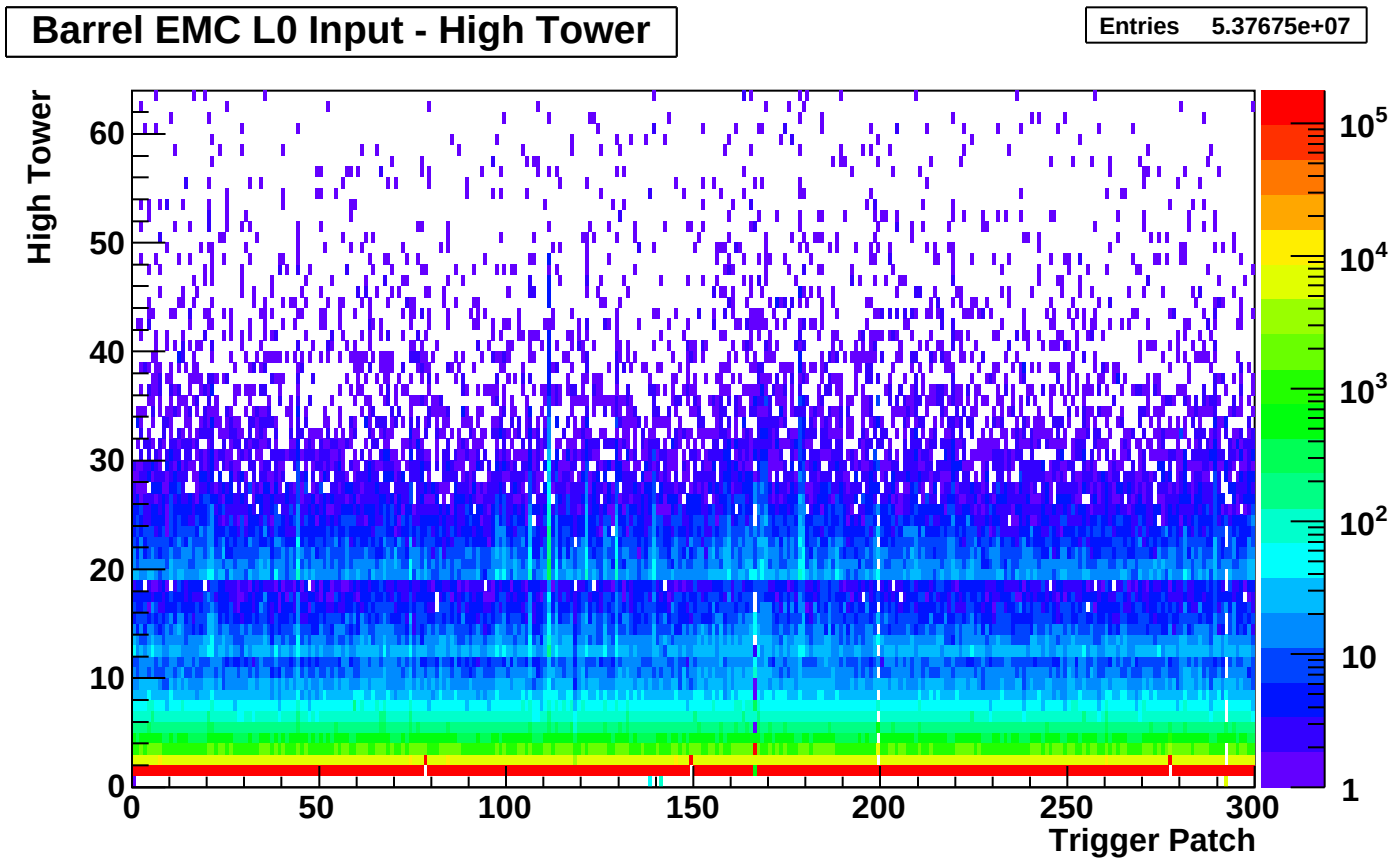

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

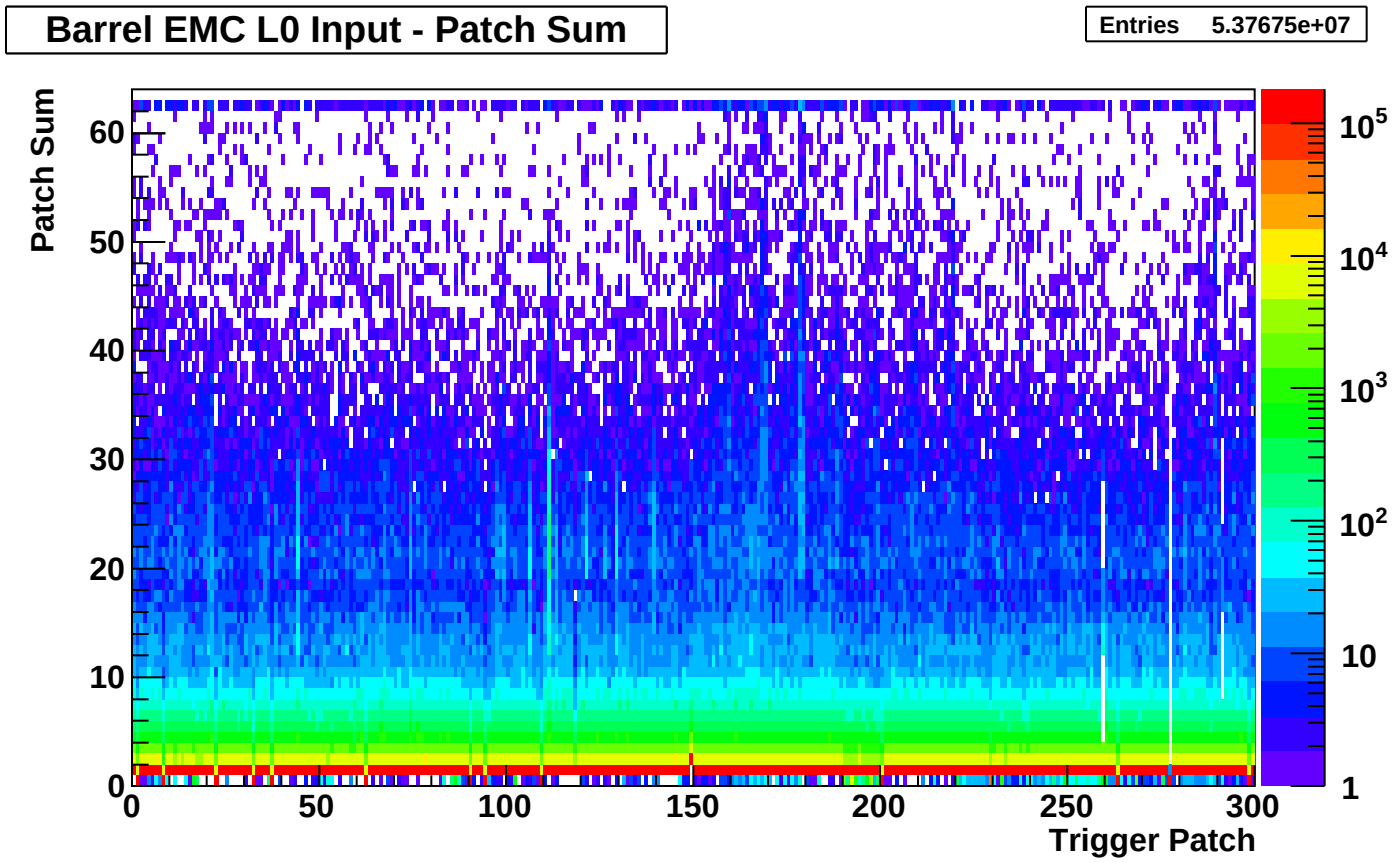

TCU bits
Entries 2867600
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

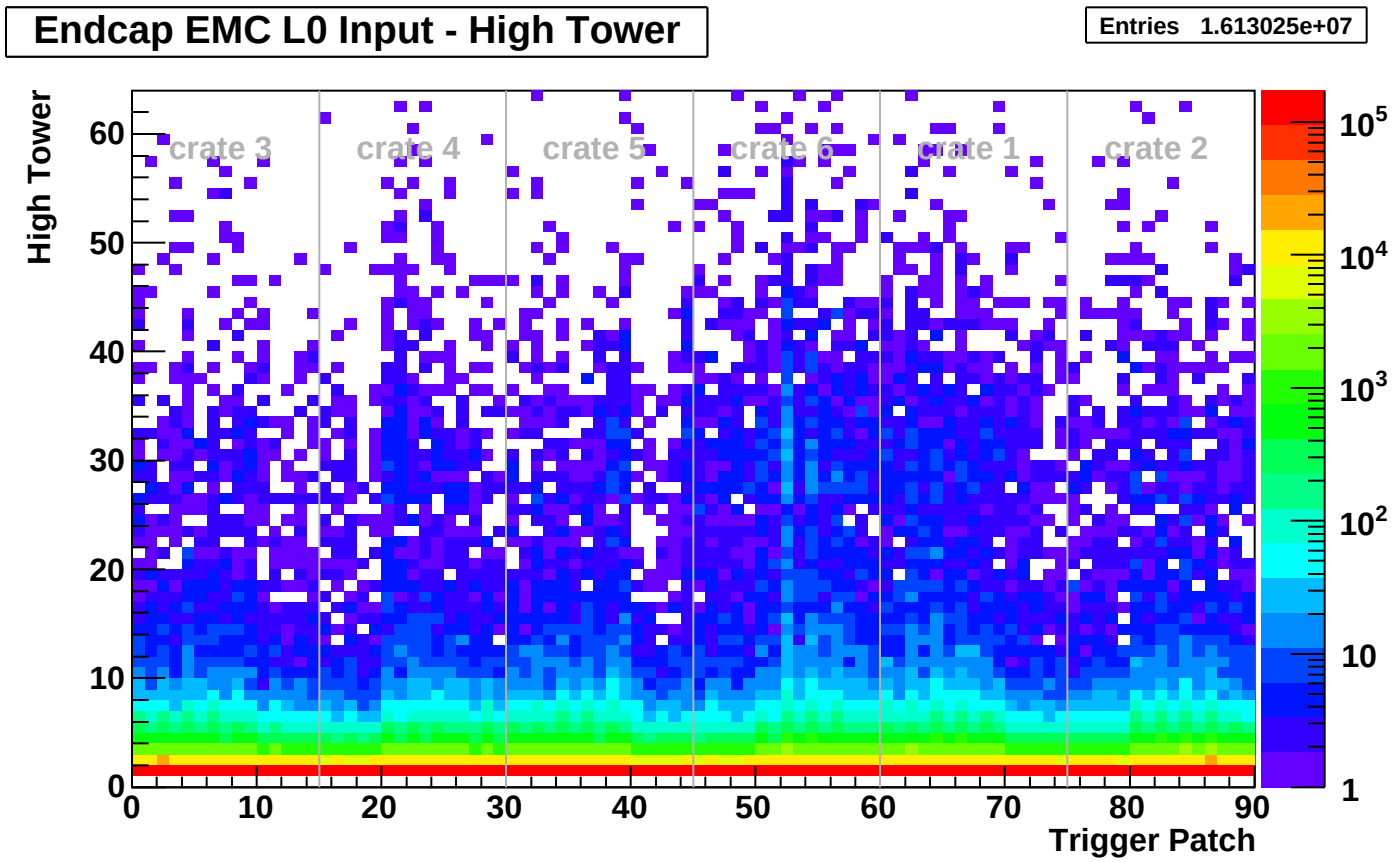

TCU bits
Entries 200963
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

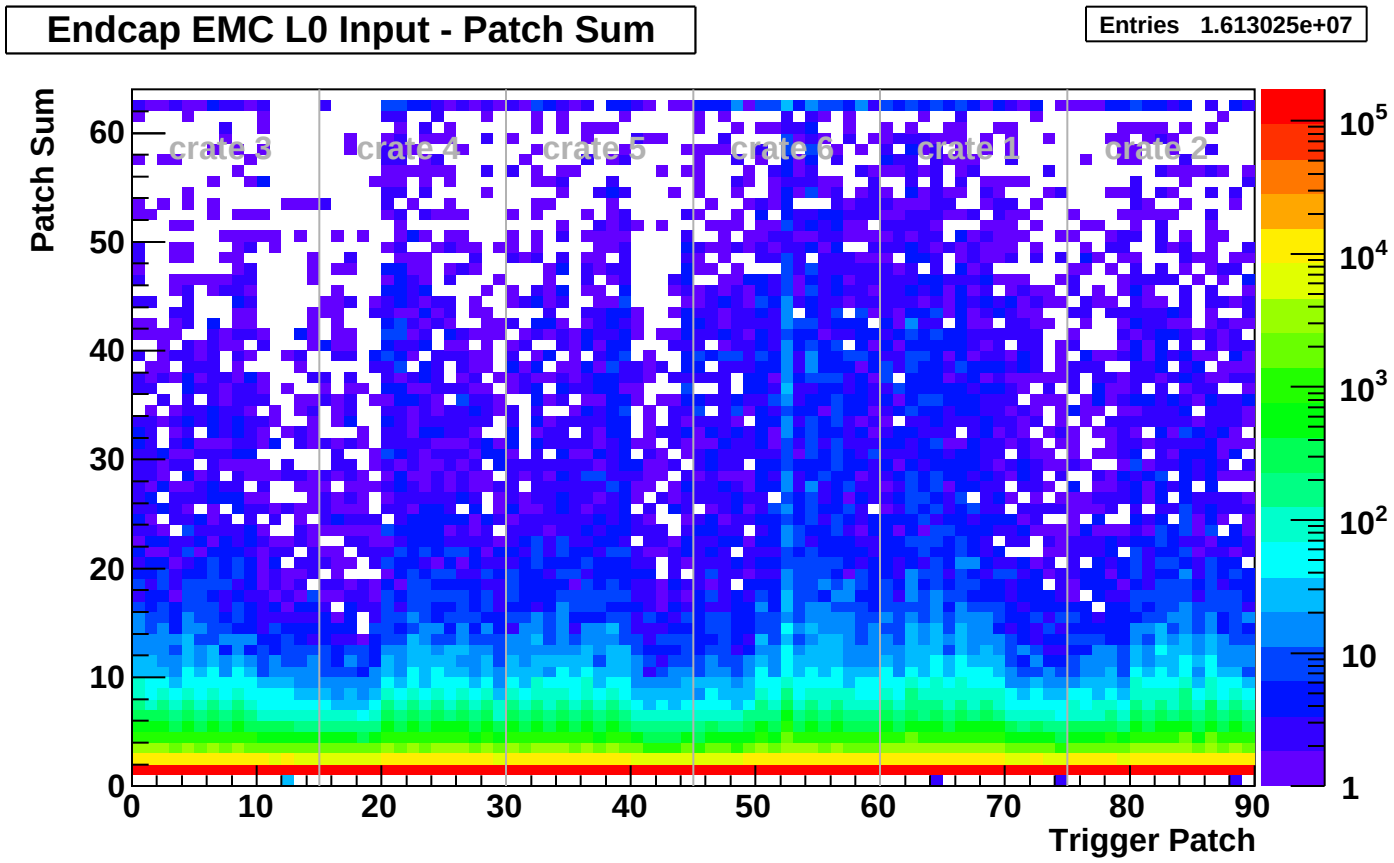

BunchId7Bit (BHT3)
Entries 2721

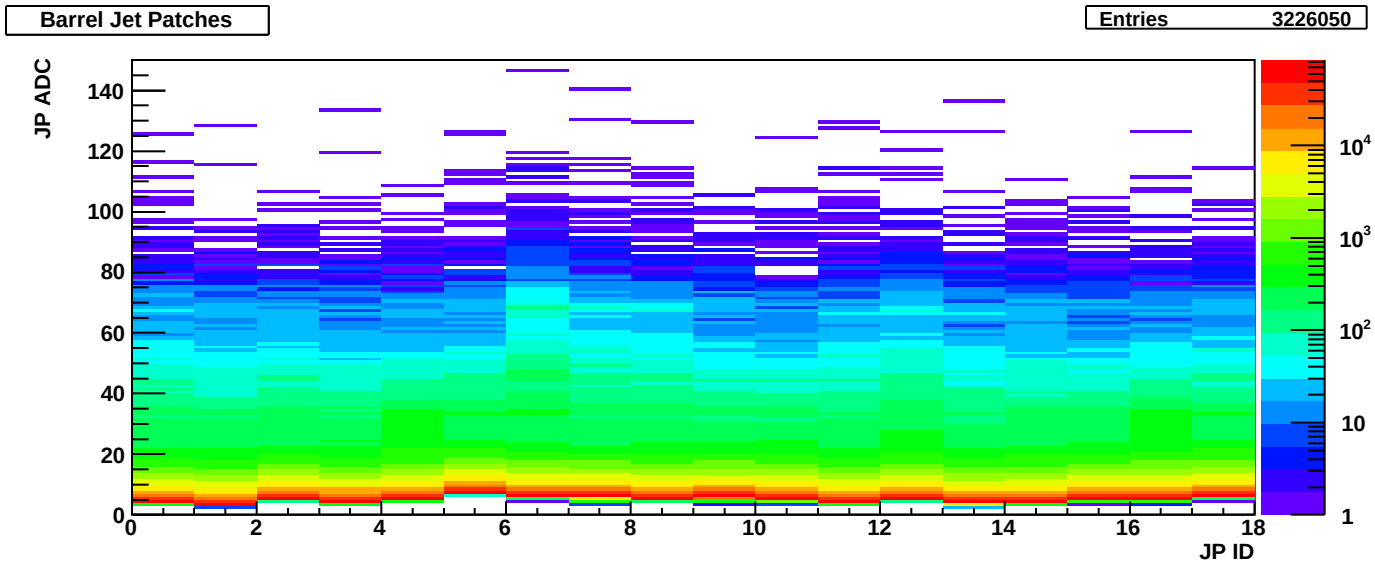

BunchId7Bit (all events)
Entries 179225

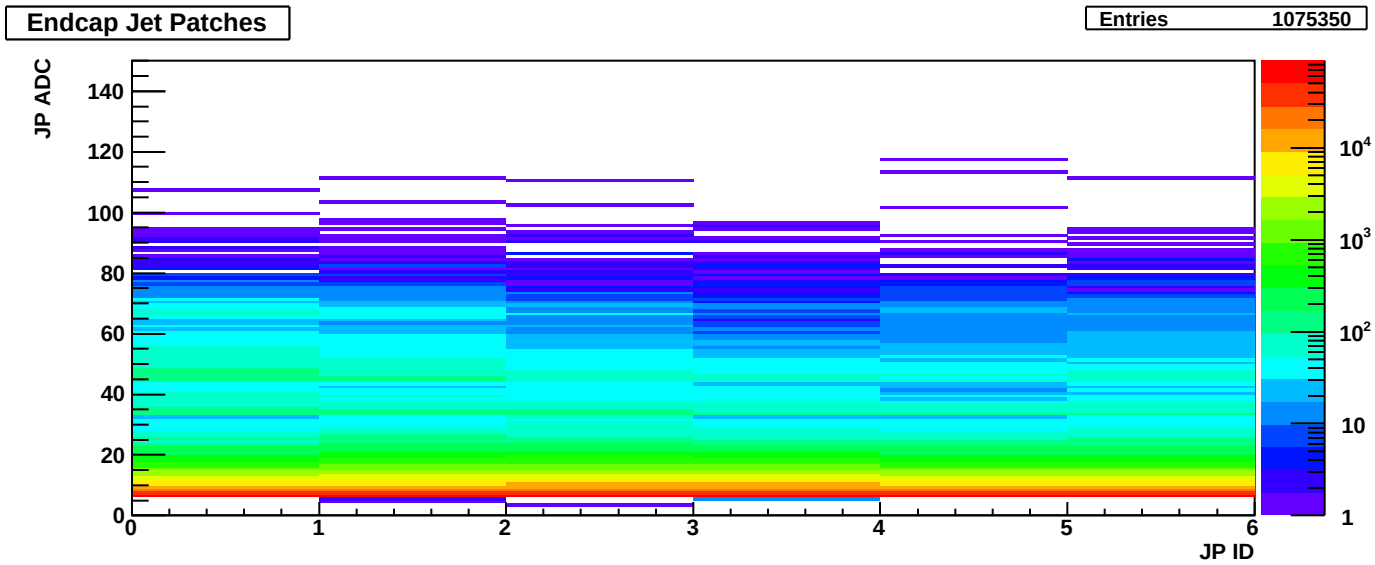

Barrel EMC L0 Input - High Tower
Entries 5.37675e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

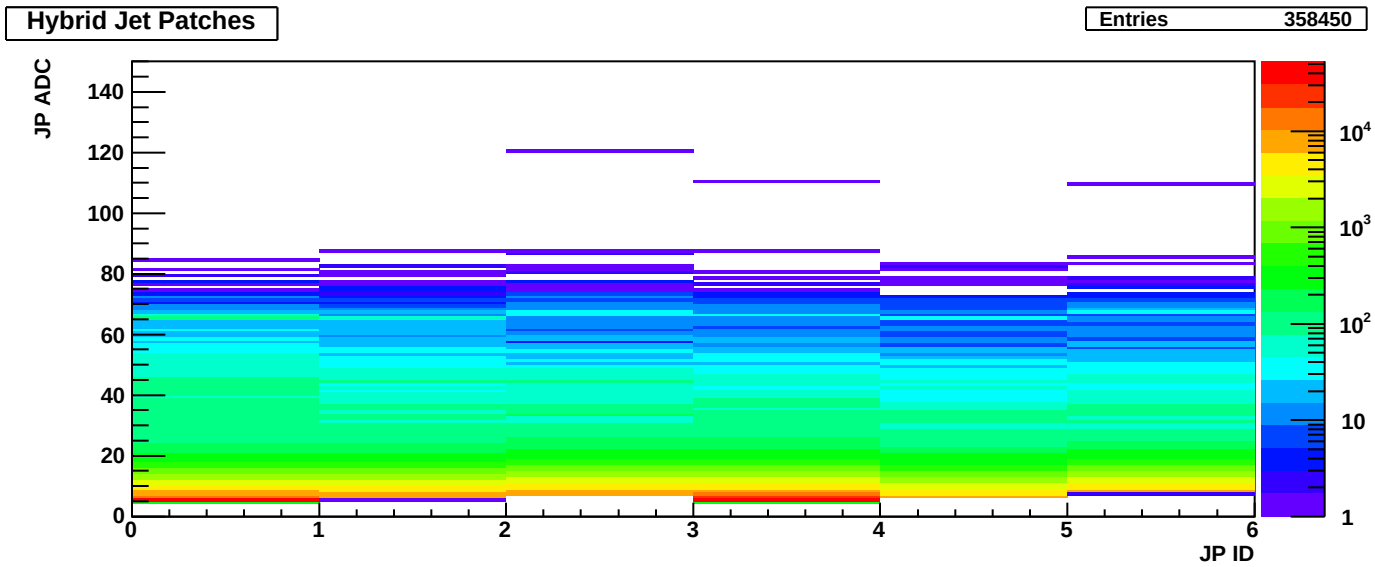

Barrel EMC L0 Input - Patch Sum
Entries 5.37675e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

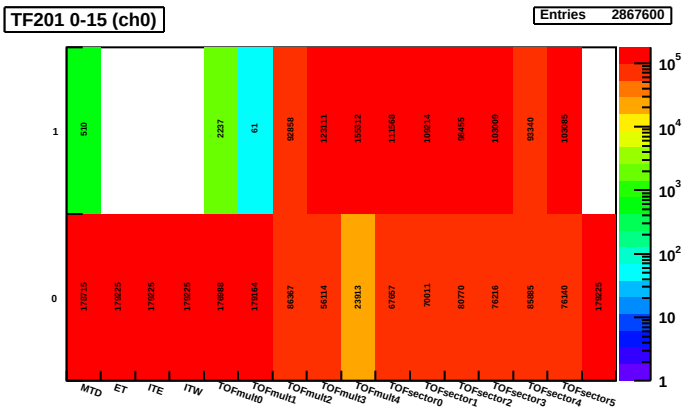

Endcap EMC L0 Input - High Tower
Entries 1.613025e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

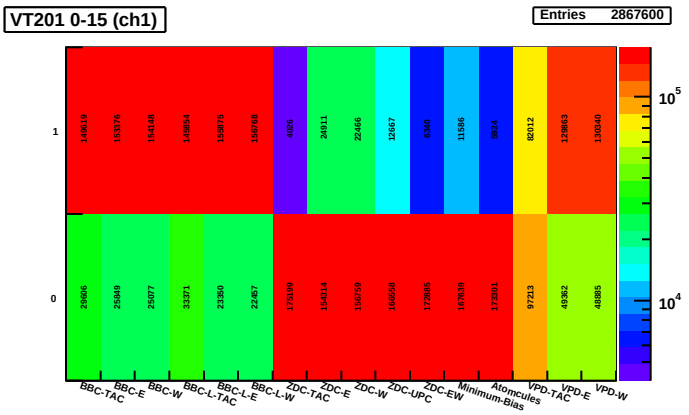

Endcap EMC L0 Input - Patch Sum
Entries 1.613025e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

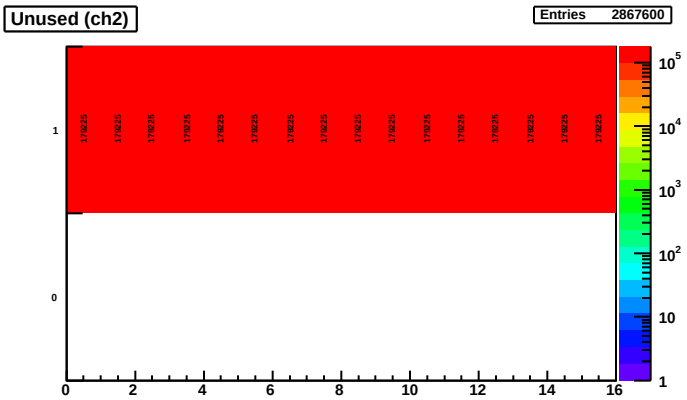

Barrel Jet Patches
Entries 3226050
JP ADC
JP ID

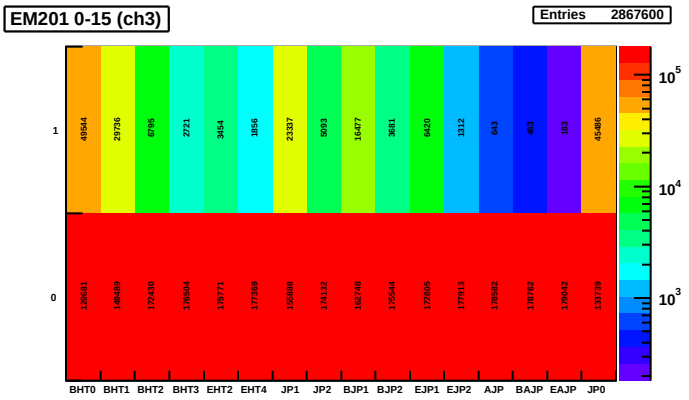

Endcap Jet Patches
Entries 1075350
JP ADC
JP ID

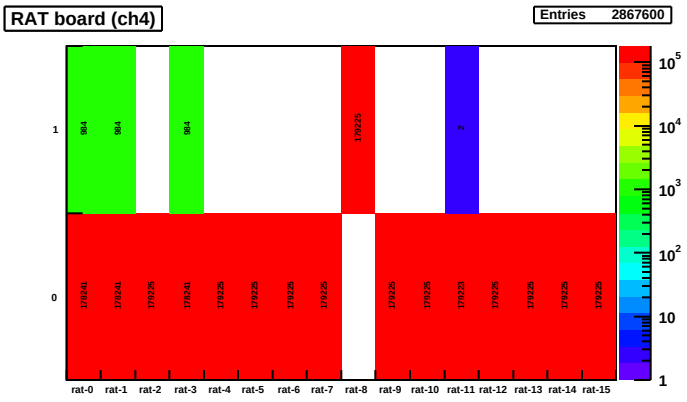

Hybrid Jet Patches
Entries 358450
JP ADC
JP ID

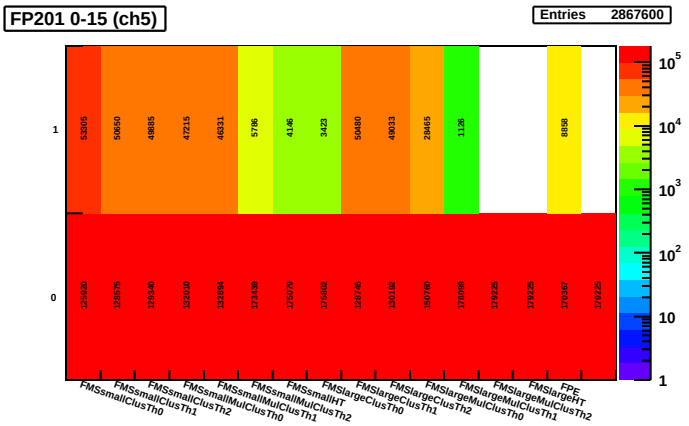

TF201 0-15 (ch0)
Entries 2867600
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

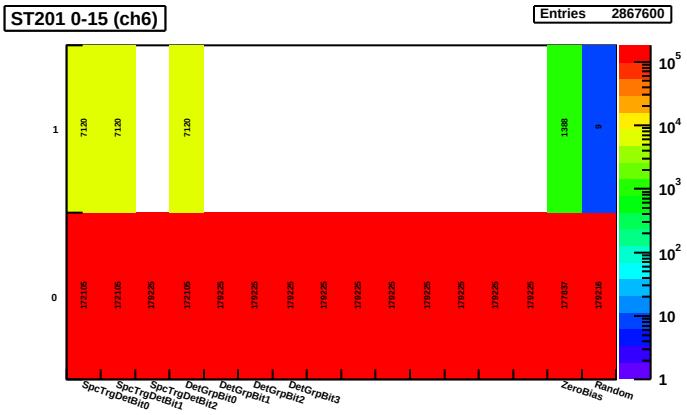

VT201 0-15 (ch1)
Entries 2867600
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

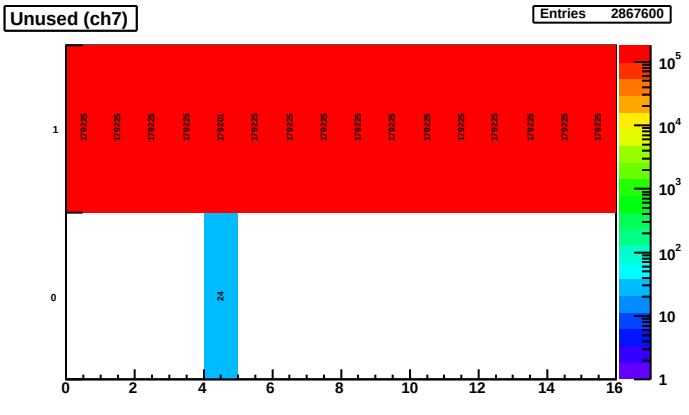

Unused (ch2)
Entries 2867600

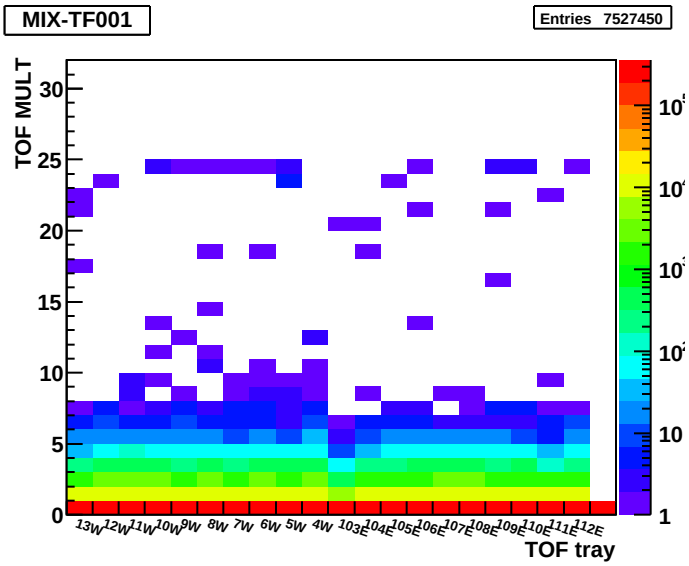

EM201 0-15 (ch3)
Entries 2867600
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

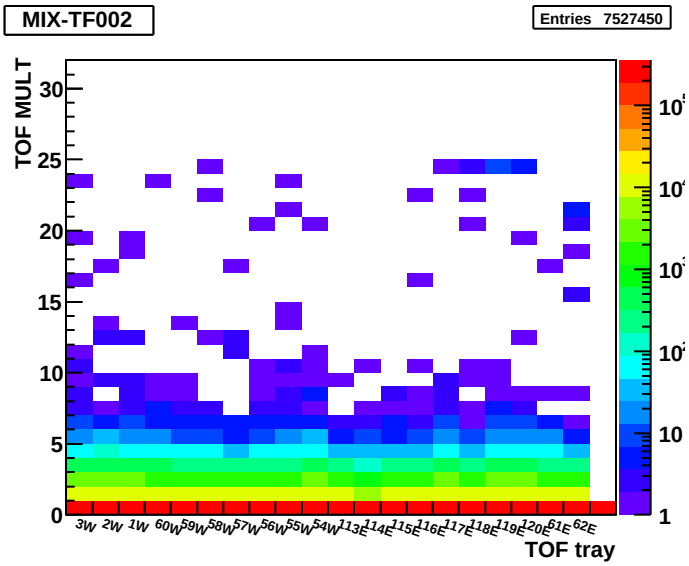

RAT board (ch4)
Entries 2867600
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

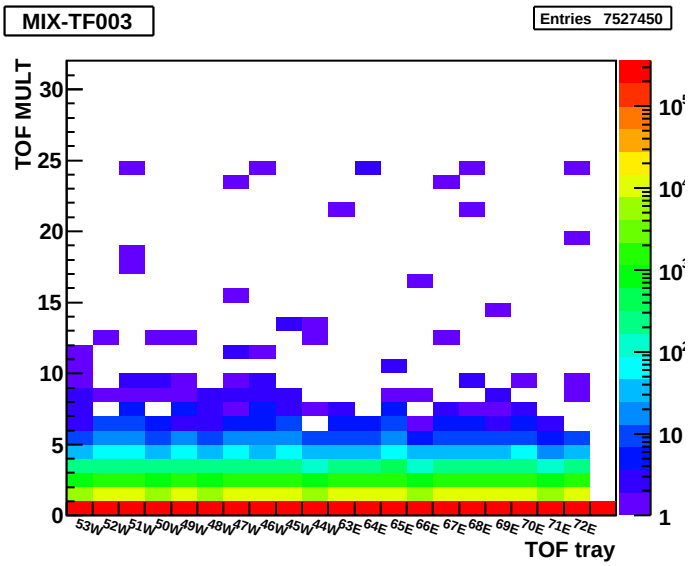

FP201 0-15 (ch5)
Entries 2867600
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

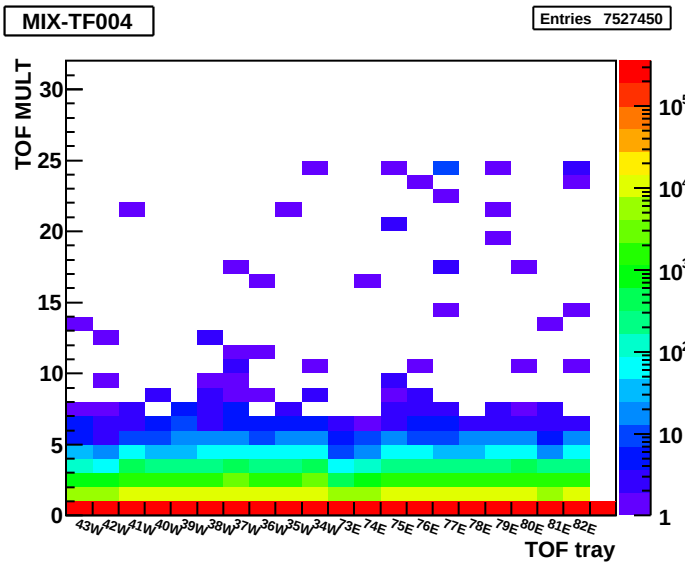

ST201 0-15 (ch6)
Entries 2867600
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

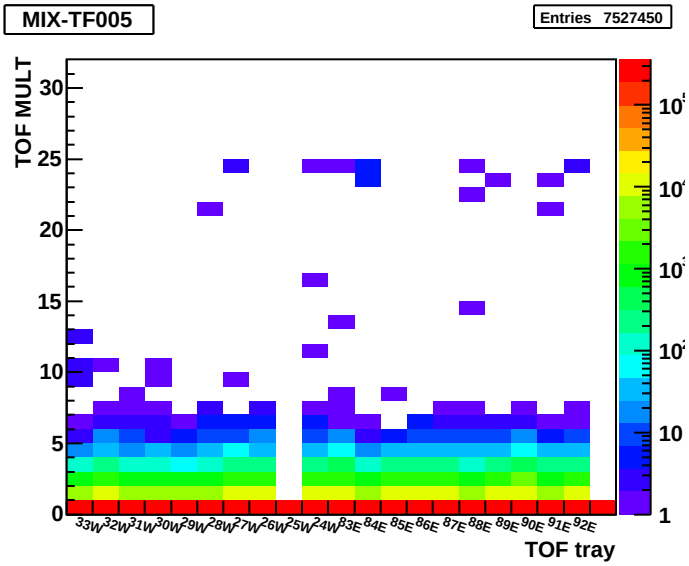

Unused (ch7)
Entries 2867600

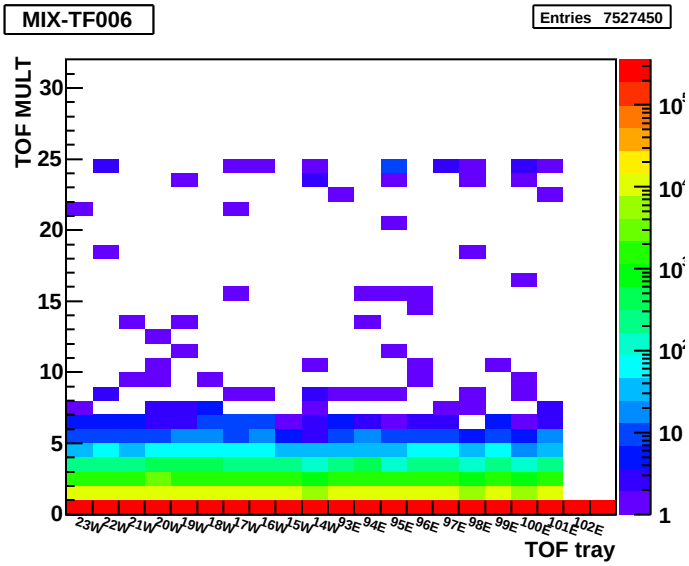

MIX-TF001
Entries 7527450
TOF MULT
TOF tray

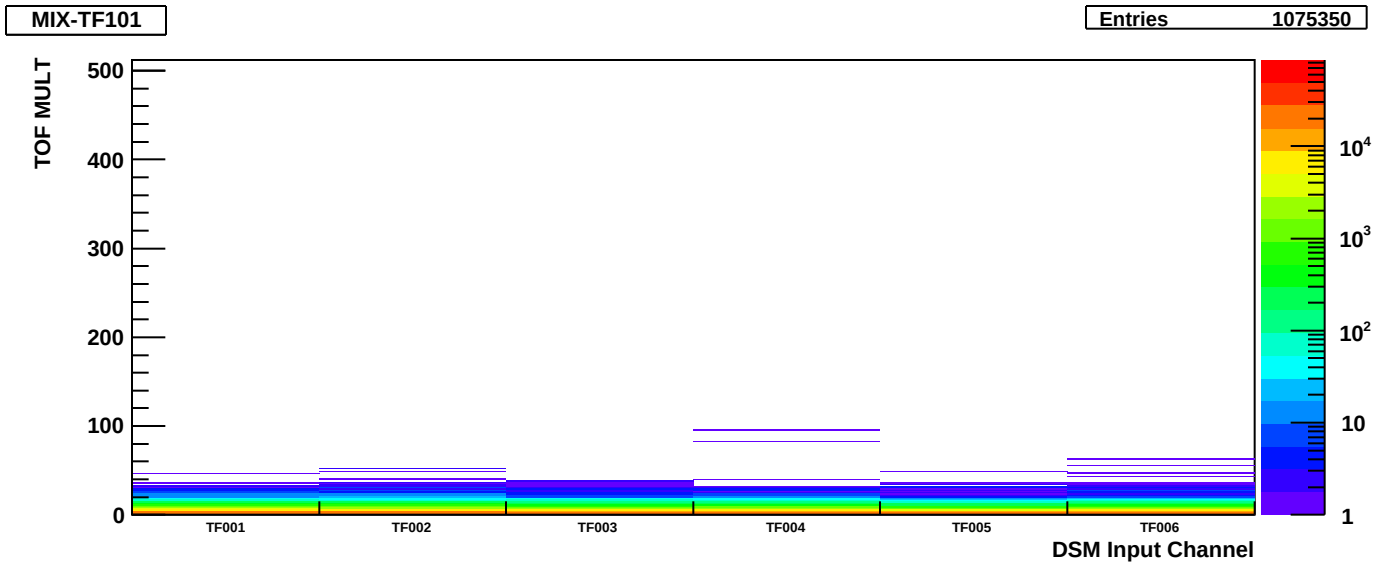

MIX-TF002
Entries 7527450
TOF MULT
TOF tray

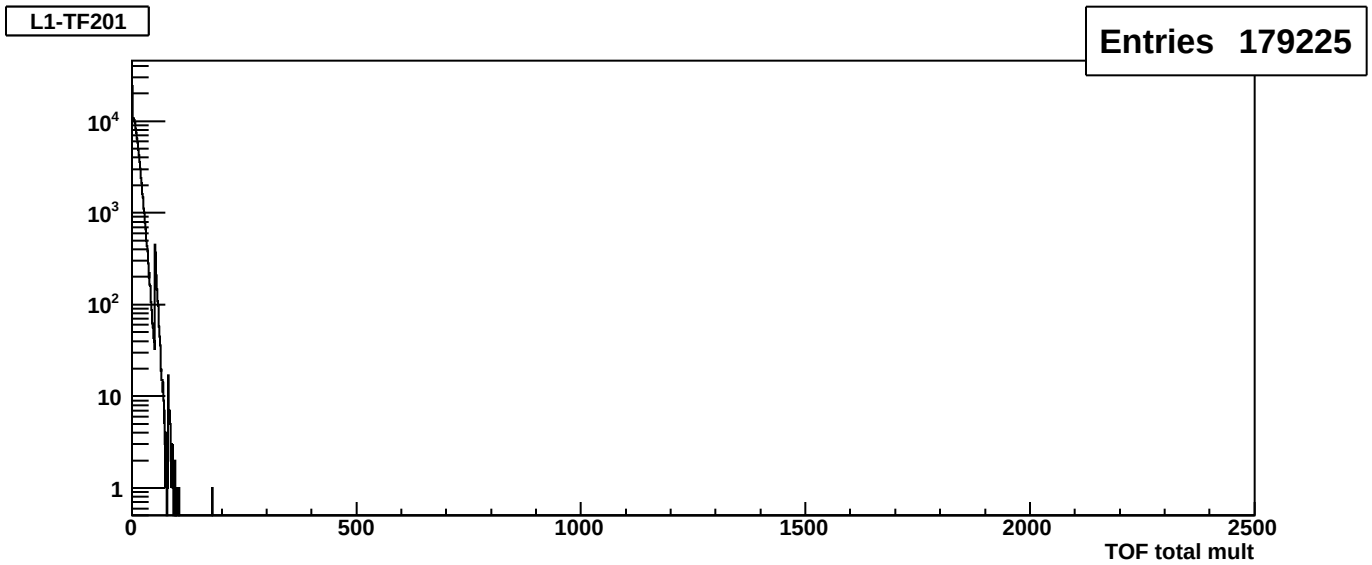

MIX-TF003
Entries 7527450
TOF MULT
TOF tray

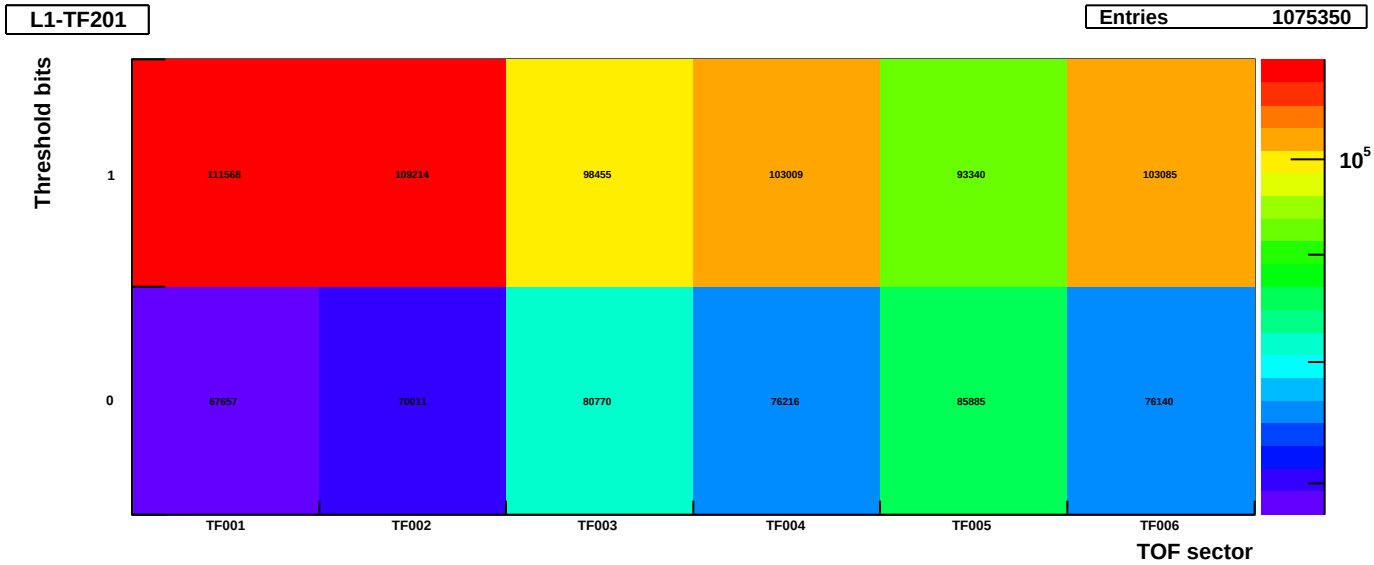

MIX-TF004
Entries 7527450
TOF MULT
TOF tray

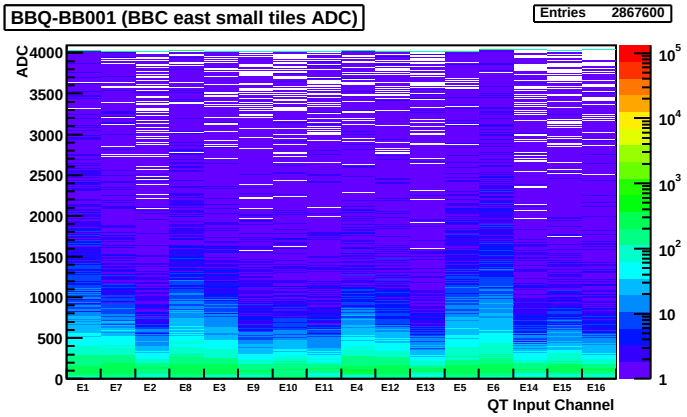

MIX-TF005
Entries 7527450
TOF MULT
TOF tray

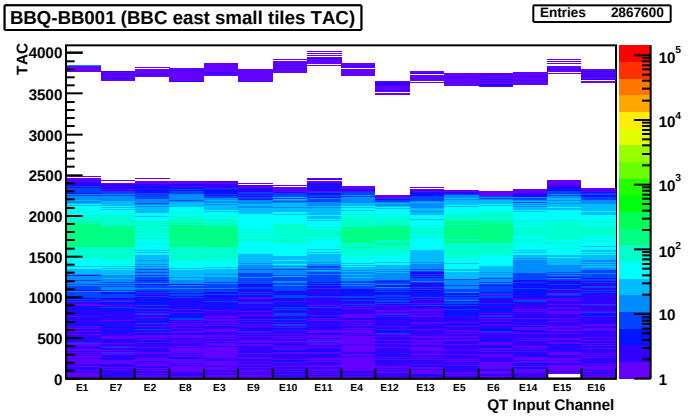

MIX-TF006
Entries 7527450
TOF MULT
TOF tray

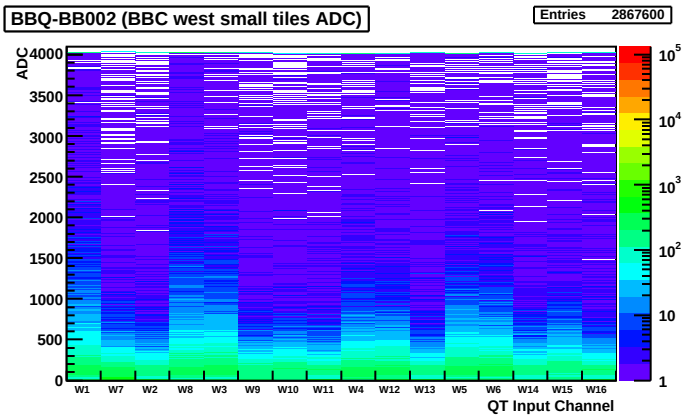

MIX-TF101
Entries 1075350
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

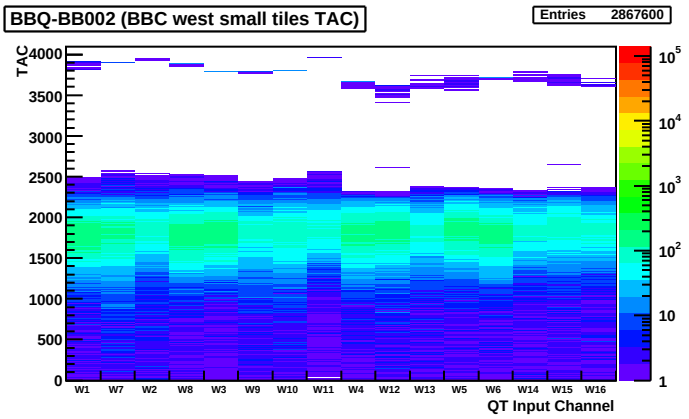

L1-TF201
Entries 179225
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

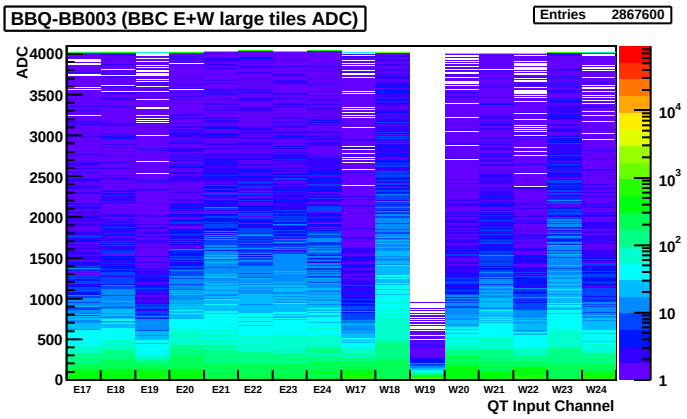

L1-TF201
Entries 1075350
Threshold bits
1
0
111568
109214
98455
103009
93340
103085
67657
70011
80770
76216
85885
76140
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

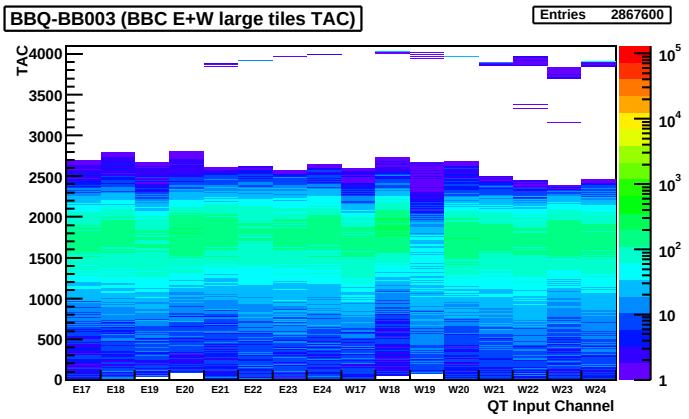

BBQ-BB001 (BBC east small tiles ADC)
Entries 2867600
ADC
QT Input Channel

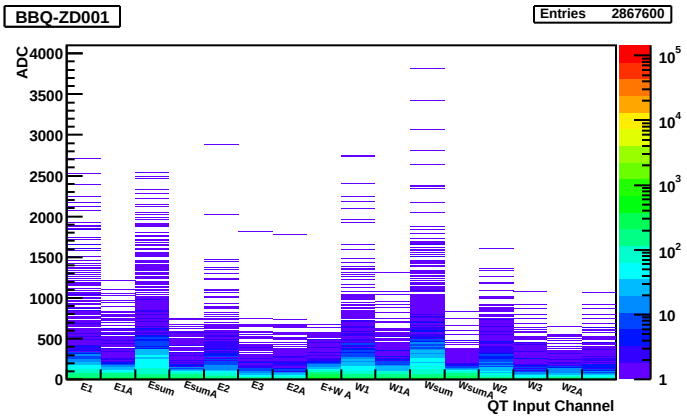

BBQ-BB001 (BBC east small tiles TAC)
Entries 2867600
TAC
QT Input Channel

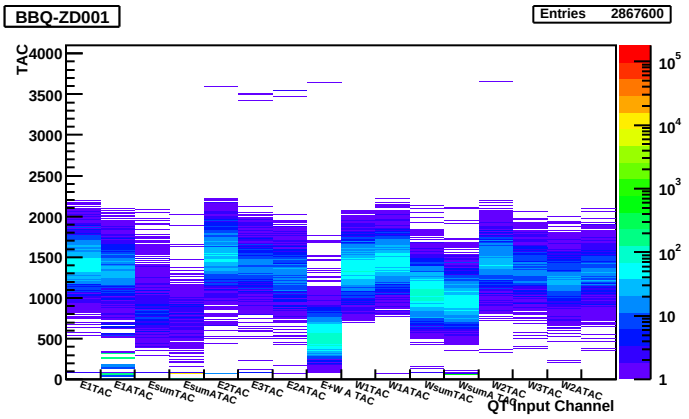

BBQ-BB002 (BBC west small tiles ADC)
Entries 2867600
ADC
QT Input Channel

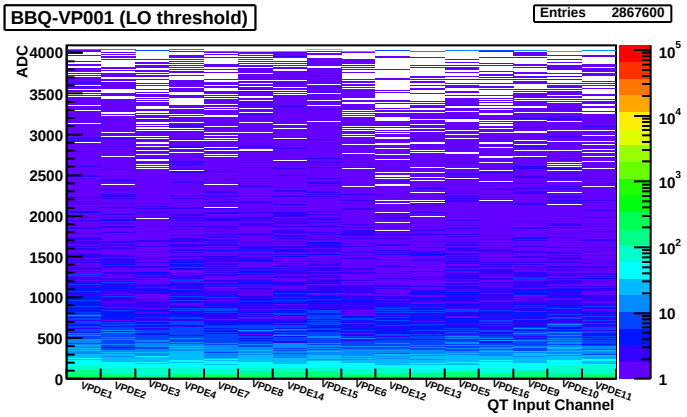

BBQ-BB002 (BBC west small tiles TAC)
Entries 2867600
TAC
QT Input Channel

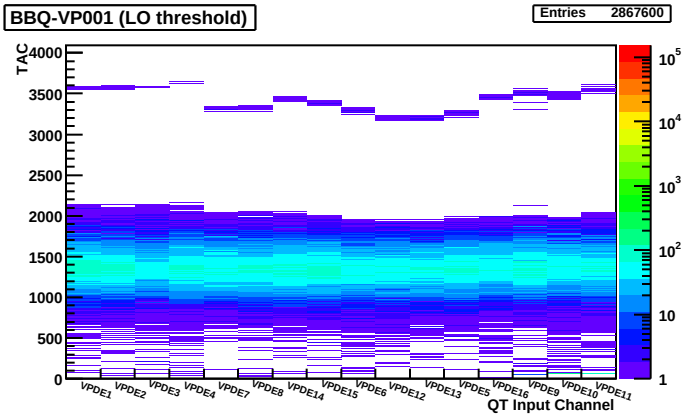

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2867600
ADC
QT Input Channel

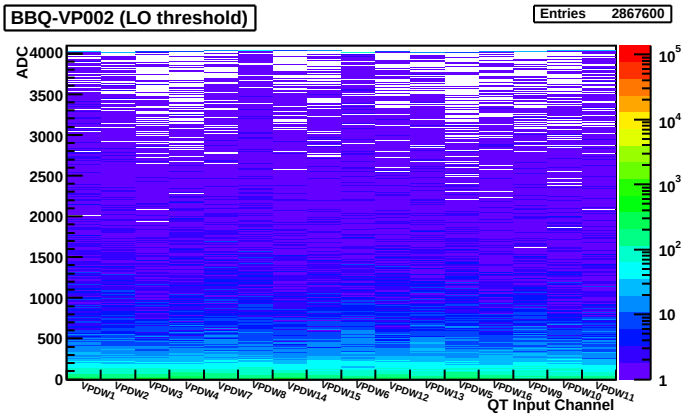

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2867600
TAC
QT Input Channel

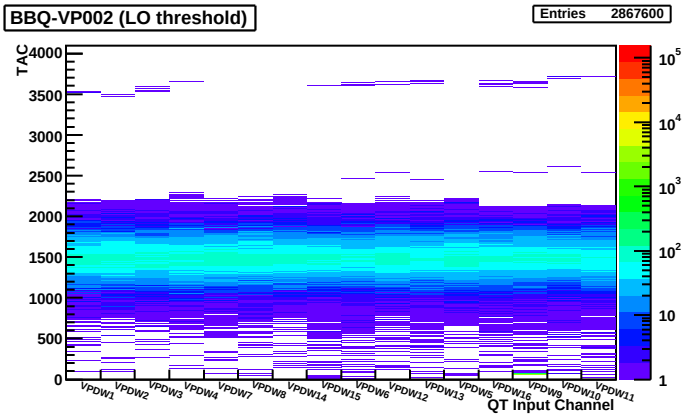

BBQ-ZD001
Entries 2867600
ADC
QT Input Channel

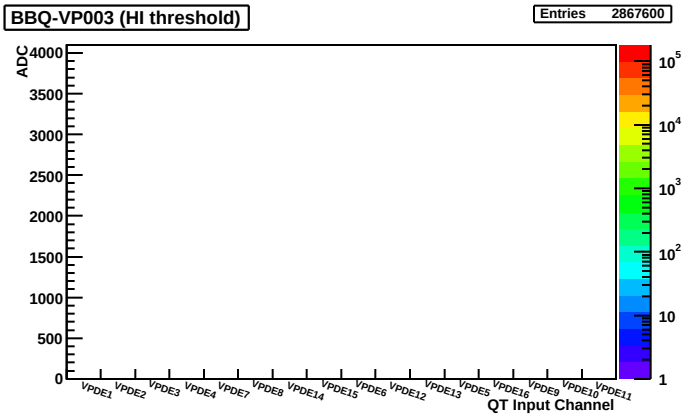

BBQ-ZD001
Entries 2867600
TAC
QT Input Channel

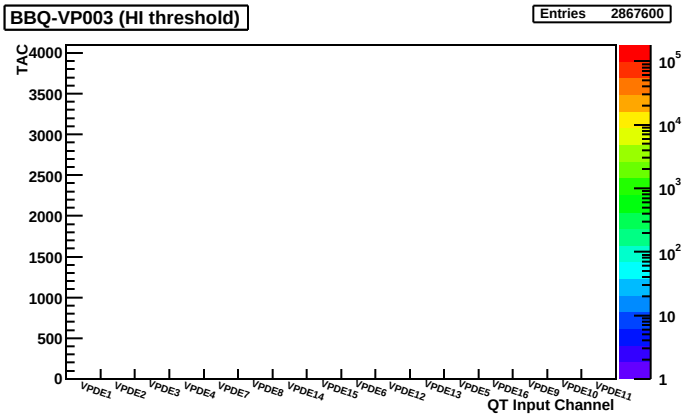

BBQ-VP001 (LO threshold)
Entries 2867600
ADC
QT Input Channel

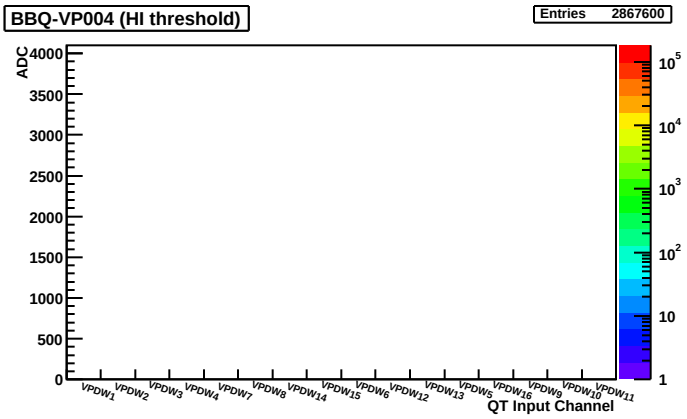

BBQ-VP001 (LO threshold)
Entries 2867600
TAC
QT Input Channel

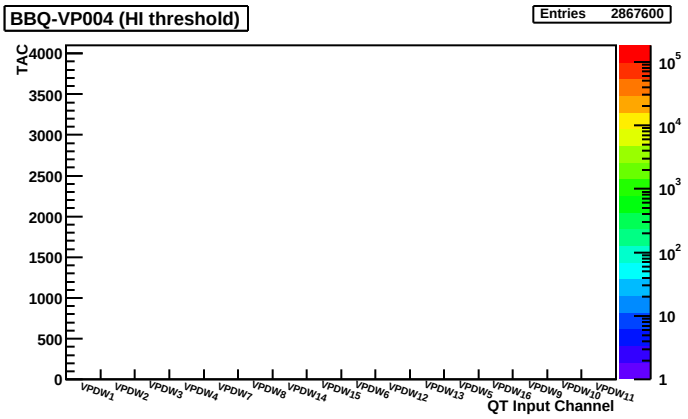

BBQ-VP002 (LO threshold)
Entries 2867600
ADC
QT Input Channel

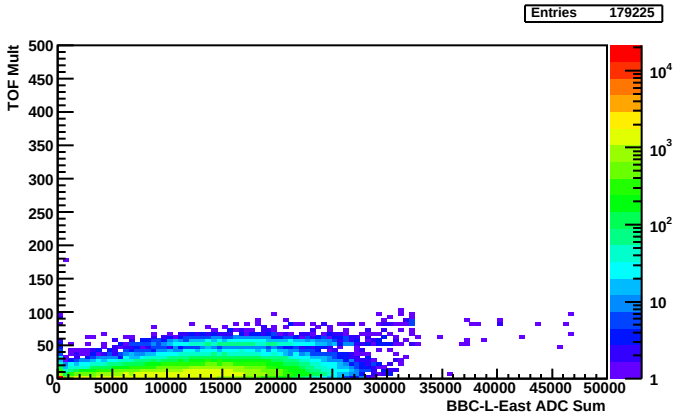

BBQ-VP002 (LO threshold)
Entries 2867600
TAC
QT Input Channel

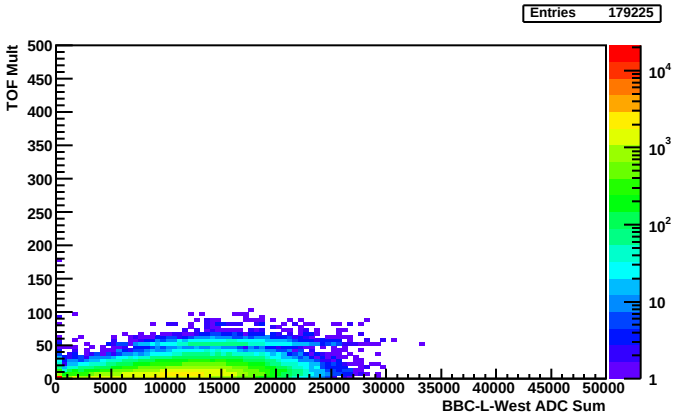

BBQ-VP003 (HI threshold)
Entries 2867600
ADC
QT Input Channel

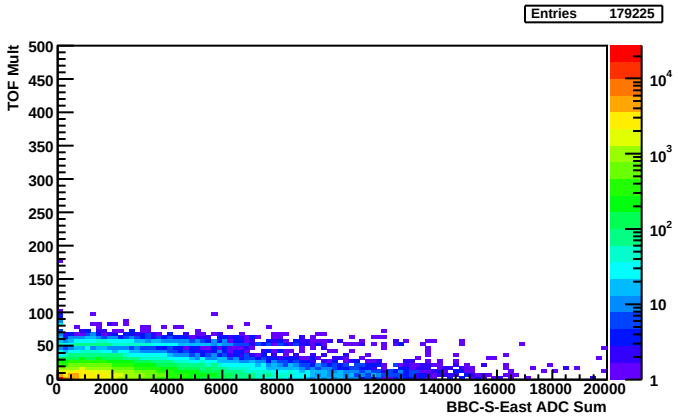

BBQ-VP003 (HI threshold)
Entries 2867600
TAC
QT Input Channel

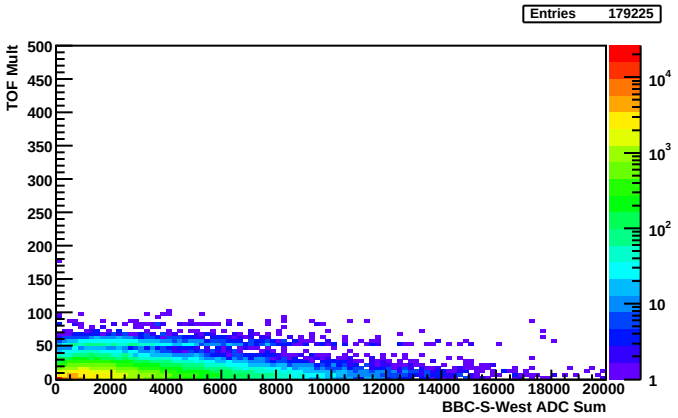

BBQ-VP004 (HI threshold)
Entries 2867600
ADC
QT Input Channel

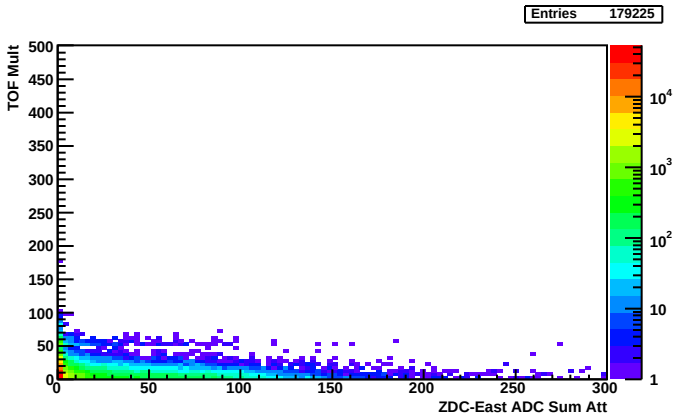

BBQ-VP004 (HI threshold)
Entries 2867600
TAC
QT Input Channel

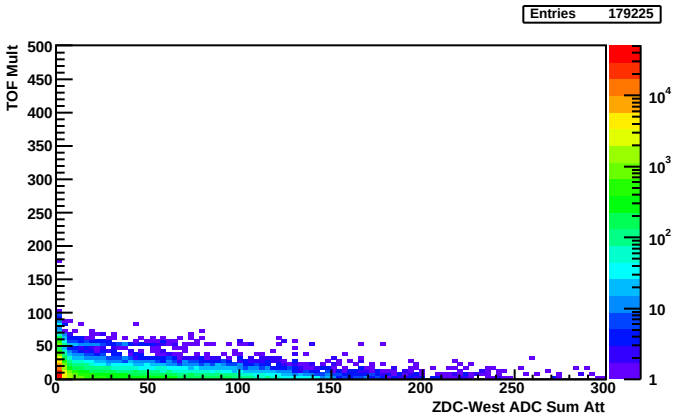

Entries 179225
TOF Mult
BBC-L-East ADC Sum

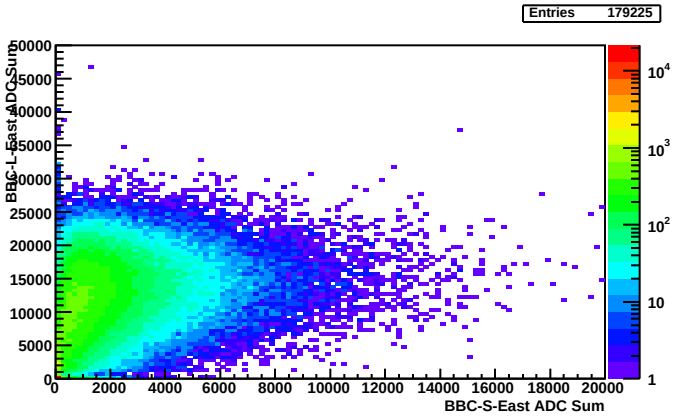

Entries 179225
TOF Mult
BBC-L-West ADC Sum

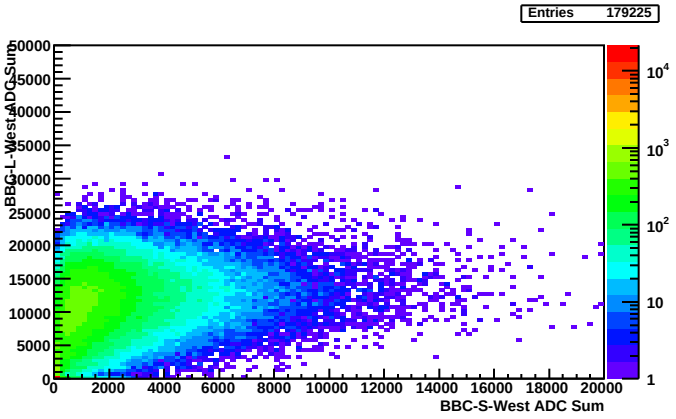

Entries 179225
TOF Mult
BBC-S-East ADC Sum

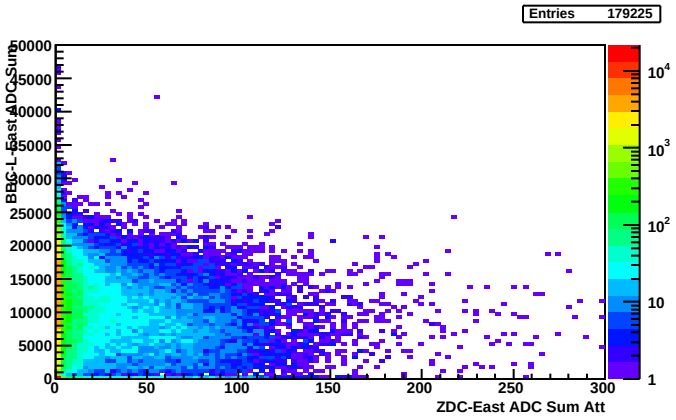

Entries 179225
TOF Mult
BBC-S-West ADC Sum

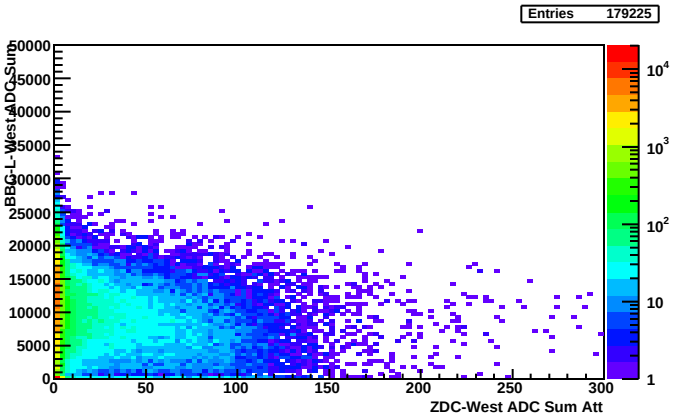

Entries 179225
TOF Mult
ZDC-East ADC Sum Att

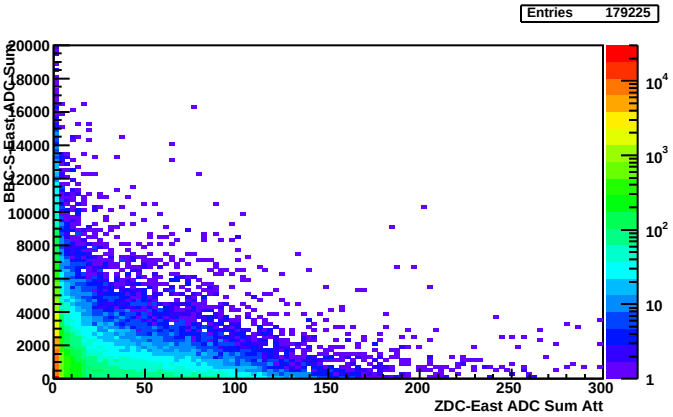

Entries 179225
TOF Mult
ZDC-West ADC Sum Att

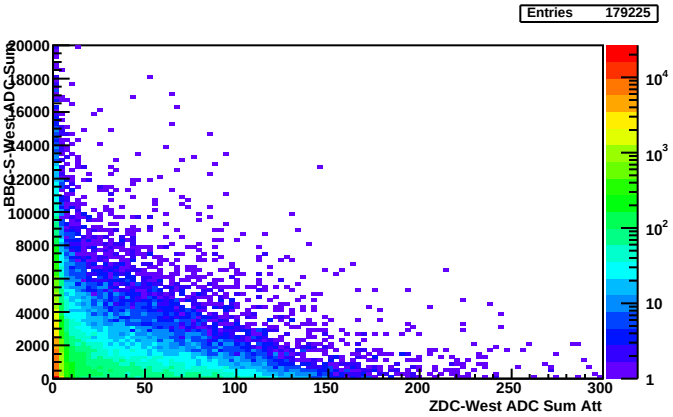

Entries 179225
BBC-L-East ADC Sum
BBC-S-East ADC Sum

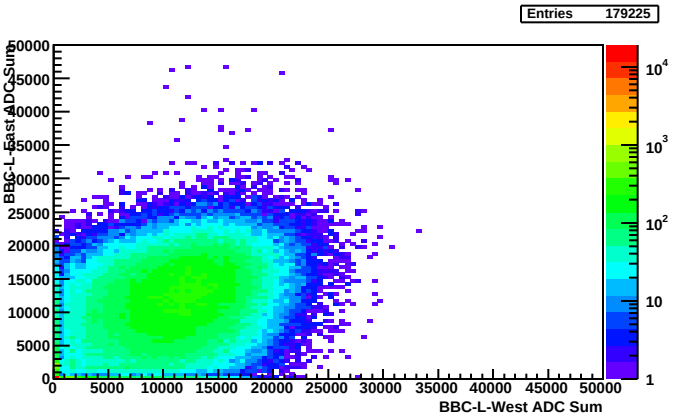

Entries 179225
BBC-L-West ADC Sum
BBC-S-West ADC Sum

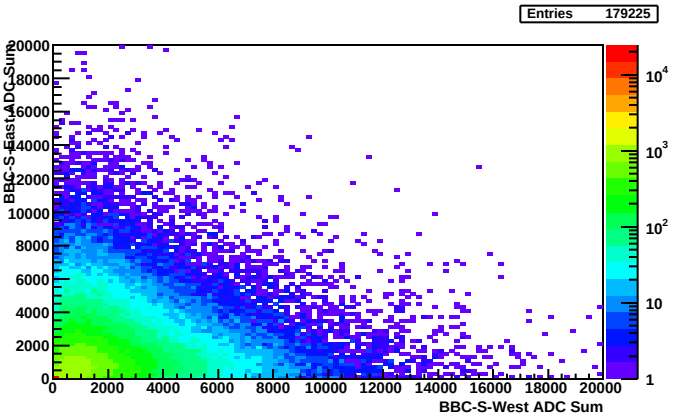

Entries 179225
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

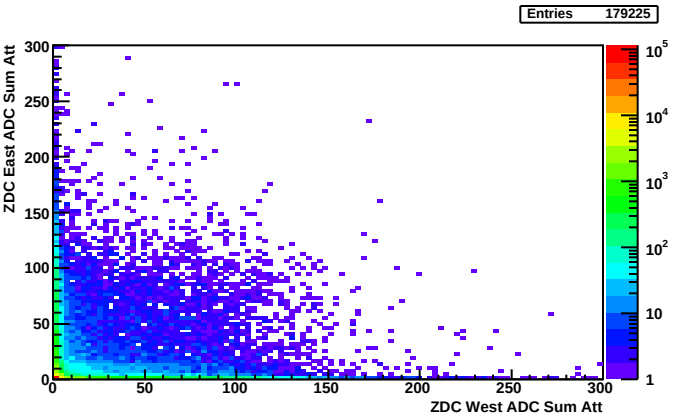

Entries 179225
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

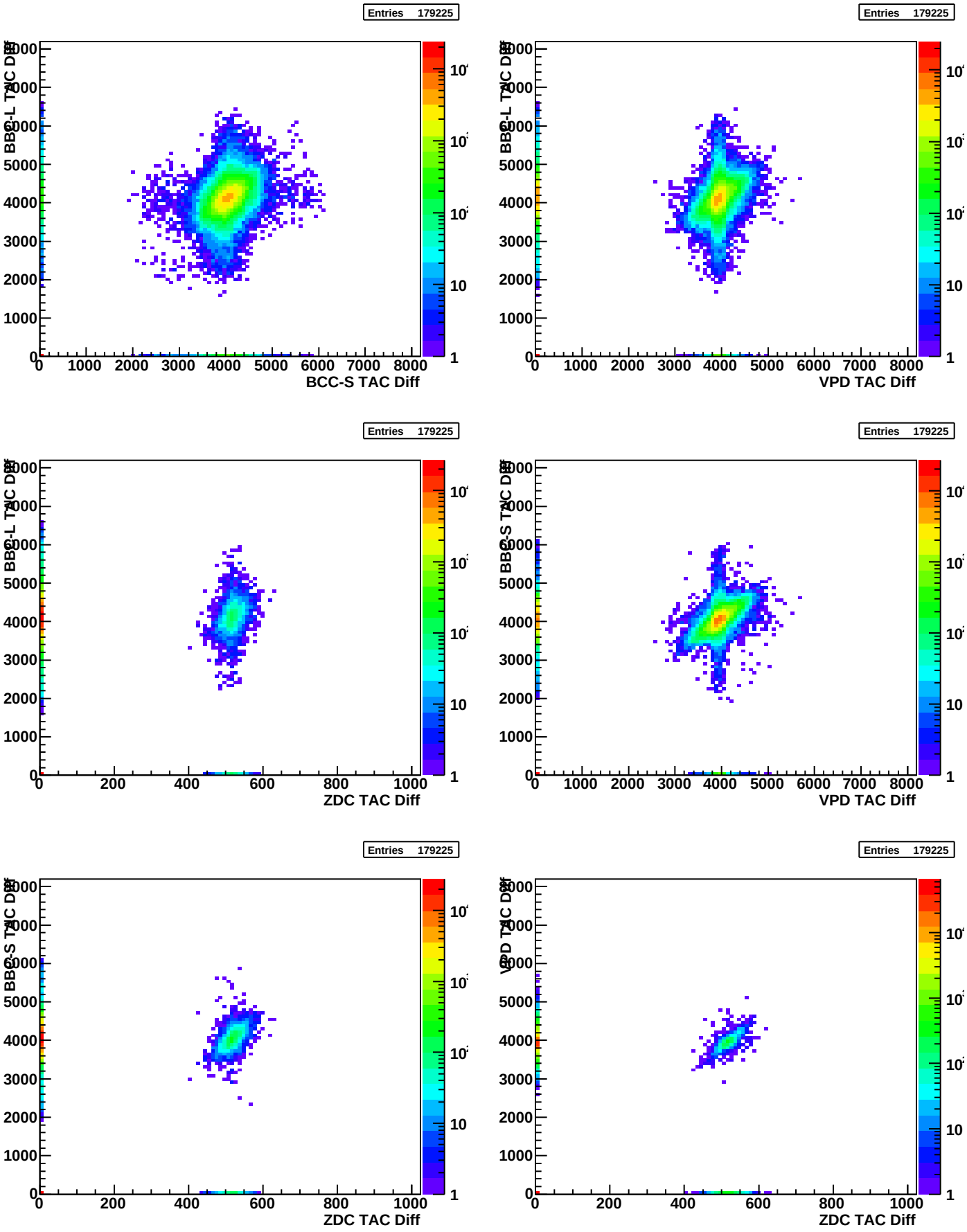

Entries 179225
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

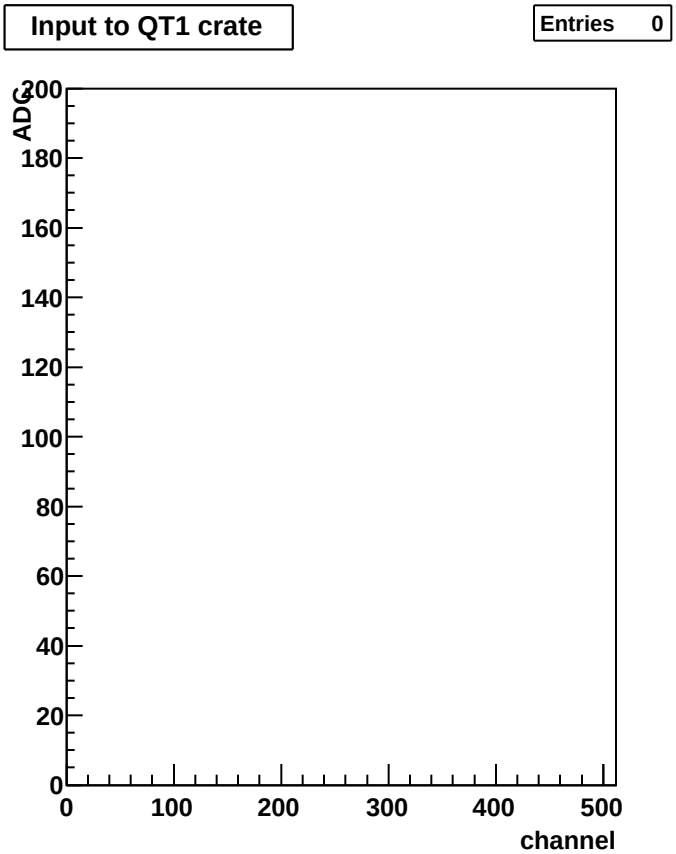

Entries 179225
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

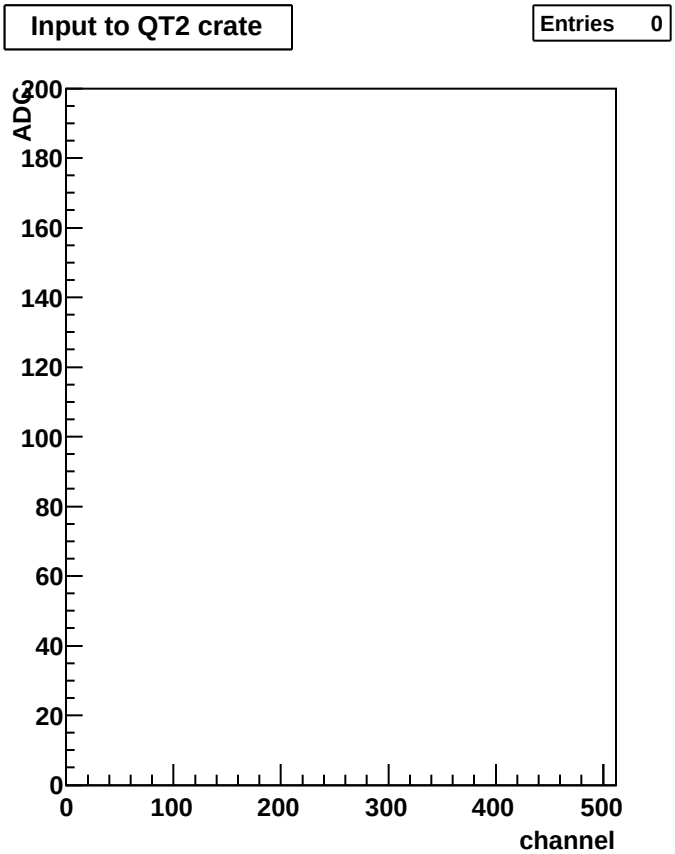

Entries 179225
BBC-L-East ADC Sum
BBC-L-West ADC Sum

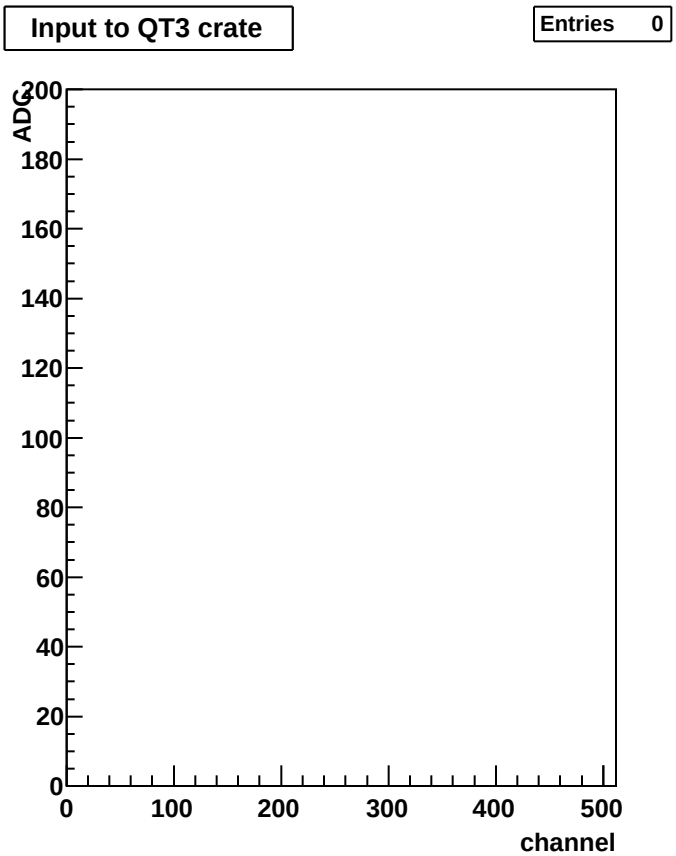

Entries 179225
BBC-S-East ADC Sum
BBC-S-West ADC Sum

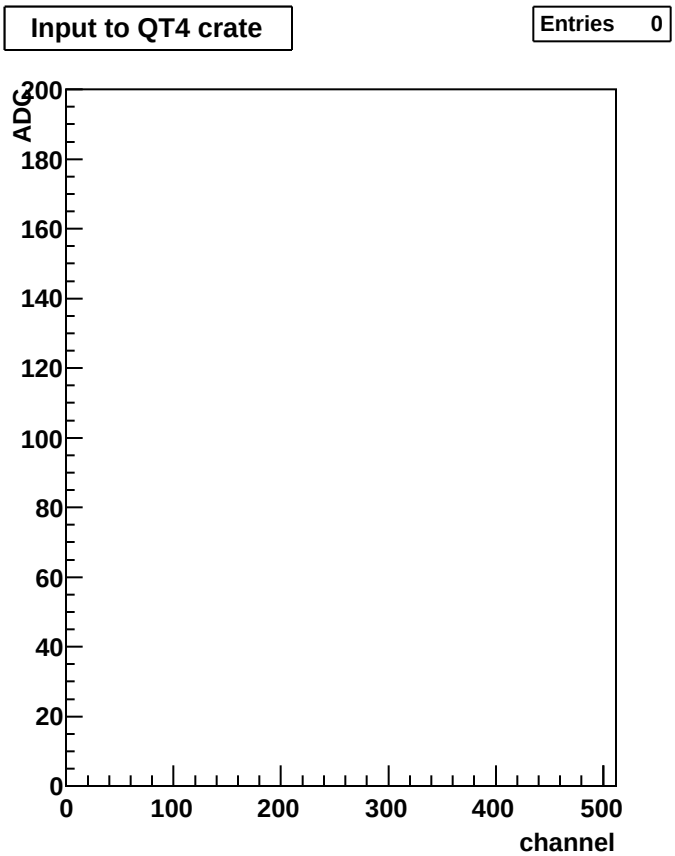

Entries 179225
ZDC East ADC Sum Att
ZDC West ADC Sum Att

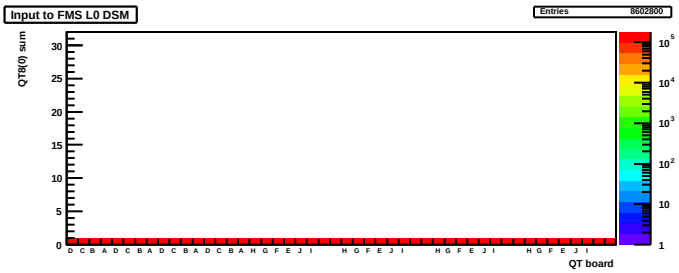

Entries 179225
BBC-L TAC Diff
BCC-S TAC Diff
Entries 179225
BBC-L TAC Diff
VPD TAC Diff
Entries 179225
BBC-L TAC Diff
ZDC TAC Diff
Entries 179225
BBC-S TAC Diff
VPD TAC Diff
Entries 179225
BBC-S TAC Diff
ZDC TAC Diff
Entries 179225
VPD TAC Diff
ZDC TAC Diff

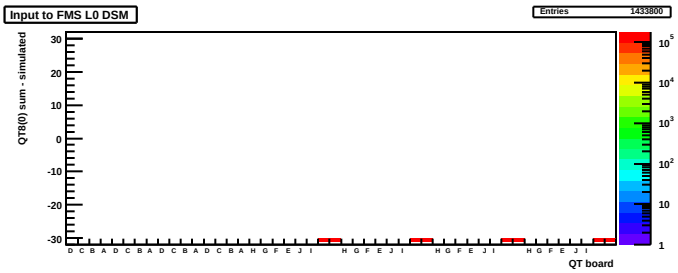

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

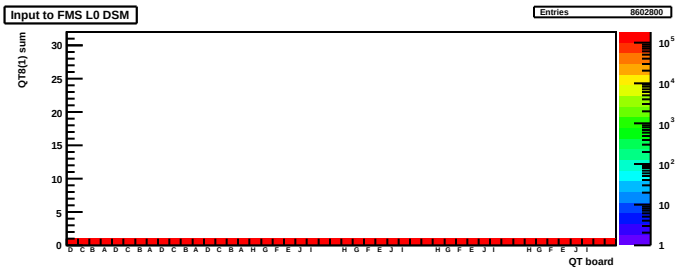

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

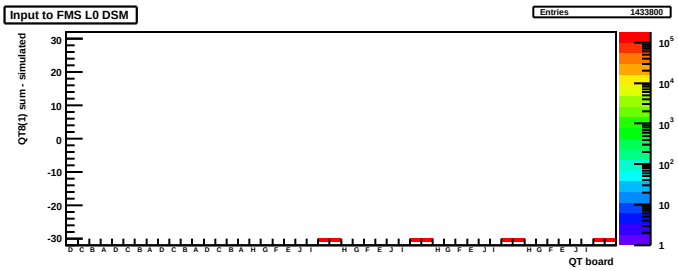

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

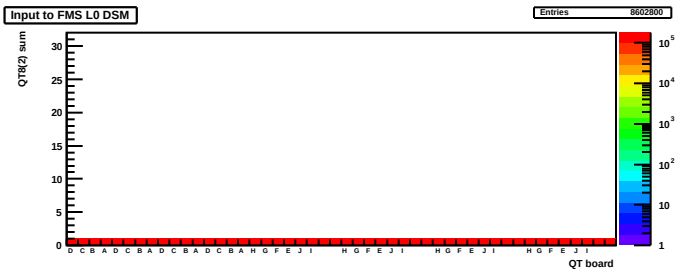

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

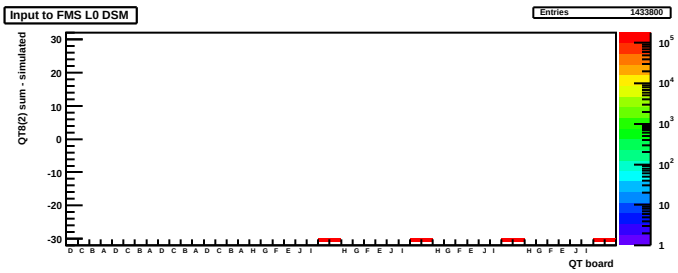

Input to FMS L0 DSM
Entries 8602800
QT8(0) sum
QT board

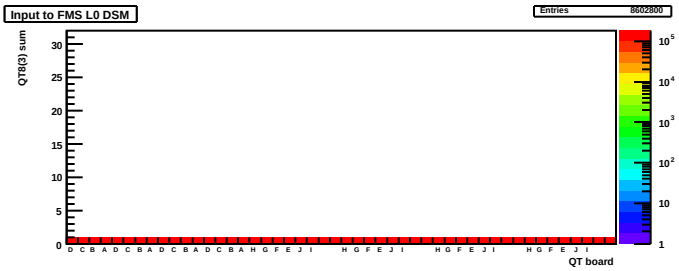

Input to FMS L0 DSM
Entries 1433800
QT8(0) sum - simulated
QT board

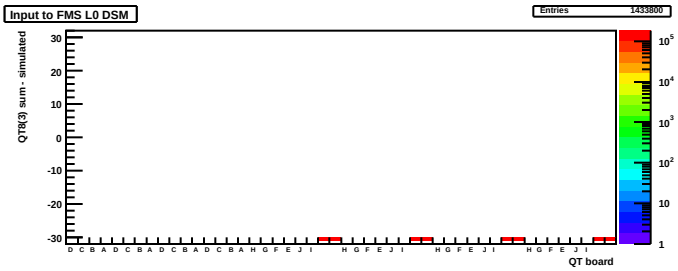

Input to FMS L0 DSM
Entries 8602800
QT8(1) sum
QT board

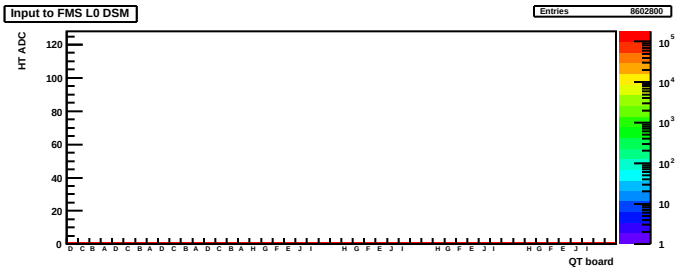

Input to FMS L0 DSM
Entries 1433800
QT8(1) sum - simulated
QT board

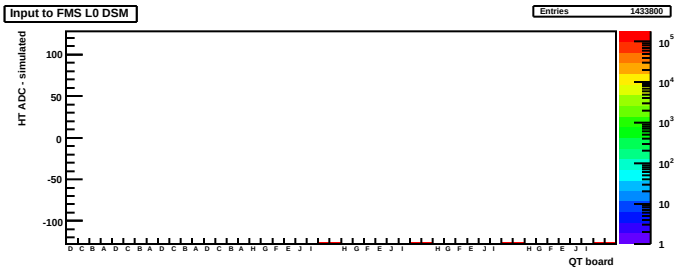

Input to FMS L0 DSM
Entries 8602800
QT8(2) sum
QT board

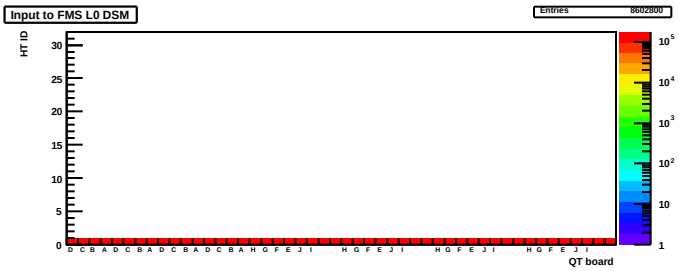

Input to FMS L0 DSM
Entries 1433800
QT8(2) sum - simulated
QT board

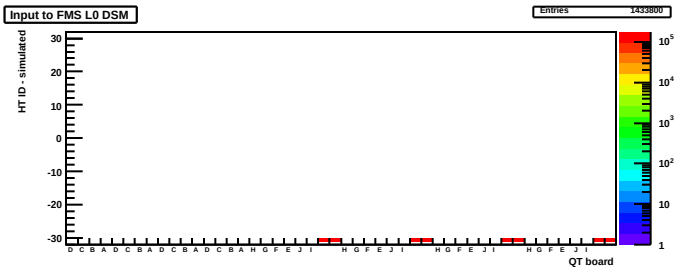

Input to FMS L0 DSM
Entries 8602800
QT8(3) sum
QT board

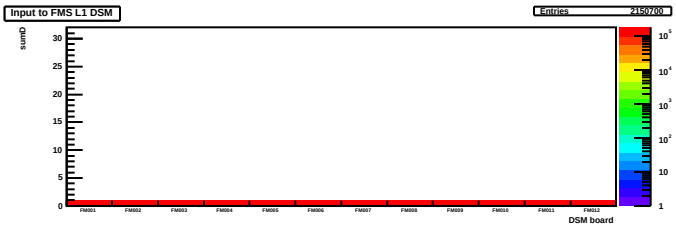

Input to FMS L0 DSM
Entries 1433800
QT8(3) sum - simulated
QT board

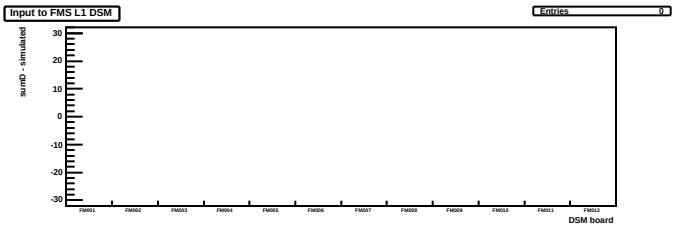

Input to FMS L0 DSM
Entries 8602800
HT ADC
QT board

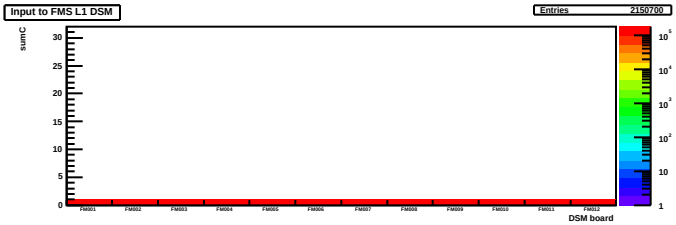

Input to FMS L0 DSM
Entries 1433800
HT ADC - simulated
QT board

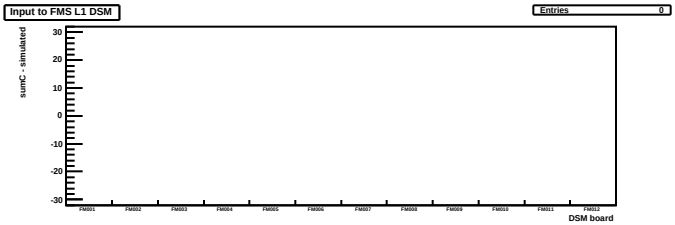

Input to FMS L0 DSM
Entries 8602800
HT ID
QT board

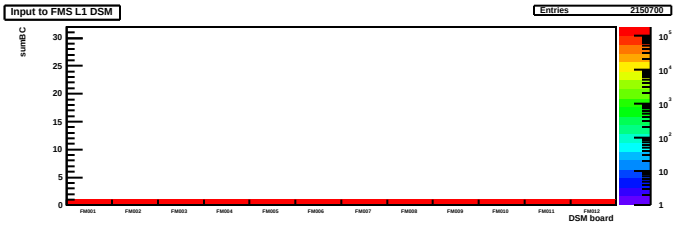

Input to FMS L0 DSM
Entries 1433800
HT ID - simulated
QT board

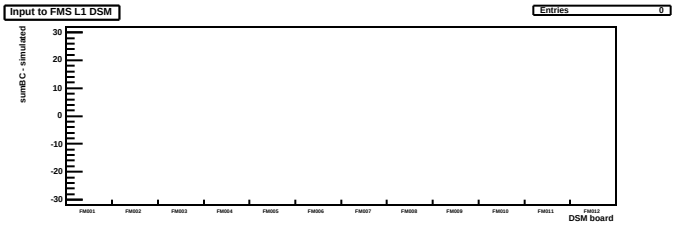

Input to FMS L1 DSM
Entries 2150700
sumD
DSM board

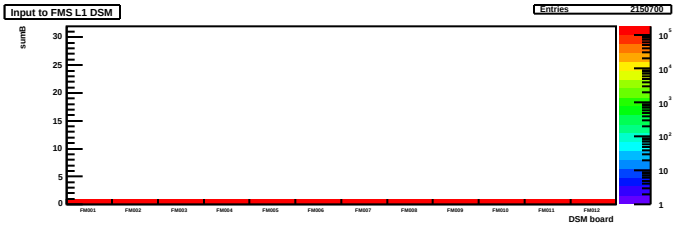

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

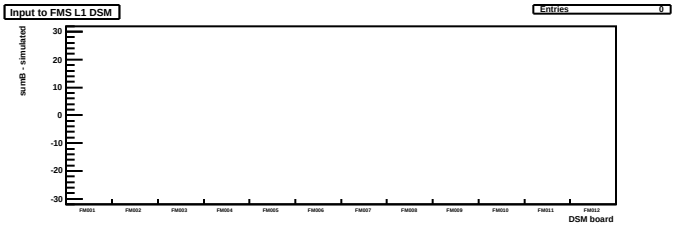

Input to FMS L1 DSM
Entries 2150700
sumC
DSM board

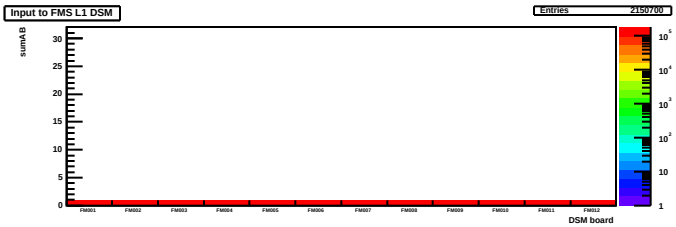

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

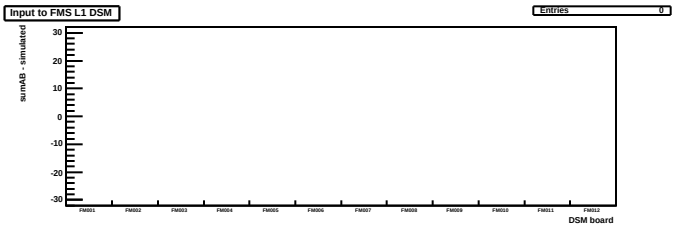

Input to FMS L1 DSM
Entries 2150700
sumBC
DSM board

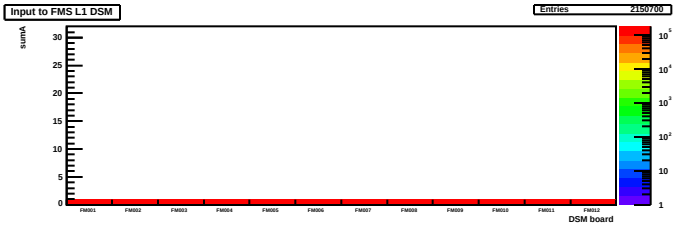

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

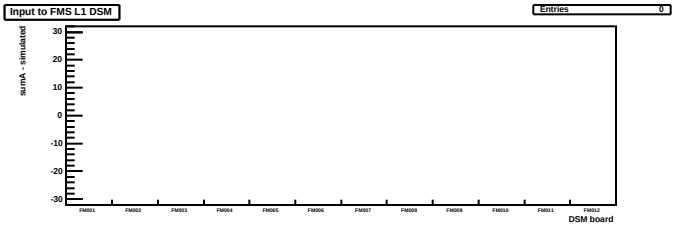

Input to FMS L1 DSM
Entries 2150700
sumB
DSM board

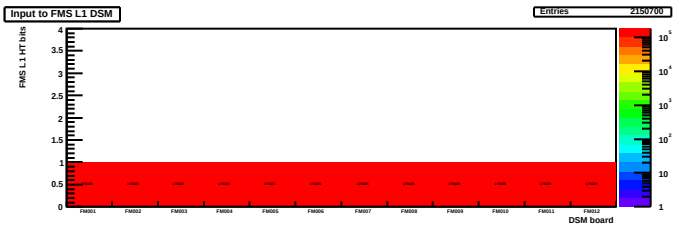

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

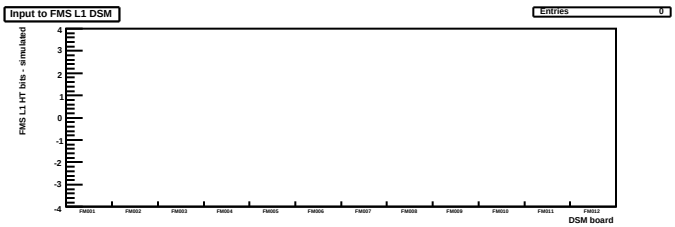

Input to FMS L1 DSM
Entries 2150700
sumAB
DSM board

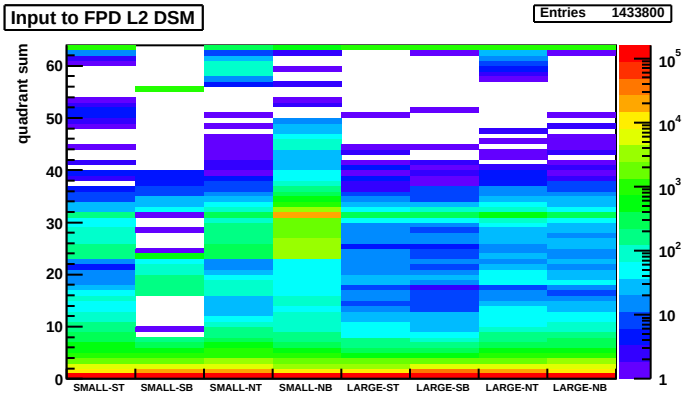

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

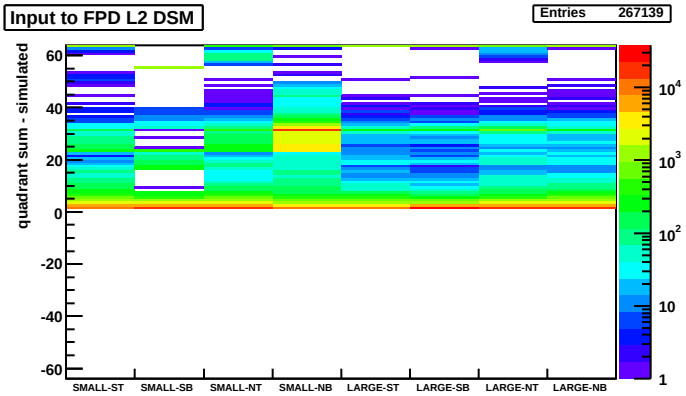

Input to FMS L1 DSM
Entries 2150700
sumA
DSM board

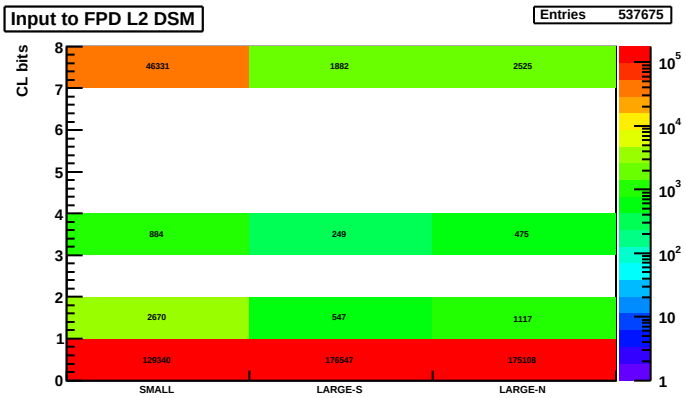

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

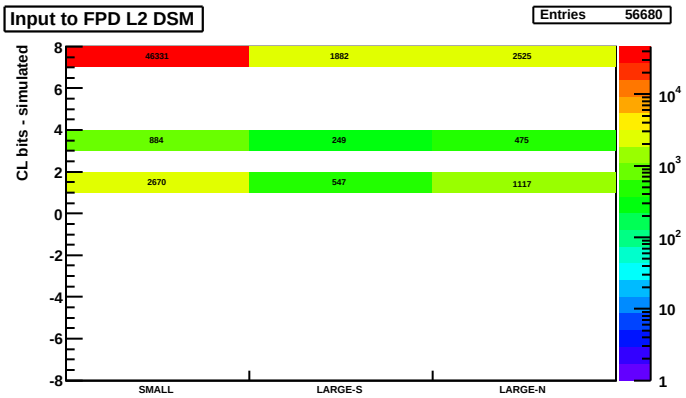

Input to FMS L1 DSM
Entries 2150700
FMS L1 HT bits
DSM board

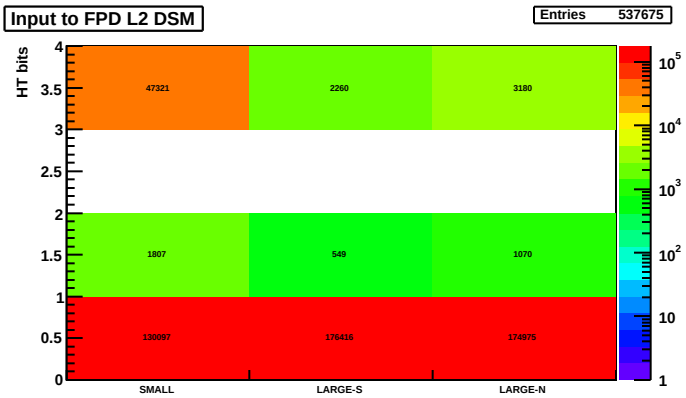

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

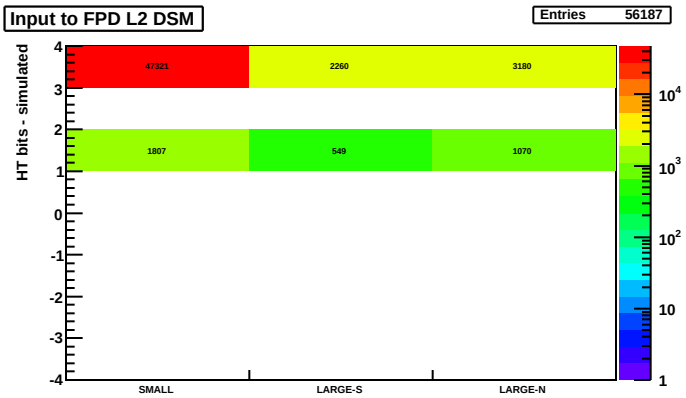

Input to FPD L2 DSM
Entries 1433800
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

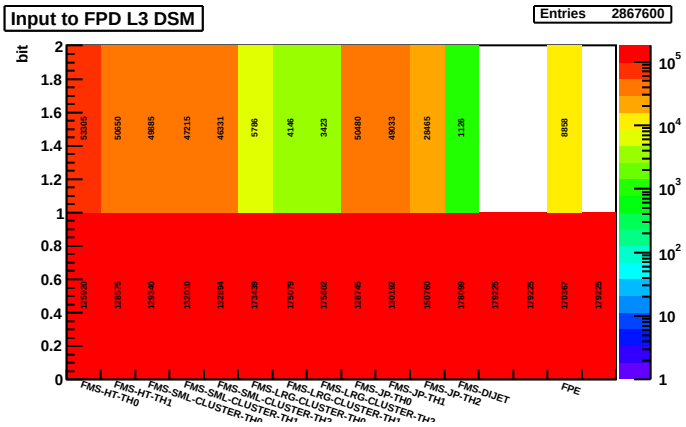

Input to FPD L2 DSM
Entries 267139
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

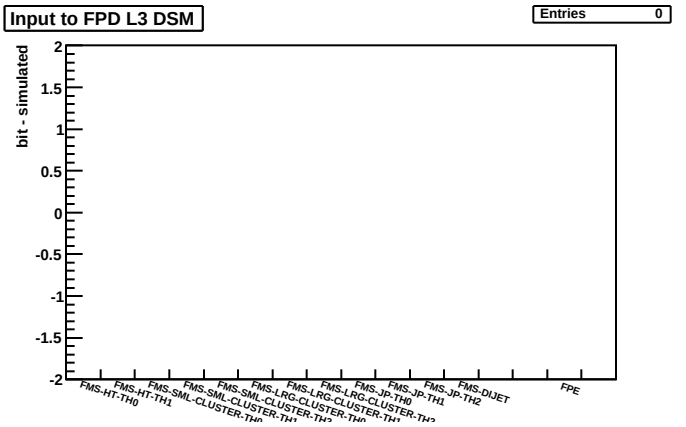

Input to FPD L2 DSM
Entries 537675
CL bits
46331
1882
2525
884
249
475
2670
547
1117
129340
176547
175108
SMALL
LARGE-S
LARGE-N

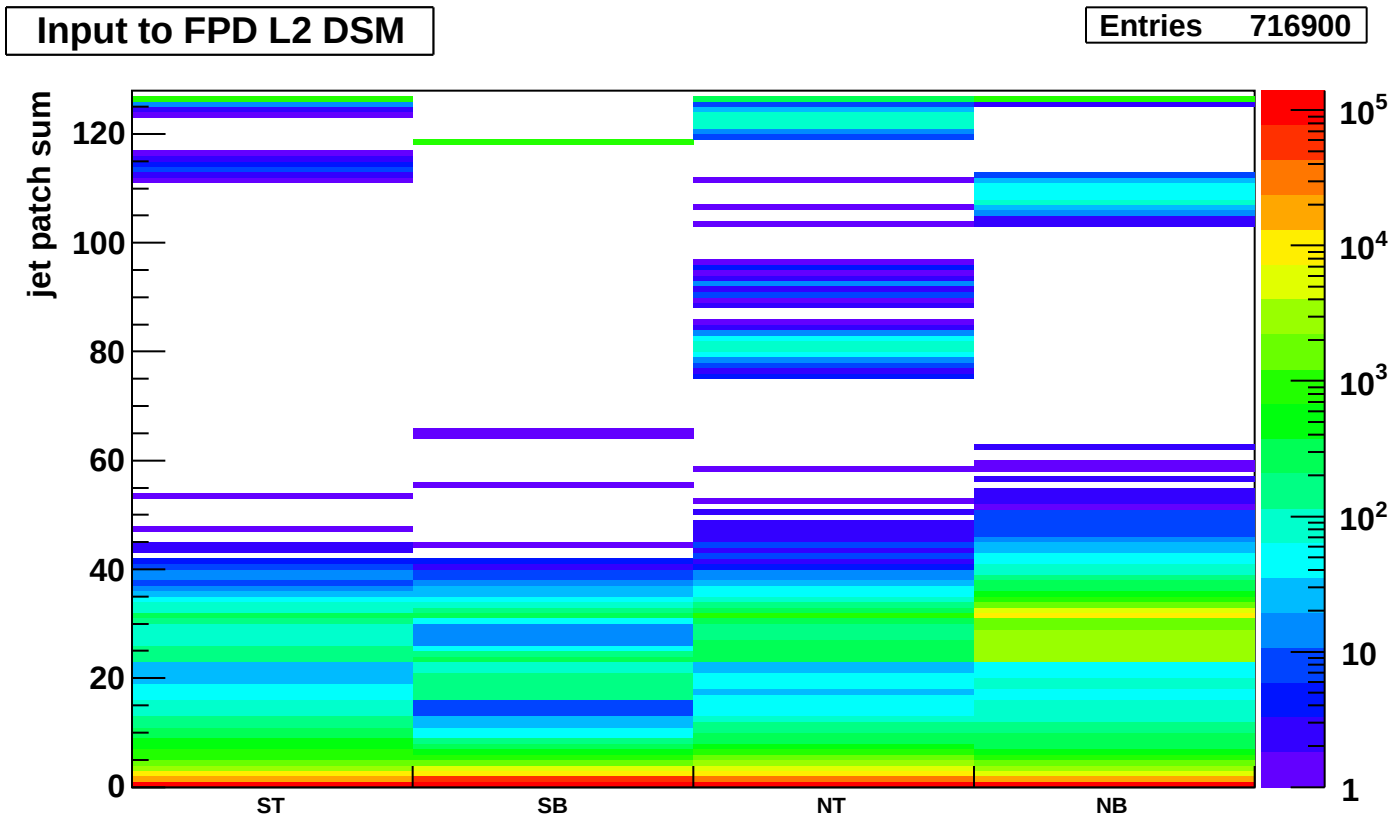

Input to FPD L2 DSM
Entries 56680
CL bits - simulated
46331
1882
2525
884
249
475
2670
547
1117
SMALL
LARGE-S
LARGE-N

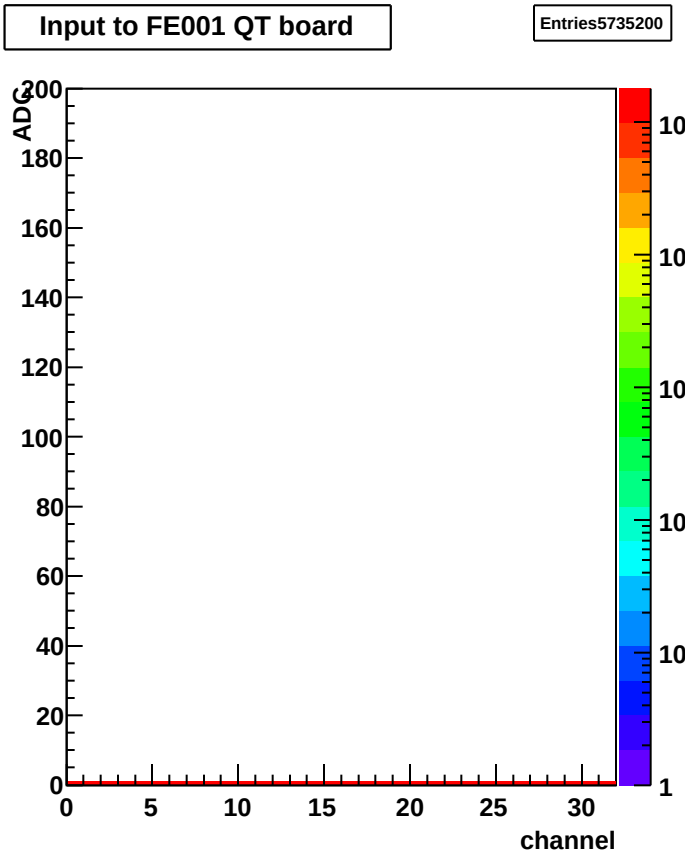

Input to FPD L2 DSM
Entries 537675
HT bits
47321
2260
3180
1807
549
1070
130097
176416
174975
SMALL
LARGE-S
LARGE-N

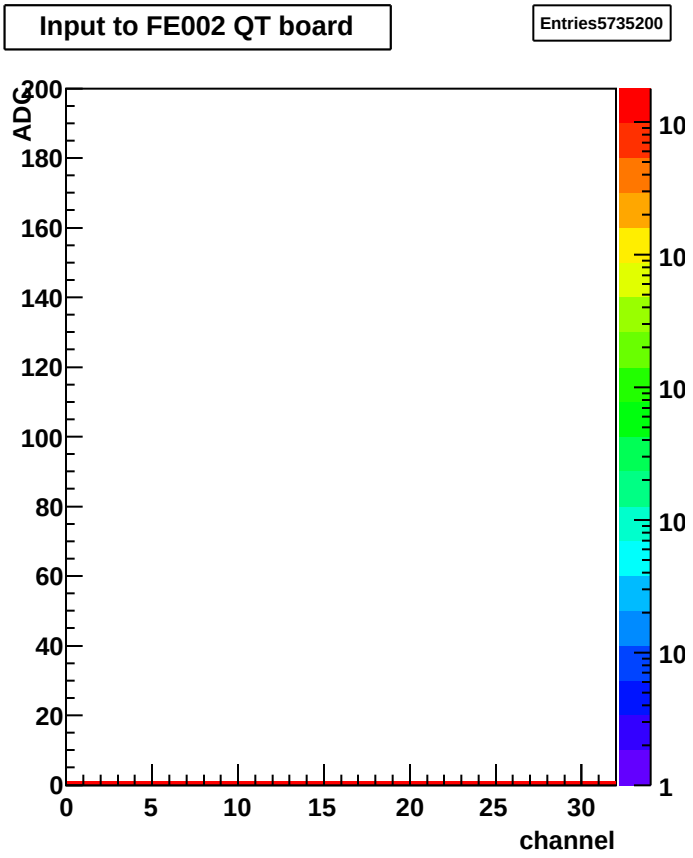

Input to FPD L2 DSM
Entries 56187
HT bits - simulated
47321
2260
3180
1807
549
1070
SMALL
LARGE-S
LARGE-N

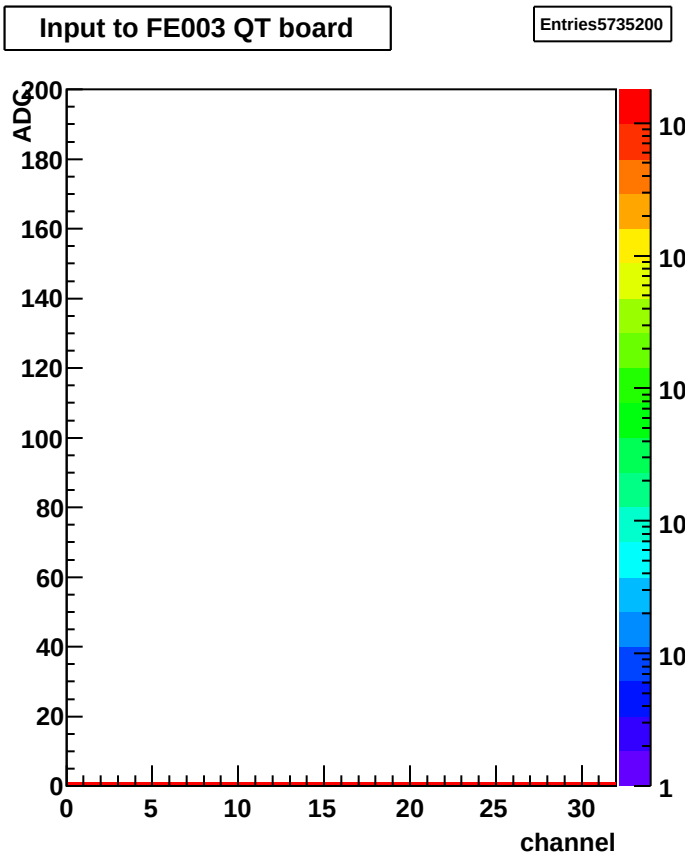

Input to FPD L3 DSM
Entries 2867600
bit
53305
50650
49895
47215
46331
5786
4146
3423
50480
49033
29465
1126
8858
125825
128575
129340
132010
132894
173439
175079
175802
128745
130182
150760
178099
179225
179225
170367
179225
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

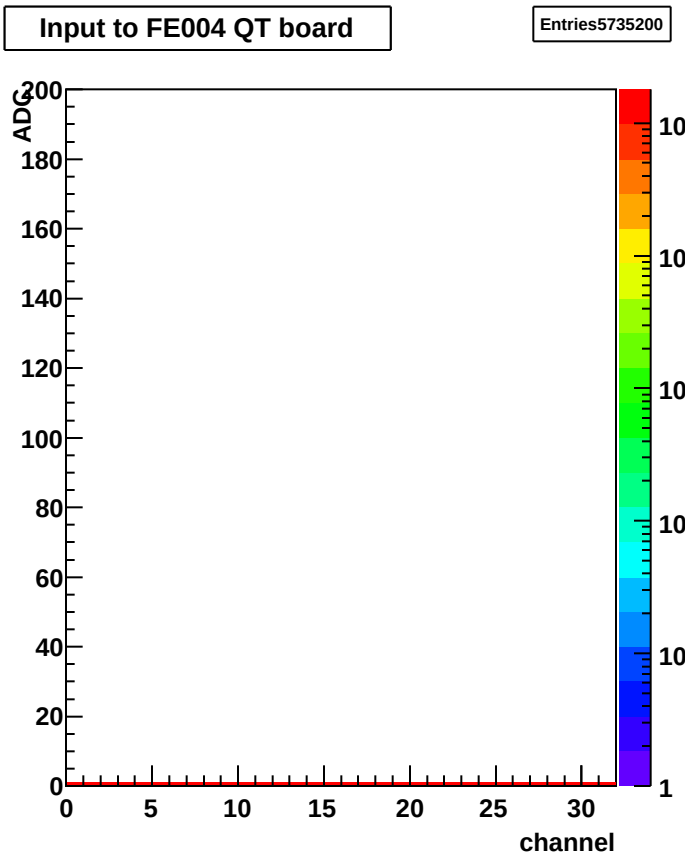

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

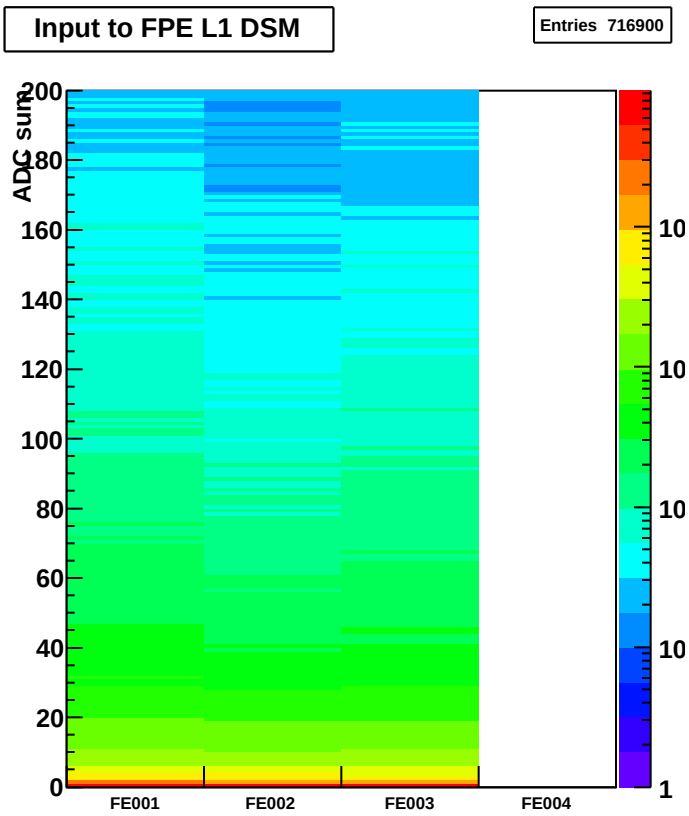

Input to FPD L2 DSM
Entries 716900
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

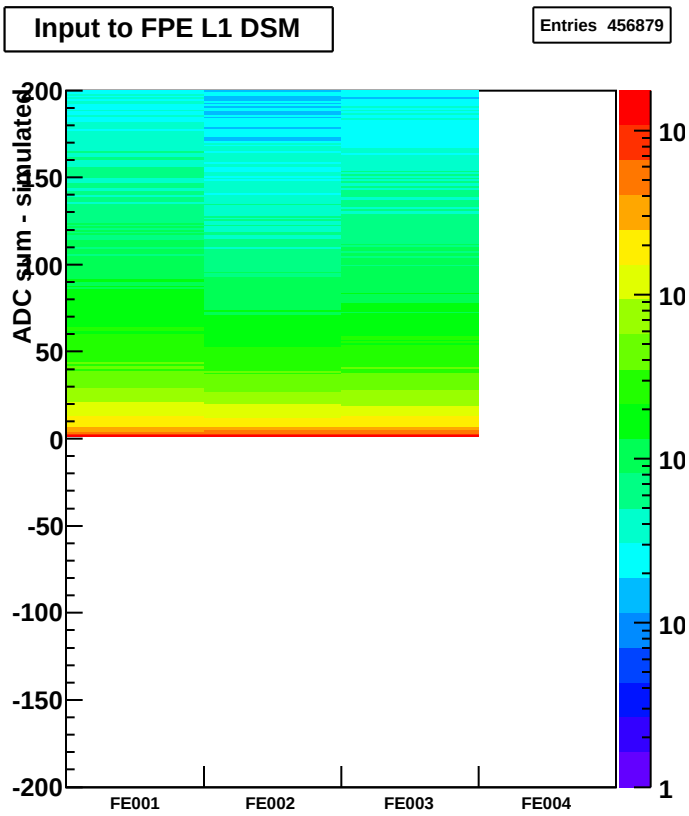

Input to FE001 QT board
Entries5735200
ADC
channel

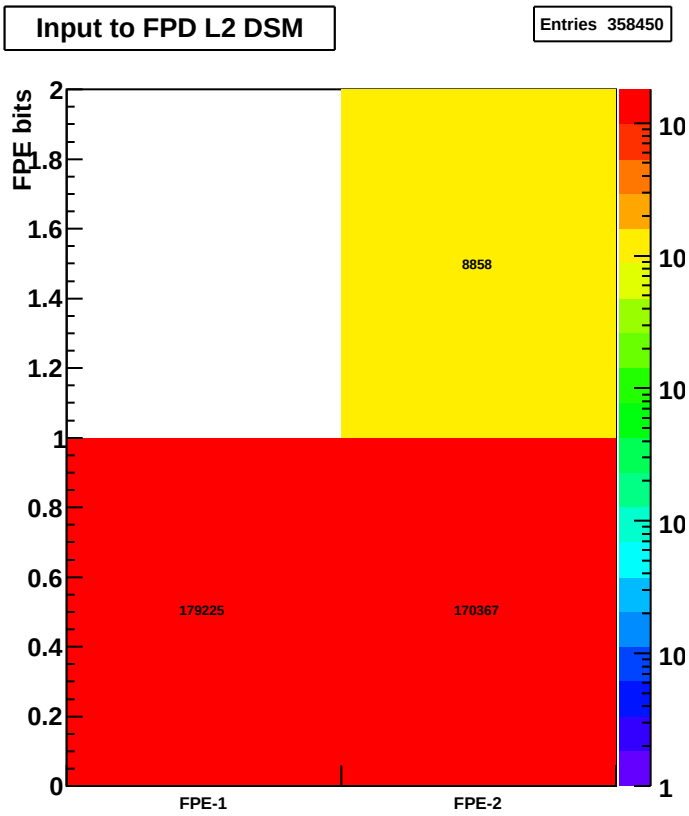

Input to FE002 QT board
Entries5735200
ADC
channel

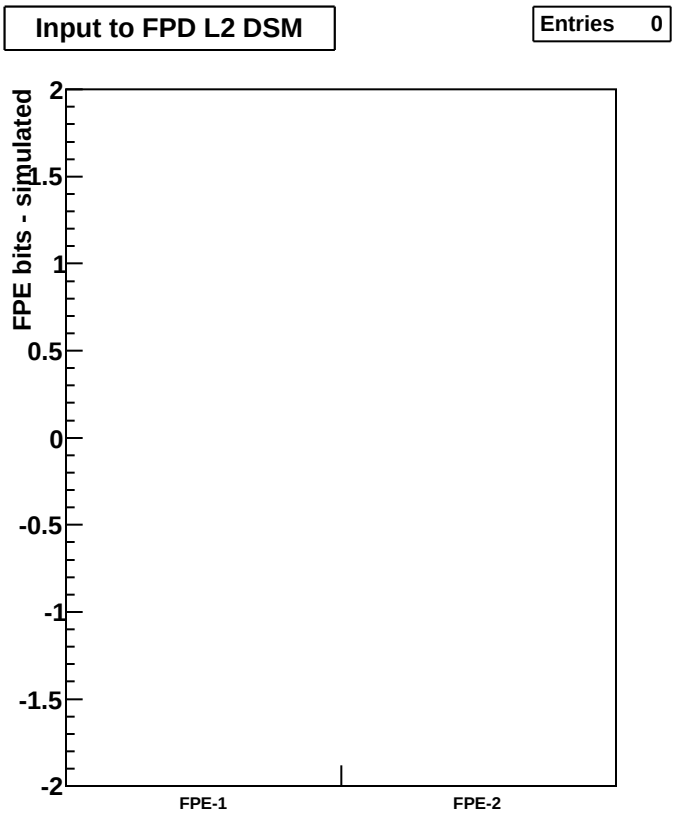

Input to FE003 QT board
Entries5735200
ADC
channel


Input to FE004 QT board
Entries5735200
ADC
channel


Input to FPE L1 DSM
Entries 716900
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 456879
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 358450
FPE bits
8858
179225
170367
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2