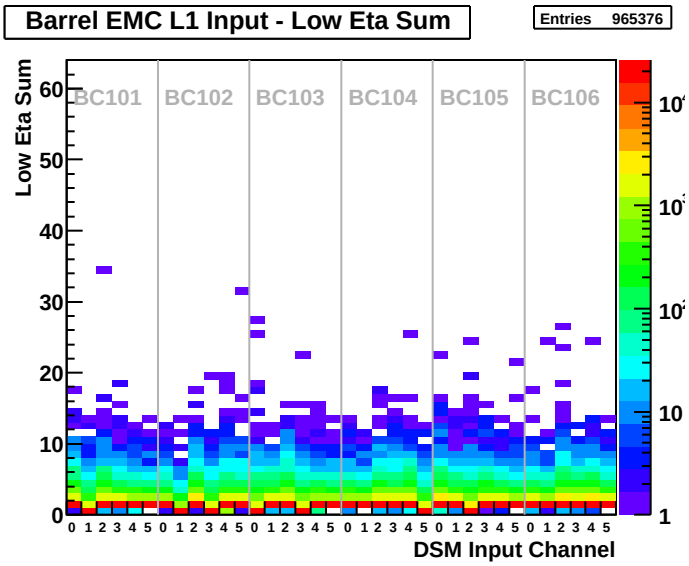
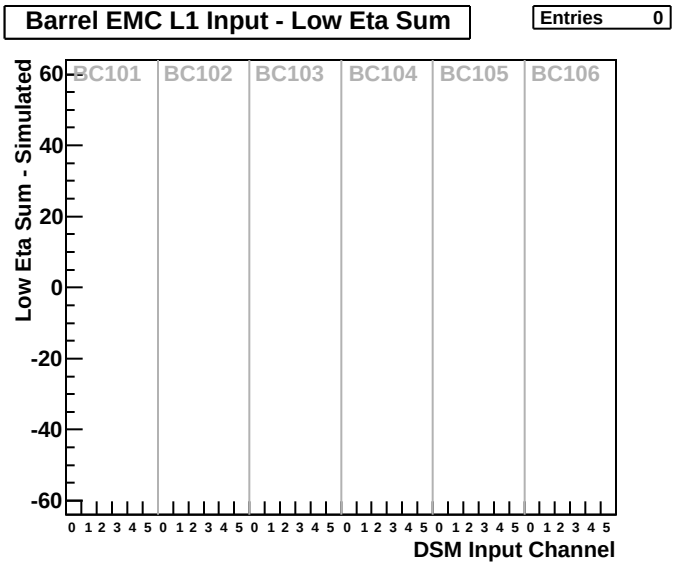
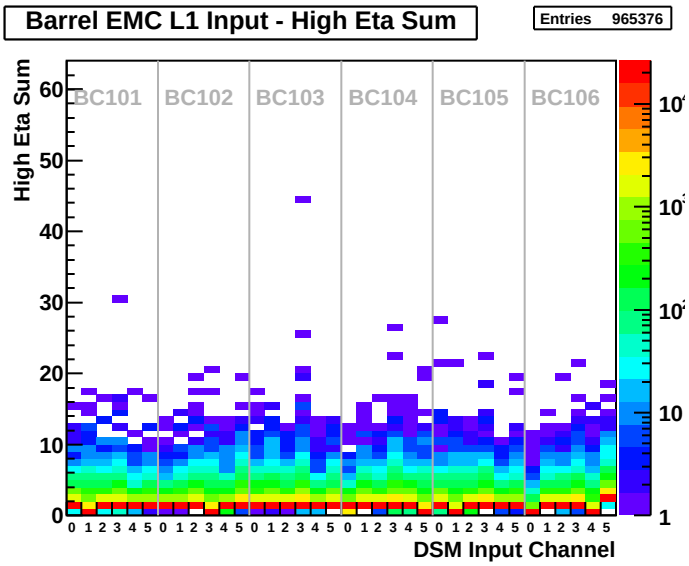
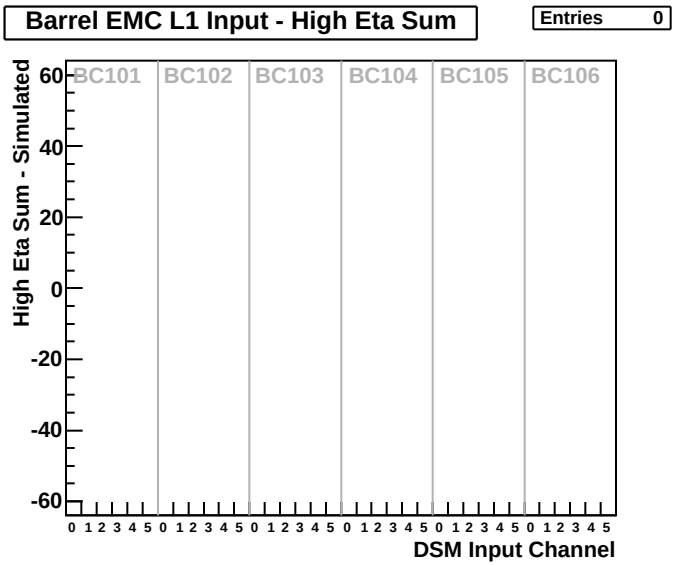
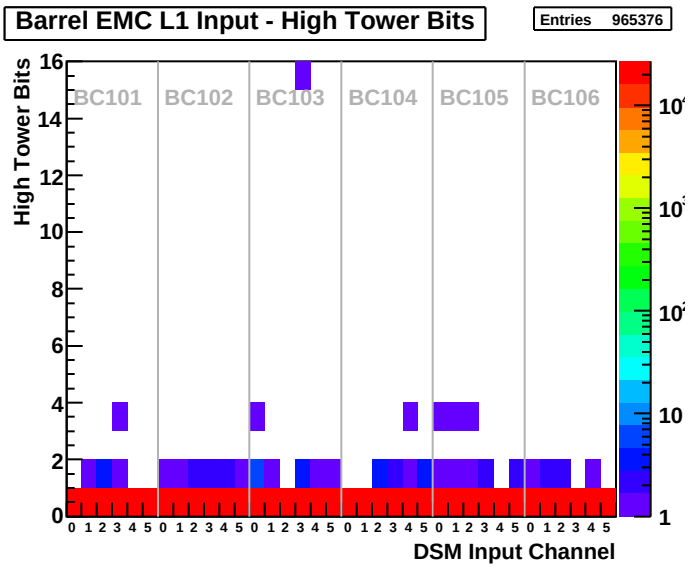
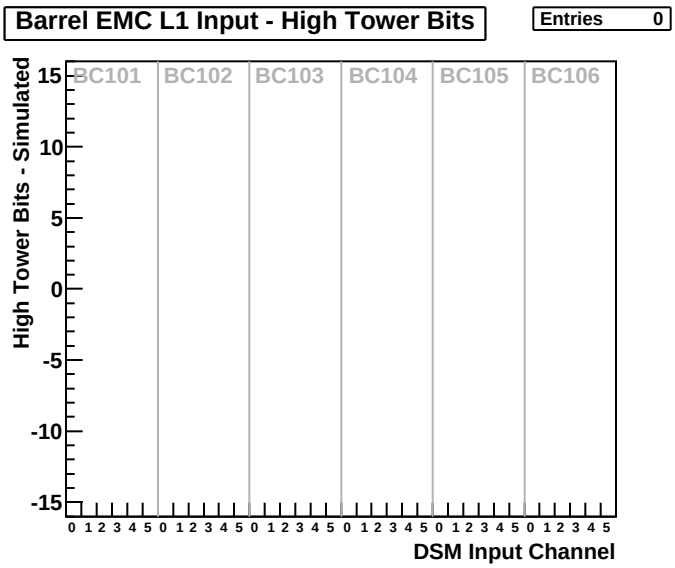

Barrel EMC L1 Input - Low Eta Sum
Entries 965376
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

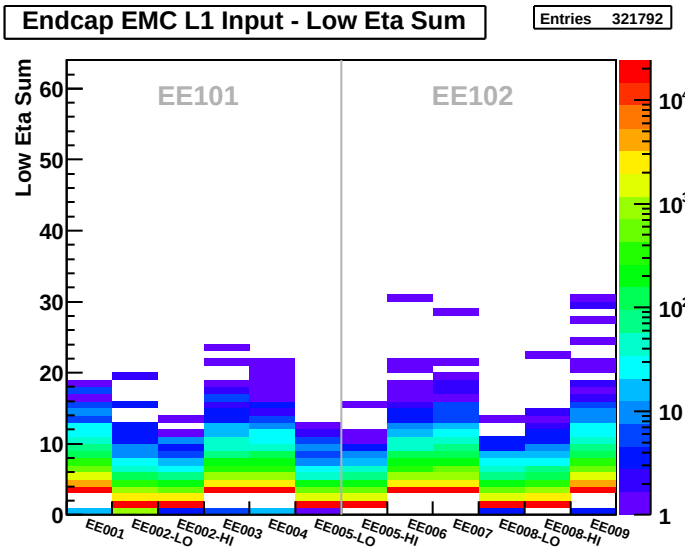

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

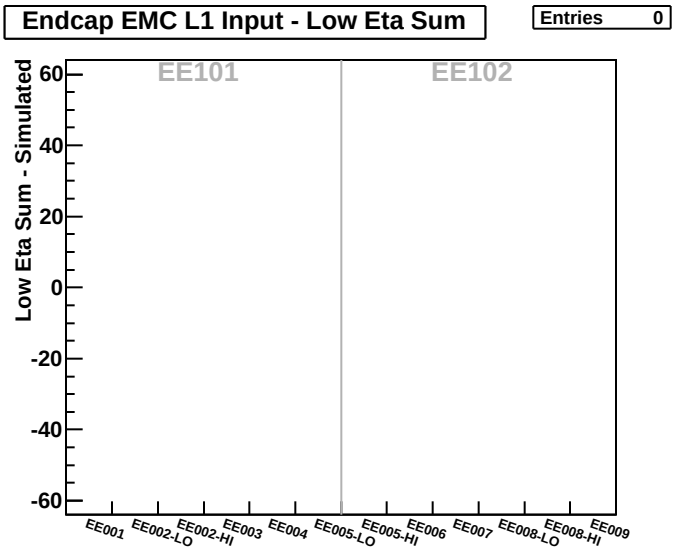

Barrel EMC L1 Input - High Eta Sum
Entries 965376
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

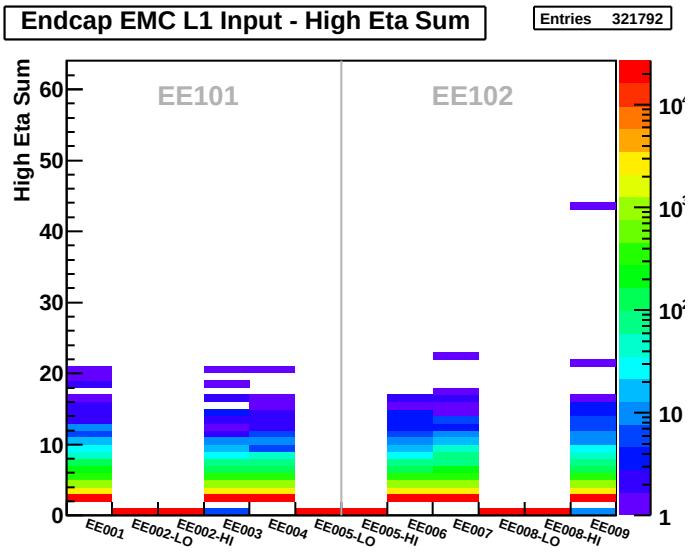

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

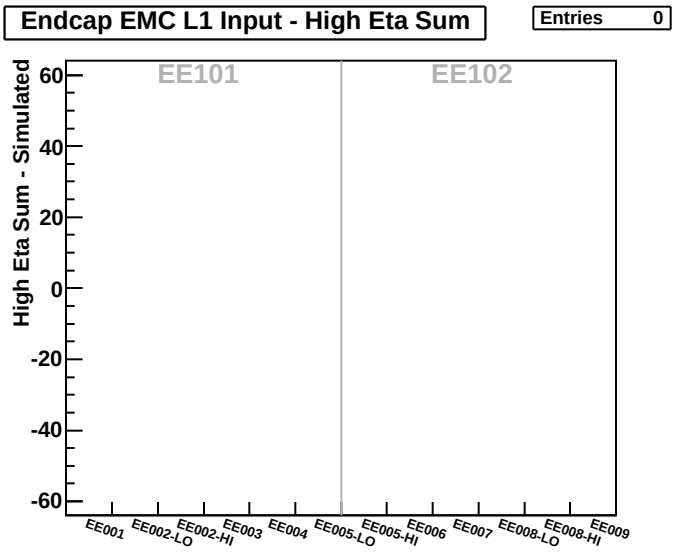

Barrel EMC L1 Input - High Tower Bits
Entries 965376
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

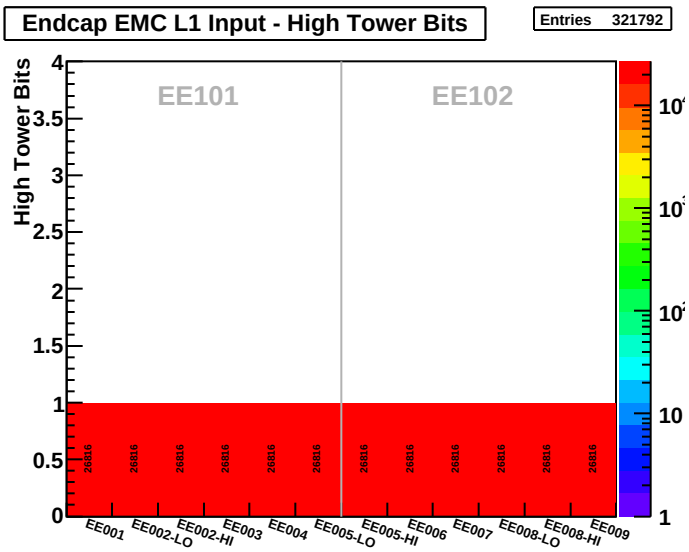

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

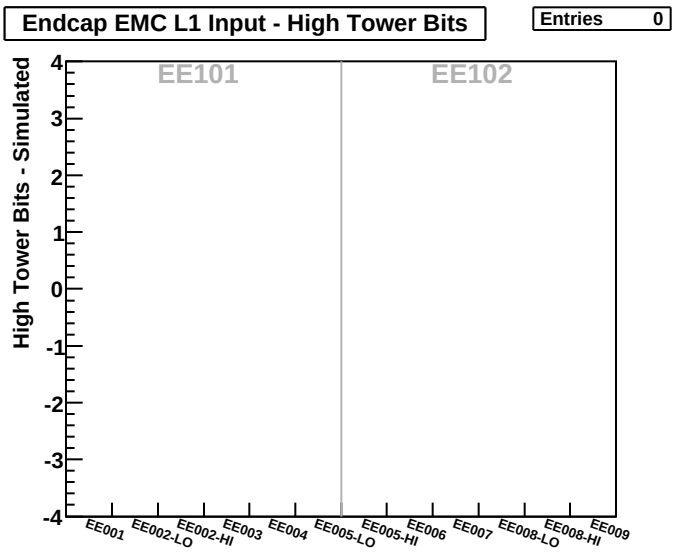

Endcap EMC L1 Input - Low Eta Sum
Entries 321792
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

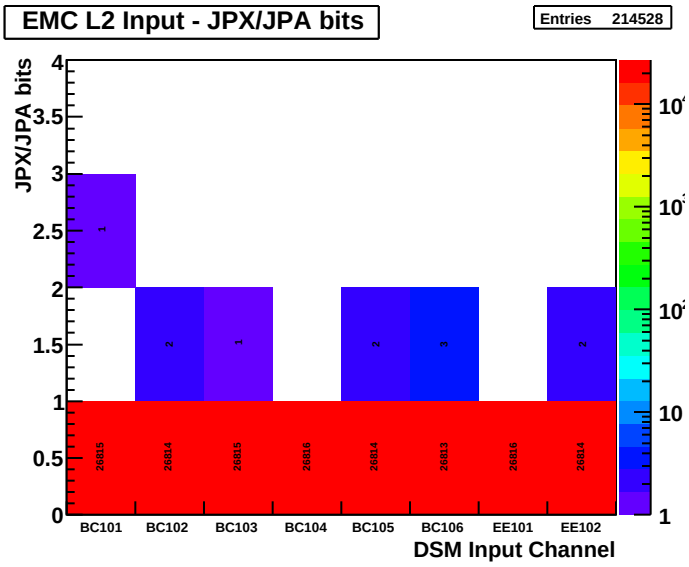

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

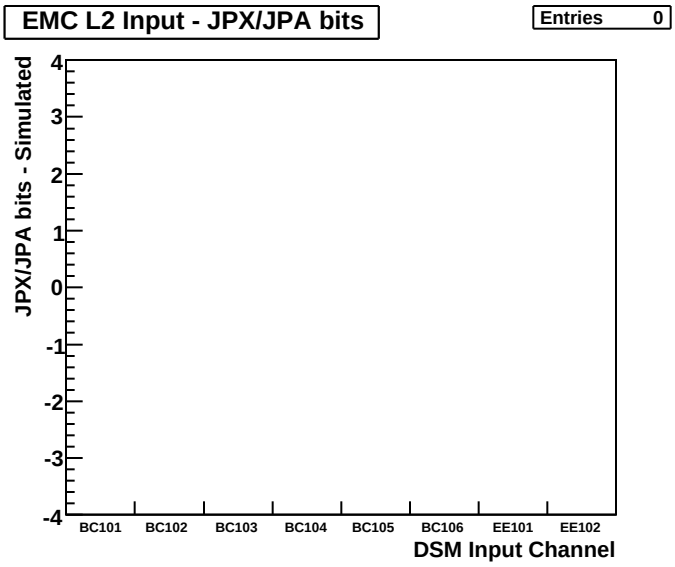

Endcap EMC L1 Input - High Eta Sum
Entries 321792
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

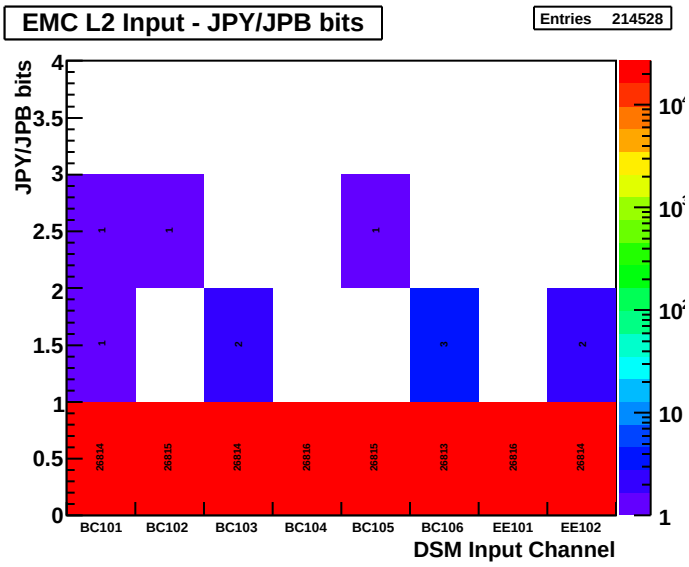

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

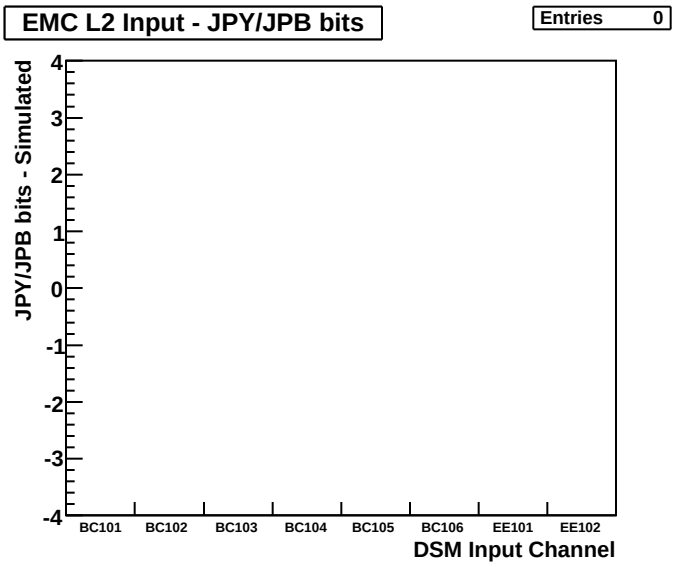

Endcap EMC L1 Input - High Tower Bits
Entries 321792
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

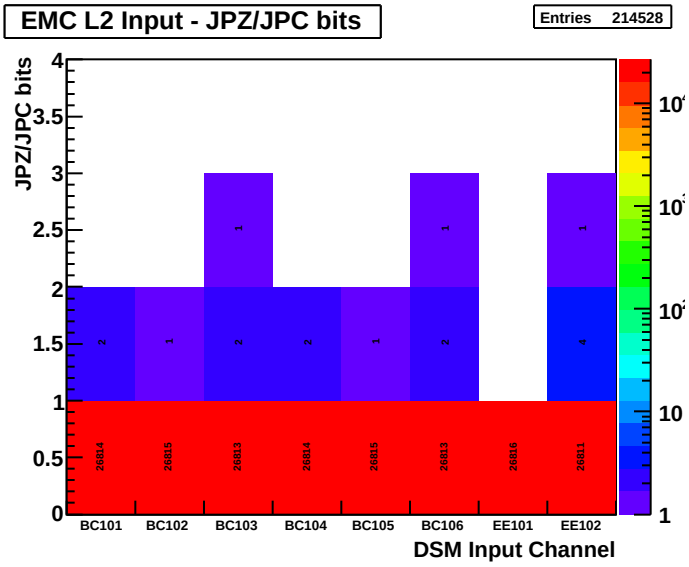

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

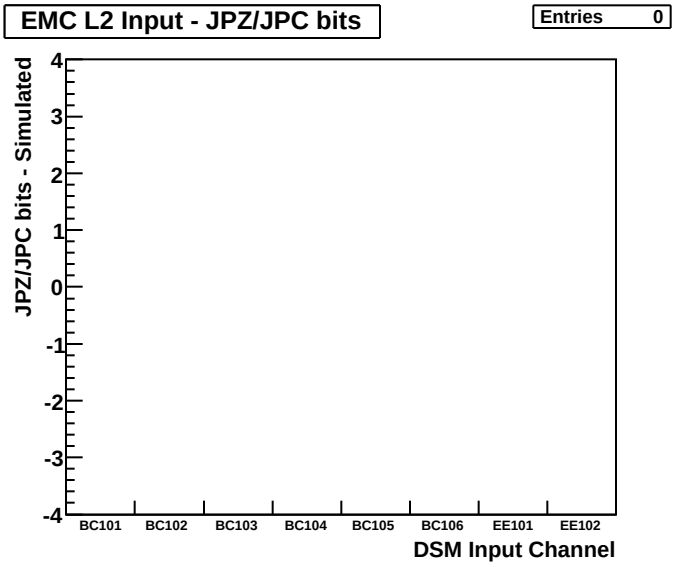

EMC L2 Input - JPX/JPA bits
Entries 214528
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

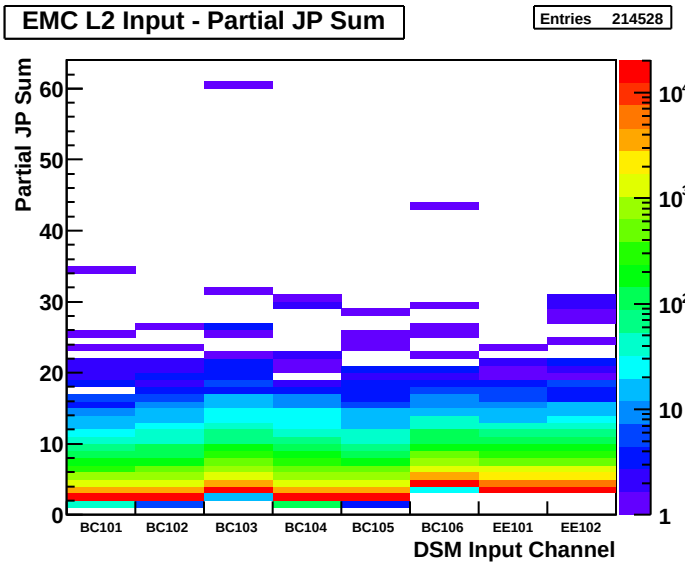

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

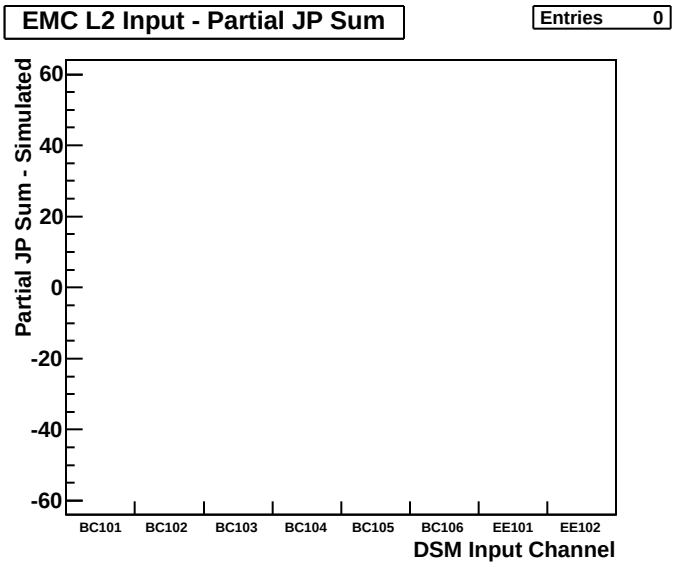

EMC L2 Input - JPY/JPB bits
Entries 214528
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

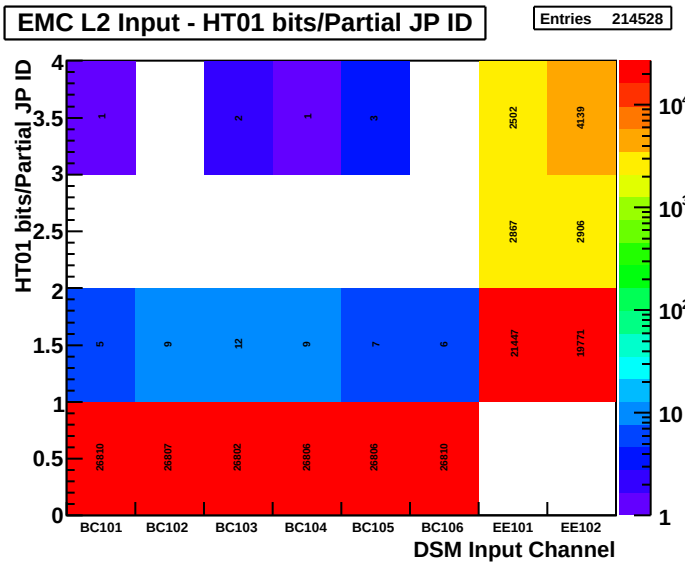

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

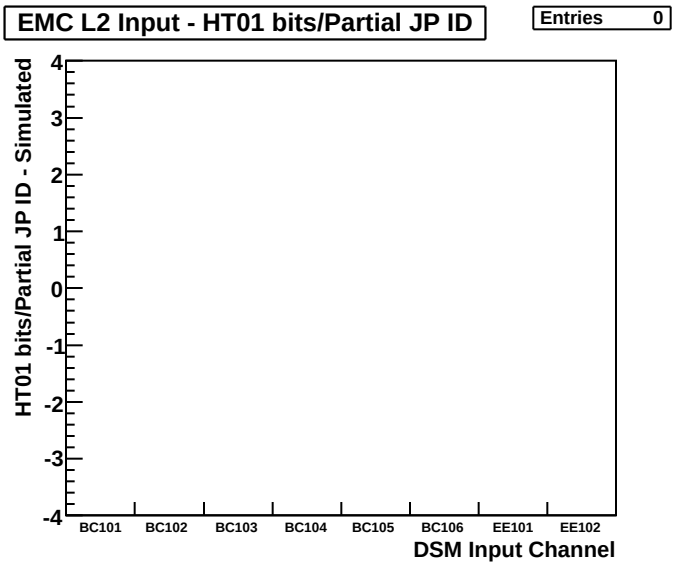

EMC L2 Input - JPZ/JPC bits
Entries 214528
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

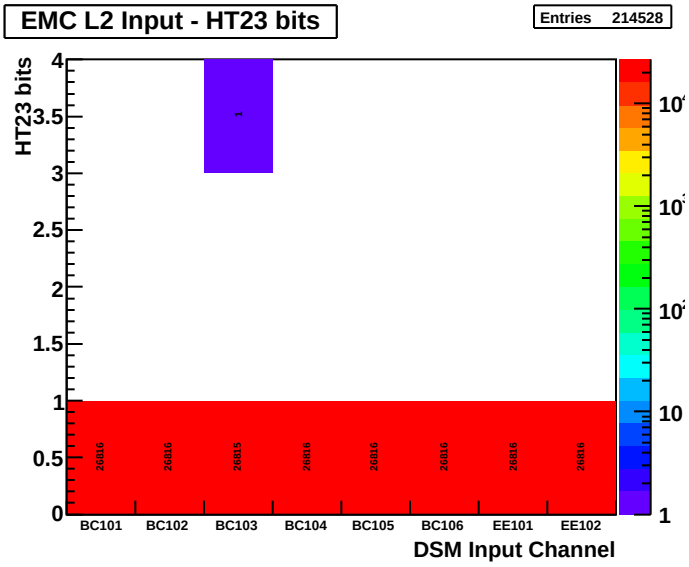

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

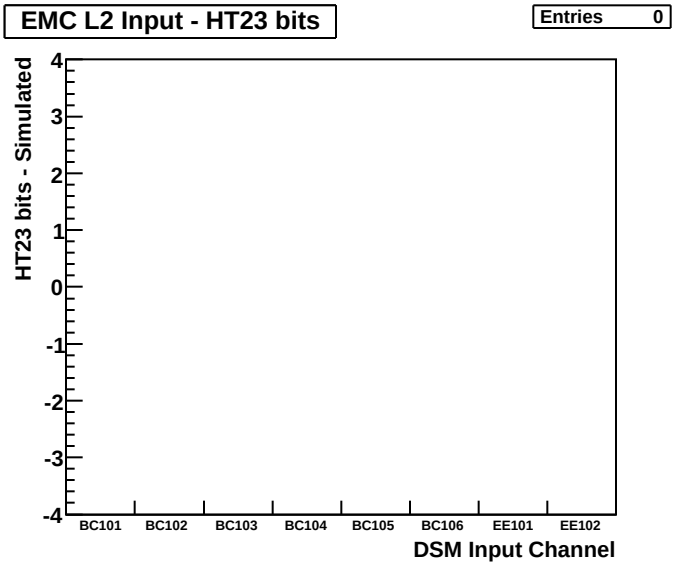

EMC L2 Input - Partial JP Sum
Entries 214528
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

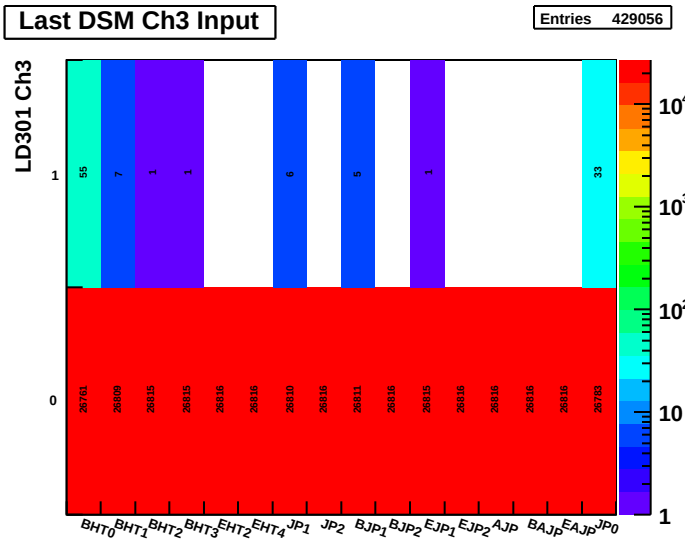

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

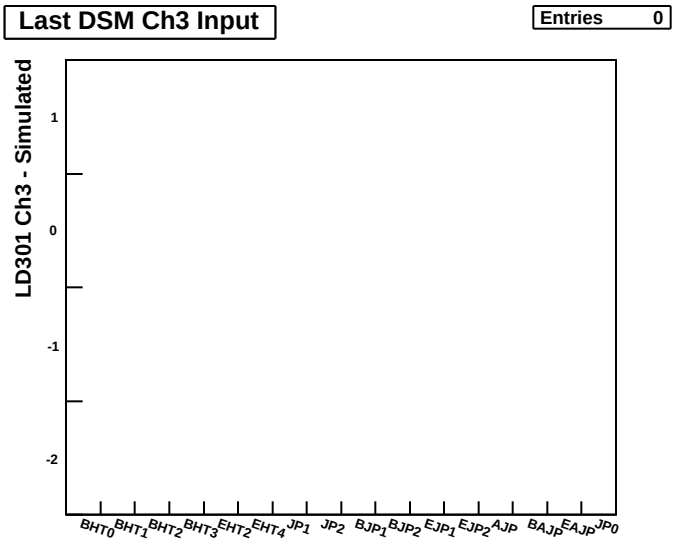

EMC L2 Input - HT01 bits/Partial JP ID
Entries 214528
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

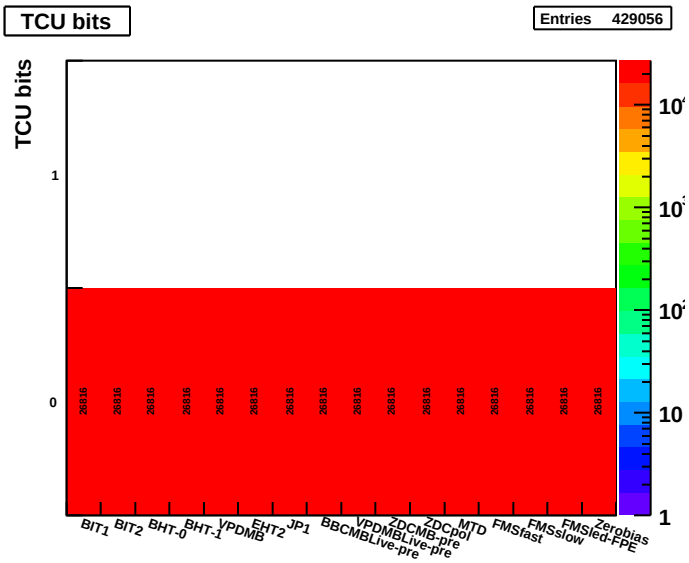

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

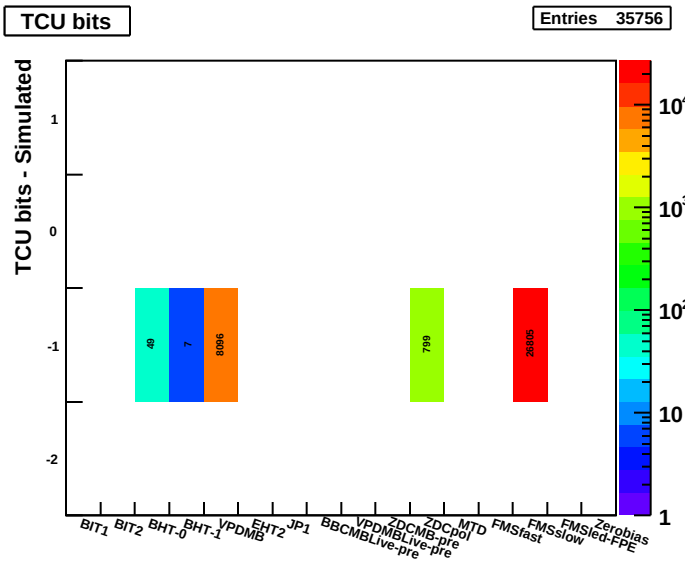

EMC L2 Input - HT23 bits
Entries 214528
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

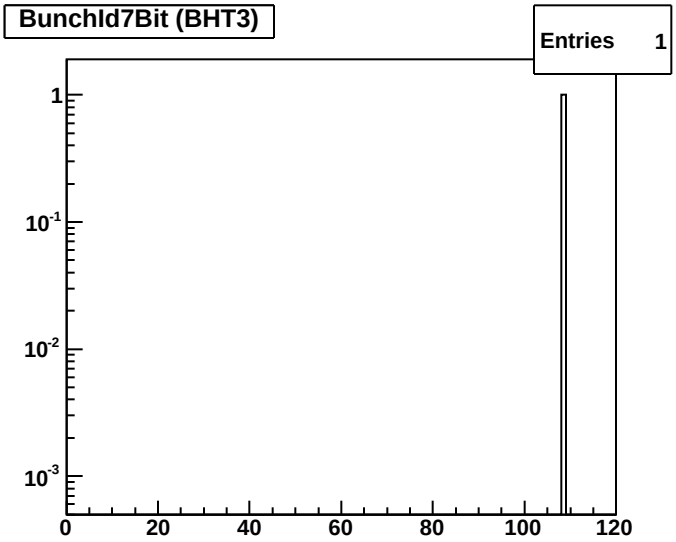

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

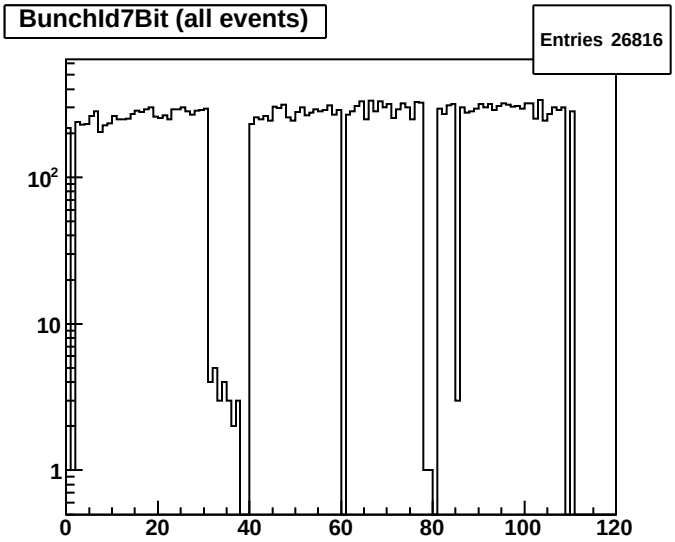

Last DSM Ch3 Input
Entries 429056
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

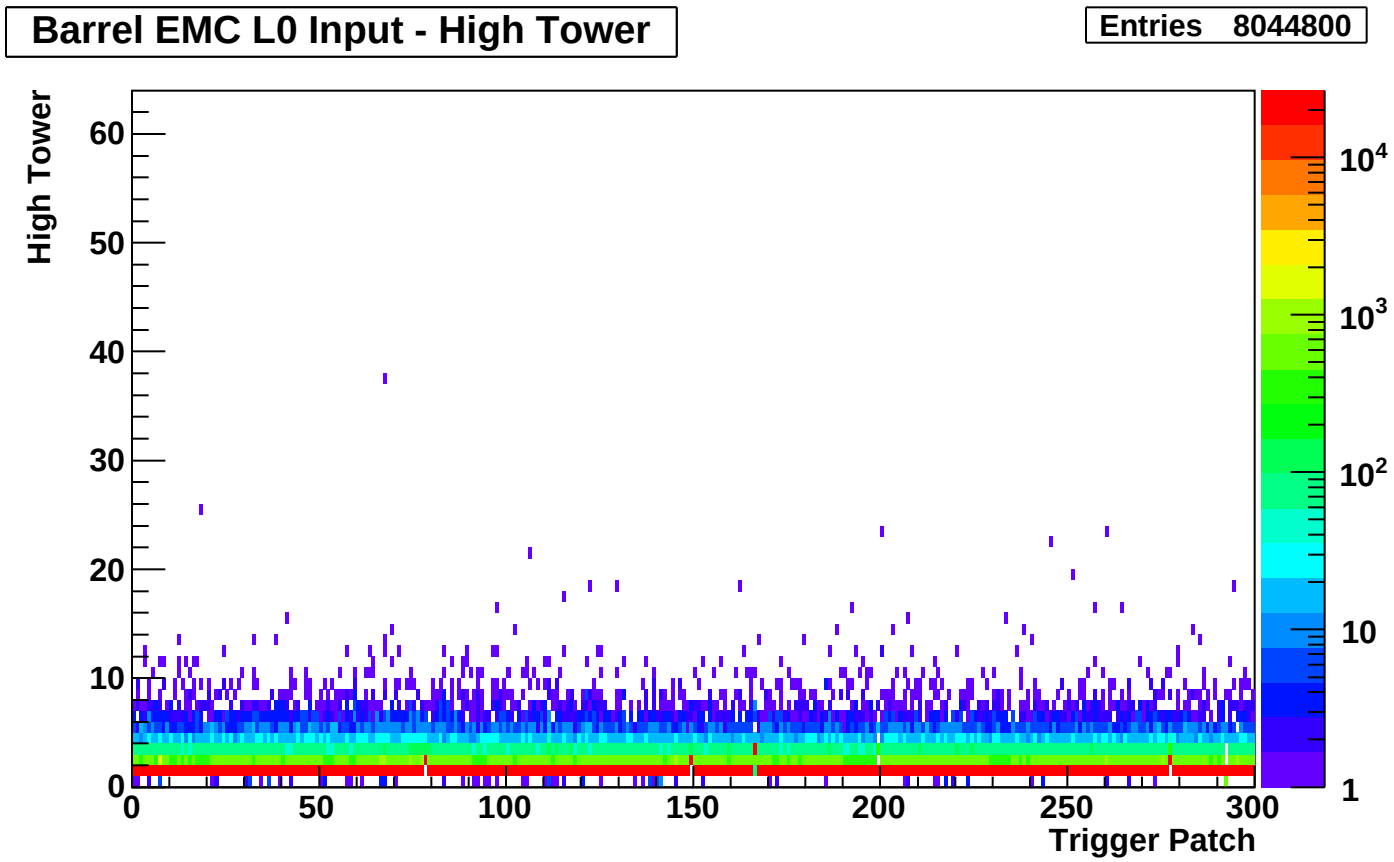

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

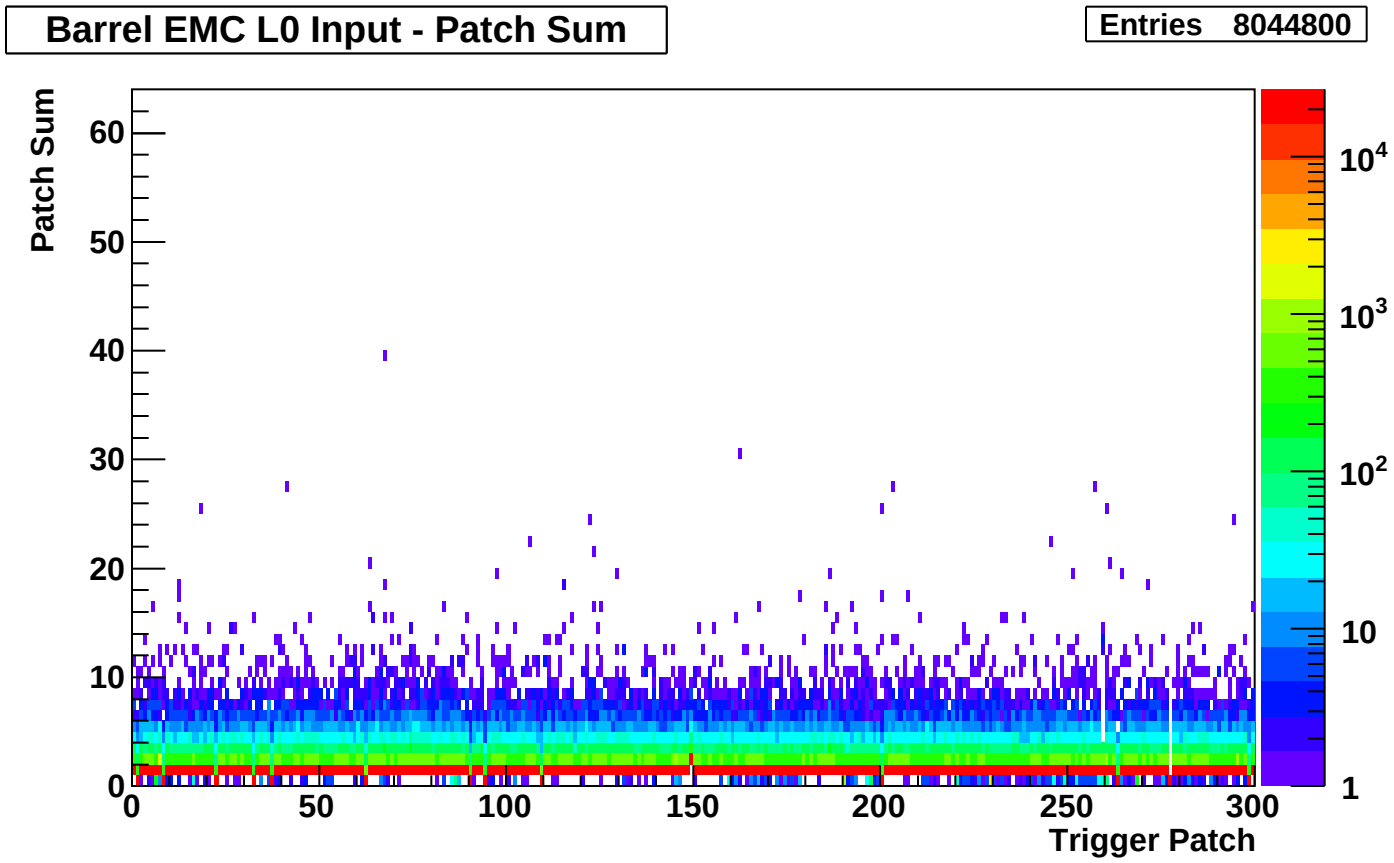

TCU bits
Entries 429056
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

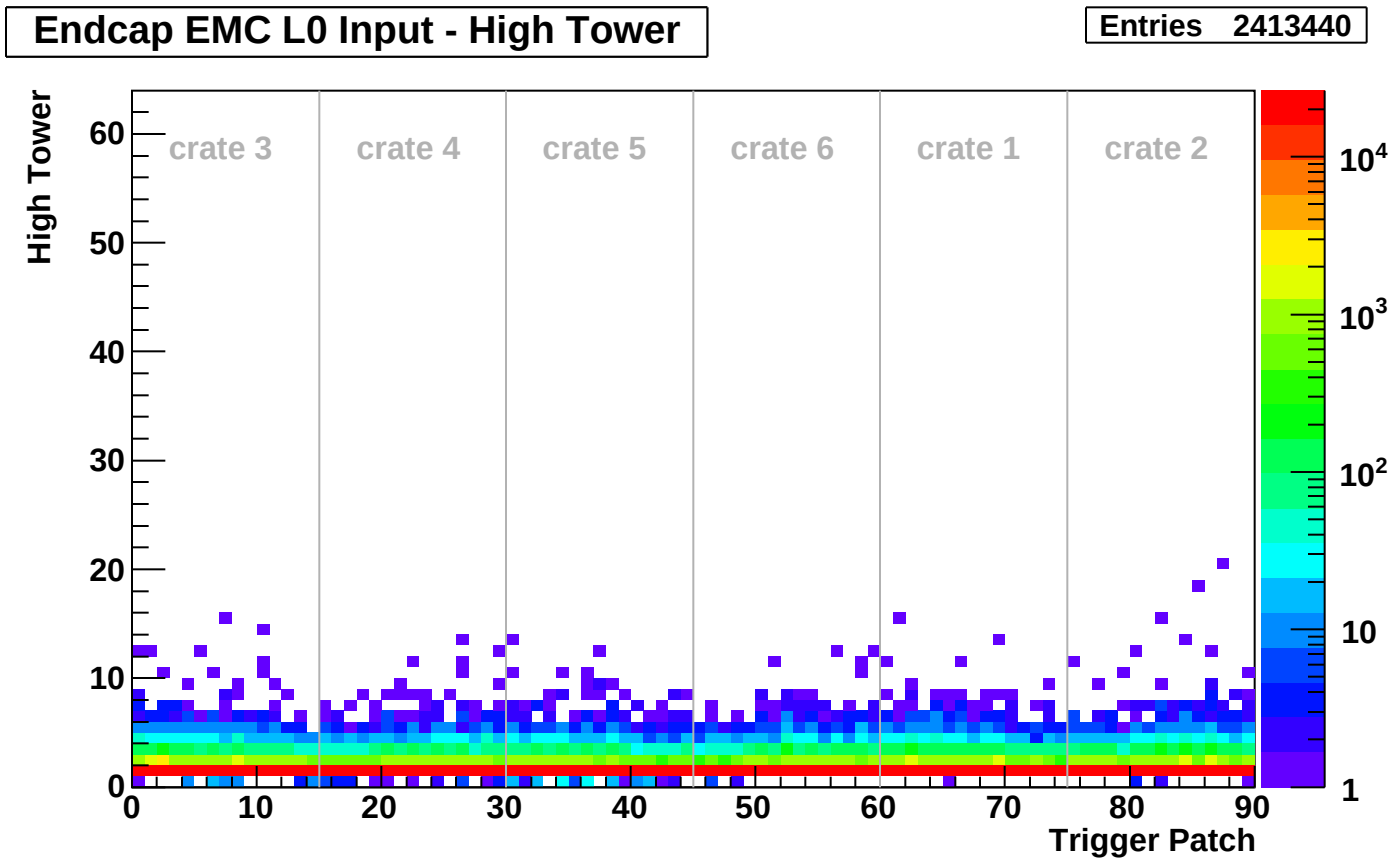

TCU bits
Entries 35756
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

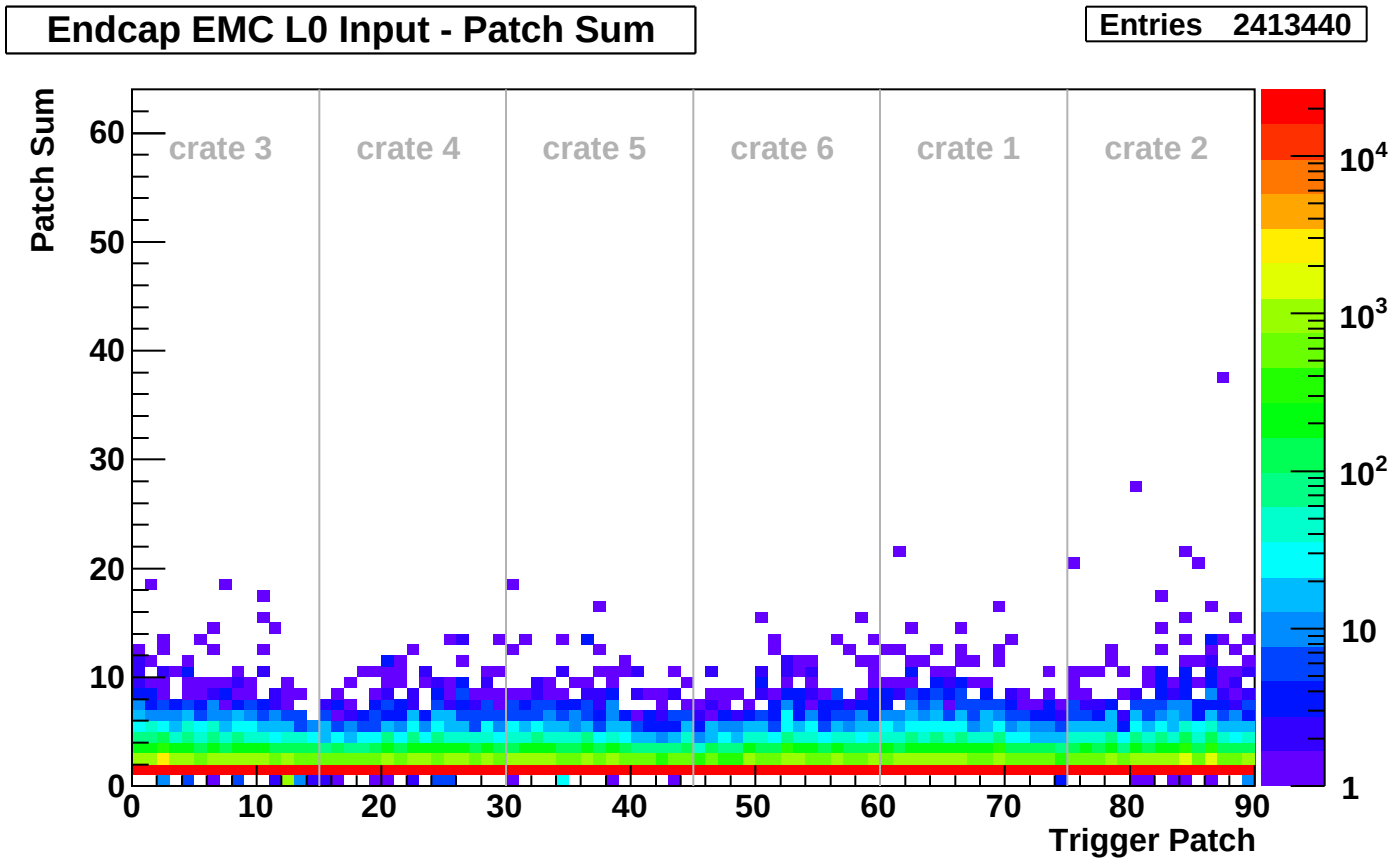

BunchId7Bit (BHT3)
Entries 1

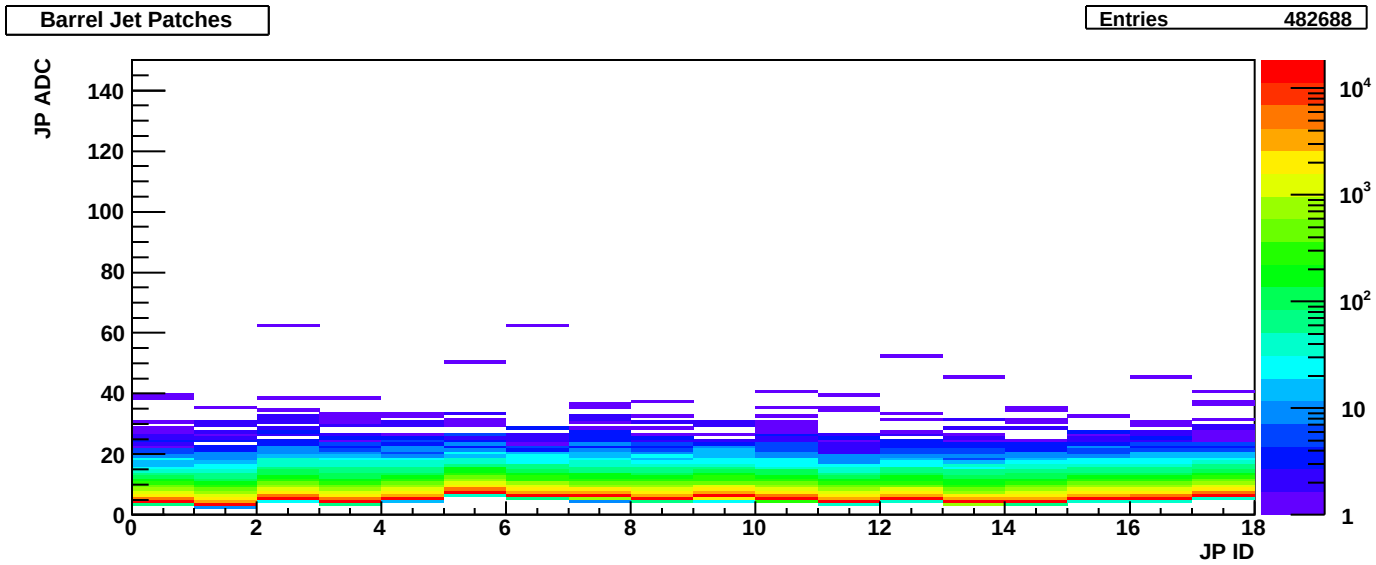

BunchId7Bit (all events)
Entries 26816

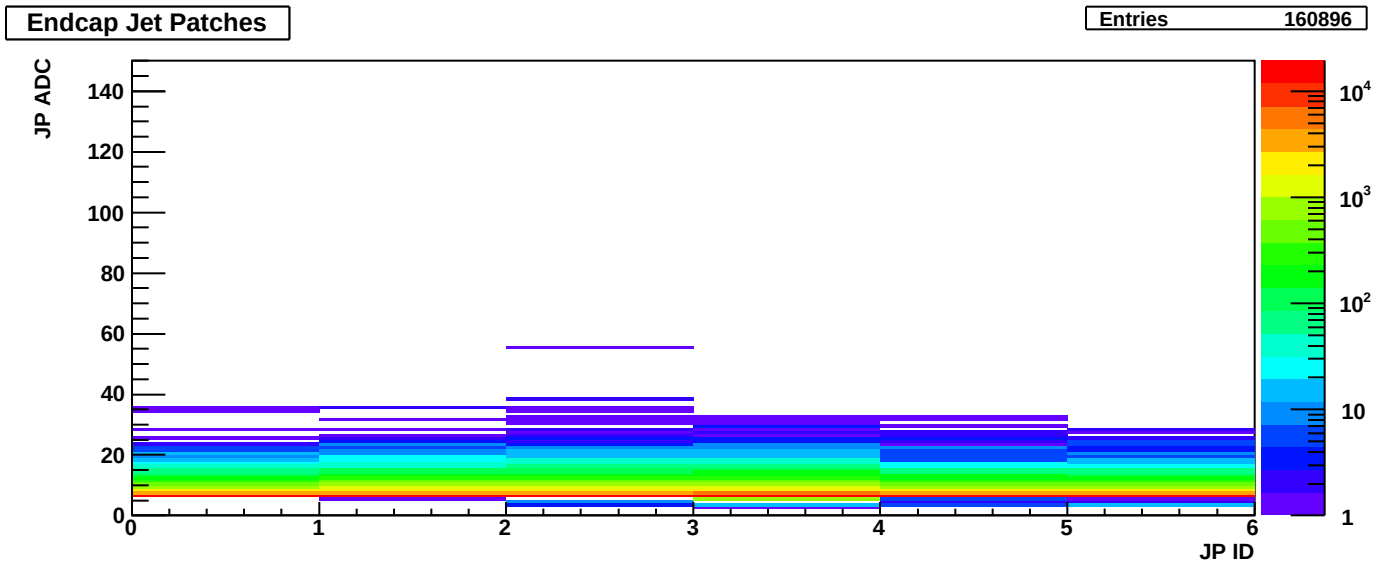

Barrel EMC L0 Input - High Tower
Entries 8044800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

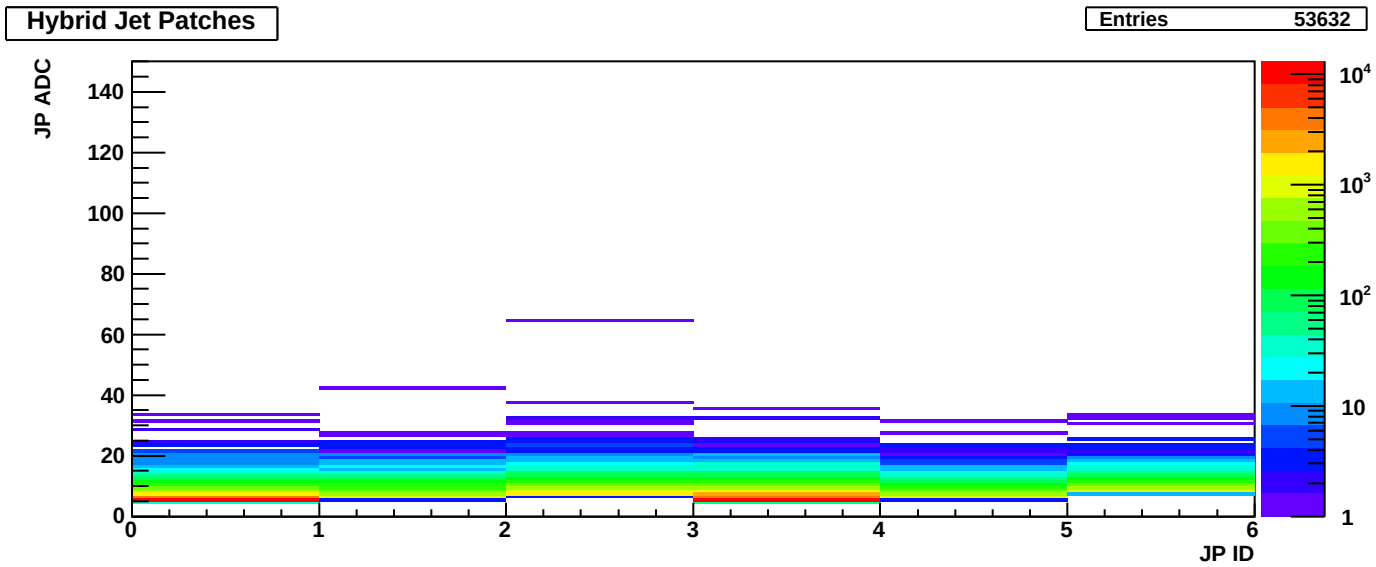

Barrel EMC L0 Input - Patch Sum
Entries 8044800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

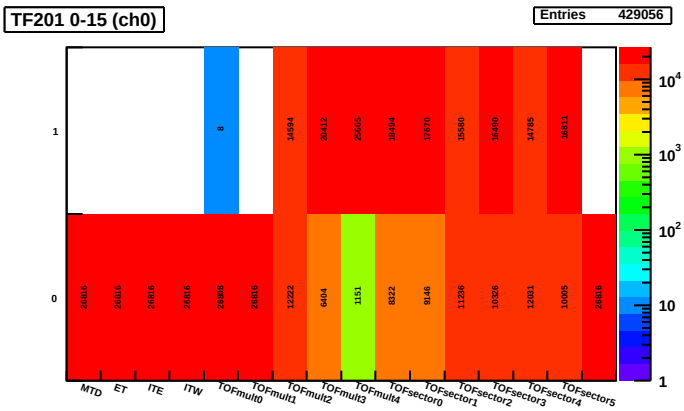

Endcap EMC L0 Input - High Tower
Entries 2413440
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

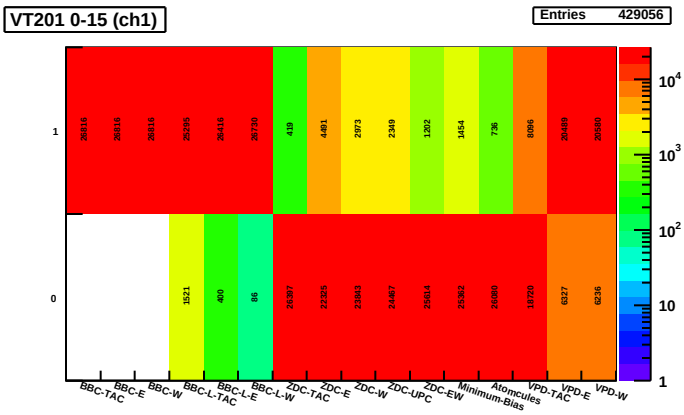

Endcap EMC L0 Input - Patch Sum
Entries 2413440
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

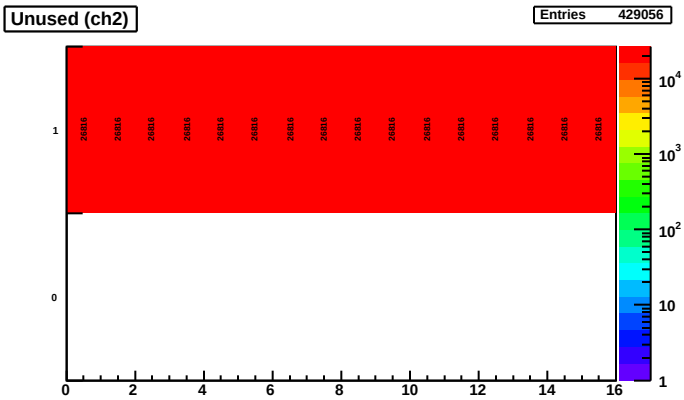

Barrel Jet Patches
Entries 482688
JP ADC
JP ID

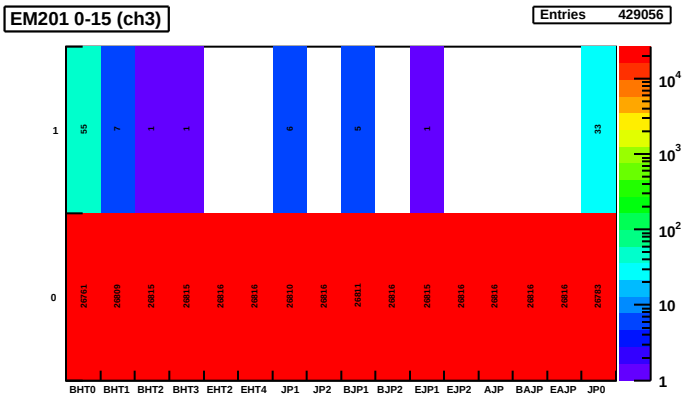

Endcap Jet Patches
Entries 160896
JP ADC
JP ID

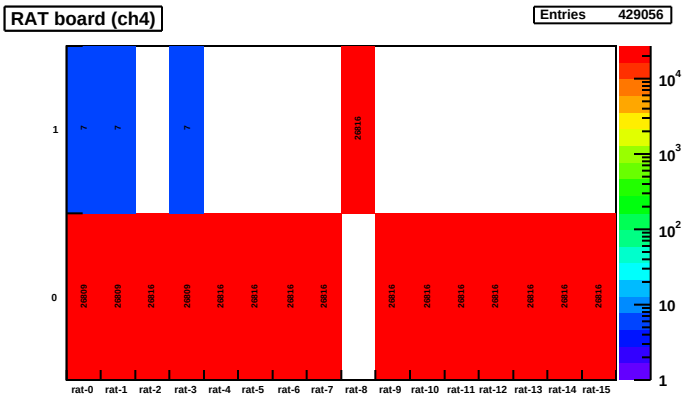

Hybrid Jet Patches
Entries 53632
JP ADC
JP ID

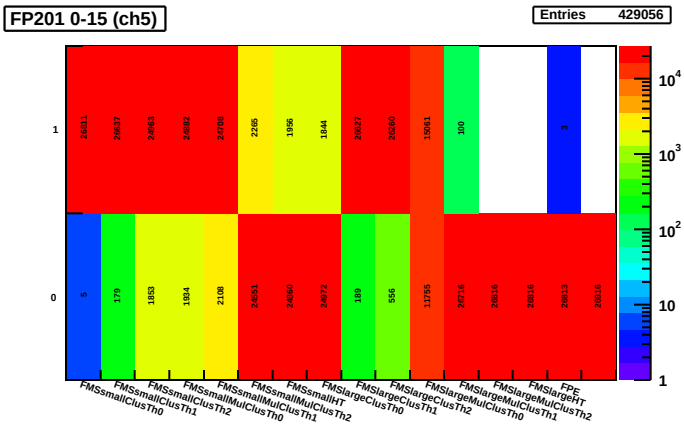

TF201 0-15 (ch0)
Entries 429056
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

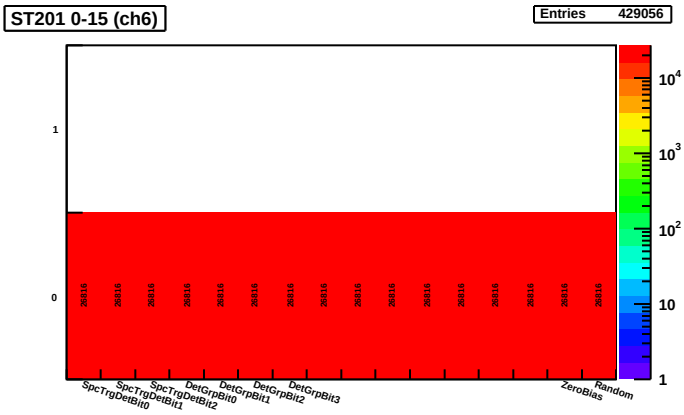

VT201 0-15 (ch1)
Entries 429056
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

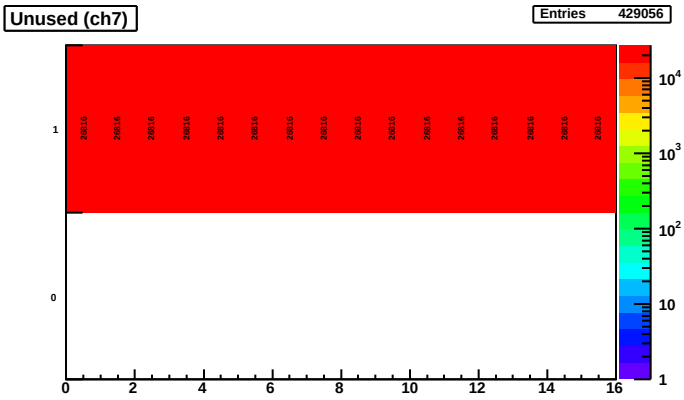

Unused (ch2)
Entries 429056

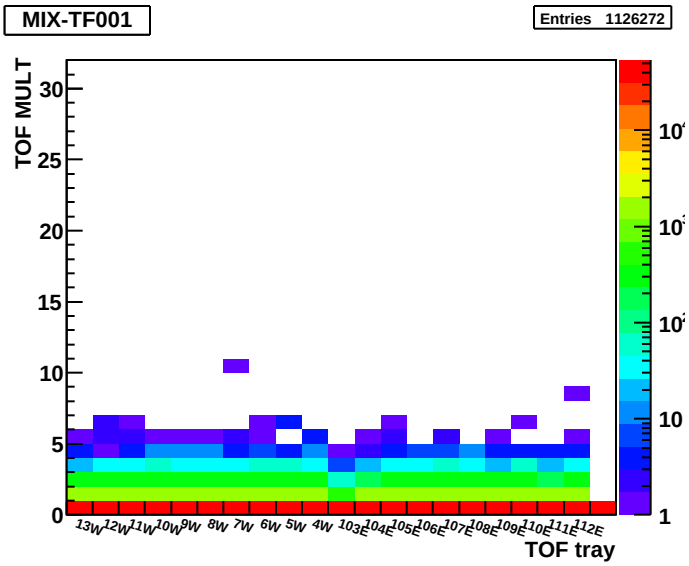

EM201 0-15 (ch3)
Entries 429056
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

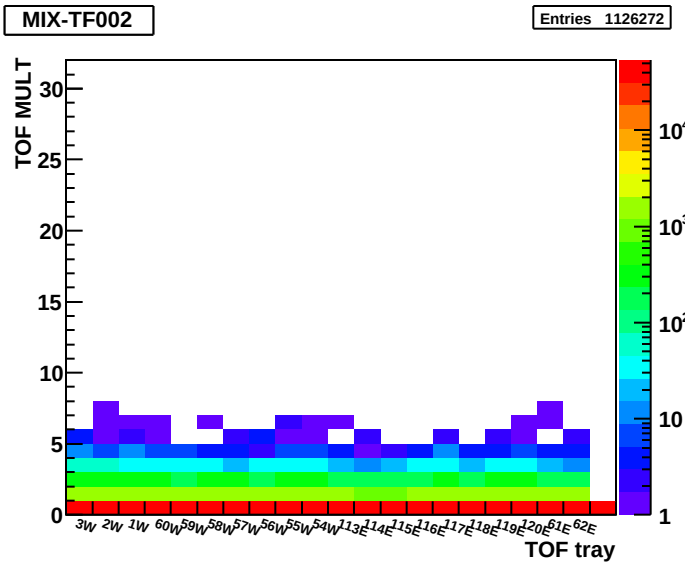

RAT board (ch4)
Entries 429056
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

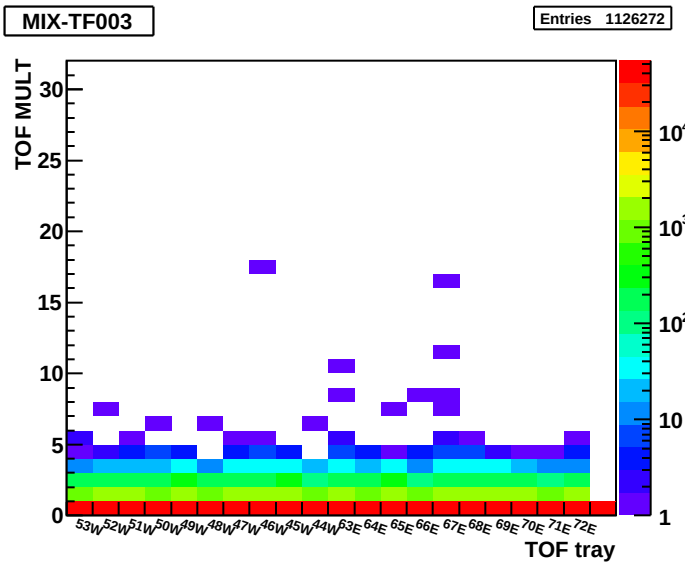

FP201 0-15 (ch5)
Entries 429056
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

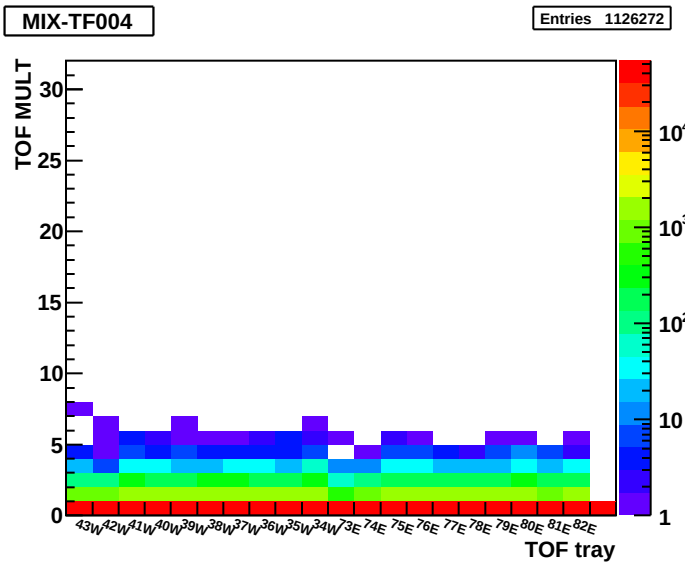

ST201 0-15 (ch6)
Entries 429056
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

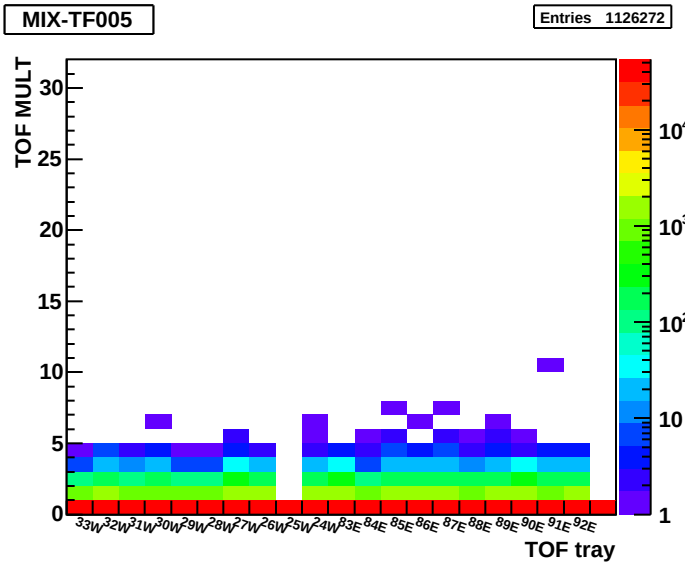

Unused (ch7)
Entries 429056

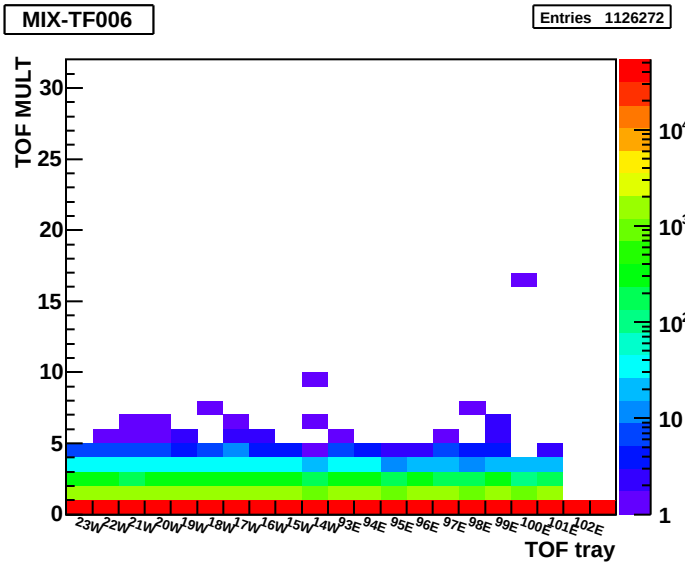

MIX-TF001
Entries 1126272
TOF MULT
TOF tray

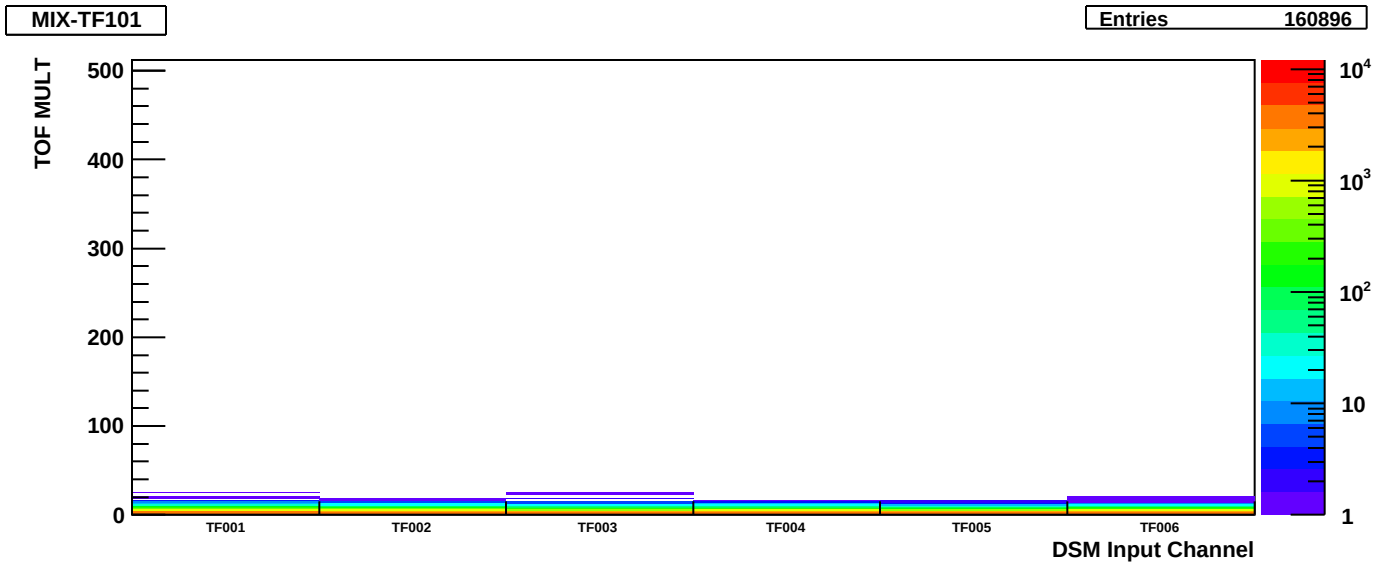

MIX-TF002
Entries 1126272
TOF MULT
TOF tray

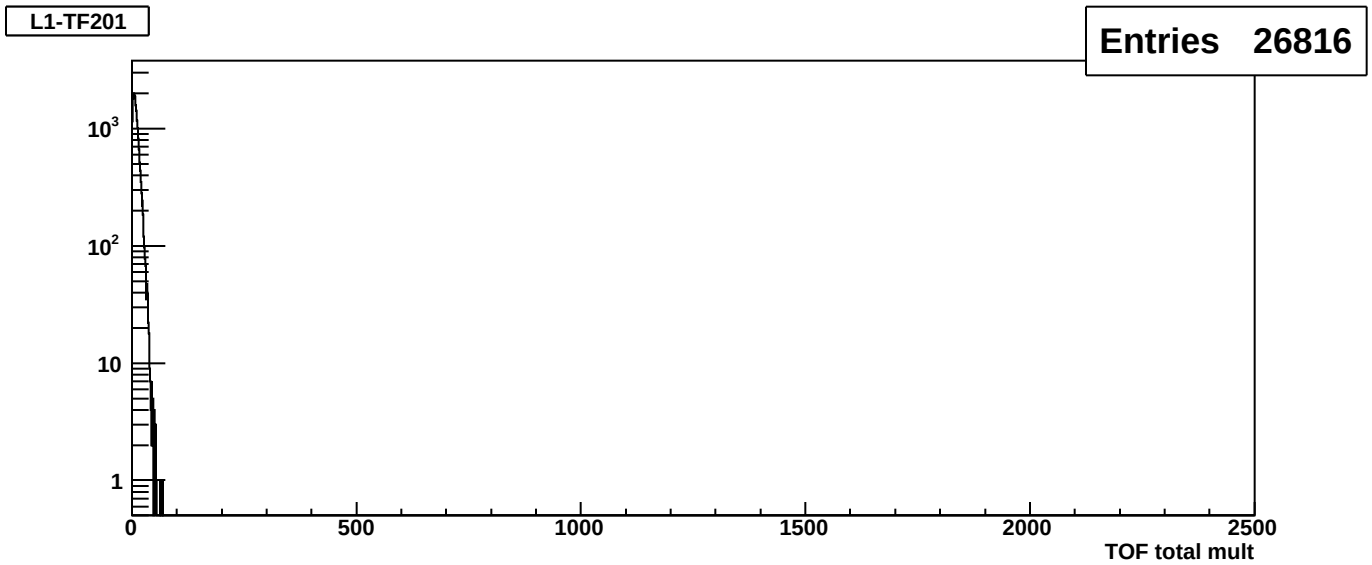

MIX-TF003
Entries 1126272
TOF MULT
TOF tray

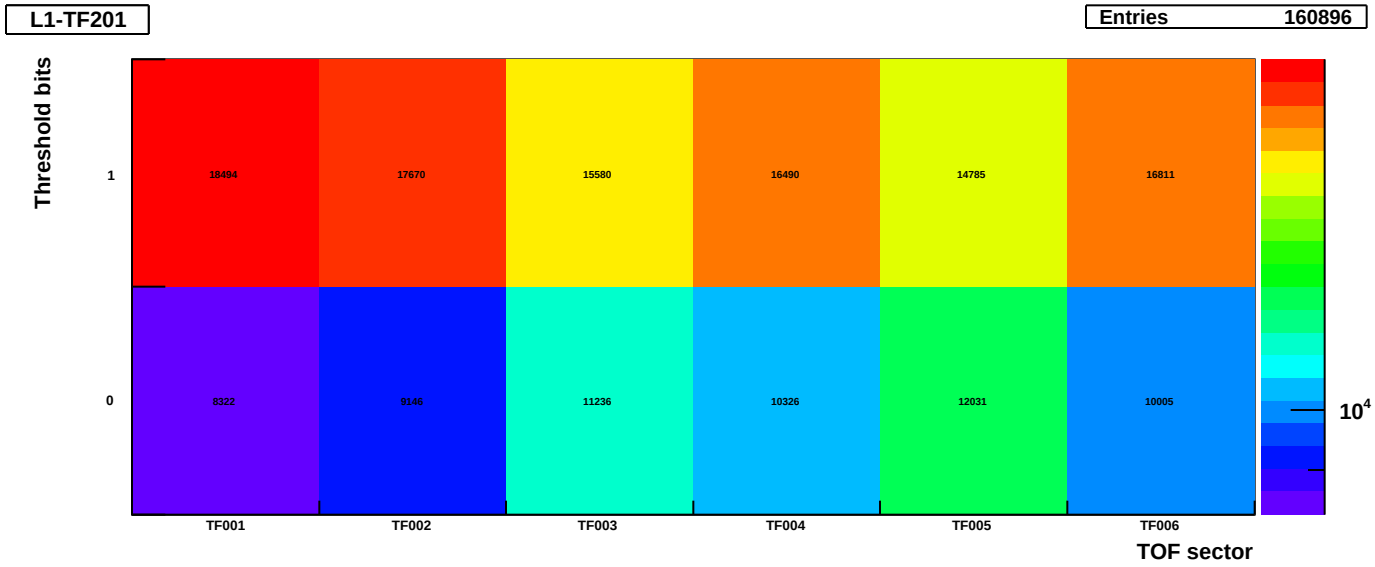

MIX-TF004
Entries 1126272
TOF MULT
TOF tray

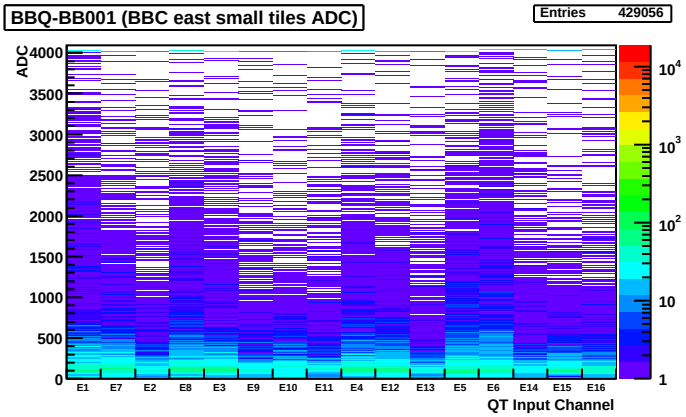

MIX-TF005
Entries 1126272
TOF MULT
TOF tray

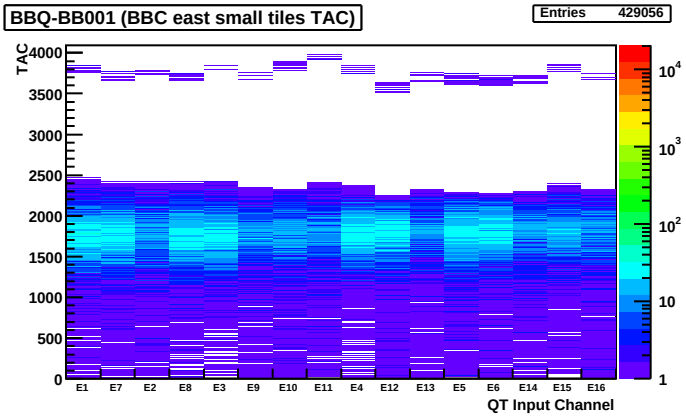

MIX-TF006
Entries 1126272
TOF MULT
TOF tray

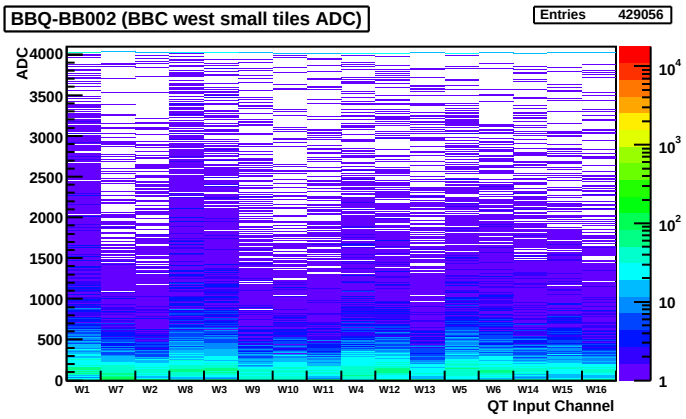

MIX-TF101
Entries 160896
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

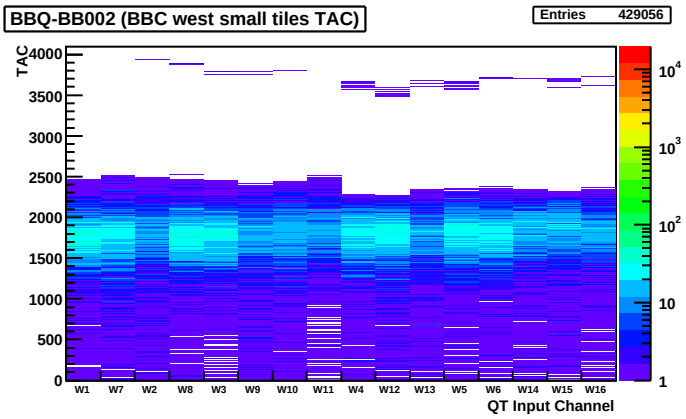

L1-TF201
Entries 26816
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

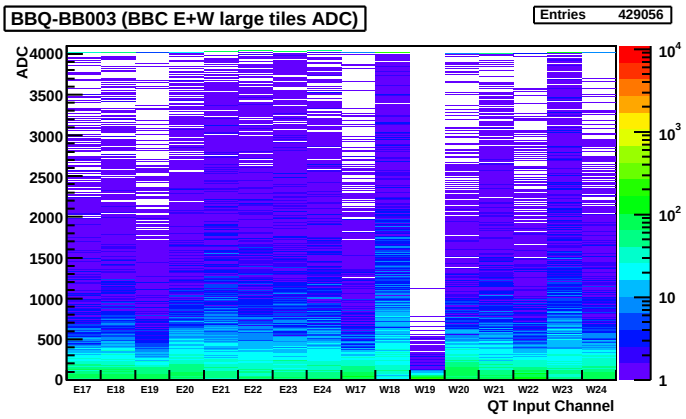

L1-TF201
Entries 160896
Threshold bits
1
0
18494
17670
15580
16490
14785
16811
8322
9146
11236
10326
12031
10005
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$

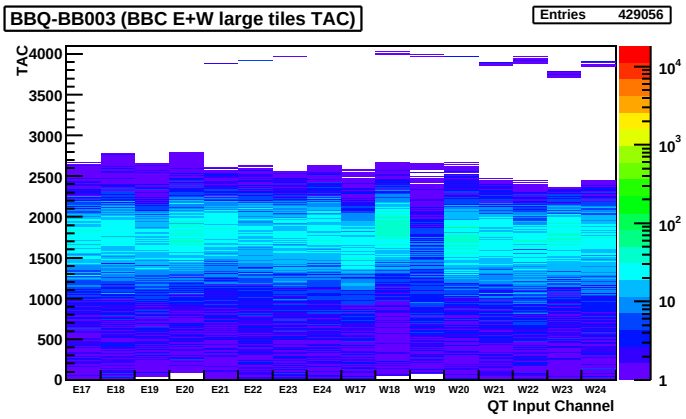

BBQ-BB001 (BBC east small tiles ADC)
Entries 429056
ADC
QT Input Channel

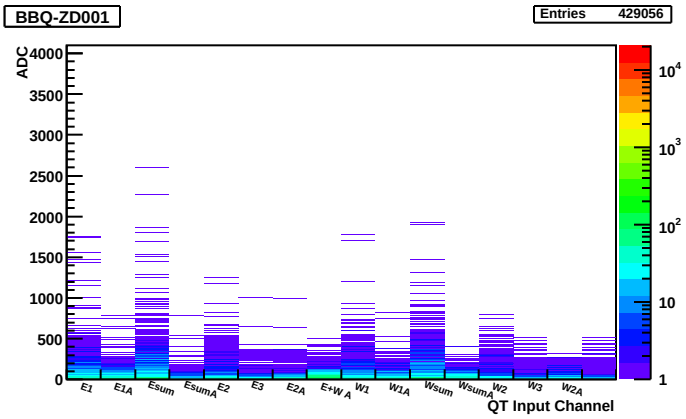

BBQ-BB001 (BBC east small tiles TAC)
Entries 429056
TAC
QT Input Channel

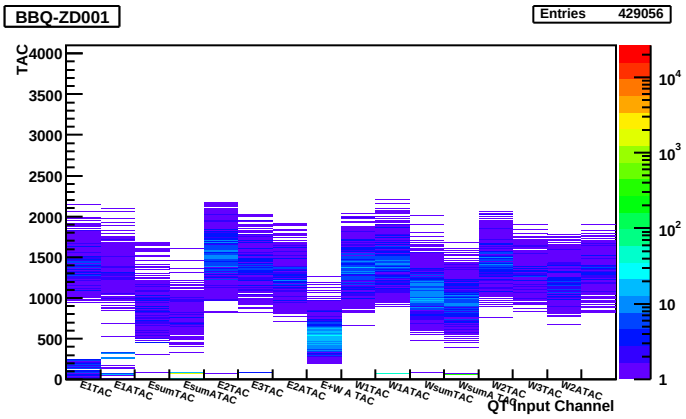

BBQ-BB002 (BBC west small tiles ADC)
Entries 429056
ADC
QT Input Channel

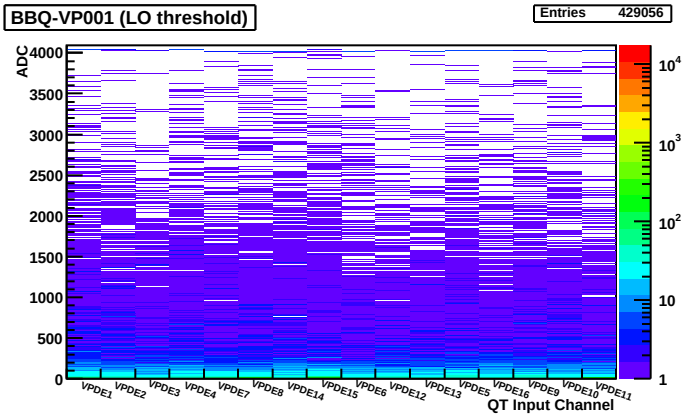

BBQ-BB002 (BBC west small tiles TAC)
Entries 429056
TAC
QT Input Channel

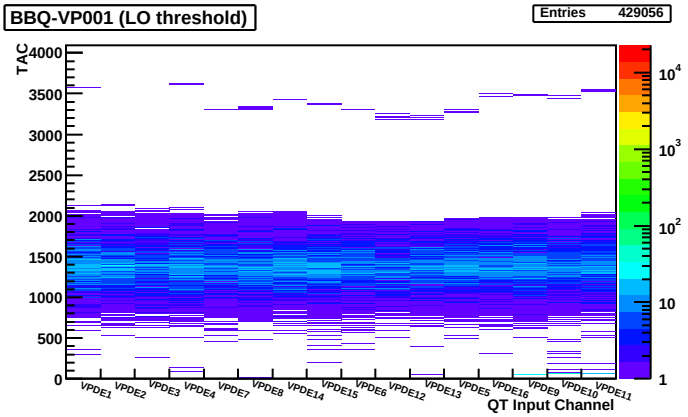

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 429056
ADC
QT Input Channel

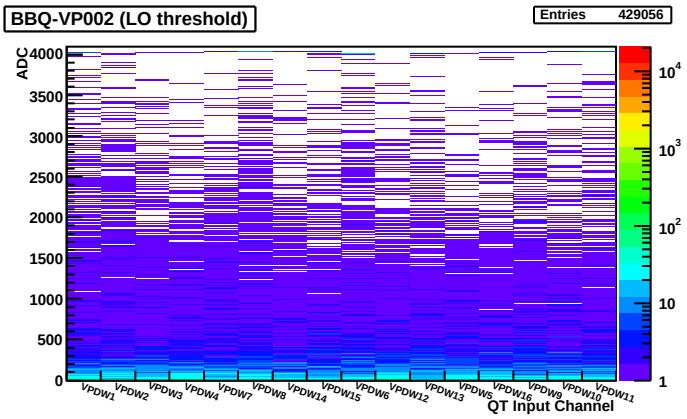

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 429056
TAC
QT Input Channel

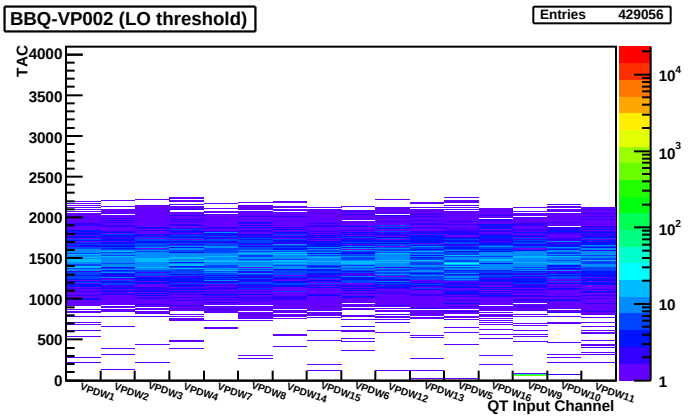

BBQ-ZD001
Entries 429056
ADC
QT Input Channel

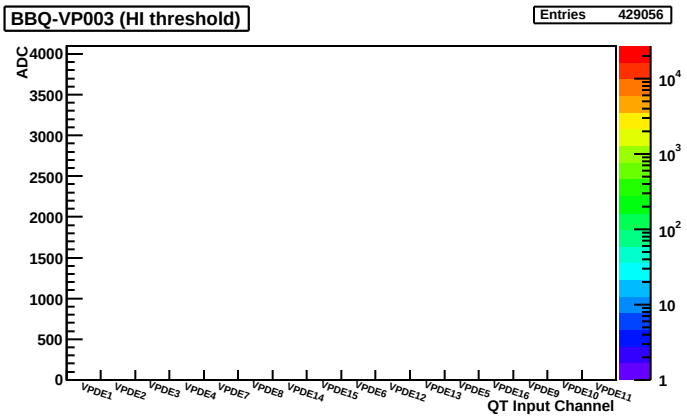

BBQ-ZD001
Entries 429056
TAC
QT Input Channel

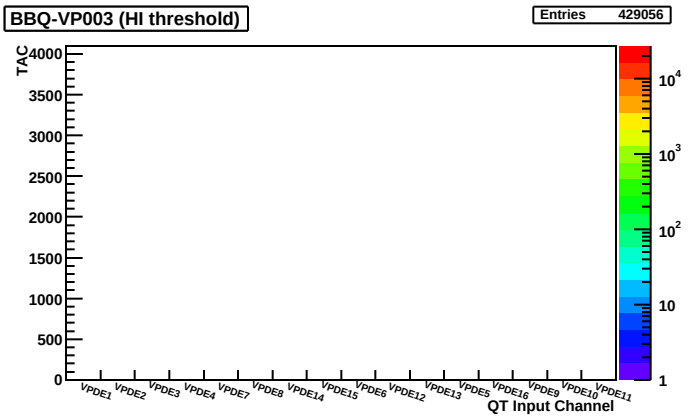

BBQ-VP001 (LO threshold)
Entries 429056
ADC
QT Input Channel

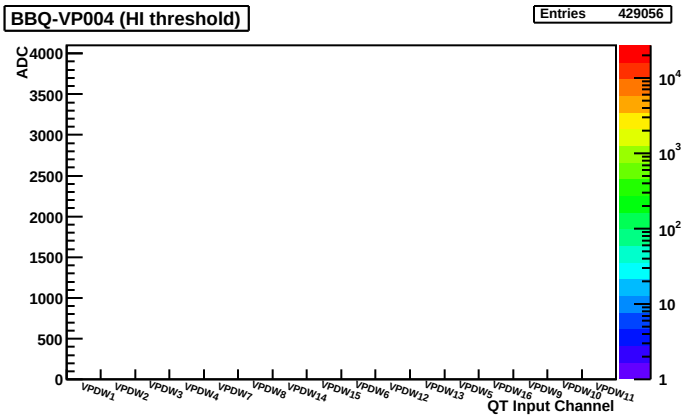

BBQ-VP001 (LO threshold)
Entries 429056
TAC
QT Input Channel

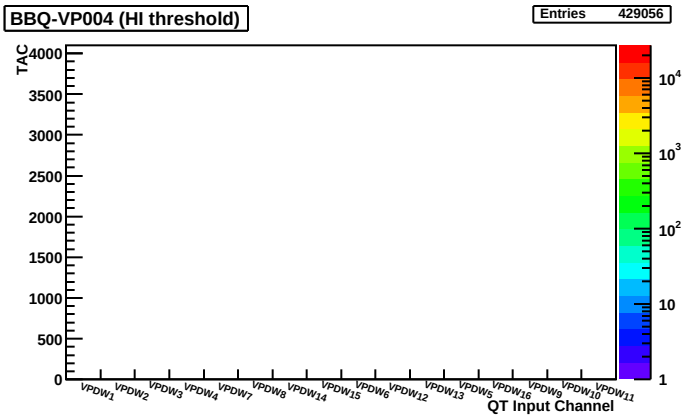

BBQ-VP002 (LO threshold)
Entries 429056
ADC
QT Input Channel

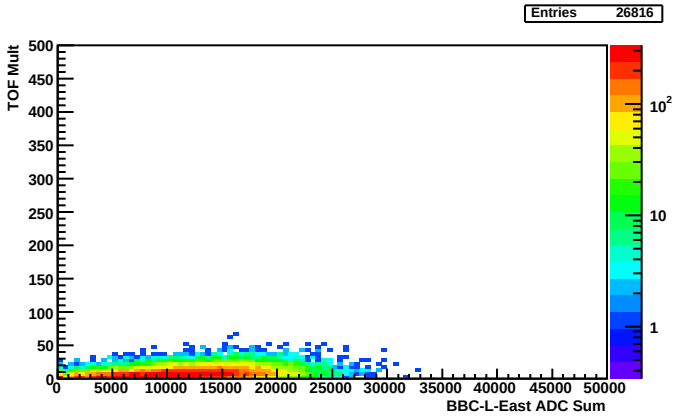

BBQ-VP002 (LO threshold)
Entries 429056
TAC
QT Input Channel

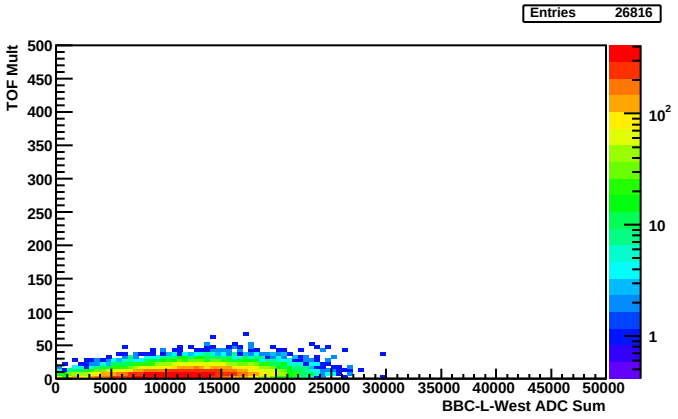

BBQ-VP003 (HI threshold)
Entries 429056
ADC
QT Input Channel

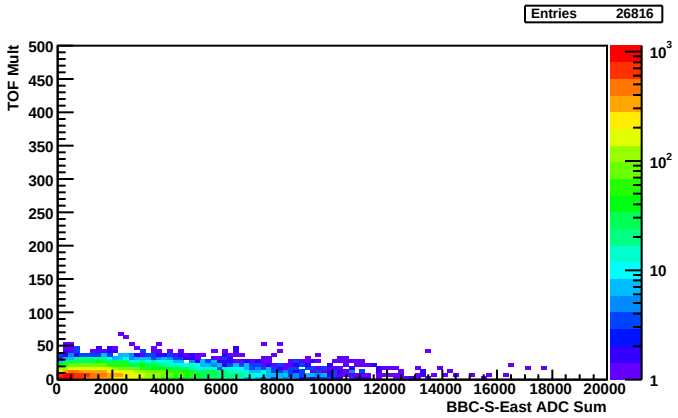

BBQ-VP003 (HI threshold)
Entries 429056
TAC
QT Input Channel

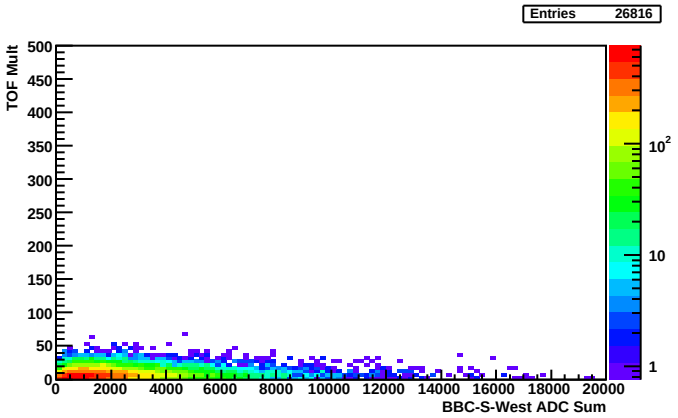

BBQ-VP004 (HI threshold)
Entries 429056
ADC
QT Input Channel

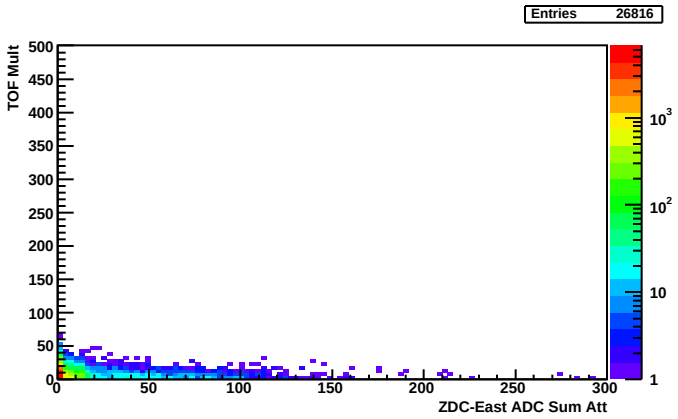

BBQ-VP004 (HI threshold)
Entries 429056
TAC
QT Input Channel

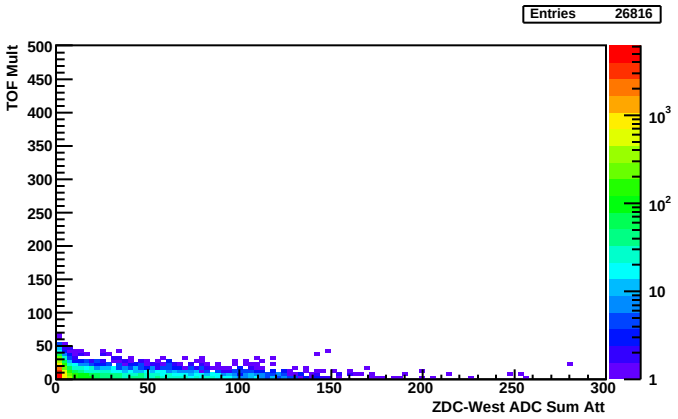

Entries 26816
TOF Mult
BBC-L-East ADC Sum

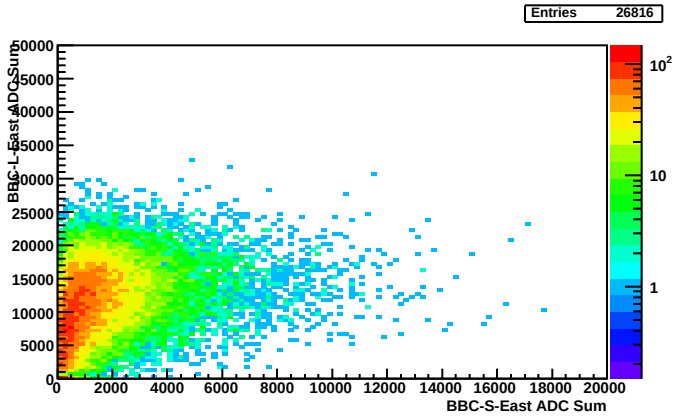

Entries 26816
TOF Mult
BBC-L-West ADC Sum

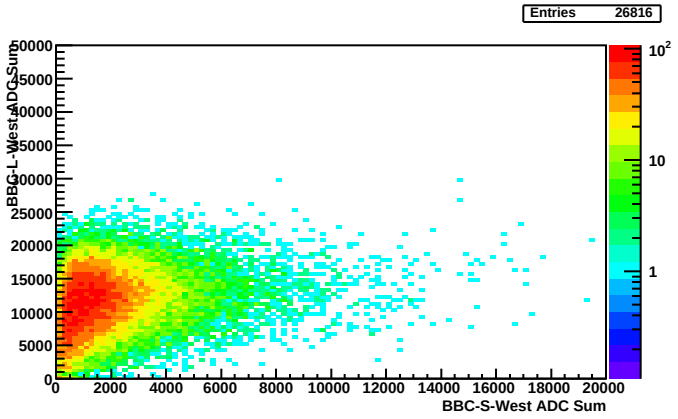

Entries 26816
TOF Mult
BBC-S-East ADC Sum

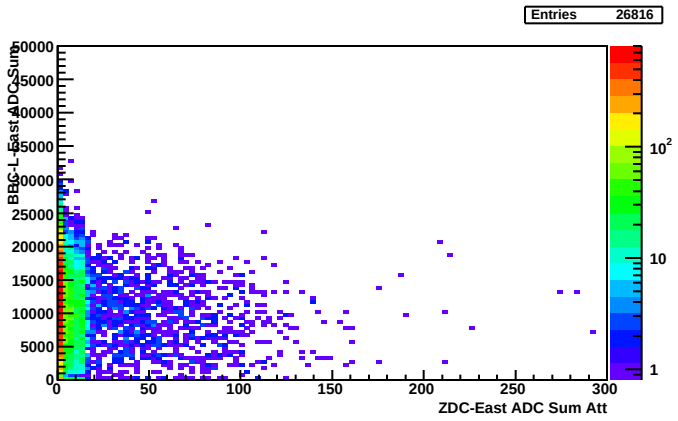

Entries 26816
TOF Mult
BBC-S-West ADC Sum

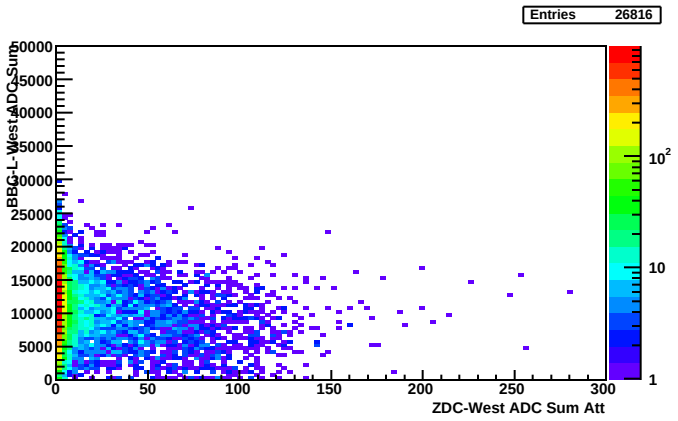

Entries 26816
TOF Mult
ZDC-East ADC Sum Att

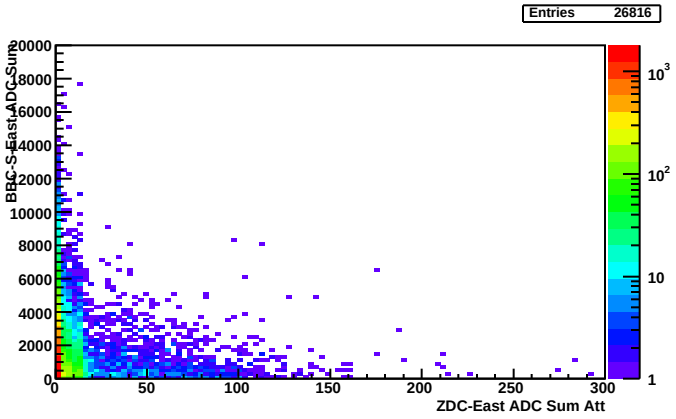

Entries 26816
TOF Mult
ZDC-West ADC Sum Att

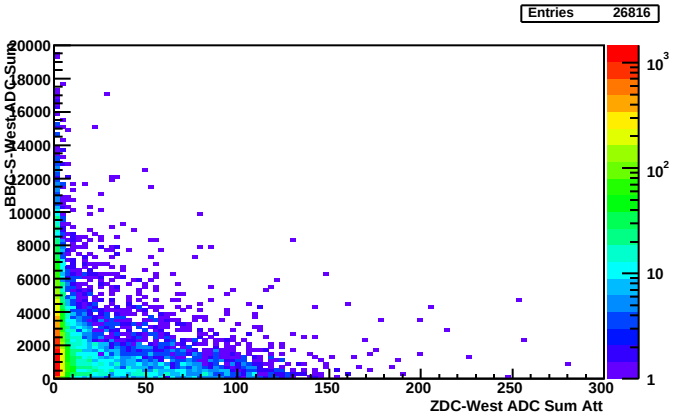

Entries 26816
BBC-L-East ADC Sum
BBC-S-East ADC Sum

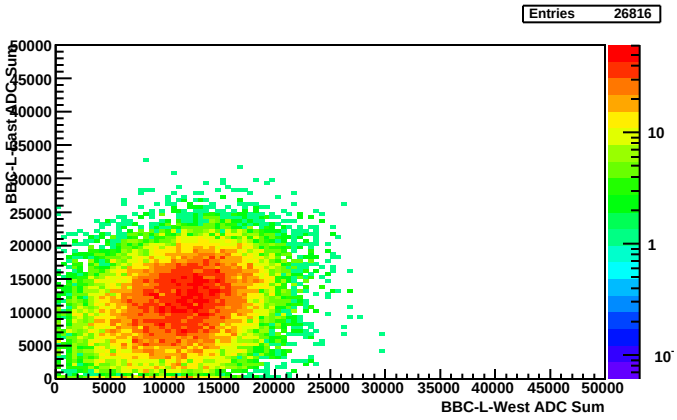

Entries 26816
BBC-L-West ADC Sum
BBC-S-West ADC Sum

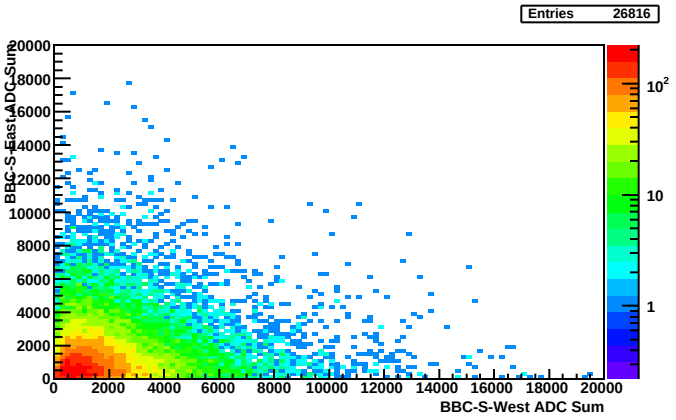

Entries 26816
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

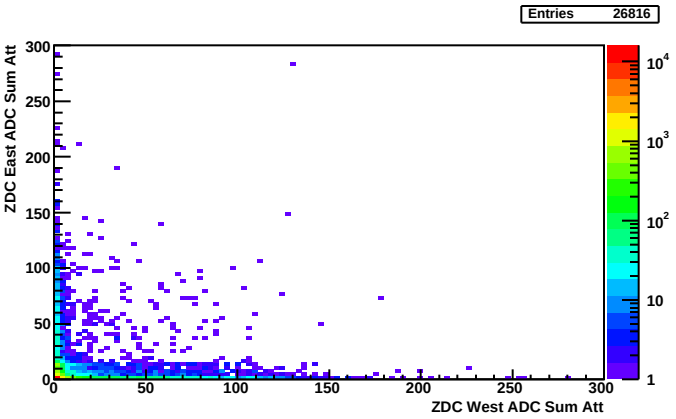

Entries 26816
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

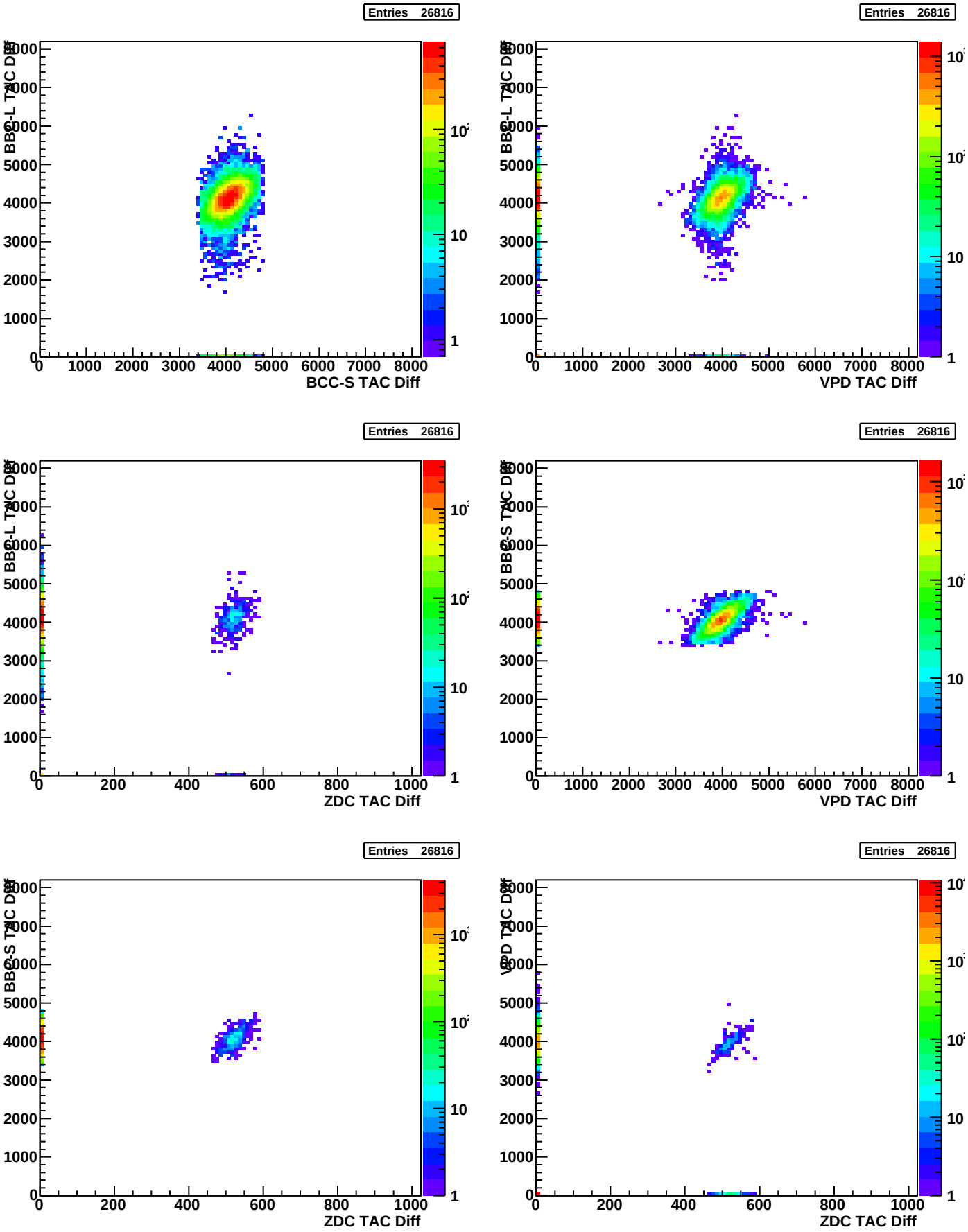

Entries 26816
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

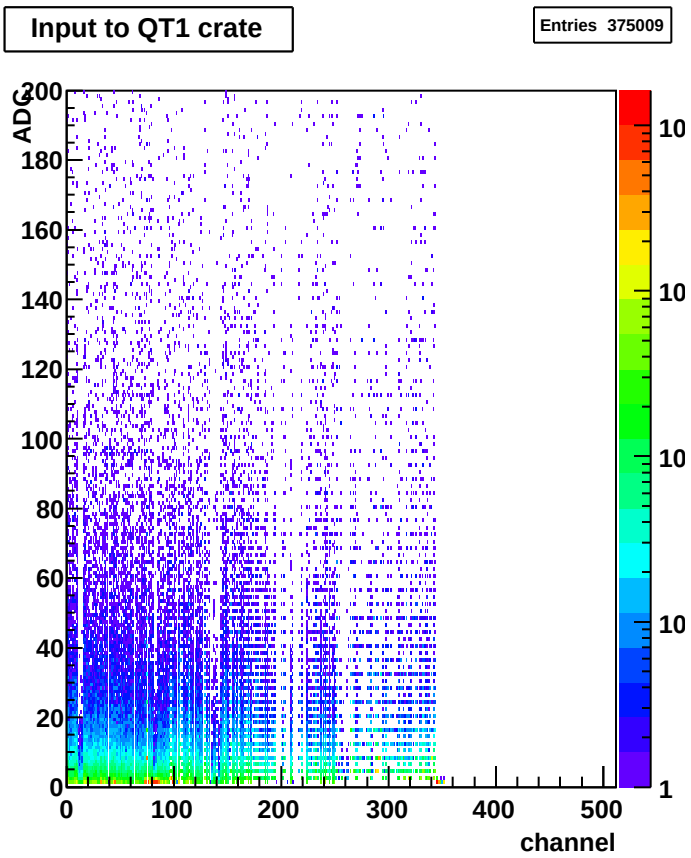

Entries 26816
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

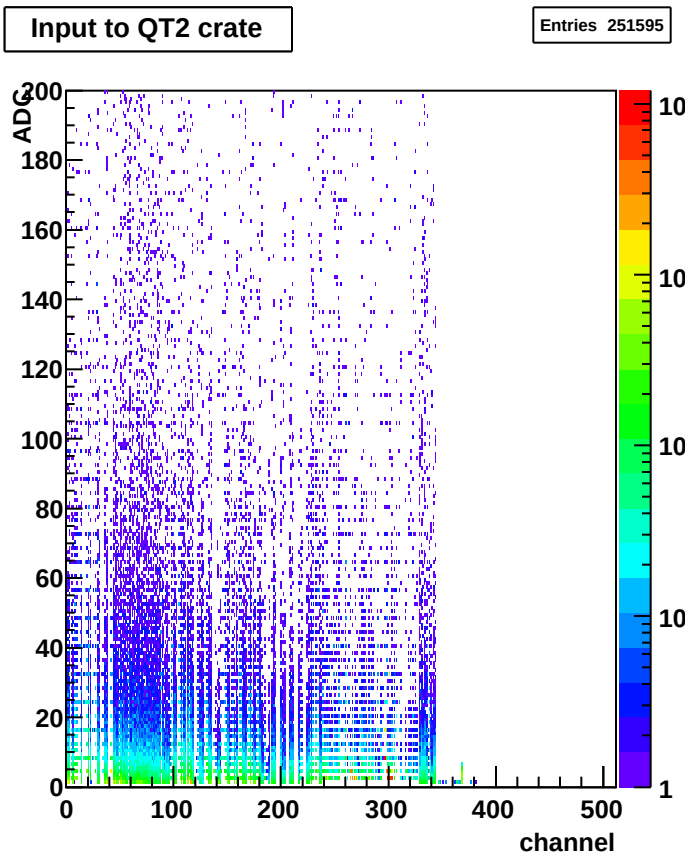

Entries 26816
BBC-L-East ADC Sum
BBC-L-West ADC Sum

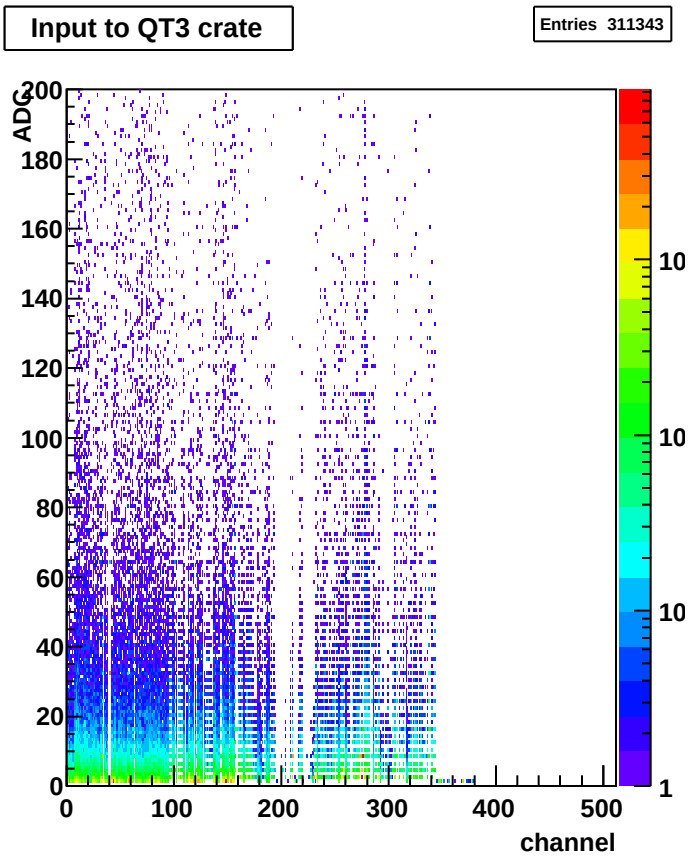

Entries 26816
BBC-S-East ADC Sum
BBC-S-West ADC Sum

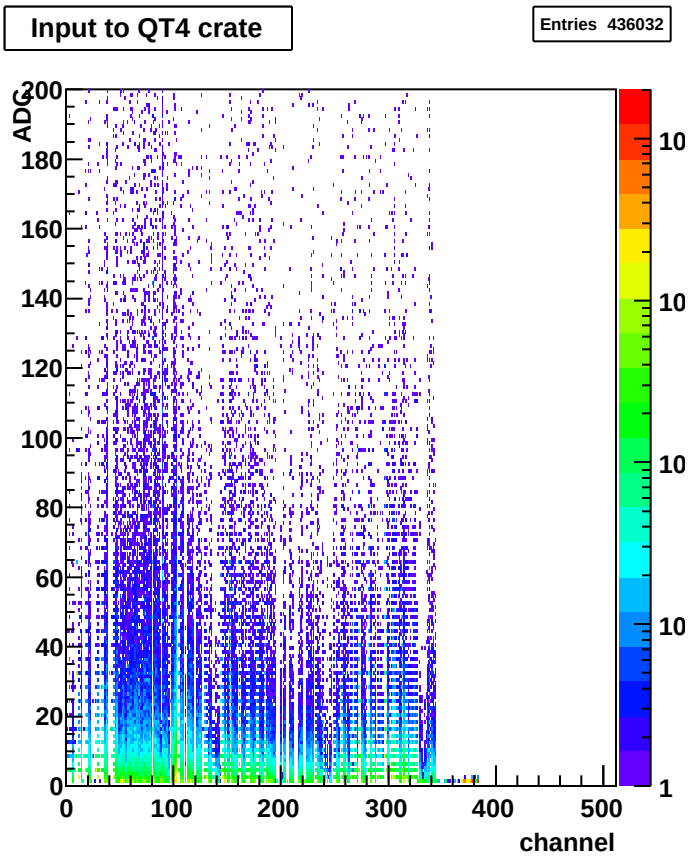

Entries 26816
ZDC East ADC Sum Att
ZDC West ADC Sum Att

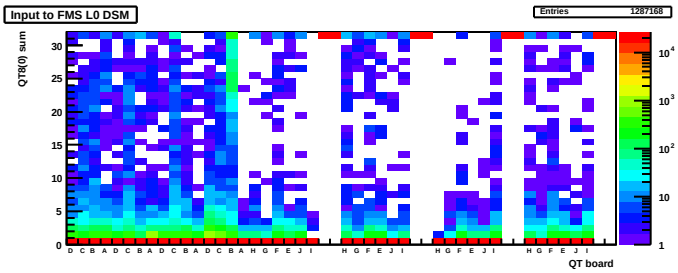

Entries 26816
BBC-L TAC Diff
BCC-S TAC Diff
Entries 26816
BBC-L TAC Diff
VPD TAC Diff
Entries 26816
BBC-L TAC Diff
ZDC TAC Diff
Entries 26816
BBC-S TAC Diff
VPD TAC Diff
Entries 26816
BBC-S TAC Diff
ZDC TAC Diff
Entries 26816
VPD TAC Diff
ZDC TAC Diff

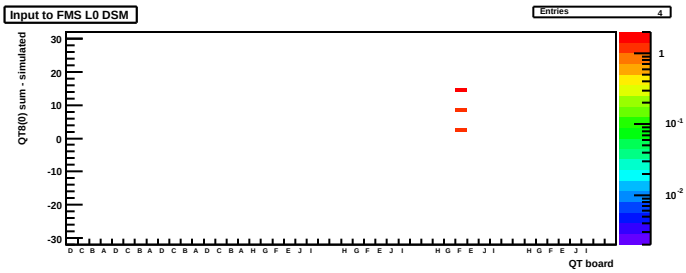

Input to QT1 crate
Entries 375009
ADC
channel

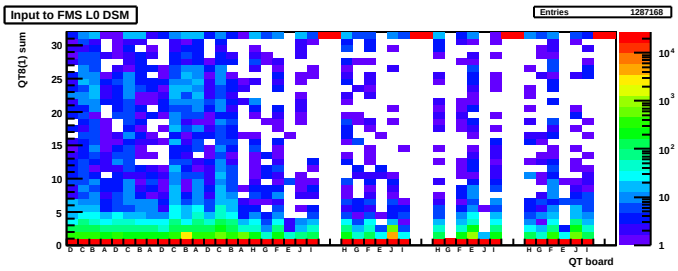

Input to QT2 crate
Entries 251595
ADC
channel

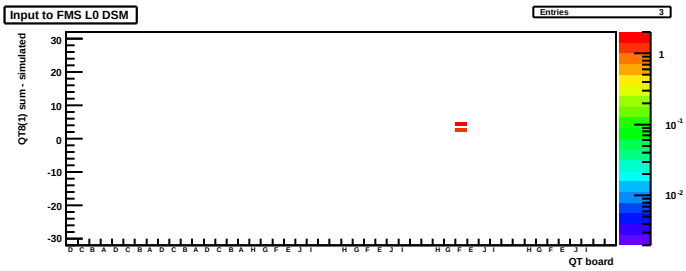

Input to QT3 crate
Entries 311343
ADC
channel

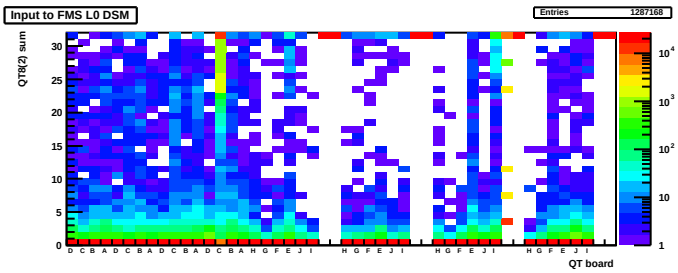

Input to QT4 crate
Entries 436032
ADC
channel

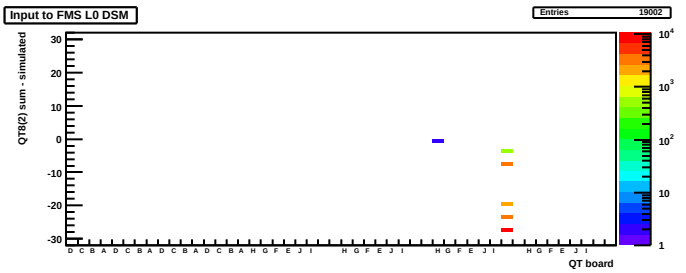

Input to FMS L0 DSM
Entries 1287168
QT8(0) sum
QT board

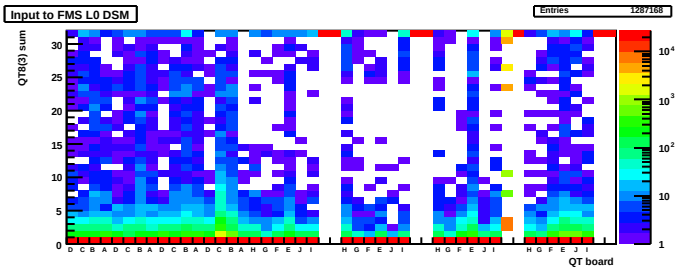

Input to FMS L0 DSM
Entries 4
QT8(0) sum - simulated
QT board

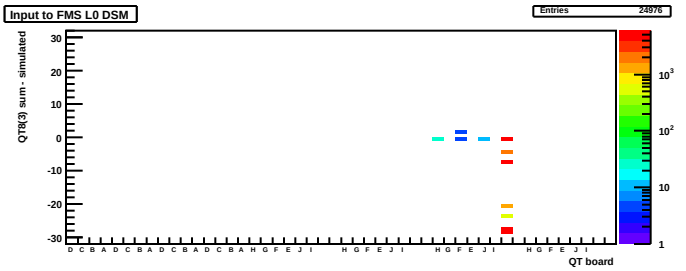

Input to FMS L0 DSM
Entries 1287168
QT8(1) sum
QT board

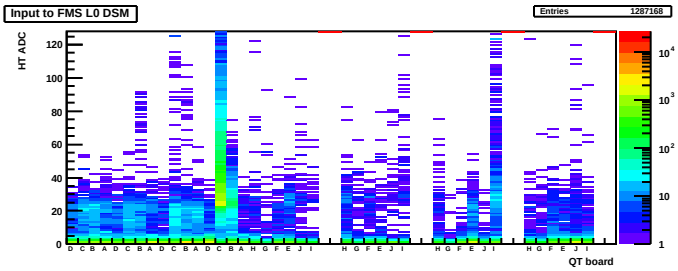

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

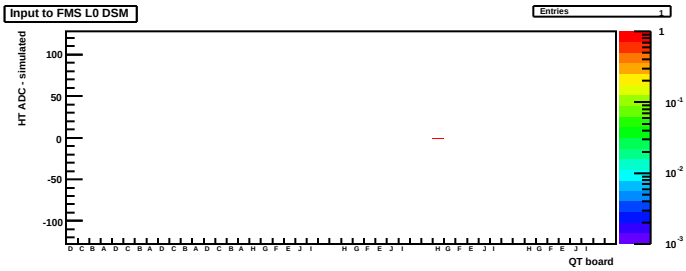

Input to FMS L0 DSM
Entries 1287168
QT8(2) sum
QT board

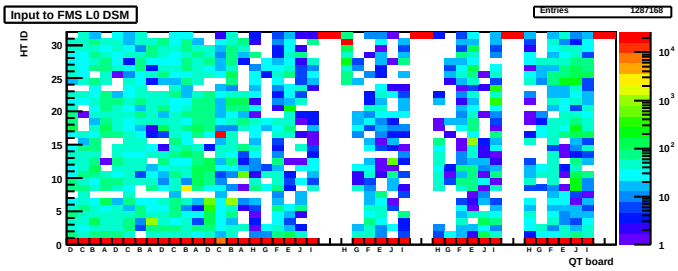

Input to FMS L0 DSM
Entries 19002
QT8(2) sum - simulated
QT board

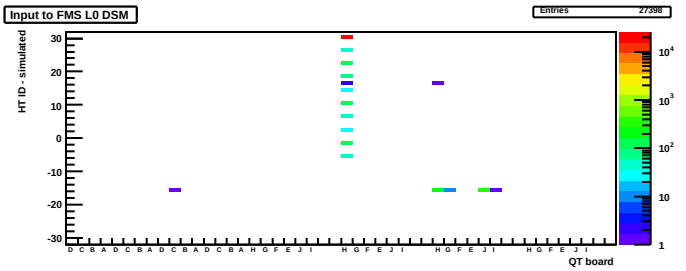

Input to FMS L0 DSM
Entries 1287168
QT8(3) sum
QT board

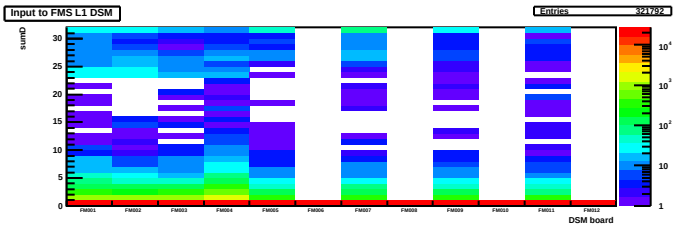

Input to FMS L0 DSM
Entries 24976
QT8(3) sum - simulated
QT board

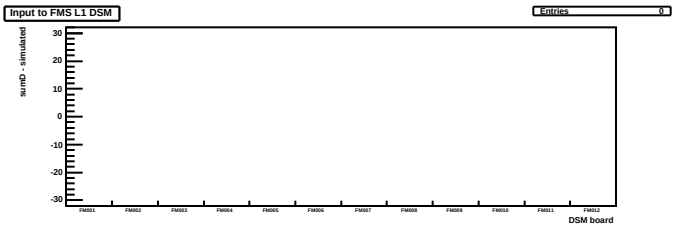

Input to FMS L0 DSM
Entries 1287168
HT ADC
QT board

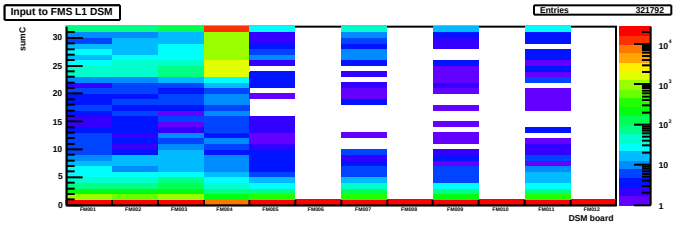

Input to FMS L0 DSM
Entries 1
HT ADC - simulated
QT board

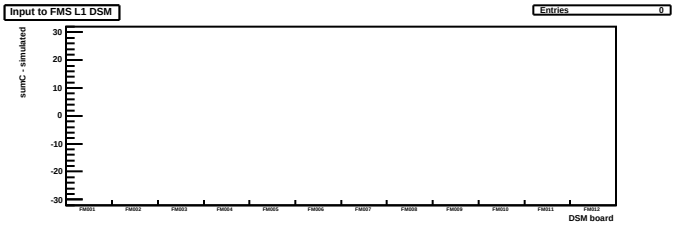

Input to FMS L0 DSM
Entries 1287168
HT ID
QT board

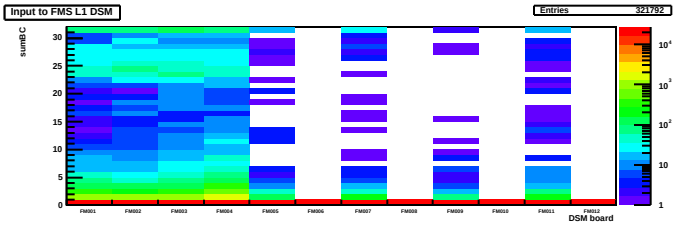

Input to FMS L0 DSM
Entries 27398
HT ID - simulated
QT board

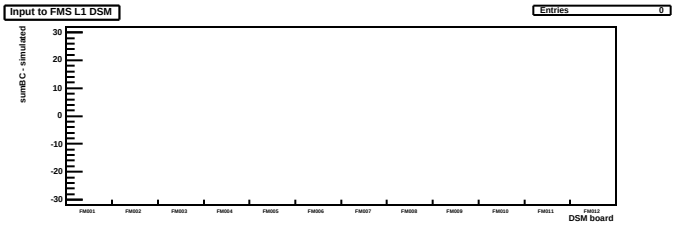

Input to FMS L1 DSM
Entries 321792
sumD
DSM board

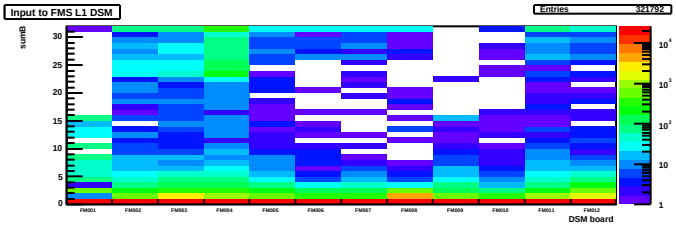

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

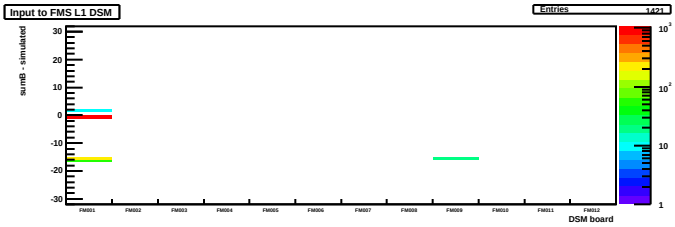

Input to FMS L1 DSM
Entries 321792
sumC
DSM board

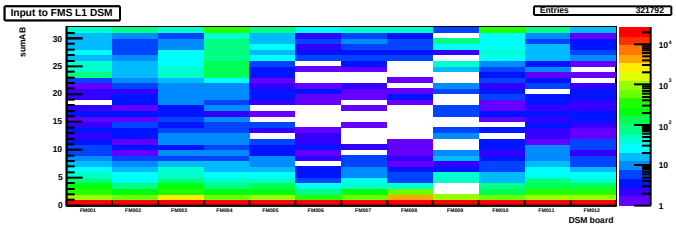

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

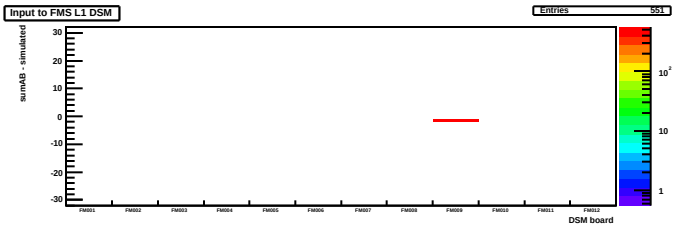

Input to FMS L1 DSM
Entries 321792
sumBC
DSM board

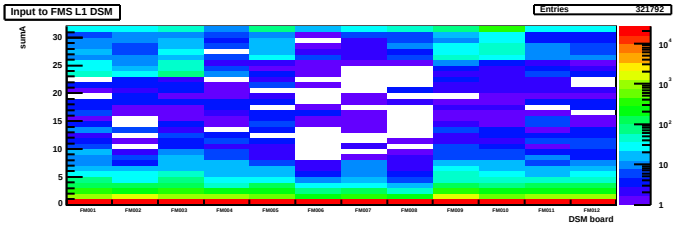

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

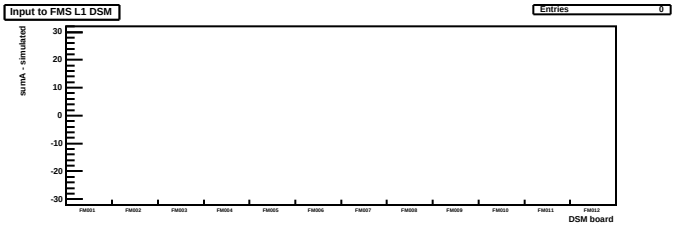

Input to FMS L1 DSM
Entries 321792
sumB
DSM board

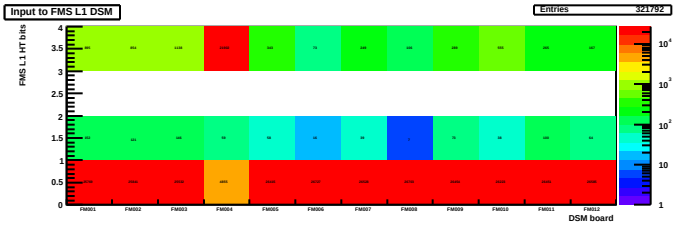

Input to FMS L1 DSM
Entries 1421
sumB - simulated
DSM board

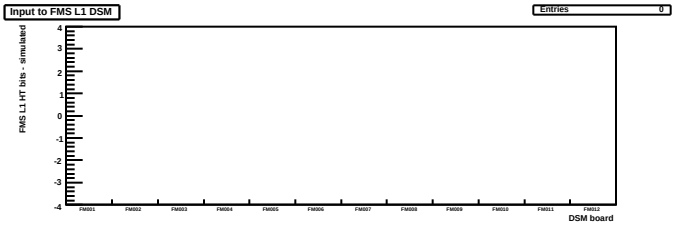

Input to FMS L1 DSM
Entries 321792
sumAB
DSM board

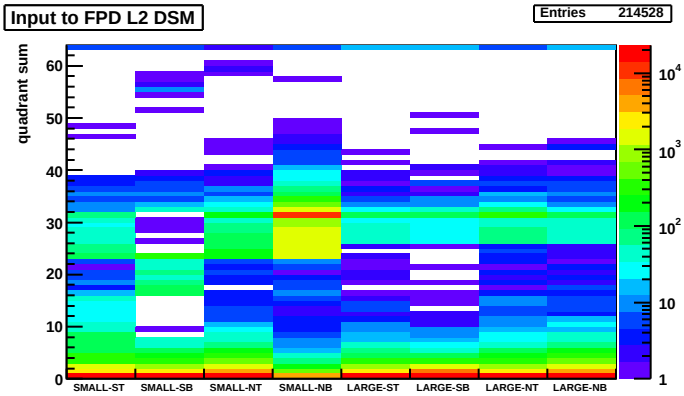

Input to FMS L1 DSM
Entries 551
sumAB - simulated
DSM board

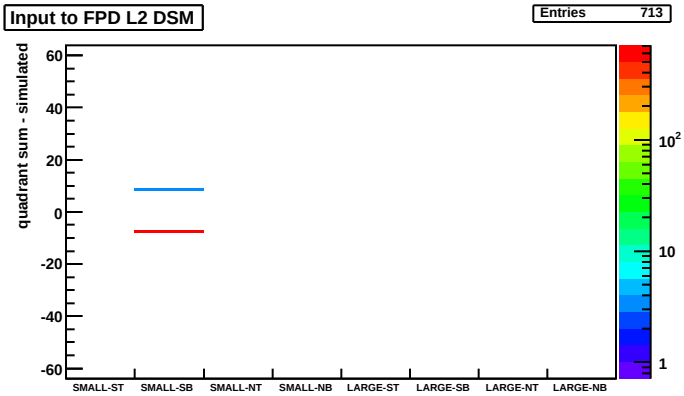

Input to FMS L1 DSM
Entries 321792
sumA
DSM board

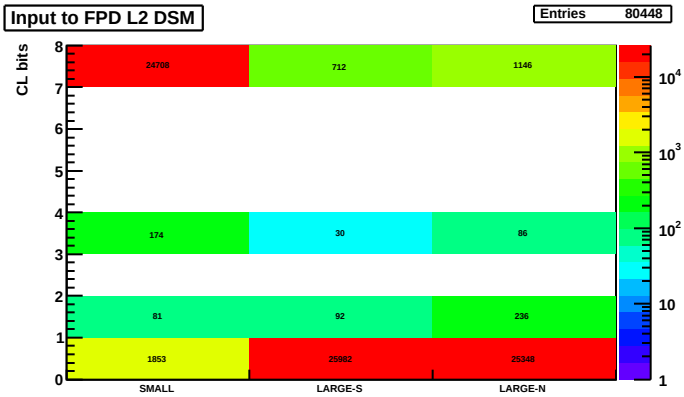

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

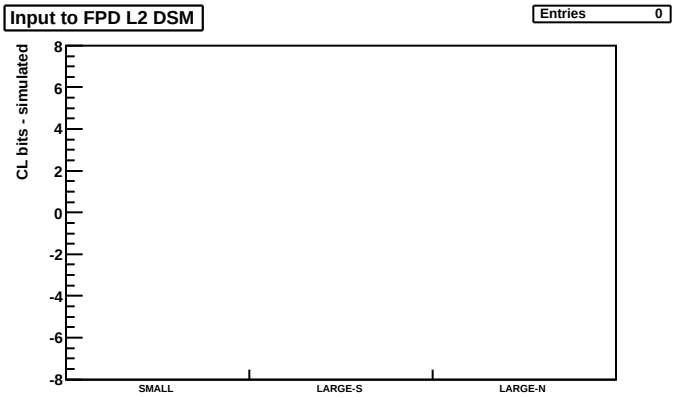

Input to FMS L1 DSM
Entries 321792
FMS L1 HT bits
DSM board

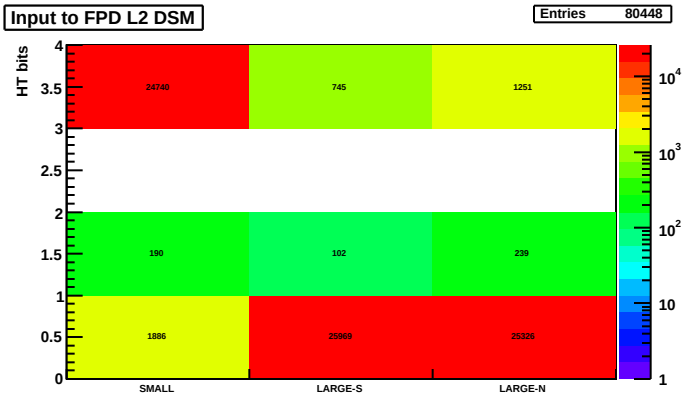

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

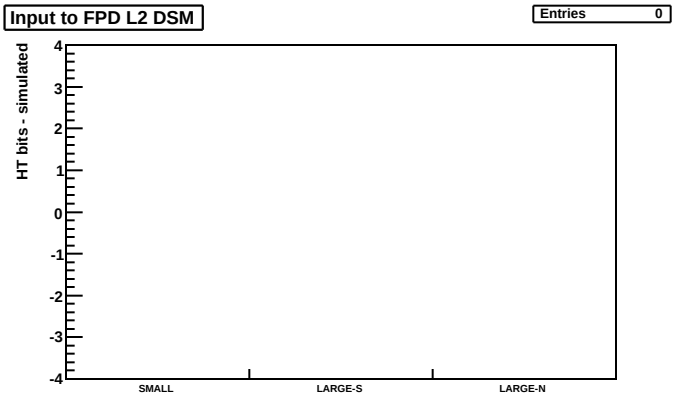

Input to FPD L2 DSM
Entries 214528
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

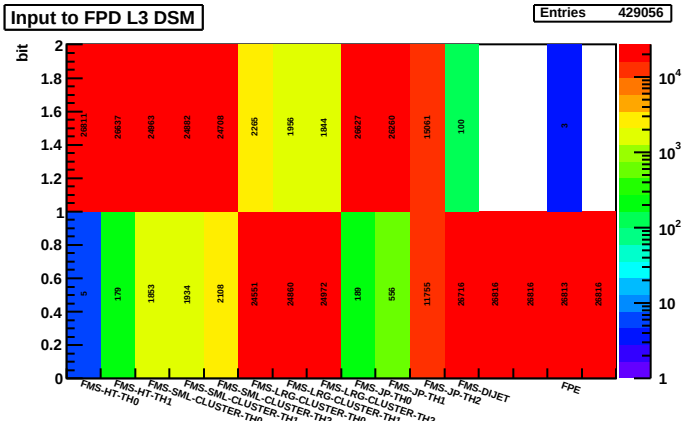

Input to FPD L2 DSM
Entries 713
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

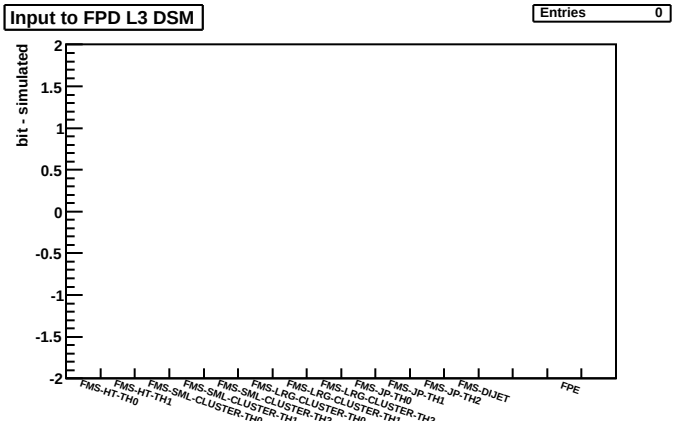

Input to FPD L2 DSM
Entries 80448
CL bits
24708
712
1146
174
30
86
81
92
236
1853
25982
25348
SMALL
LARGE-S
LARGE-N

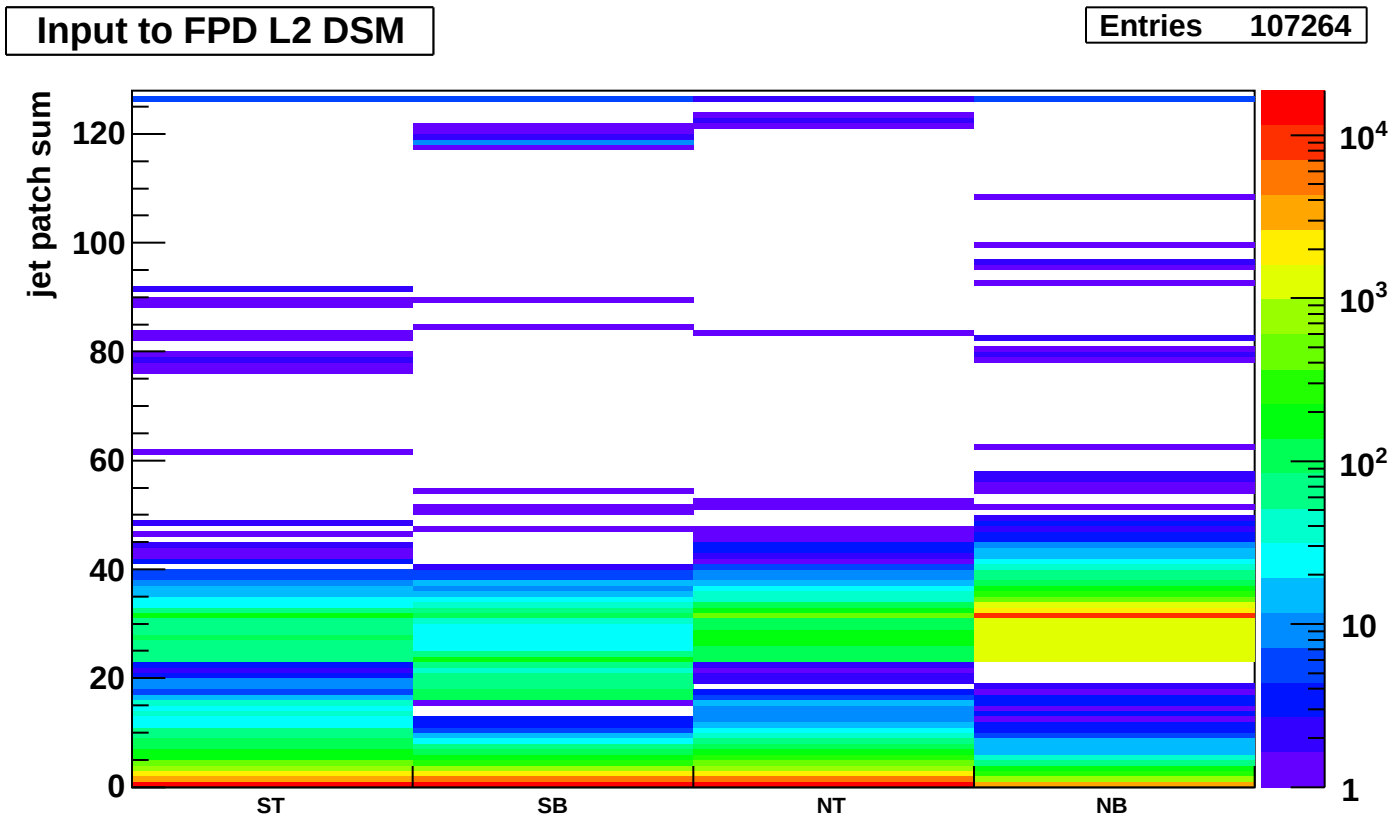

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

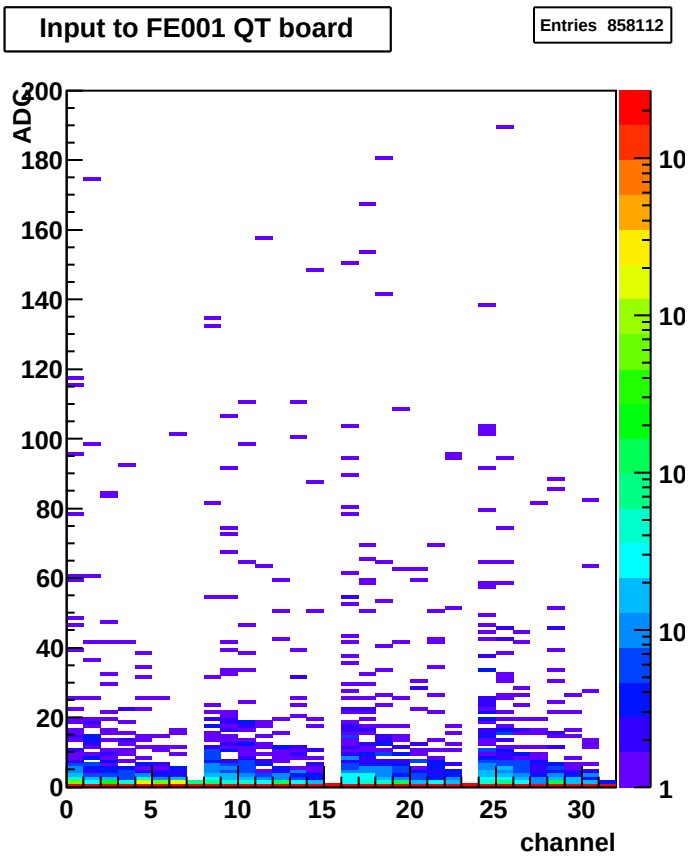

Input to FPD L2 DSM
Entries 80448
HT bits
24740
745
1251
190
102
239
1886
25969
25326
SMALL
LARGE-S
LARGE-N

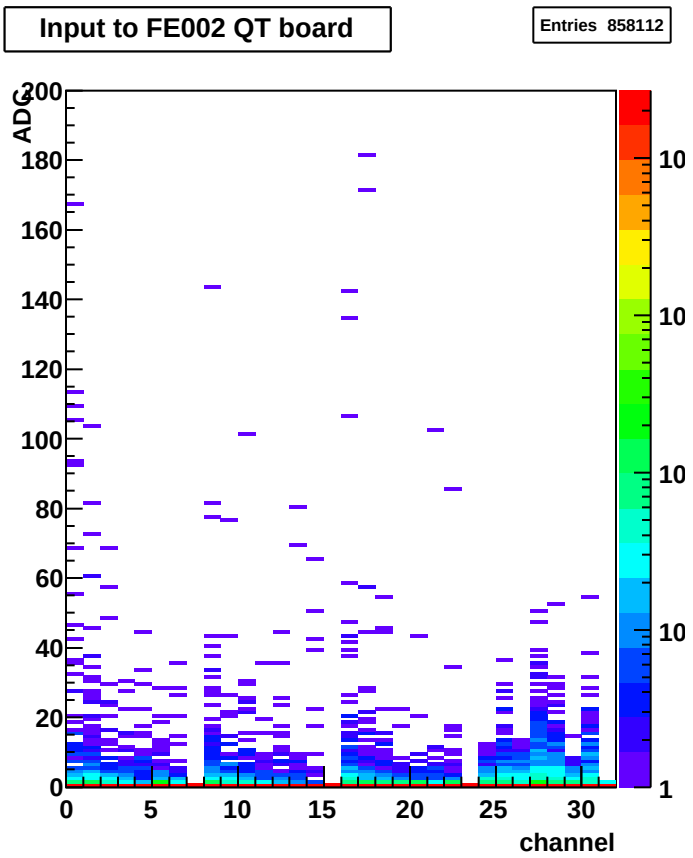

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

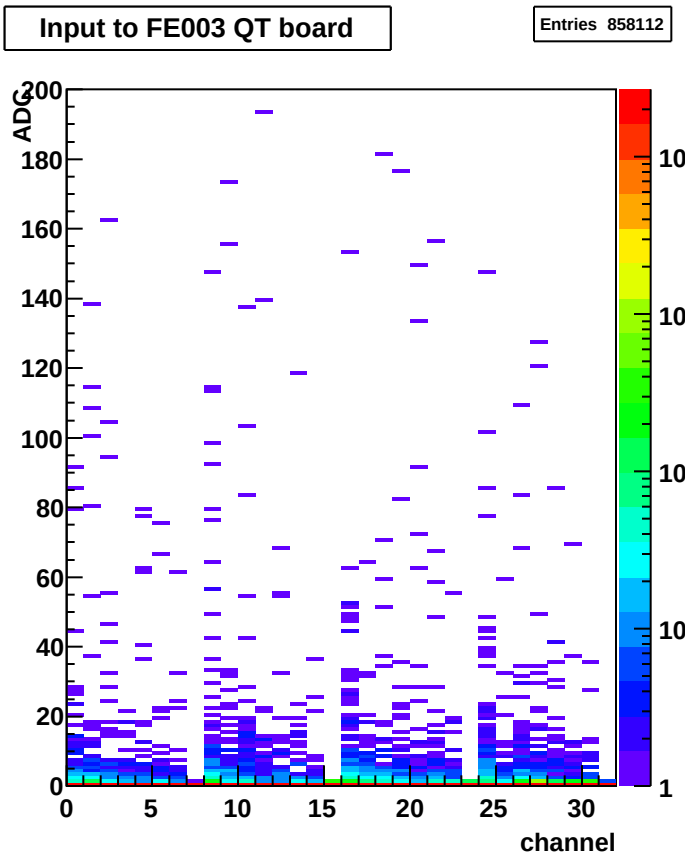

Input to FPD L3 DSM
Entries 429056
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

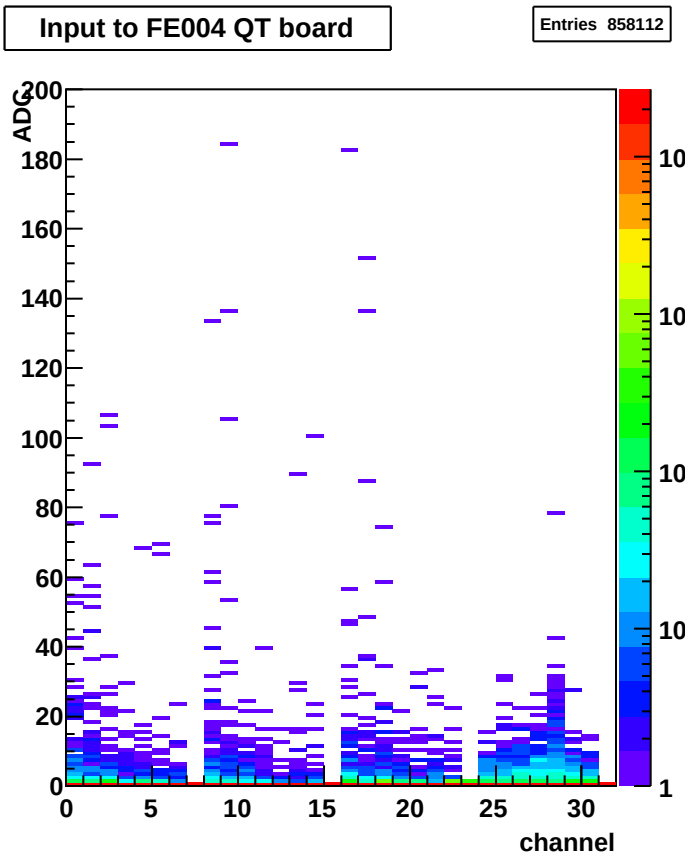

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

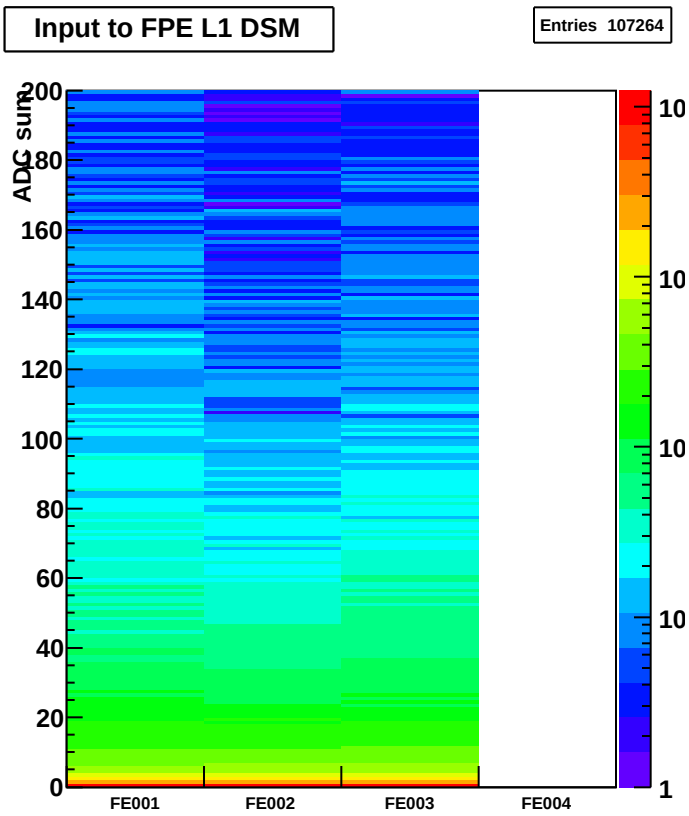

Input to FPD L2 DSM
Entries 107264
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$

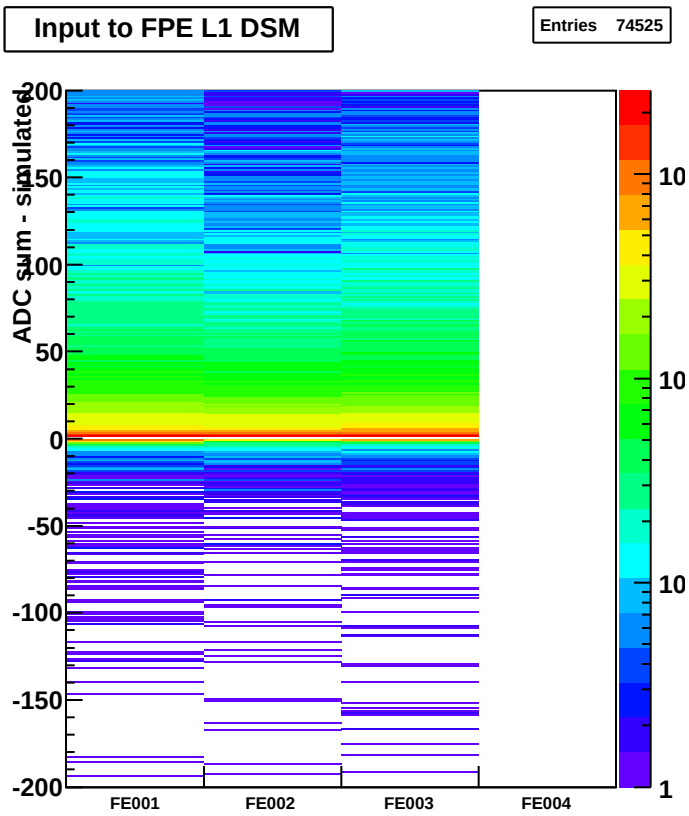

Input to FE001 QT board
Entries 858112
ADC
channel

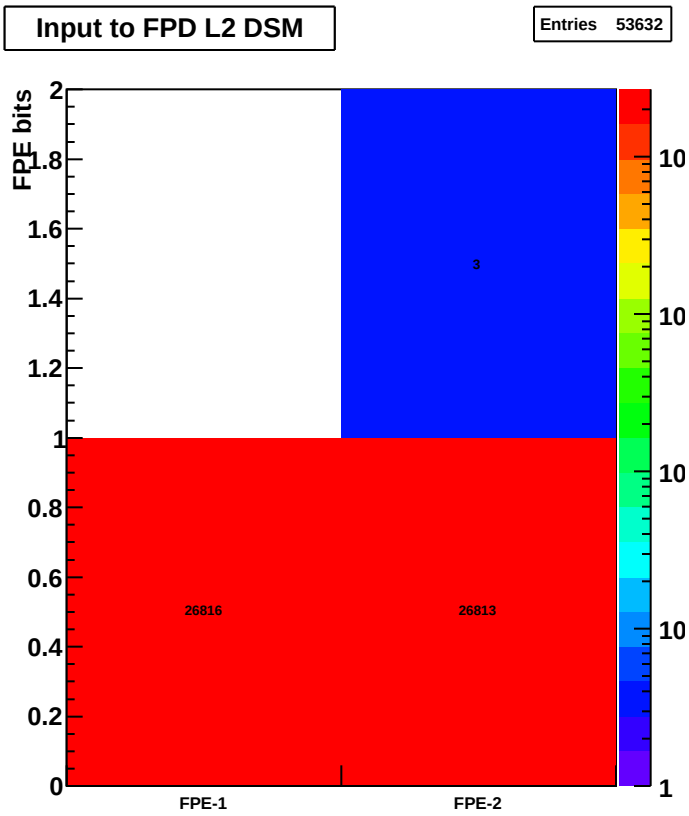

Input to FE002 QT board
Entries 858112
ADC
channel

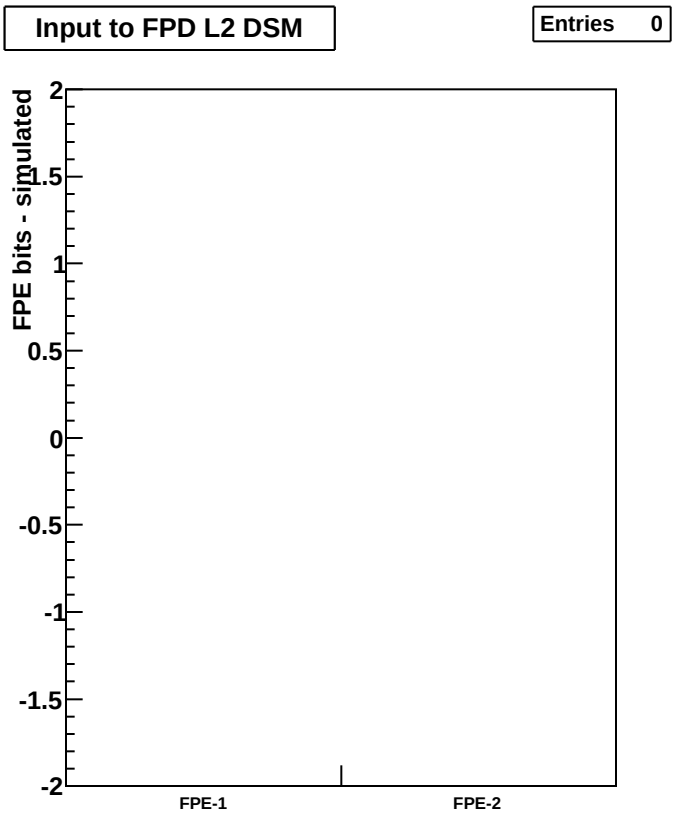

Input to FE003 QT board
Entries 858112
ADC
channel


Input to FE004 QT board
Entries 858112
ADC
channel


Input to FPE L1 DSM
Entries 107264
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 74525
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 53632
FPE bits
3
26816
26813
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2