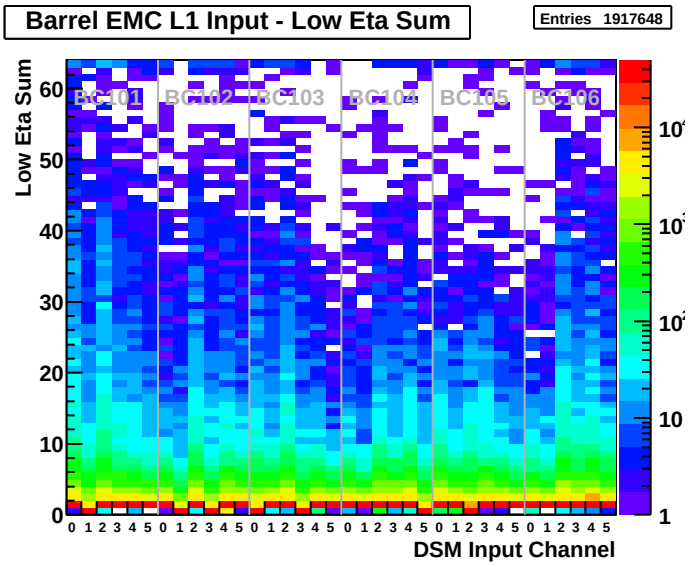
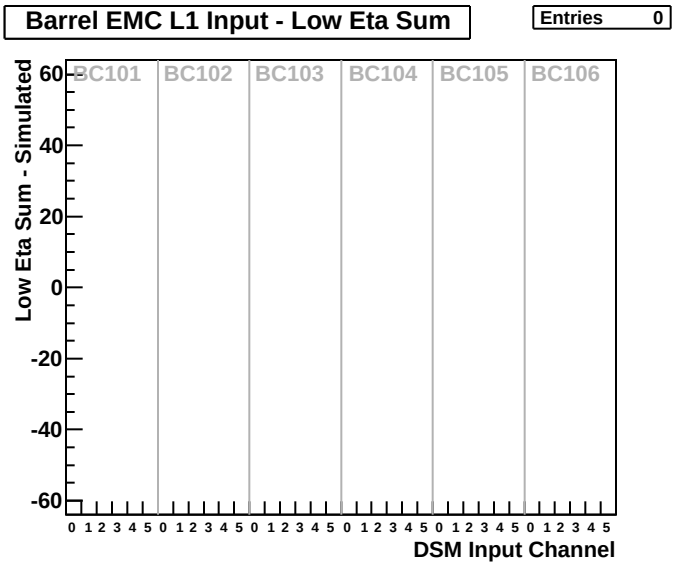
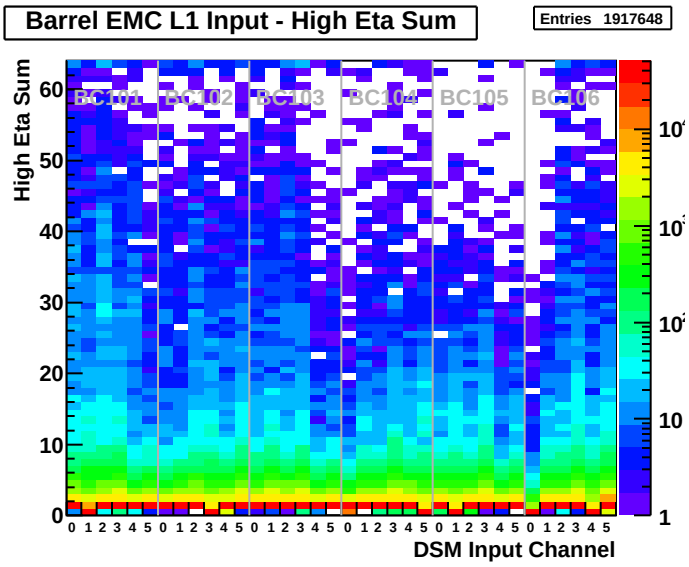
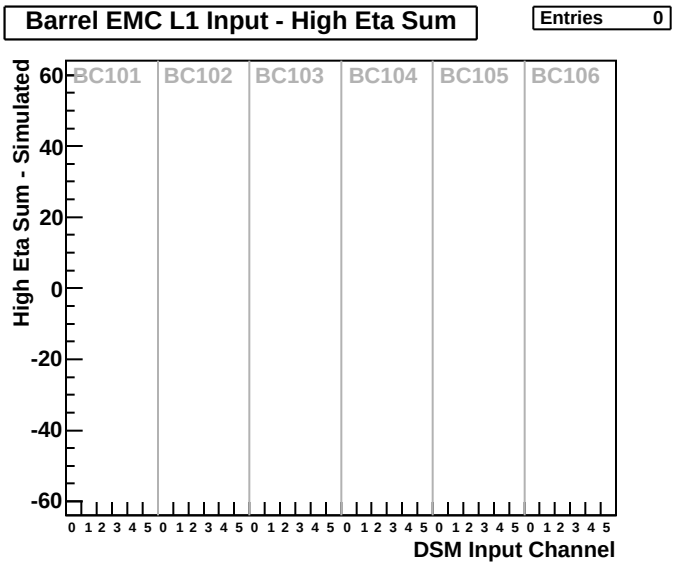
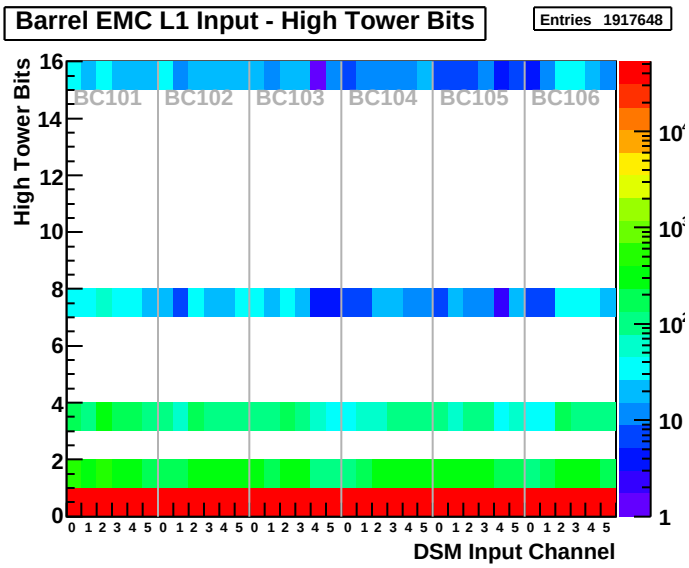
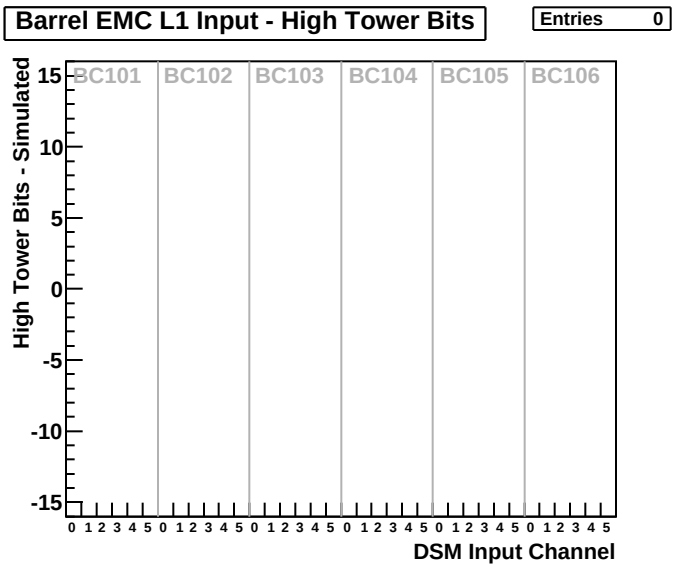

Barrel EMC L1 Input - Low Eta Sum
Entries 1917648
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

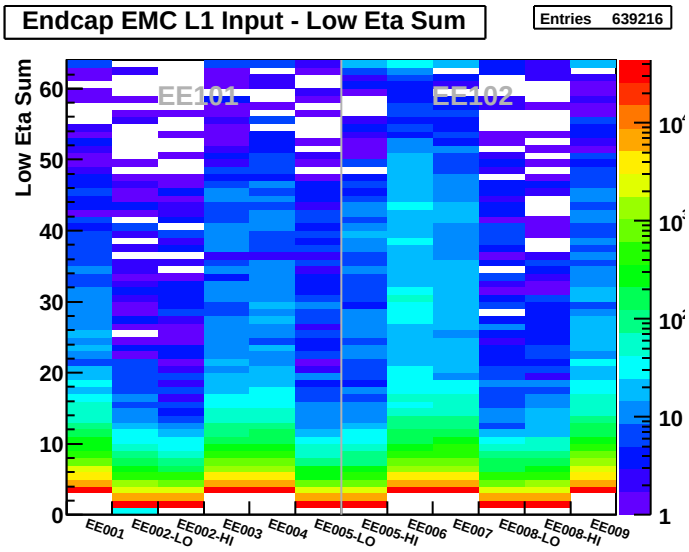

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

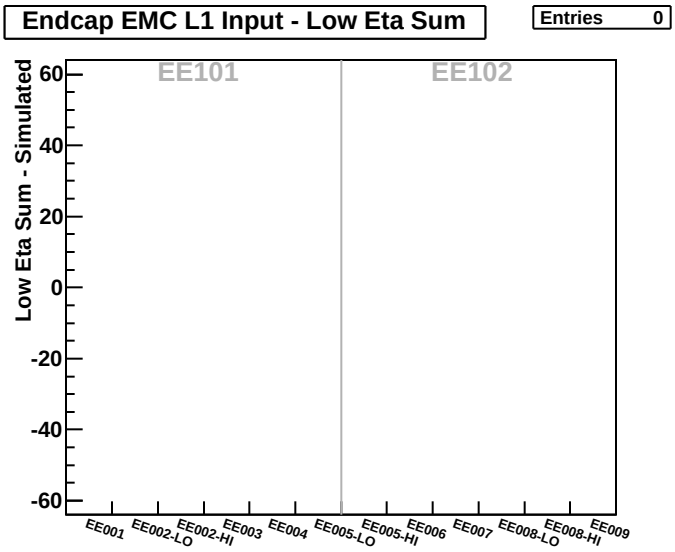

Barrel EMC L1 Input - High Eta Sum
Entries 1917648
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

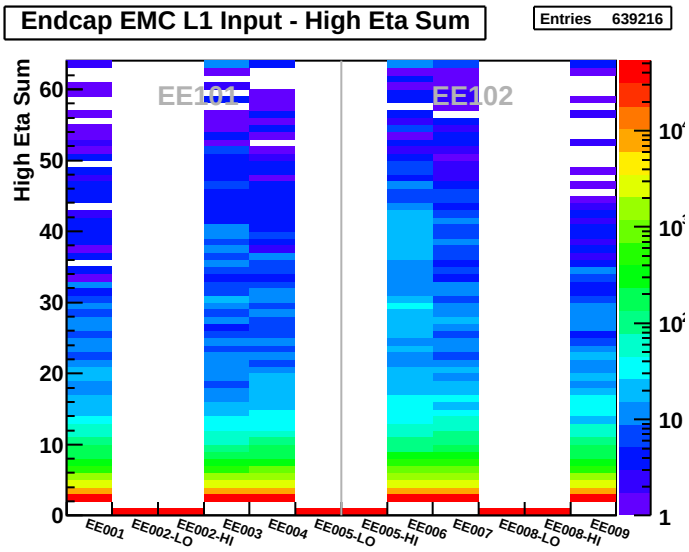

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

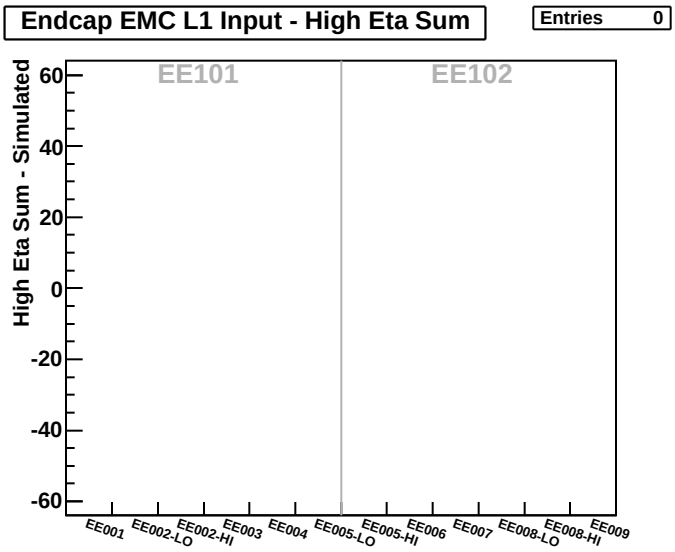

Barrel EMC L1 Input - High Tower Bits
Entries 1917648
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

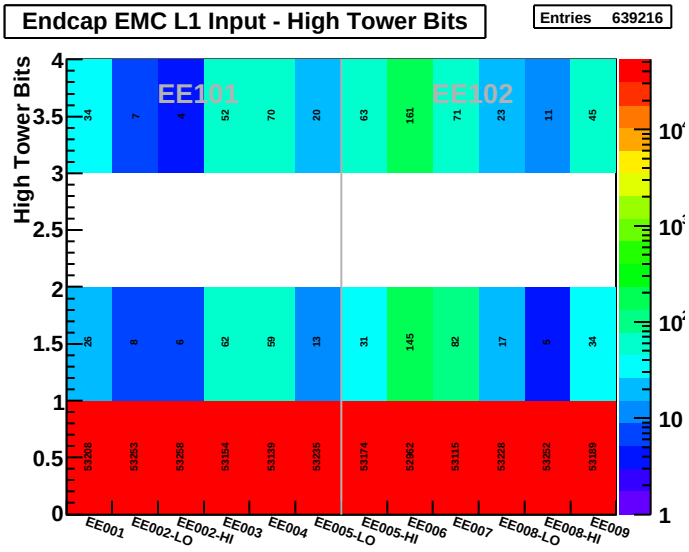

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

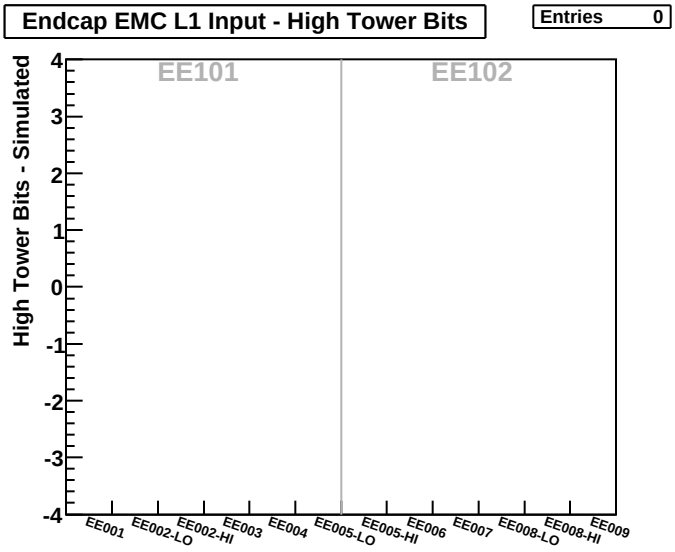

Endcap EMC L1 Input - Low Eta Sum
Entries 639216
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

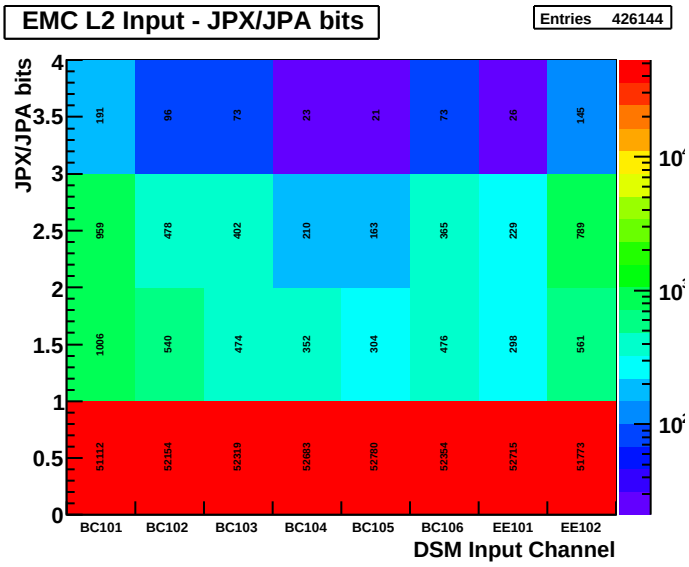

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

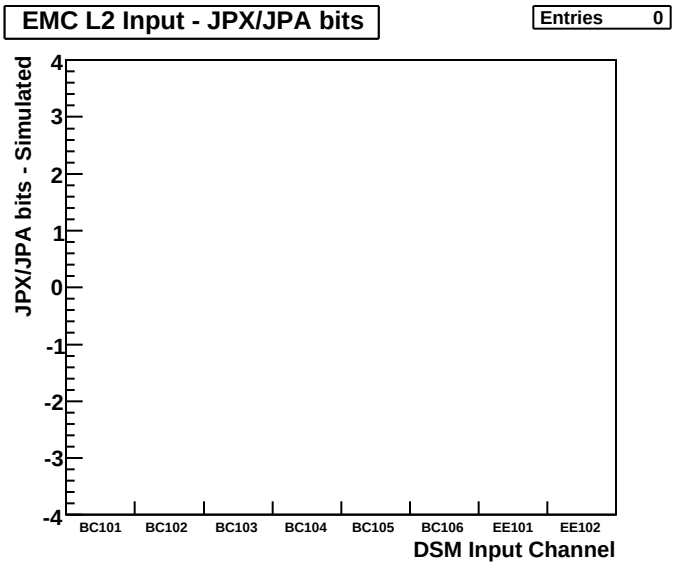

Endcap EMC L1 Input - High Eta Sum
Entries 639216
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

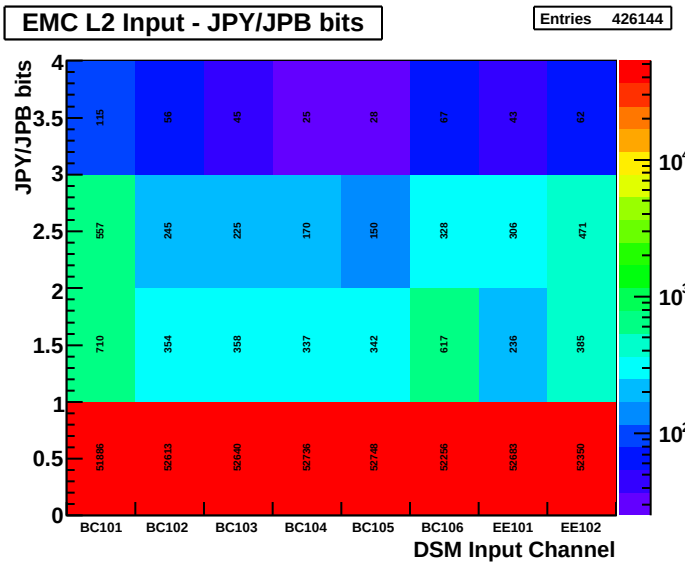

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

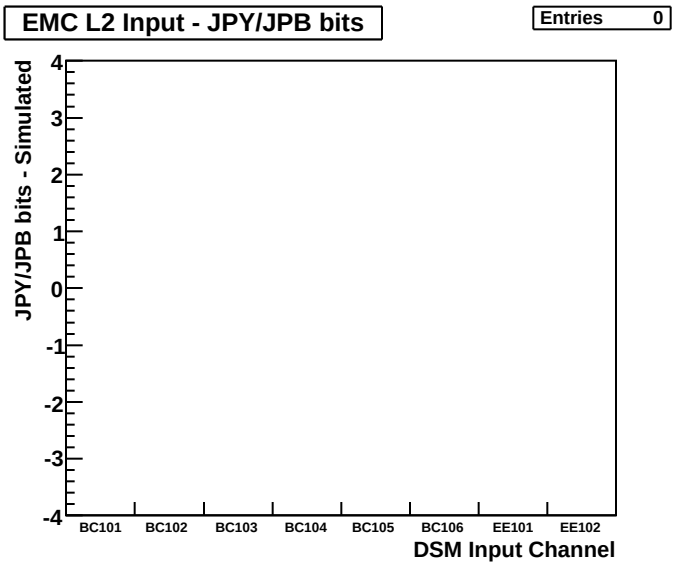

Endcap EMC L1 Input - High Tower Bits
Entries 639216
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

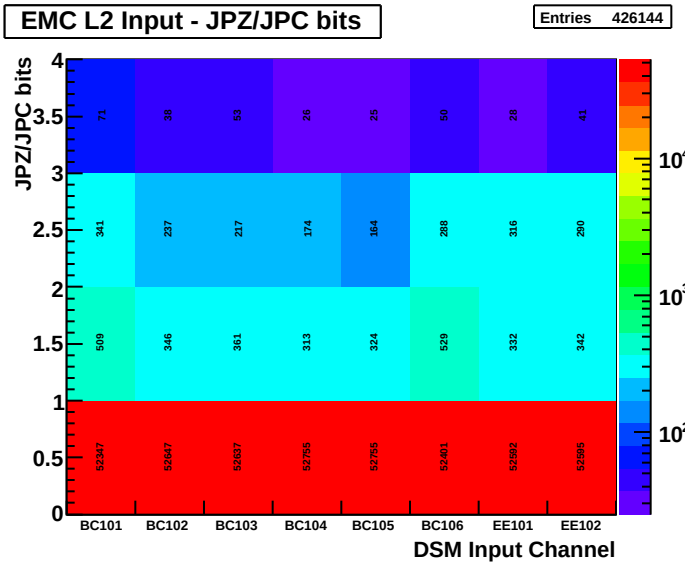

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

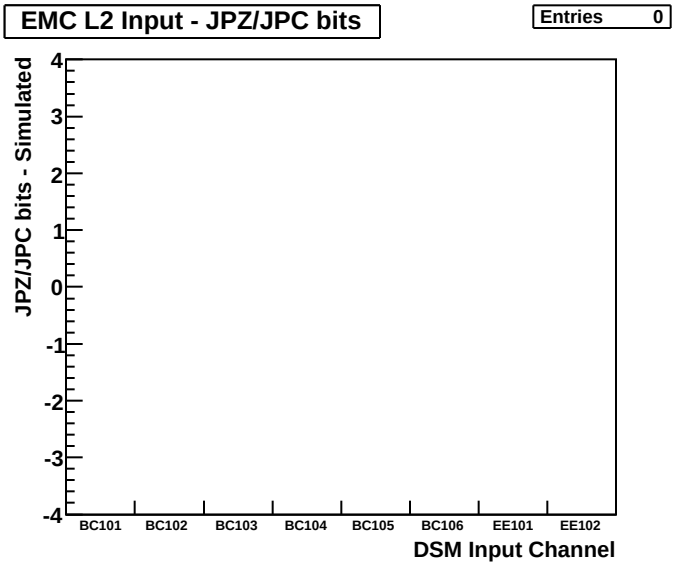

EMC L2 Input - JPX/JPA bits
Entries 426144
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
191 96 73 23 21 73 26 145
959 478 402 210 183 365 229 789
1006 540 474 352 304 476 298 561
51112 52154 52319 52683 52780 52354 52715 51773

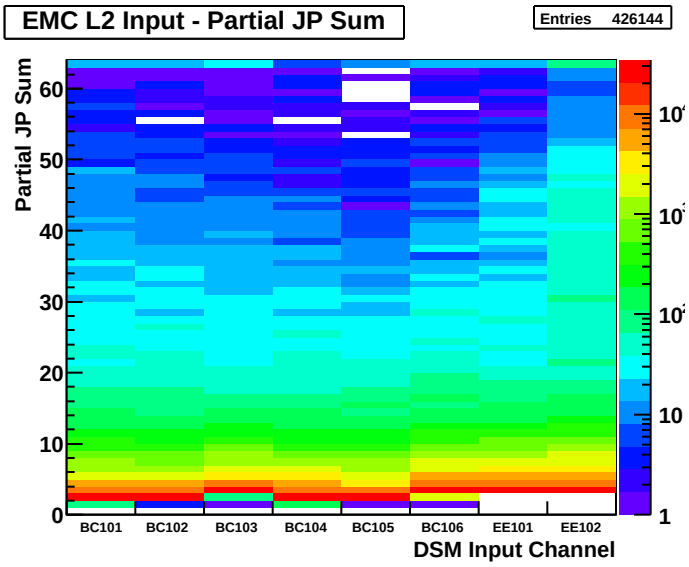

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

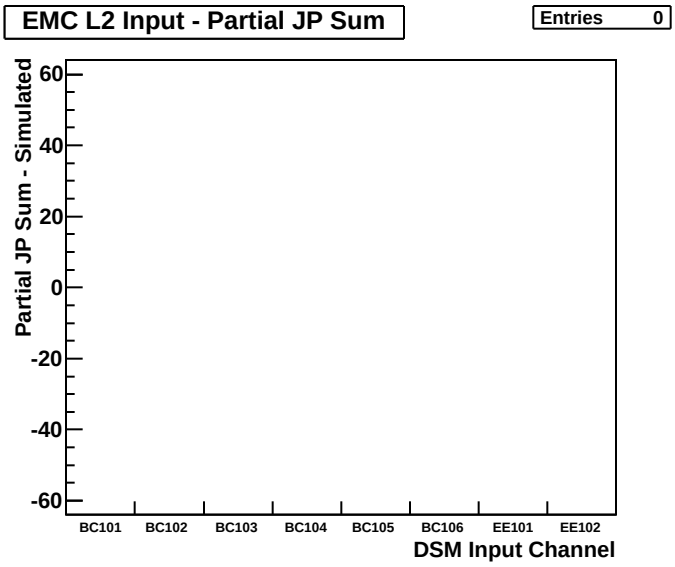

EMC L2 Input - JPY/JPB bits
Entries 426144
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
115 56 45 25 28 67 43 62
557 245 225 170 150 328 306 471
710 354 358 337 342 617 236 385
51886 52613 52640 52736 52748 52256 52683 52350

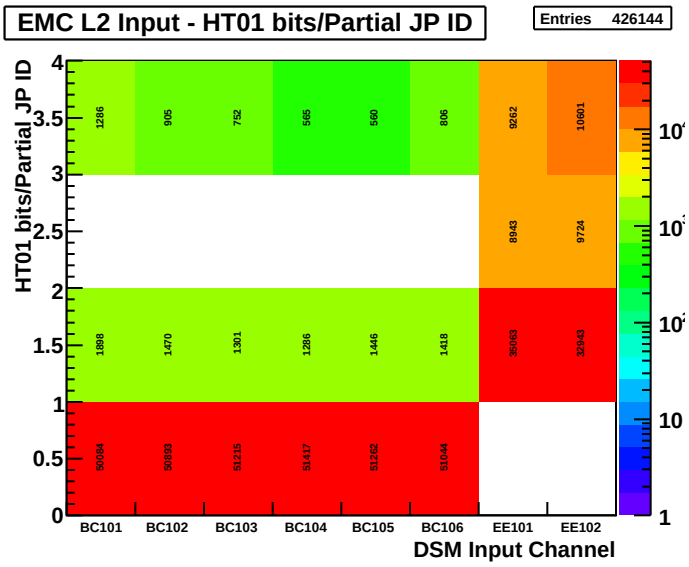

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

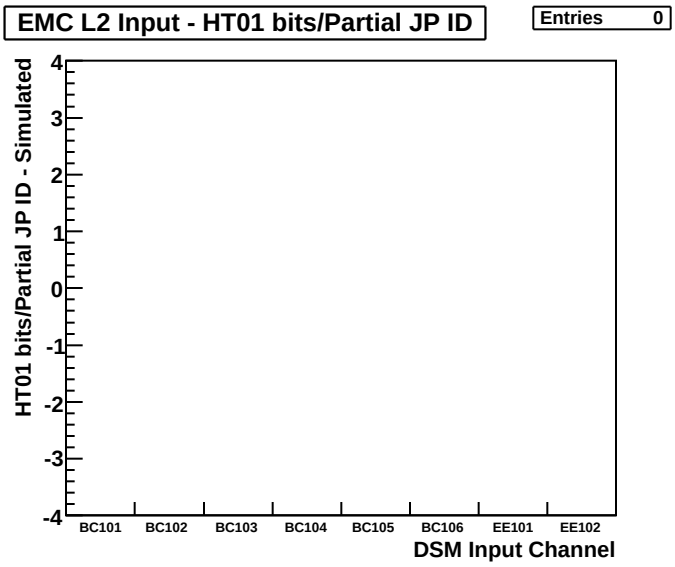

EMC L2 Input - JPZ/JPC bits
Entries 426144
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
71 38 53 26 25 50 28 41
341 237 217 174 164 288 316 290
509 346 361 313 324 529 332 342
52347 52647 52637 52755 52755 52401 52592 52595

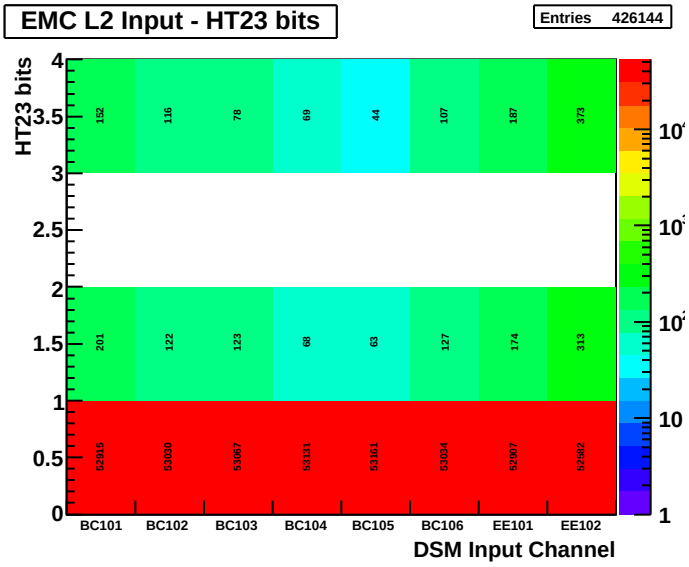

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

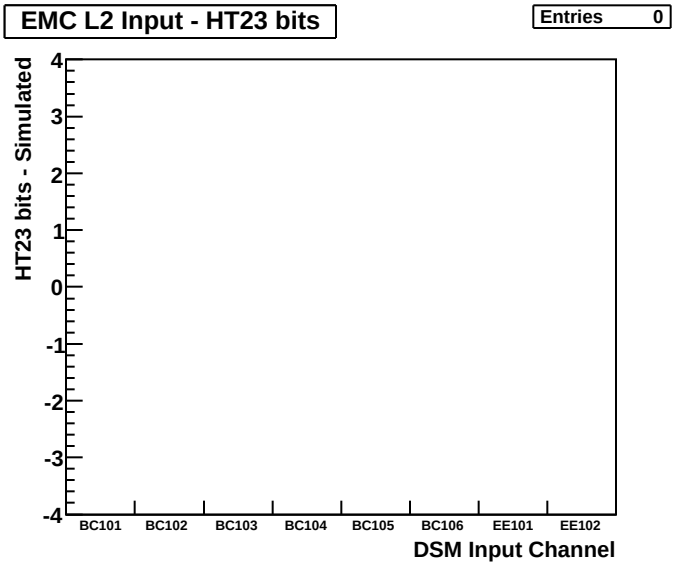

EMC L2 Input - Partial JP Sum
Entries 426144
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

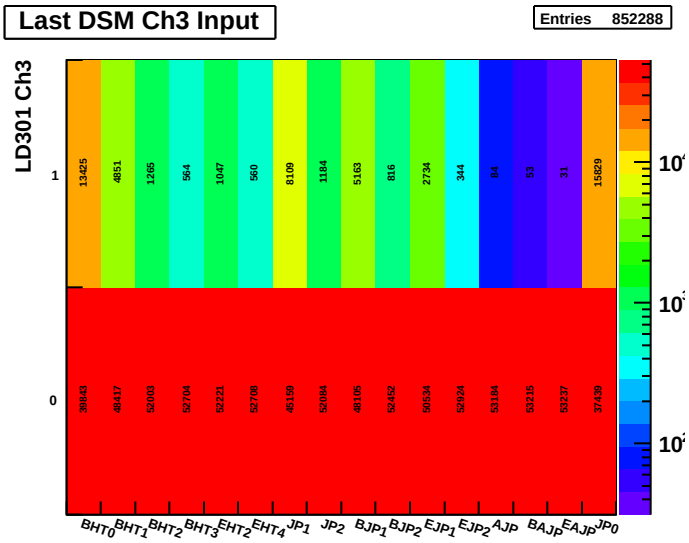

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

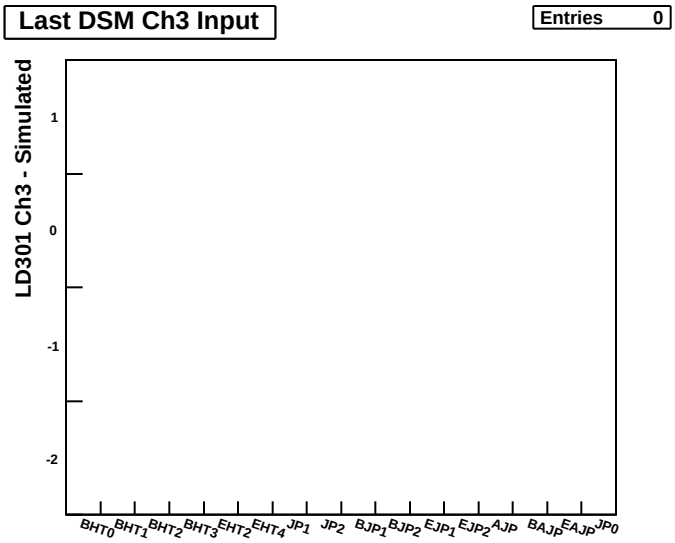

EMC L2 Input - HT01 bits/Partial JP ID
Entries 426144
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

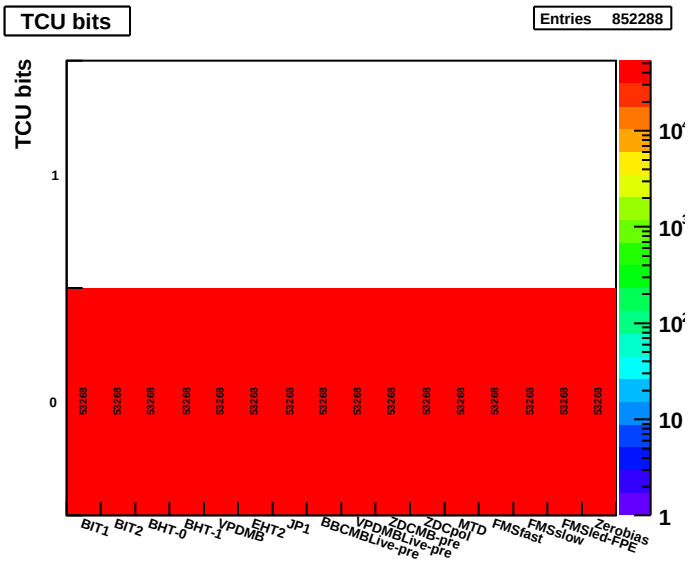

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

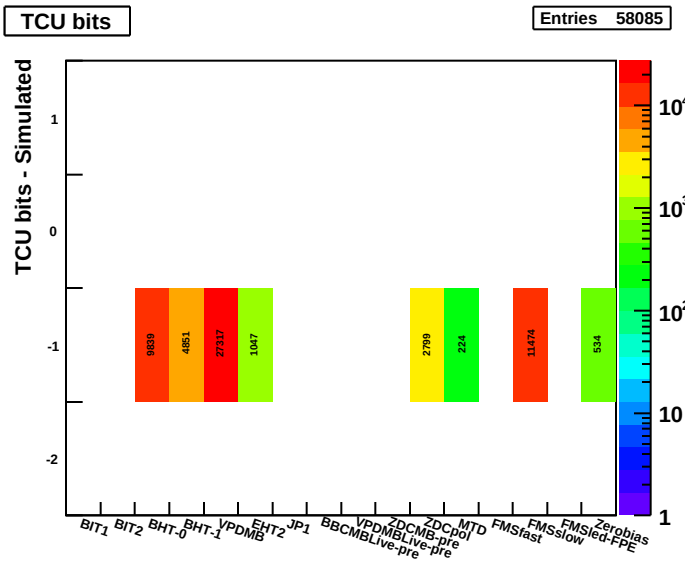

EMC L2 Input - HT23 bits
Entries 426144
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

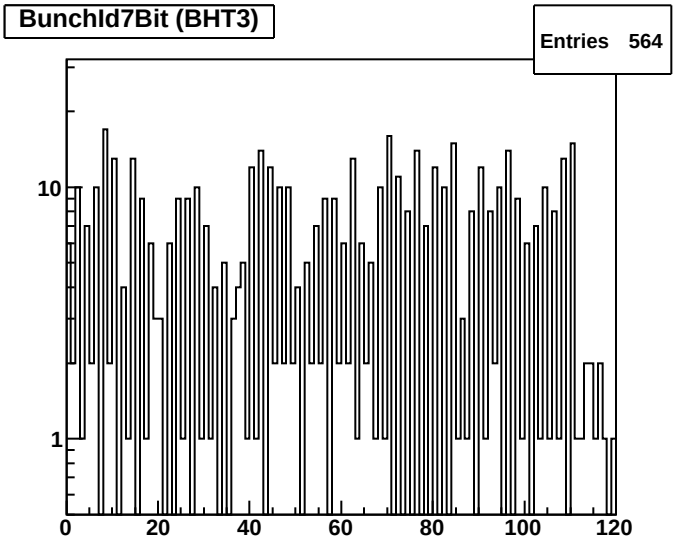

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

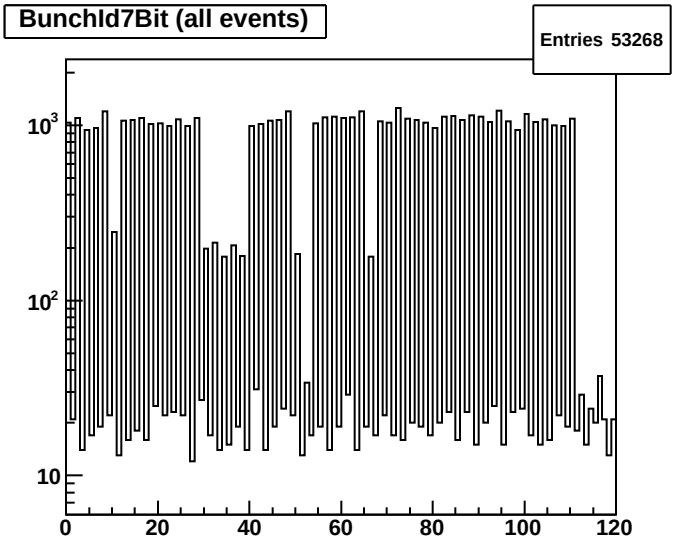

Last DSM Ch3 Input
Entries 852288
LD301 Ch3
13425 4851 1265 564 1047 560 8109 1184 5163 816 2734 344 84 53 31 15829
39843 49417 52003 52704 52221 52708 45159 52084 49105 52452 50534 52924 53184 53215 53237 37439
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

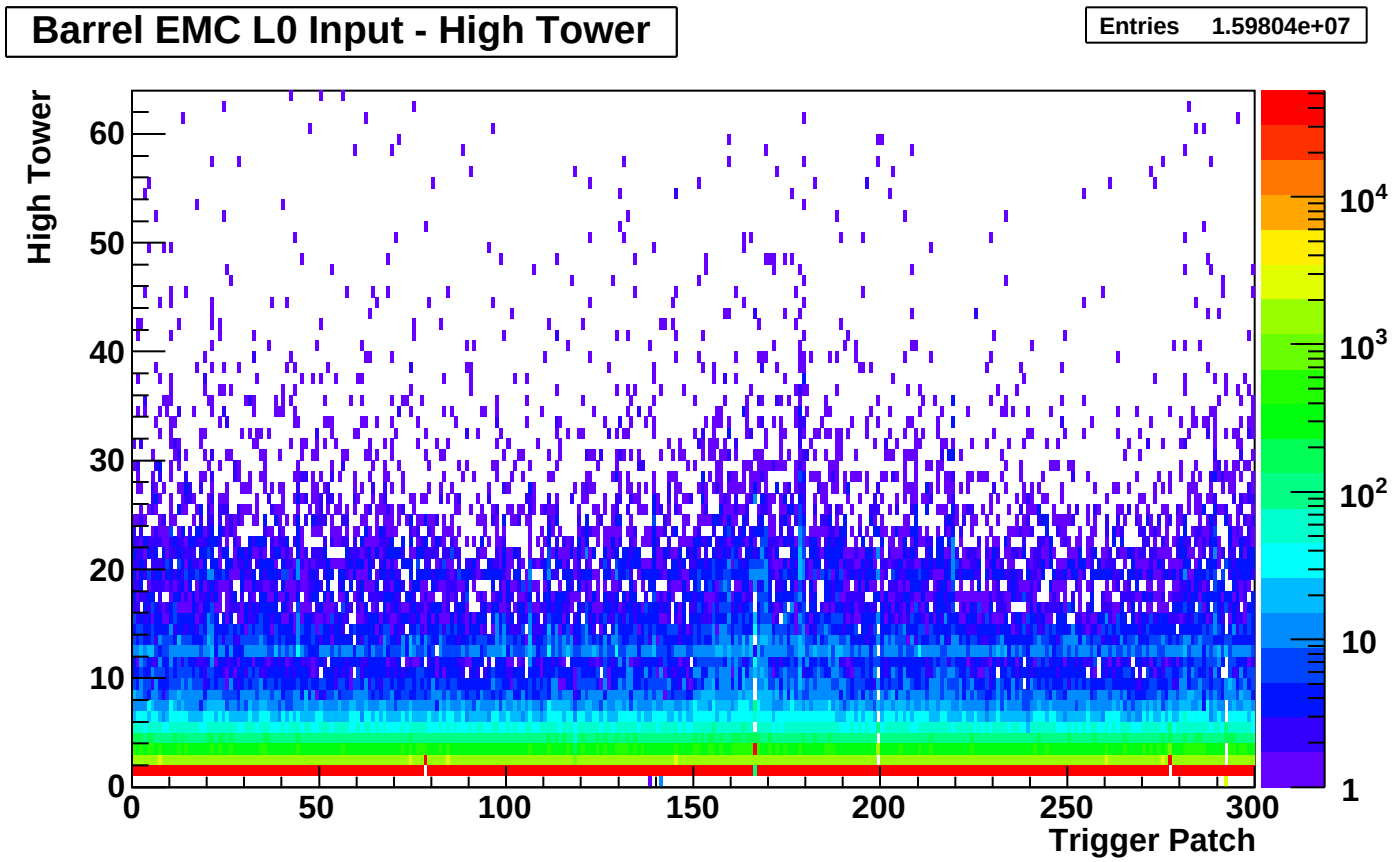

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

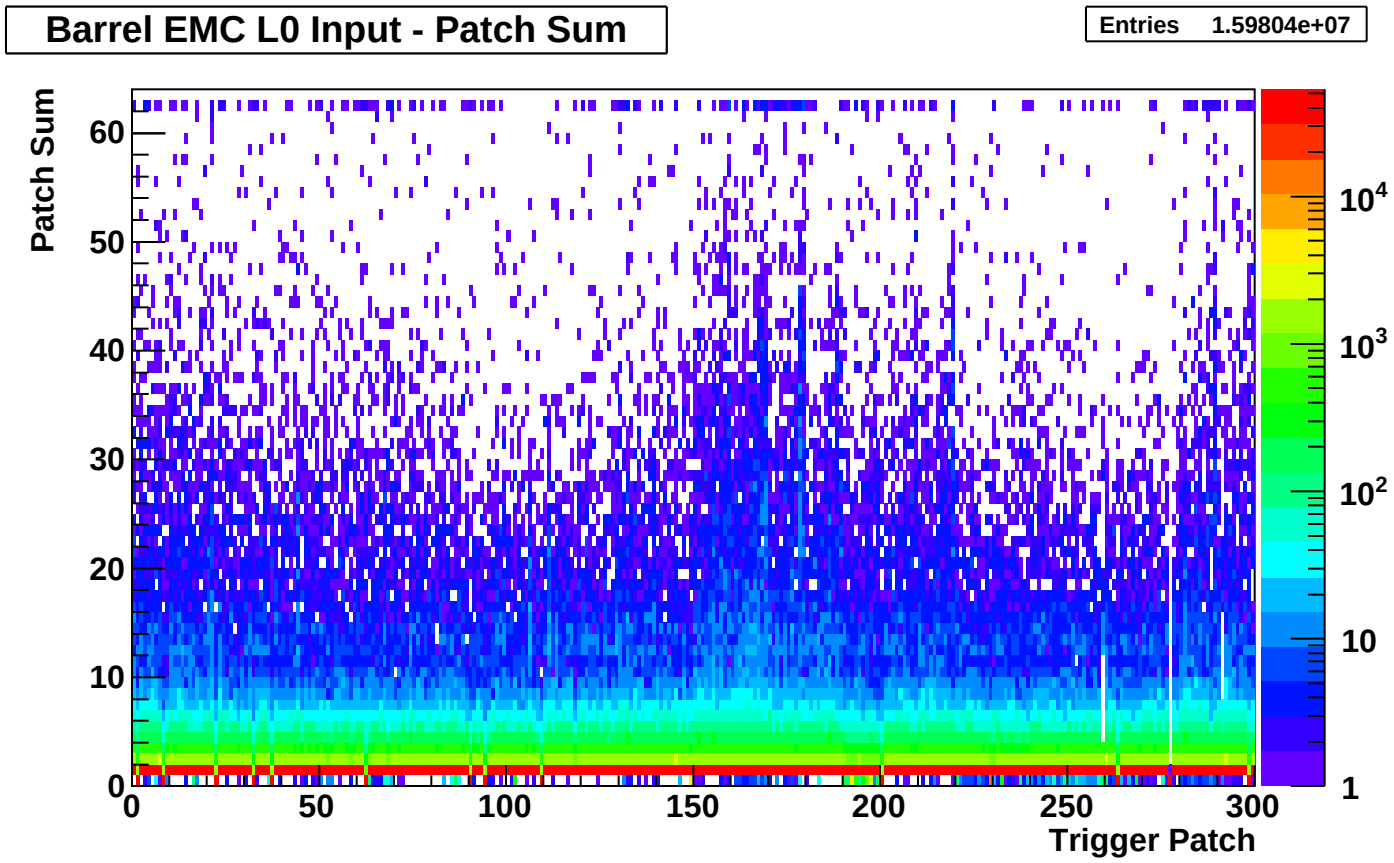

TCU bits
Entries 852288
TCU bits
53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268 53268
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

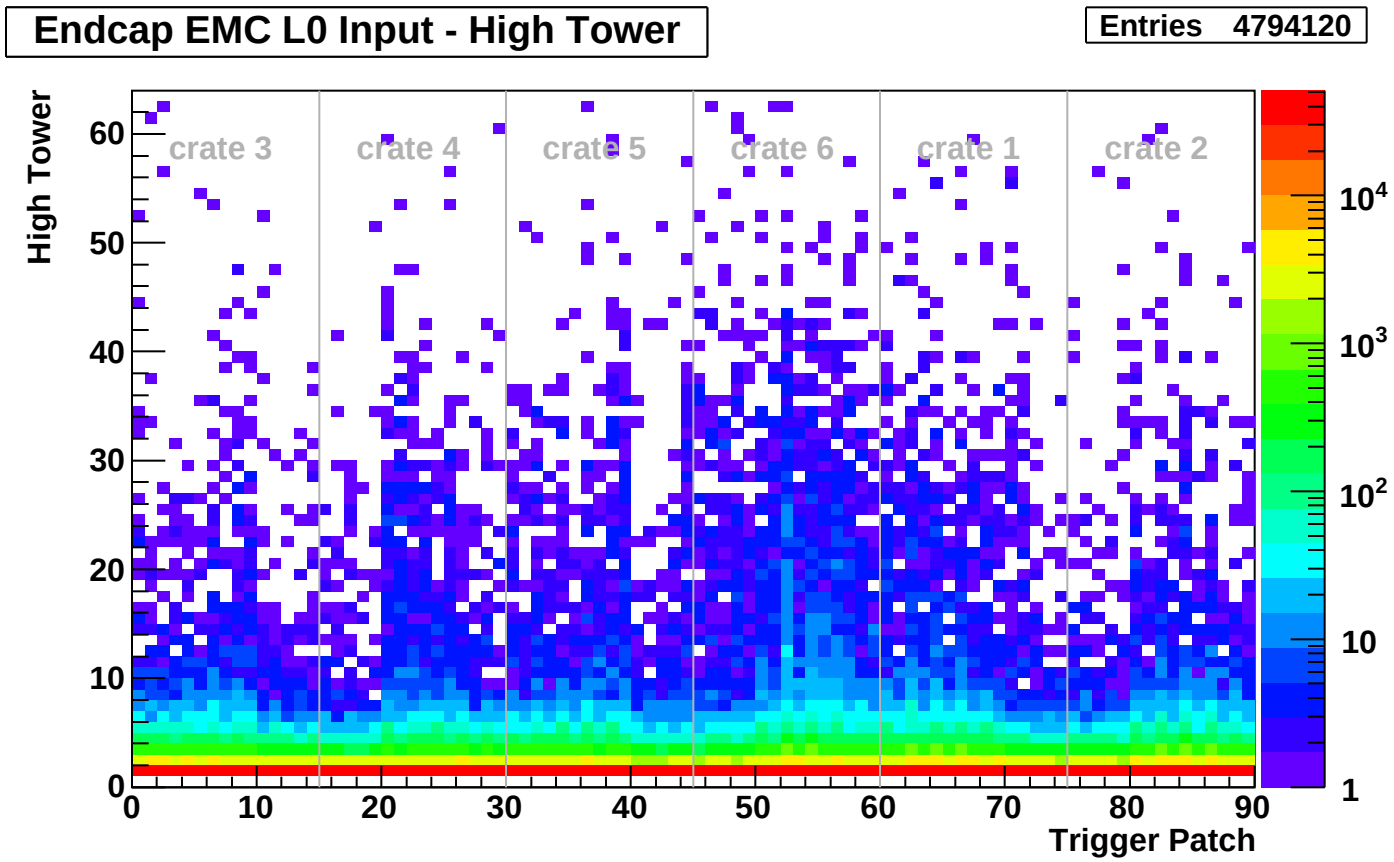

TCU bits
Entries 58085
TCU bits - Simulated
9839 4851 27317 1047 2799 224 11474 534
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

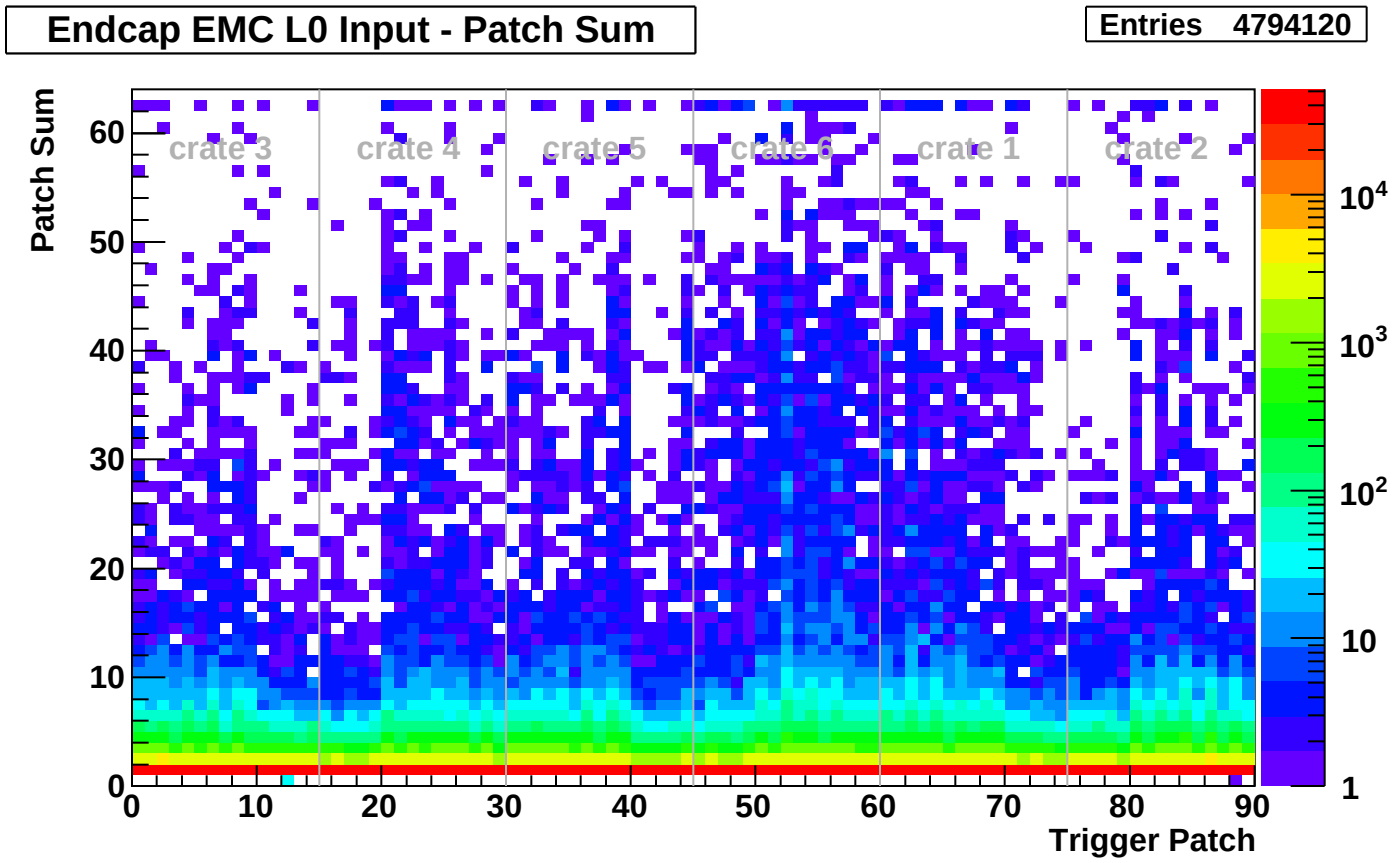

BunchId7Bit (BHT3)
Entries 564

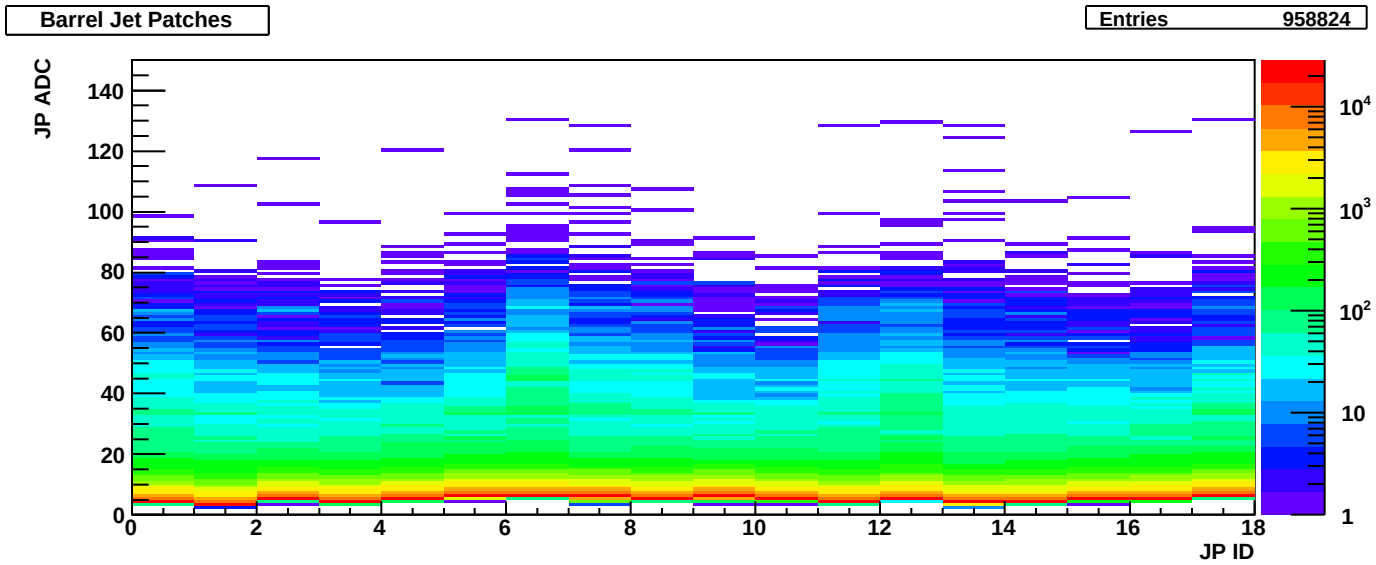

BunchId7Bit (all events)
Entries 53268

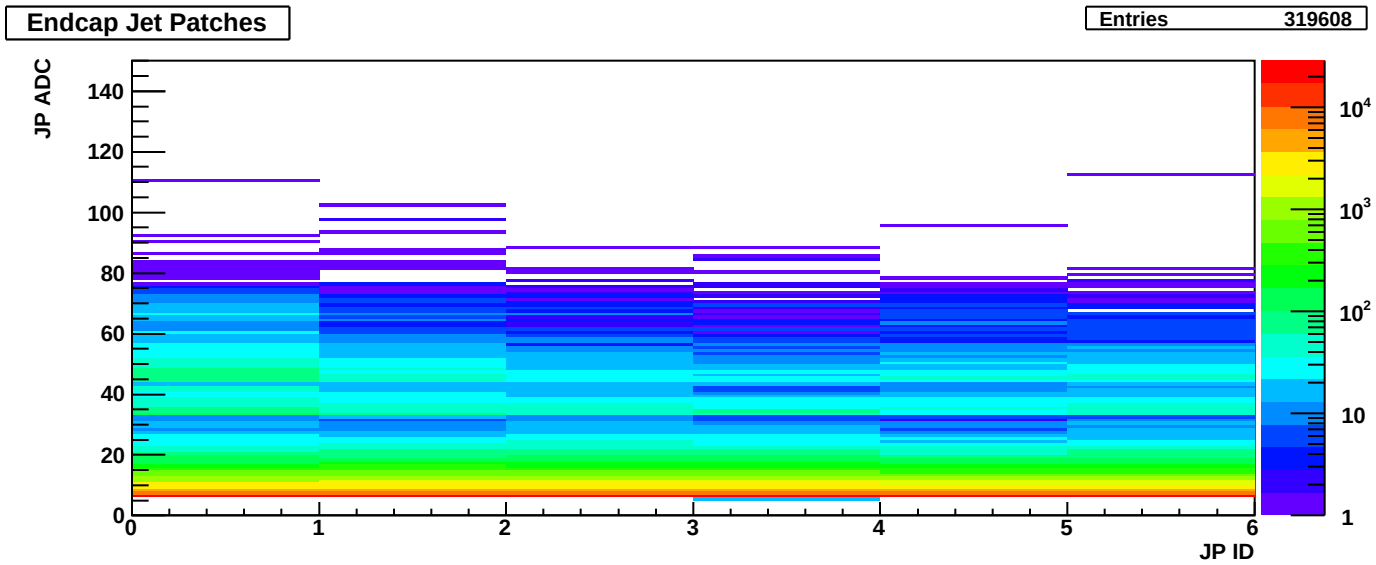

Barrel EMC L0 Input - High Tower
Entries 1.59804e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

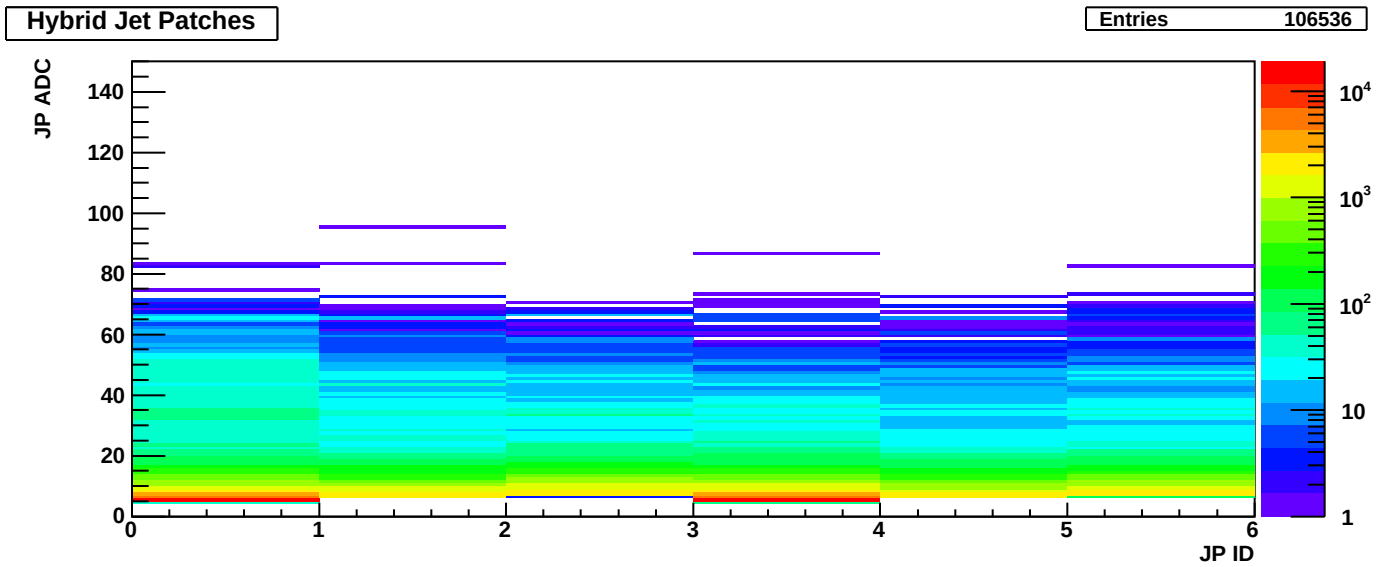

Barrel EMC L0 Input - Patch Sum
Entries 1.59804e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4


Endcap EMC L0 Input - High Tower
Entries 4794120
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

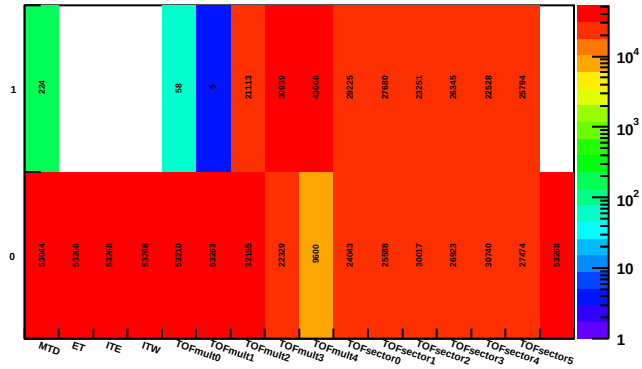

Endcap EMC L0 Input - Patch Sum
Entries 4794120
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 958824
JP ADC
JP ID

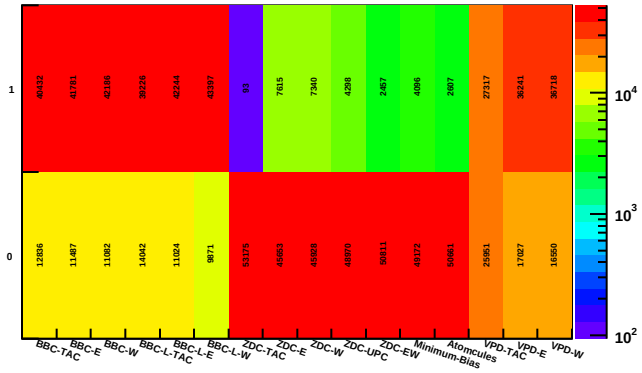

Endcap Jet Patches
Entries 319608
JP ADC
JP ID


Hybrid Jet Patches
Entries 106536
JP ADC
JP ID

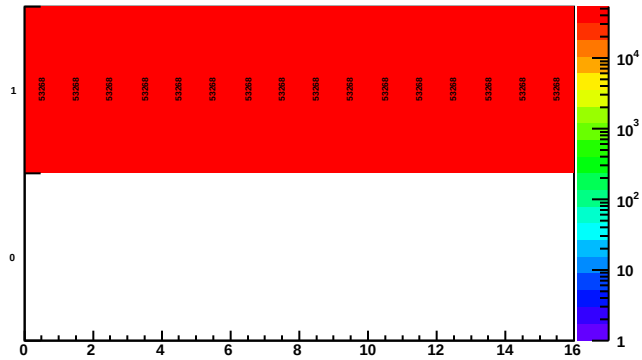
TF201 0-15 (ch0) Entries 852288



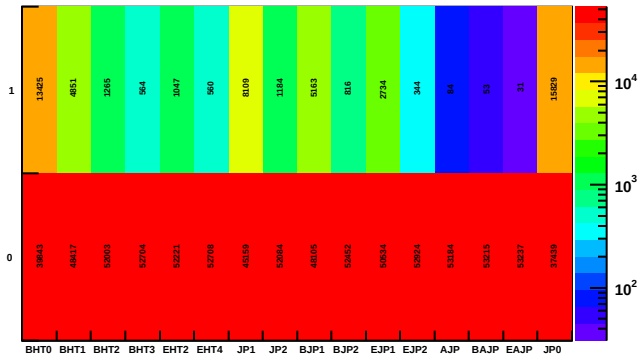
VT201 0-15 (ch1) Entries 852288



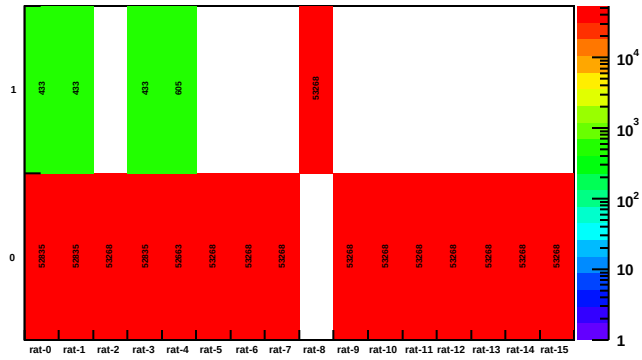
Unused (ch2) Entries 852288



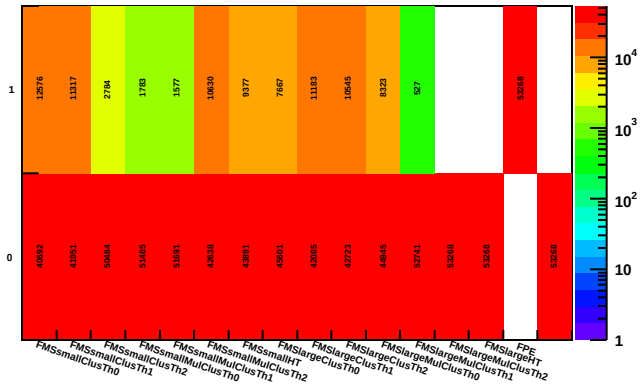
EM201 0-15 (ch3) Entries 852288



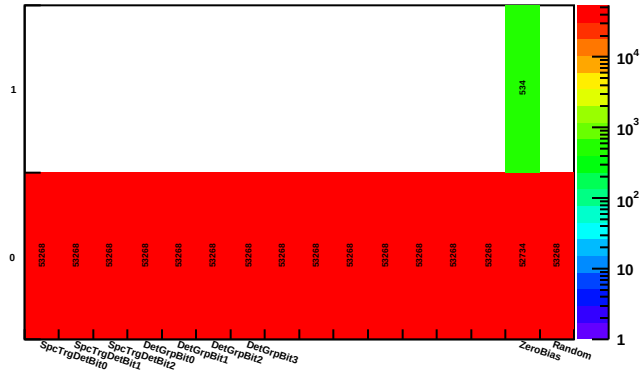
RAT board (ch4) Entries 852288



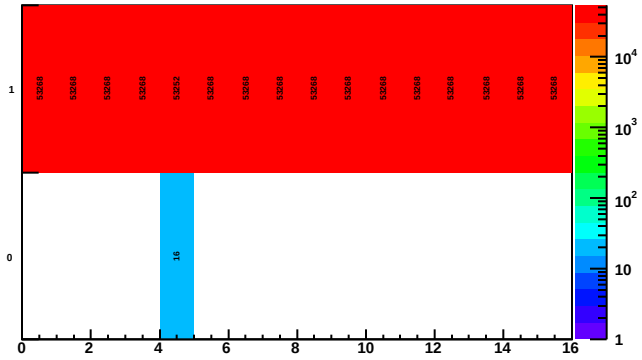
FP201 0-15 (ch5) Entries 852288

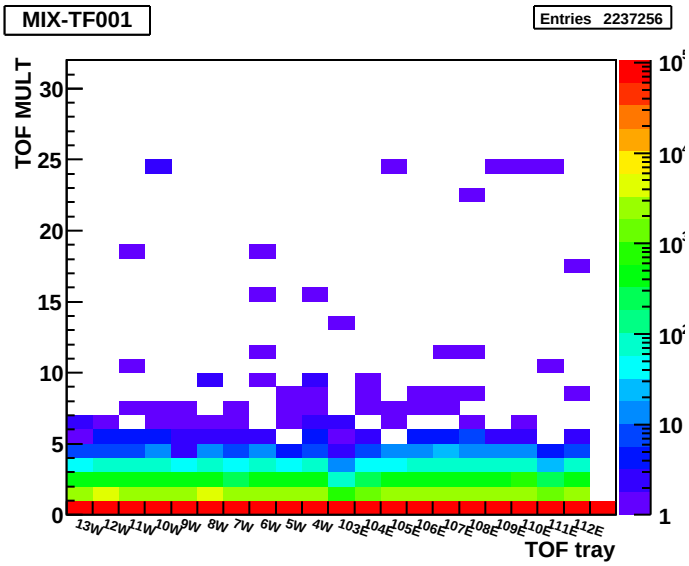


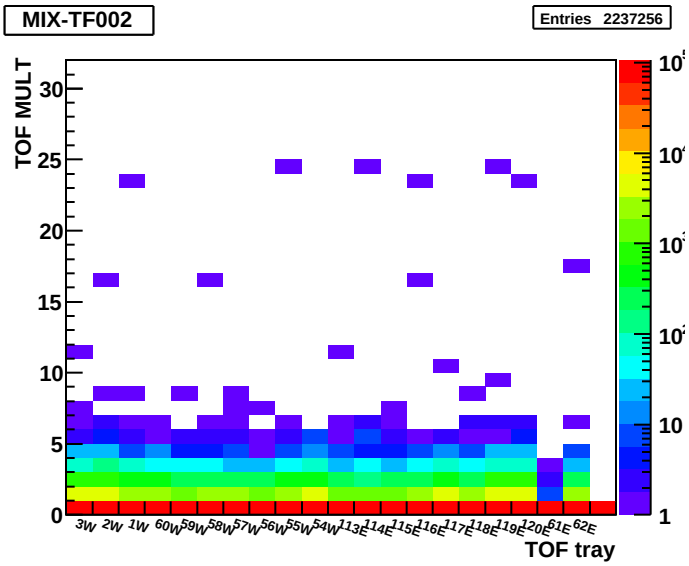
ST201 0-15 (ch6) Entries 852288

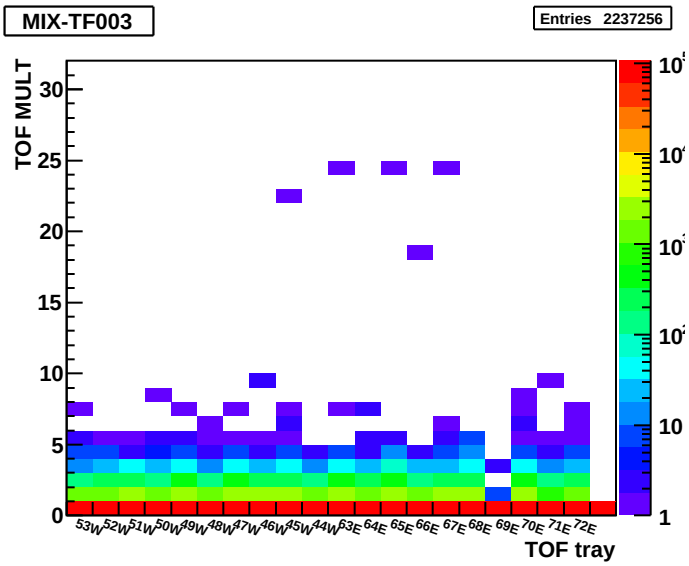


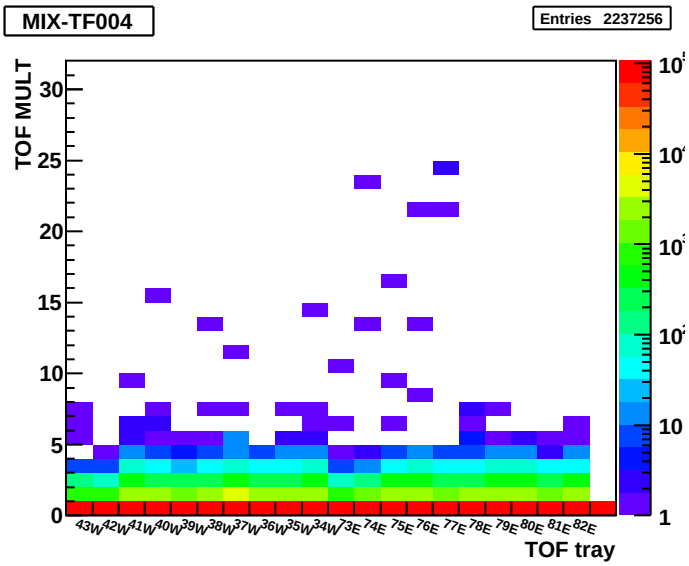
Unused (ch7) Entries 852288

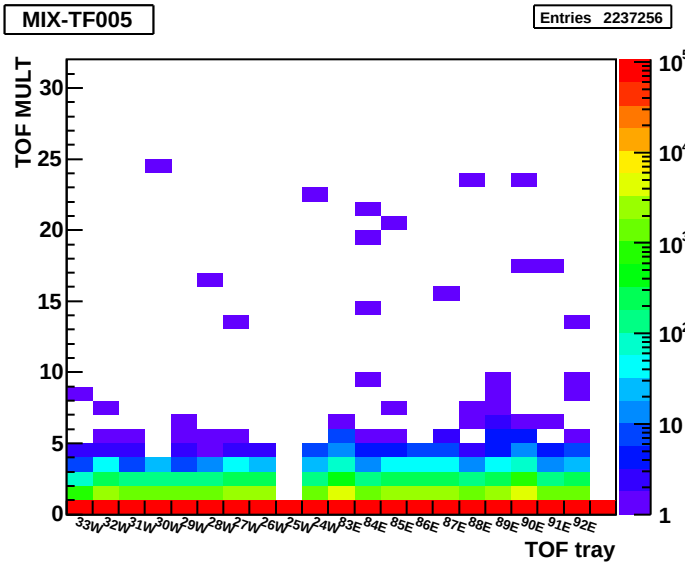


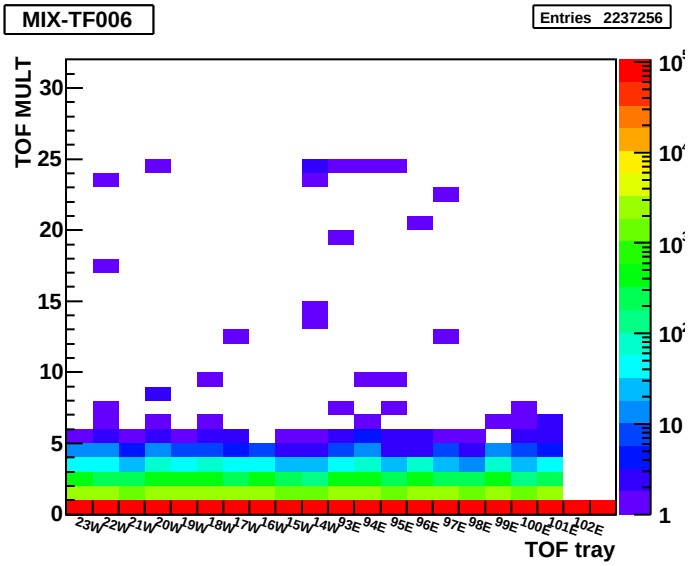

MIX-TF001
Entries 2237256
TOF MULT
TOF tray

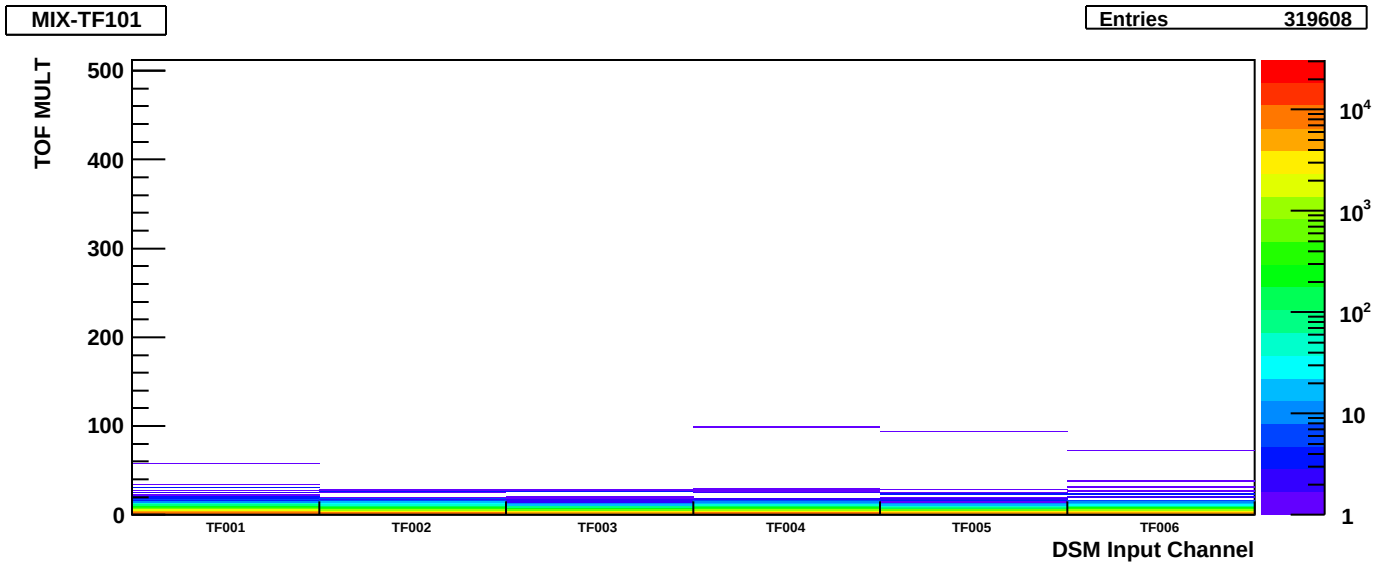

MIX-TF002
Entries 2237256
TOF MULT
TOF tray

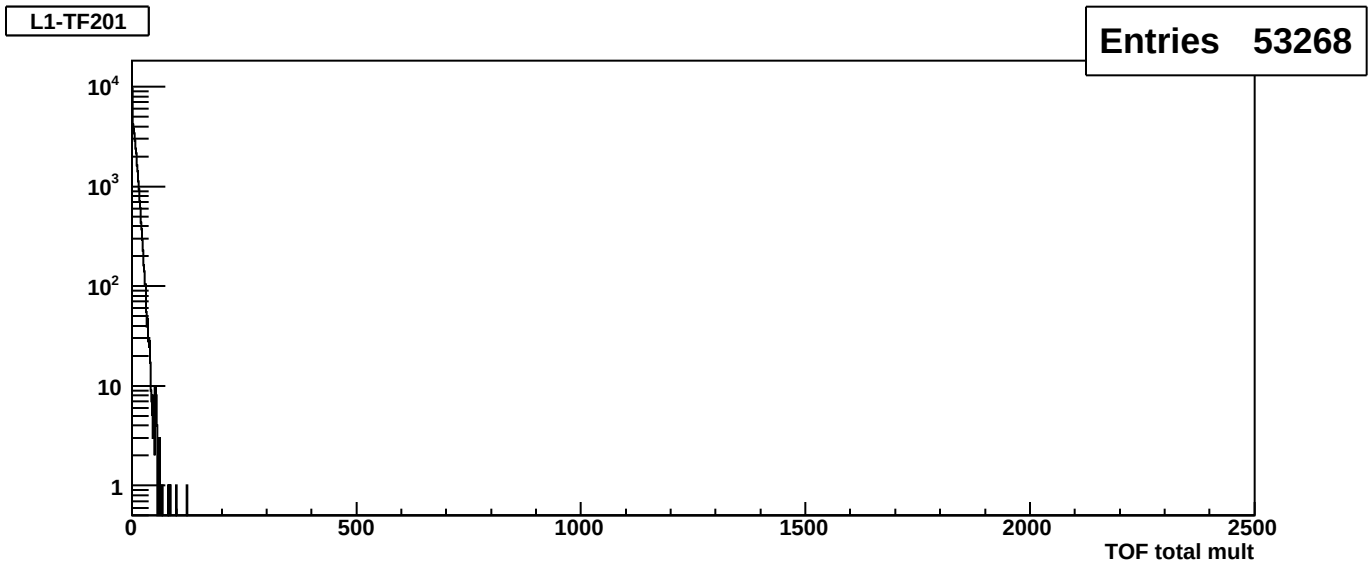

MIX-TF003
Entries 2237256
TOF MULT
TOF tray

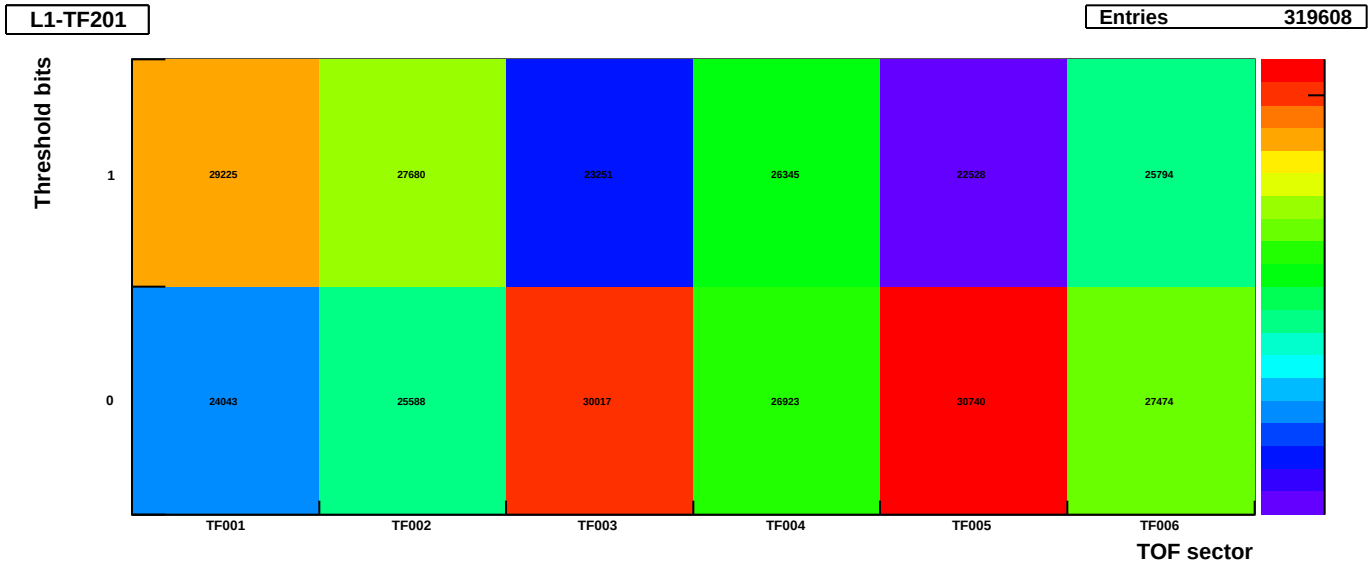

MIX-TF004
Entries 2237256
TOF MULT
TOF tray

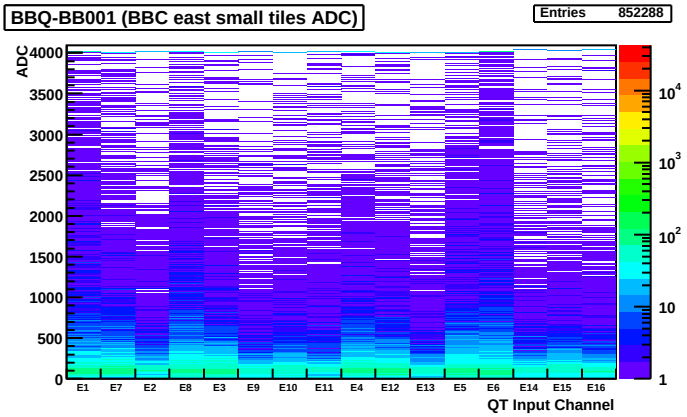

MIX-TF005
Entries 2237256
TOF MULT
TOF tray

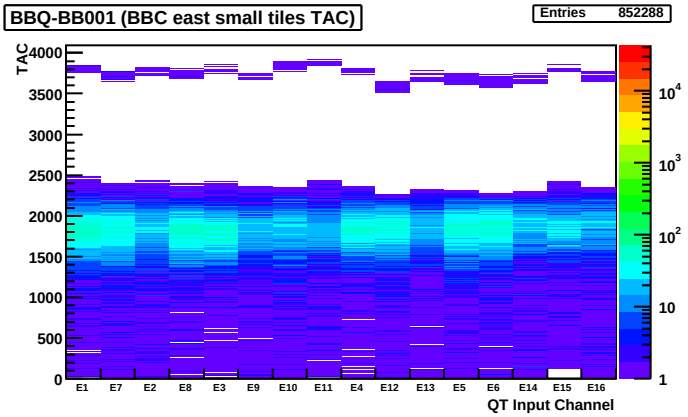

MIX-TF006
Entries 2237256
TOF MULT
TOF tray

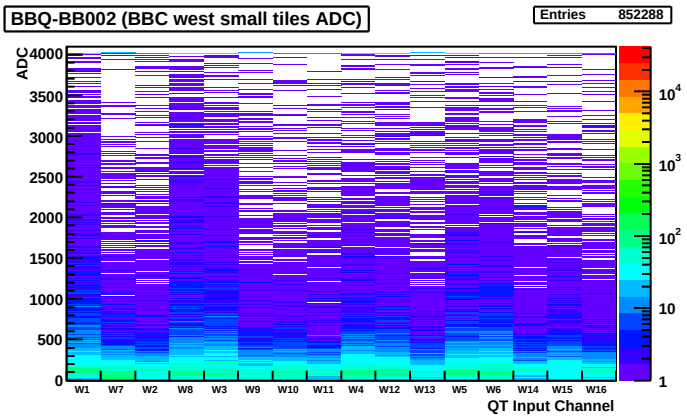

MIX-TF101
Entries 319608
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

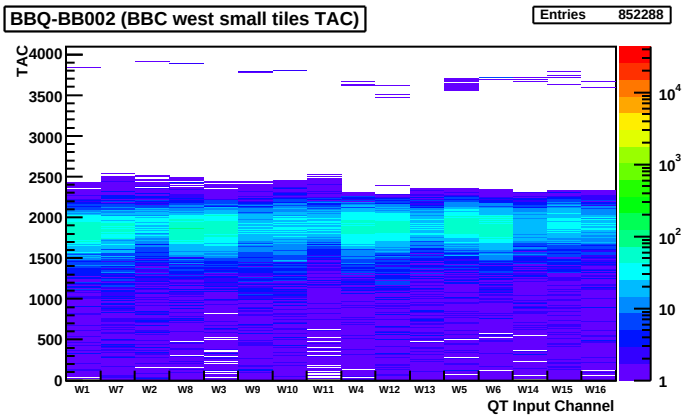

L1-TF201
Entries 53268
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

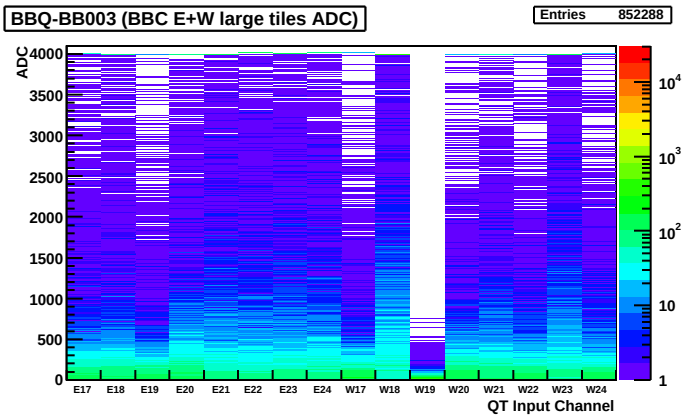

L1-TF201
Entries 319608
Threshold bits
1
0
29225
27680
23251
26345
22528
25794
24043
25588
30017
26923
30740
27474
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

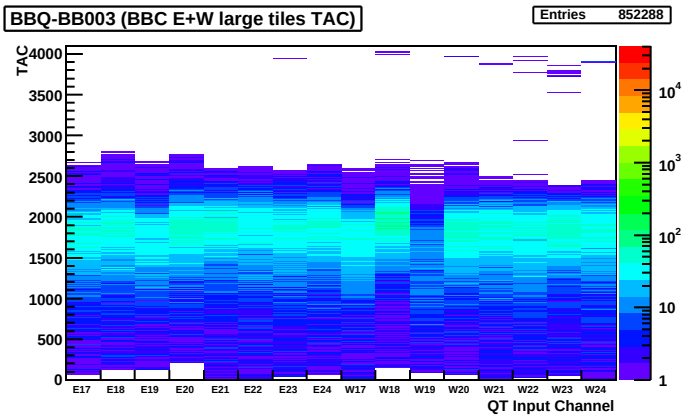

BBQ-BB001 (BBC east small tiles ADC)
Entries 852288
ADC
QT Input Channel

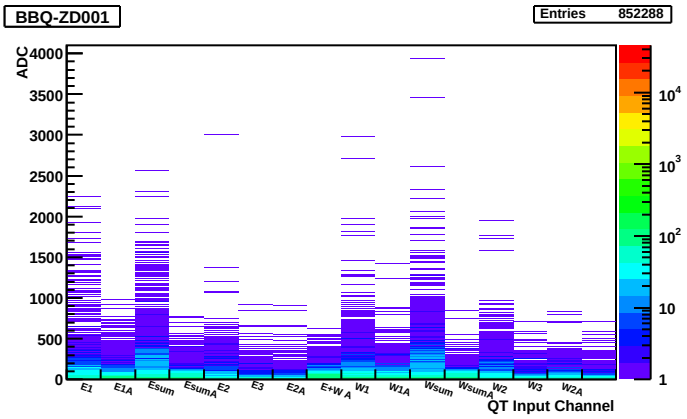

BBQ-BB001 (BBC east small tiles TAC)
Entries 852288
TAC
QT Input Channel

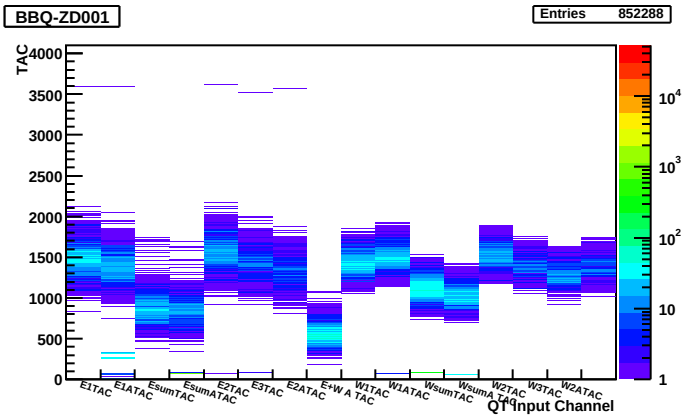

BBQ-BB002 (BBC west small tiles ADC)
Entries 852288
ADC
QT Input Channel

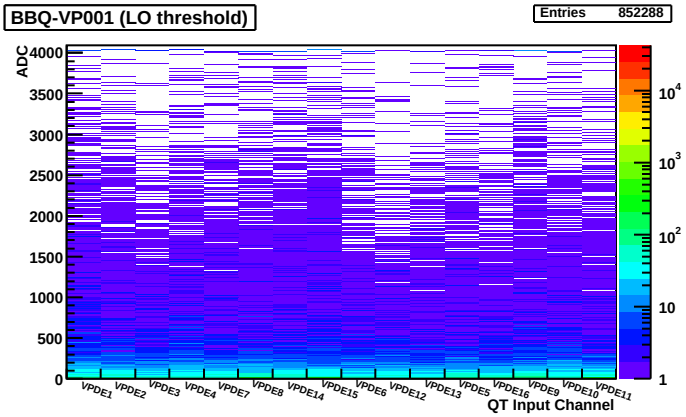

BBQ-BB002 (BBC west small tiles TAC)
Entries 852288
TAC
QT Input Channel

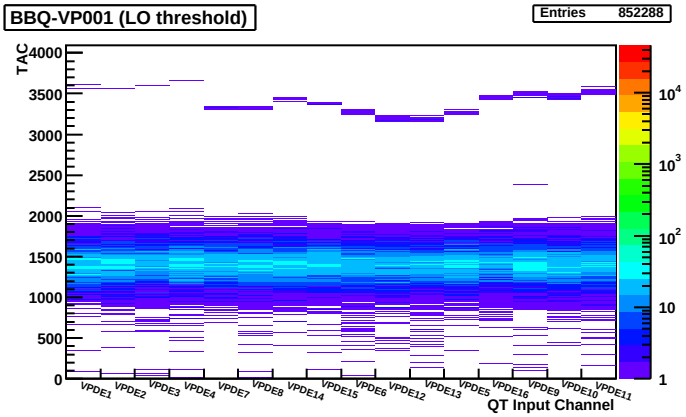

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 852288
ADC
QT Input Channel

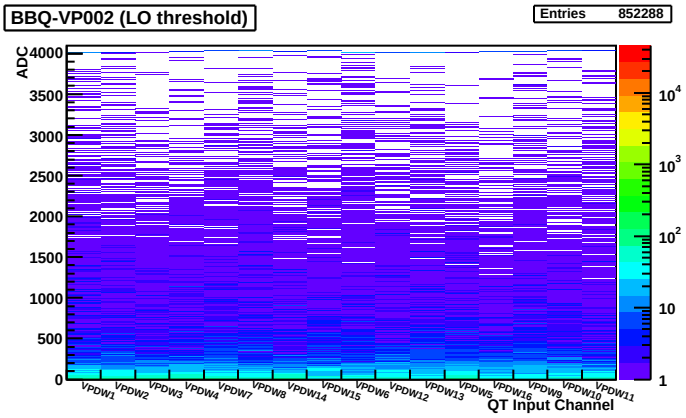

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 852288
TAC
QT Input Channel

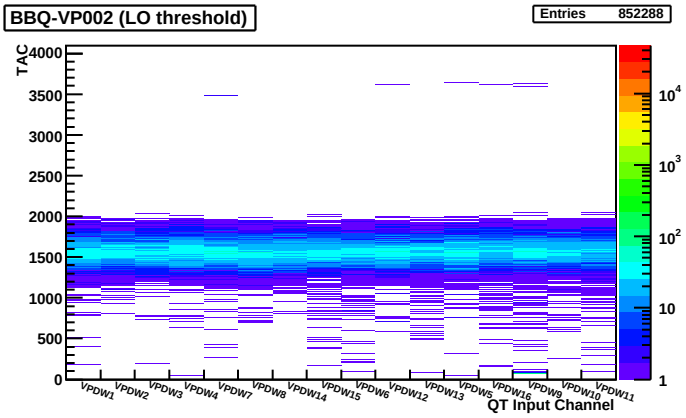

BBQ-ZD001
Entries 852288
ADC
QT Input Channel

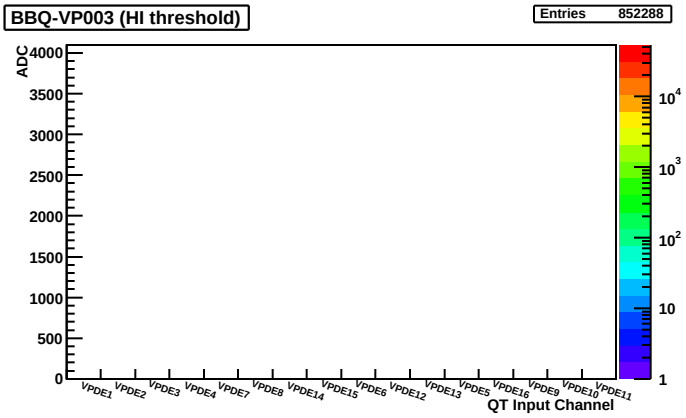

BBQ-ZD001
Entries 852288
TAC
QT Input Channel

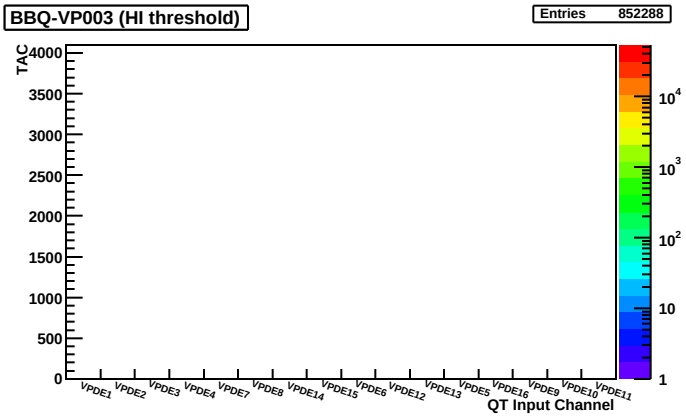

BBQ-VP001 (LO threshold)
Entries 852288
ADC
QT Input Channel

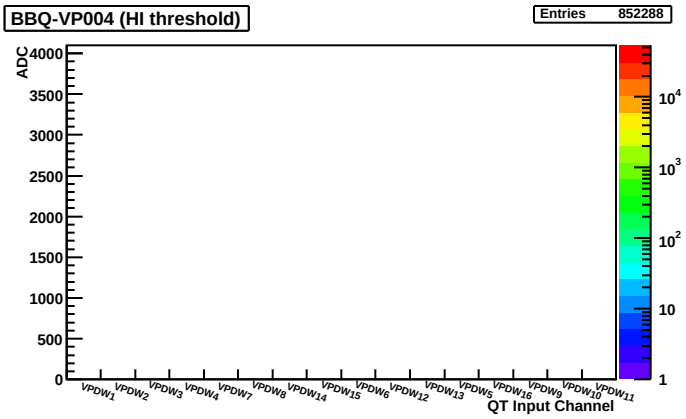

BBQ-VP001 (LO threshold)
Entries 852288
TAC
QT Input Channel

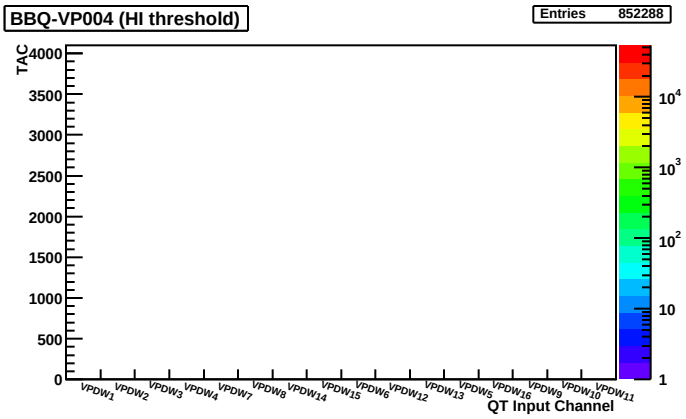

BBQ-VP002 (LO threshold)
Entries 852288
ADC
QT Input Channel

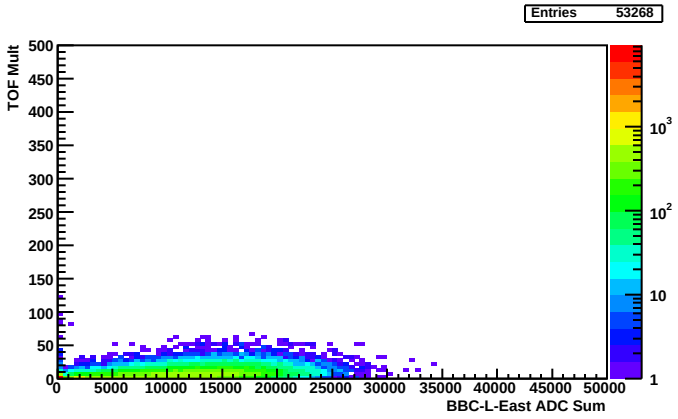

BBQ-VP002 (LO threshold)
Entries 852288
TAC
QT Input Channel

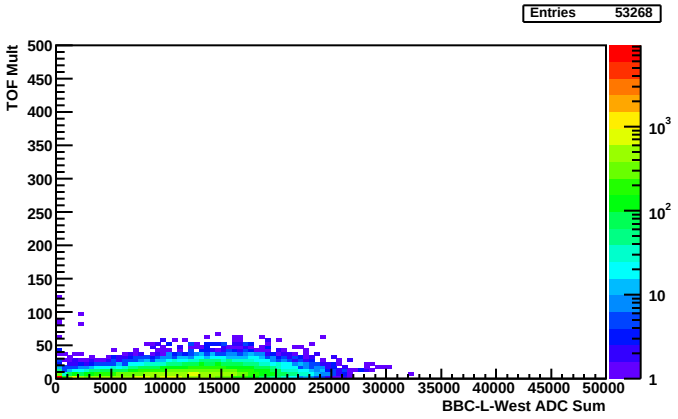

BBQ-VP003 (HI threshold)
Entries 852288
ADC
QT Input Channel

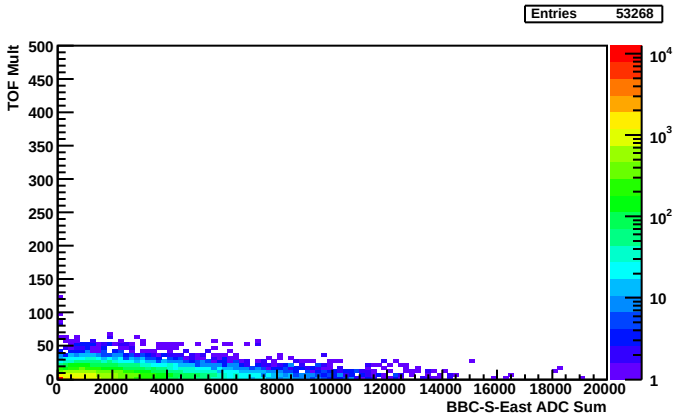

BBQ-VP003 (HI threshold)
Entries 852288
TAC
QT Input Channel

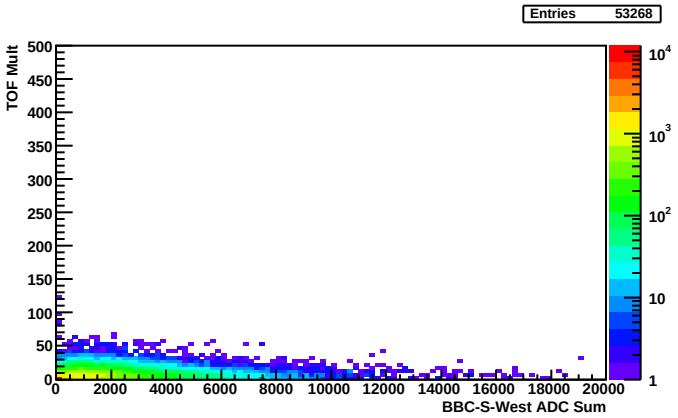

BBQ-VP004 (HI threshold)
Entries 852288
ADC
QT Input Channel

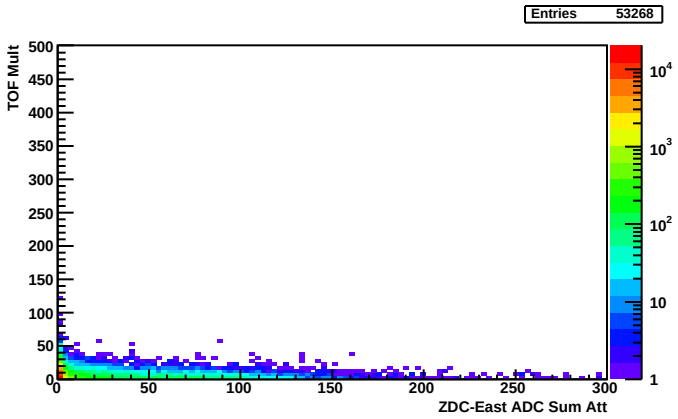

BBQ-VP004 (HI threshold)
Entries 852288
TAC
QT Input Channel

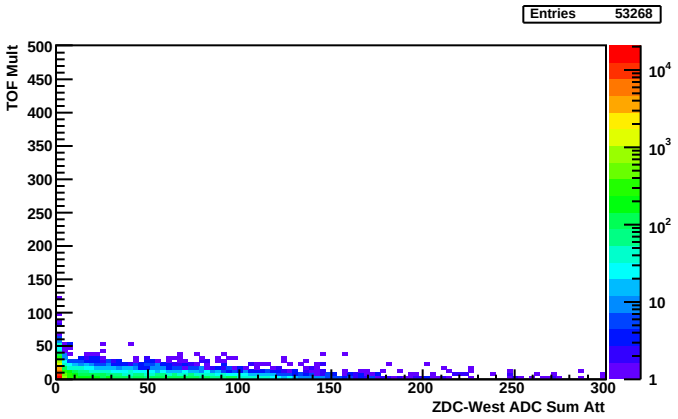

Entries 53268
TOF Mult
BBC-L-East ADC Sum

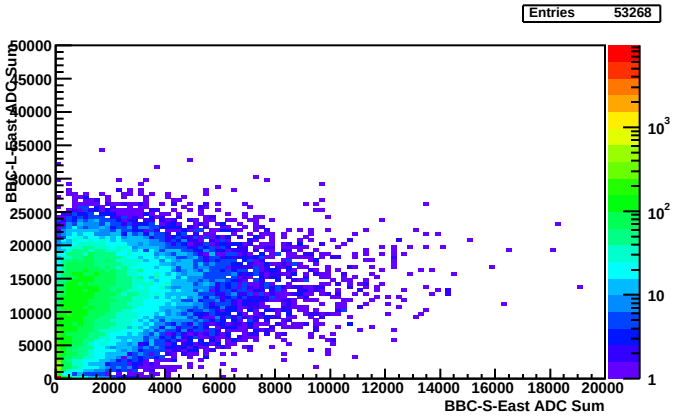

Entries 53268
TOF Mult
BBC-L-West ADC Sum

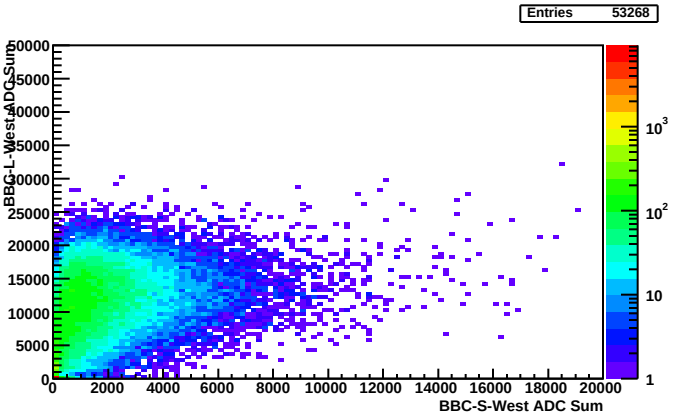

Entries 53268
TOF Mult
BBC-S-East ADC Sum

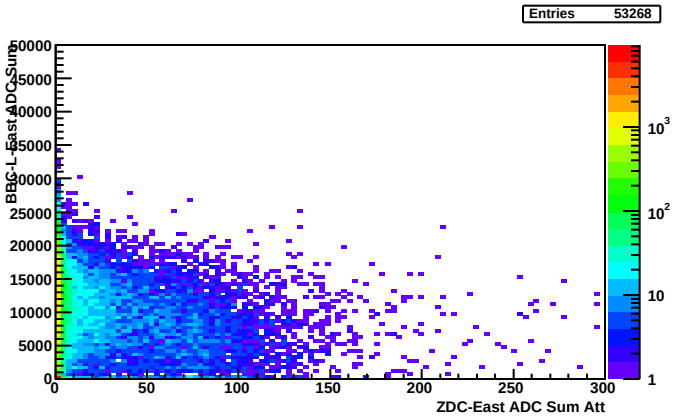

Entries 53268
TOF Mult
BBC-S-West ADC Sum

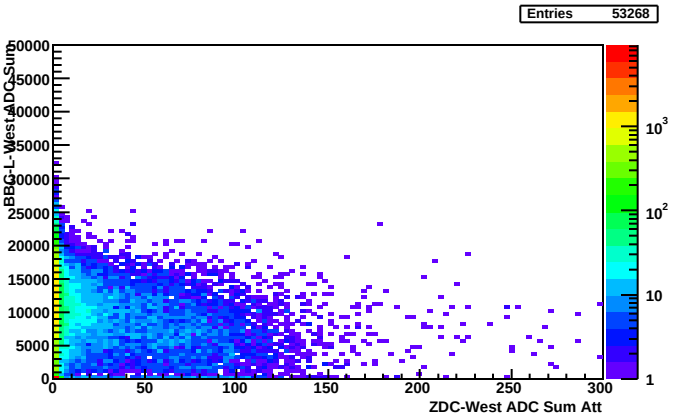

Entries 53268
TOF Mult
ZDC-East ADC Sum Att

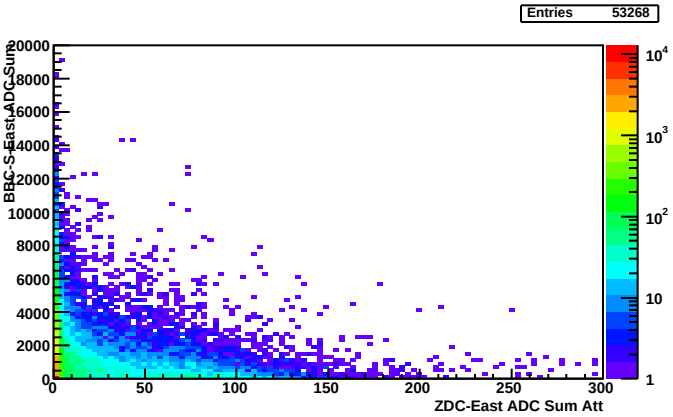

Entries 53268
TOF Mult
ZDC-West ADC Sum Att

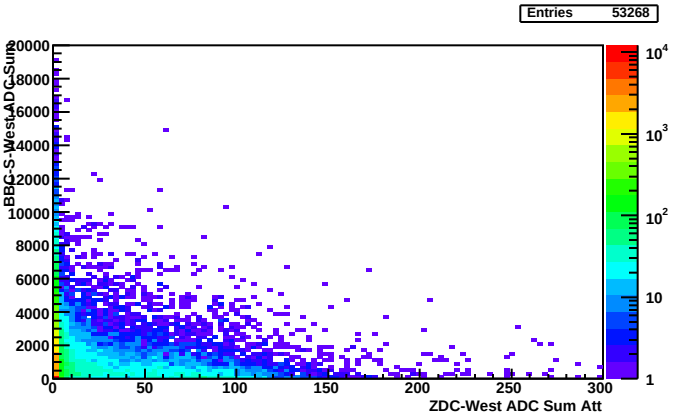

Entries 53268
BBC-L-East ADC Sum
BBC-S-East ADC Sum

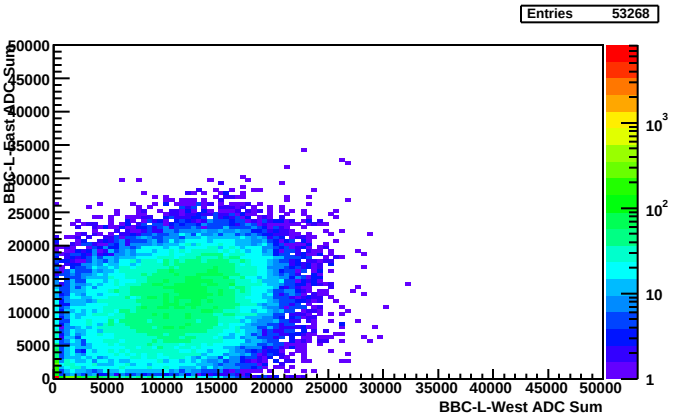

Entries 53268
BBC-L-West ADC Sum
BBC-S-West ADC Sum

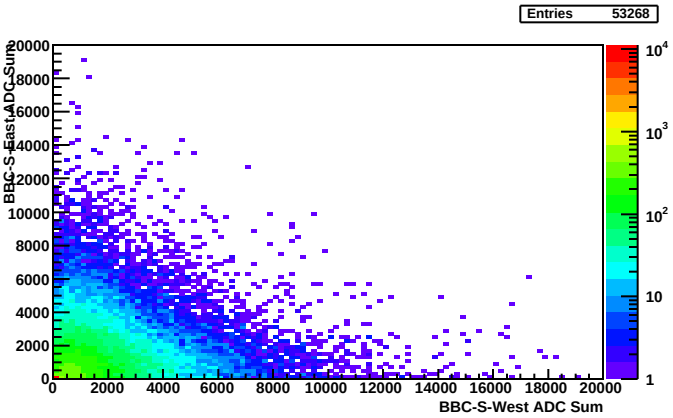

Entries 53268
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

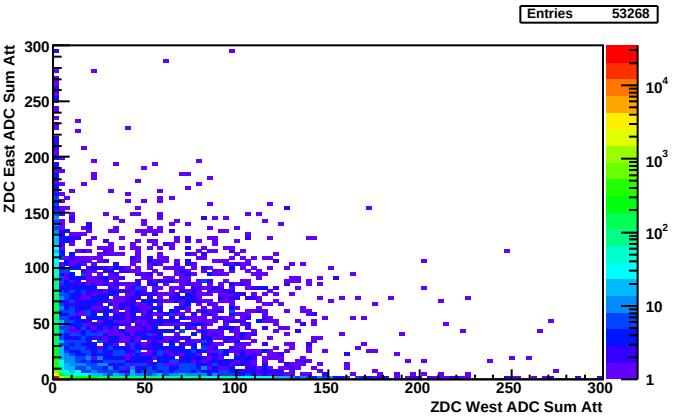

Entries 53268
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

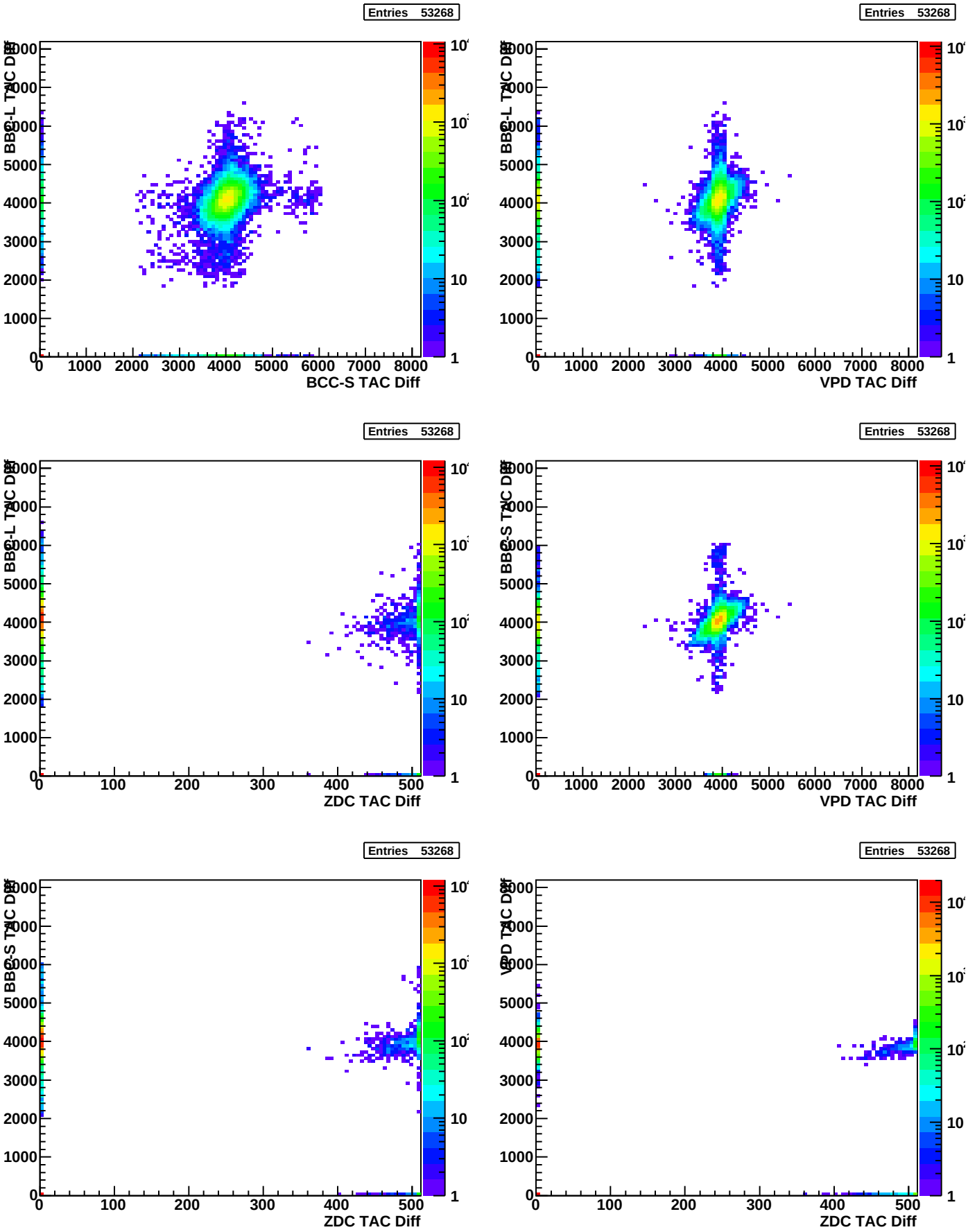

Entries 53268
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

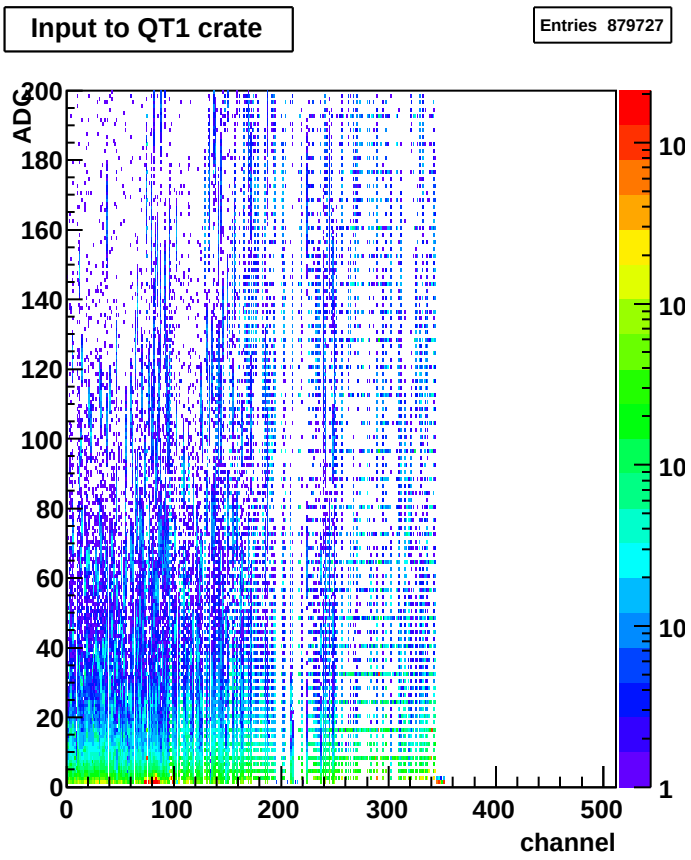

Entries 53268
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

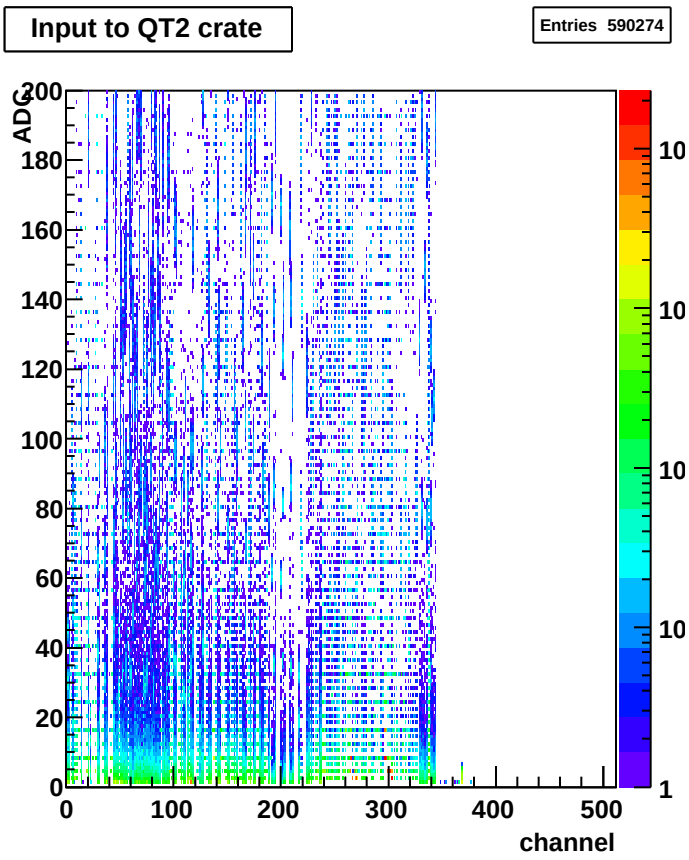

Entries 53268
BBC-L-East ADC Sum
BBC-L-West ADC Sum

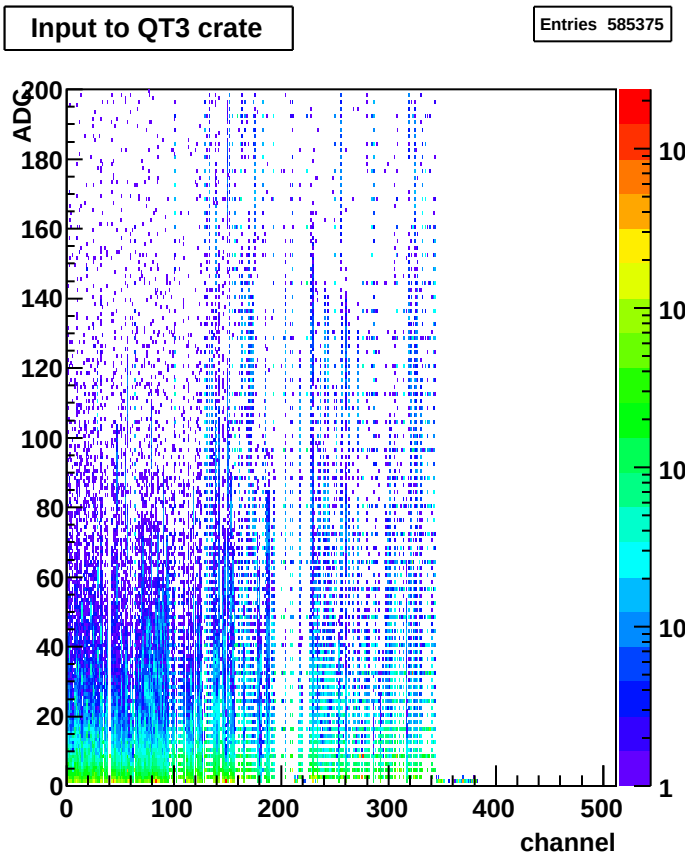

Entries 53268
BBC-S-East ADC Sum
BBC-S-West ADC Sum

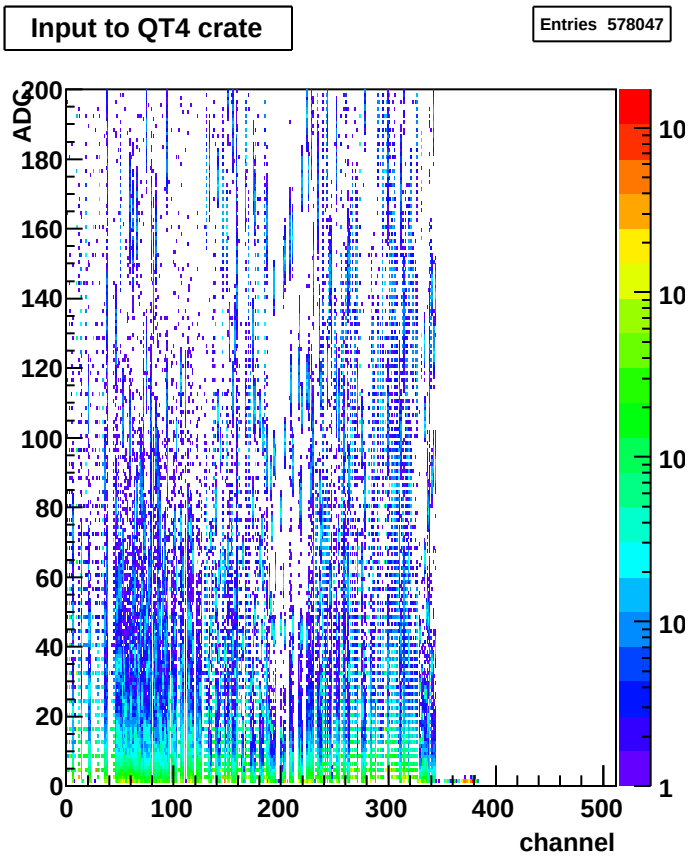

Entries 53268
ZDC East ADC Sum Att
ZDC West ADC Sum Att

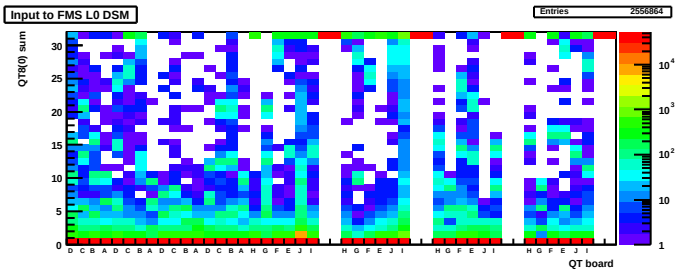

Entries 53268
BBC-L TAC Diff
BCC-S TAC Diff
Entries 53268
BBC-L TAC Diff
VPD TAC Diff
Entries 53268
BBC-L TAC Diff
ZDC TAC Diff
Entries 53268
BBC-S TAC Diff
VPD TAC Diff
Entries 53268
BBC-S TAC Diff
ZDC TAC Diff
Entries 53268
VPD TAC Diff
ZDC TAC Diff

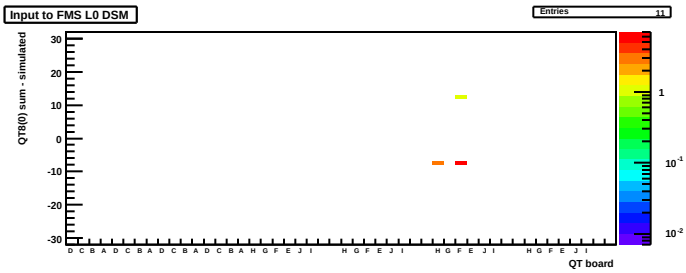

Input to QT1 crate
Entries 879727
ADC
channel

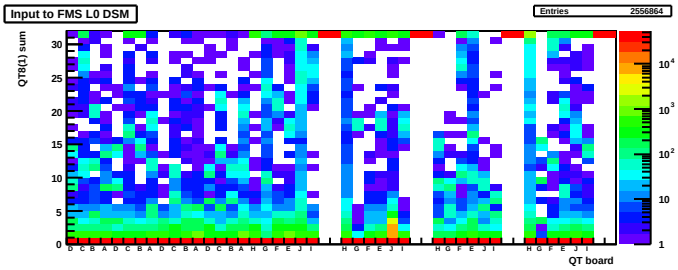

Input to QT2 crate
Entries 590274
ADC
channel

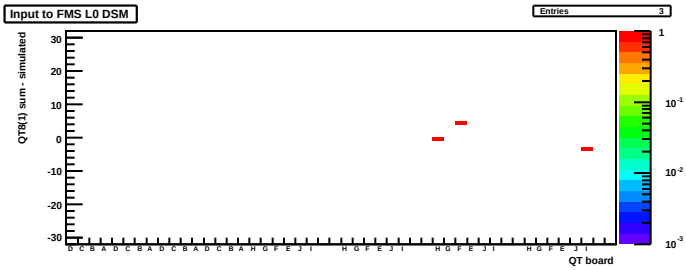

Input to QT3 crate
Entries 585375
ADC
channel

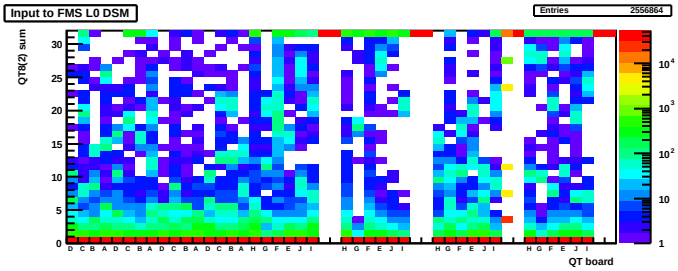

Input to QT4 crate
Entries 578047
ADC
channel

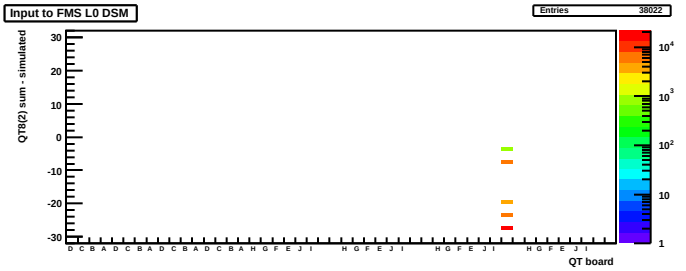

Input to FMS L0 DSM
Entries 2556864
QT8(0) sum
QT board

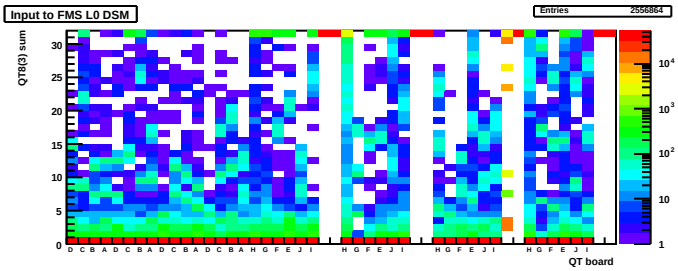

Input to FMS L0 DSM
Entries 11
QT8(0) sum - simulated
QT board

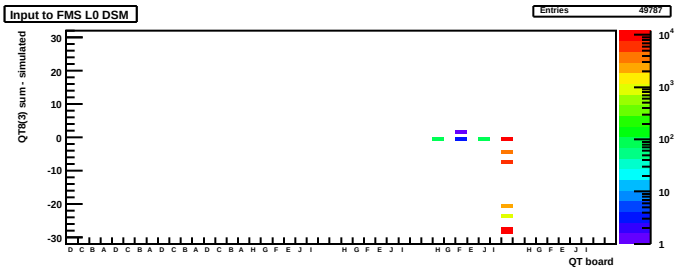

Input to FMS L0 DSM
Entries 2556864
QT8(1) sum
QT board

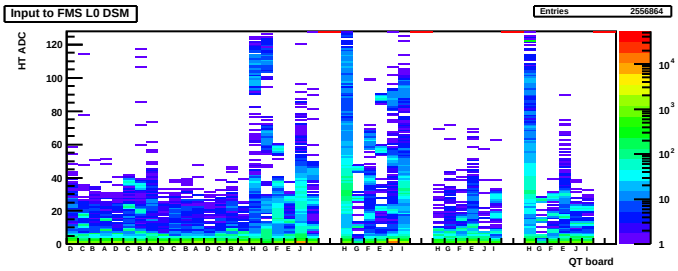

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

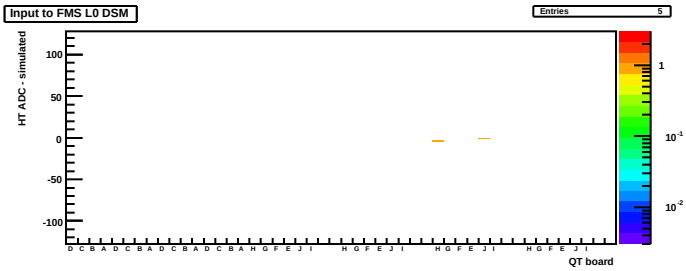

Input to FMS L0 DSM
Entries 2556864
QT8(2) sum
QT board

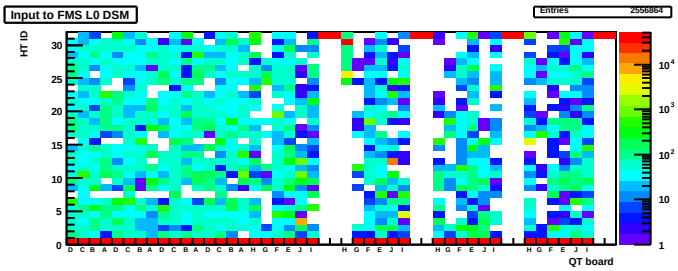

Input to FMS L0 DSM
Entries 38022
QT8(2) sum - simulated
QT board

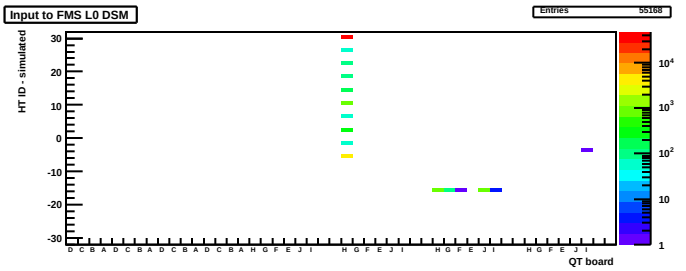

Input to FMS L0 DSM
Entries 2556864
QT8(3) sum
QT board

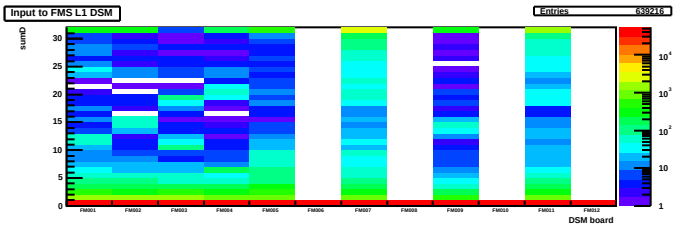

Input to FMS L0 DSM
Entries 49787
QT8(3) sum - simulated
QT board

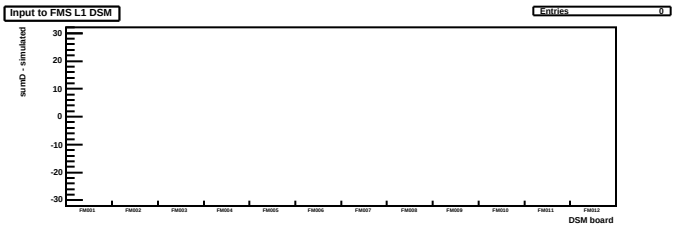

Input to FMS L0 DSM
Entries 2556864
HT ADC
QT board

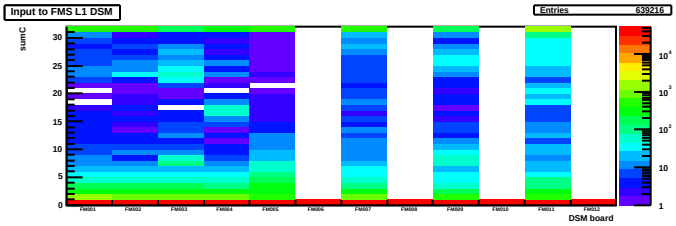

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

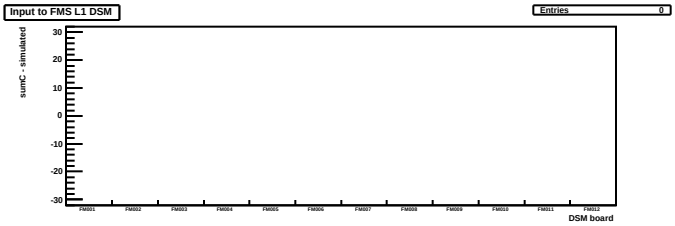

Input to FMS L0 DSM
Entries 2556864
HT ID
QT board

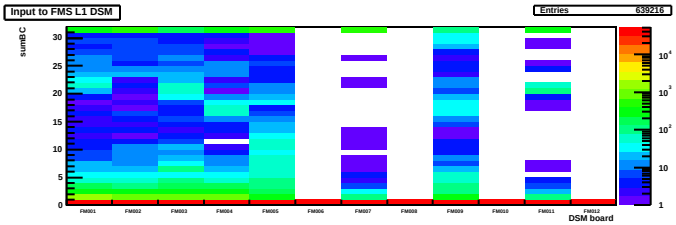

Input to FMS L0 DSM
Entries 55168
HT ID - simulated
QT board

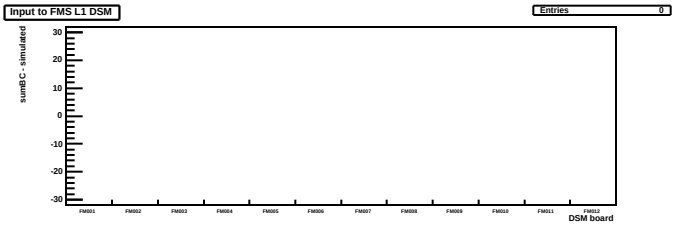

Input to FMS L1 DSM
Entries 639216
sumD
DSM board

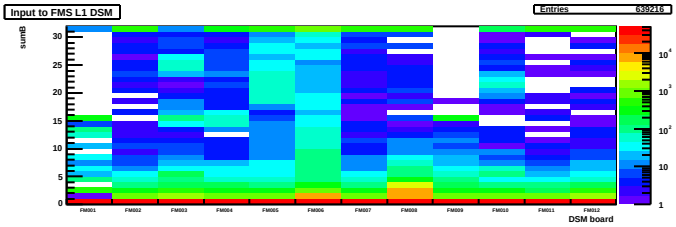

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

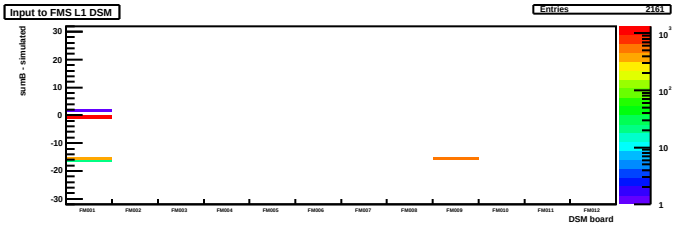

Input to FMS L1 DSM
Entries 639216
sumC
DSM board

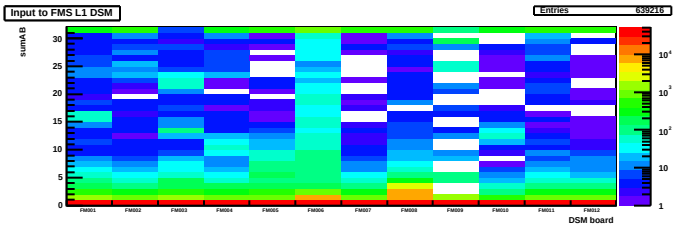

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

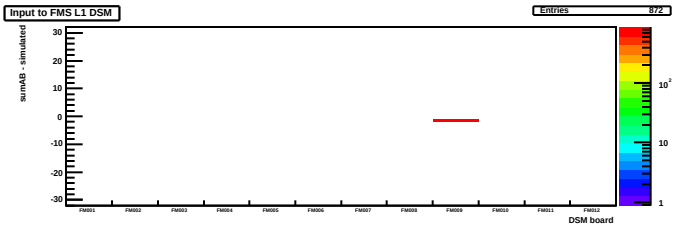

Input to FMS L1 DSM
Entries 639216
sumBC
DSM board

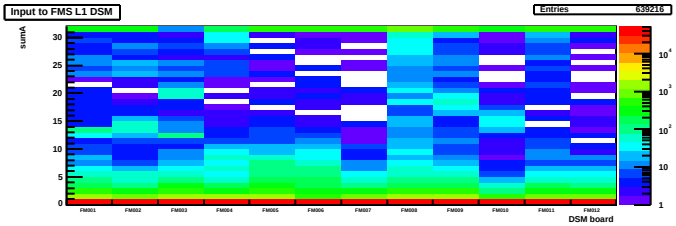

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

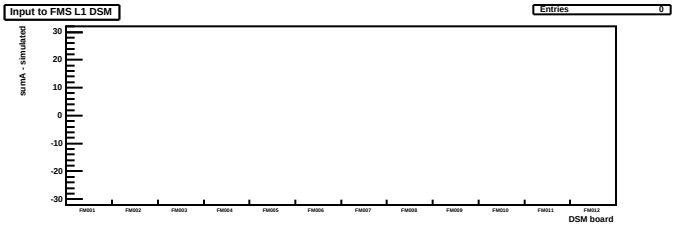

Input to FMS L1 DSM
Entries 639216
sumB
DSM board

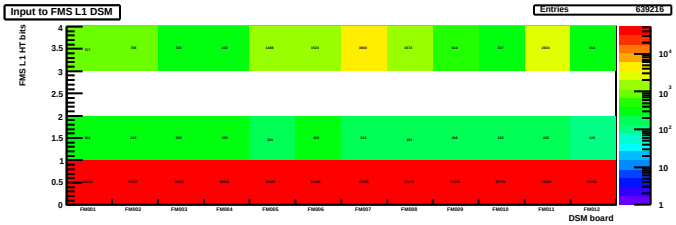

Input to FMS L1 DSM
Entries 2161
sumB - simulated
DSM board

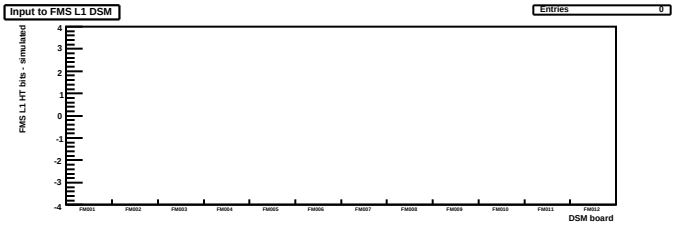

Input to FMS L1 DSM
Entries 639216
sumAB
DSM board

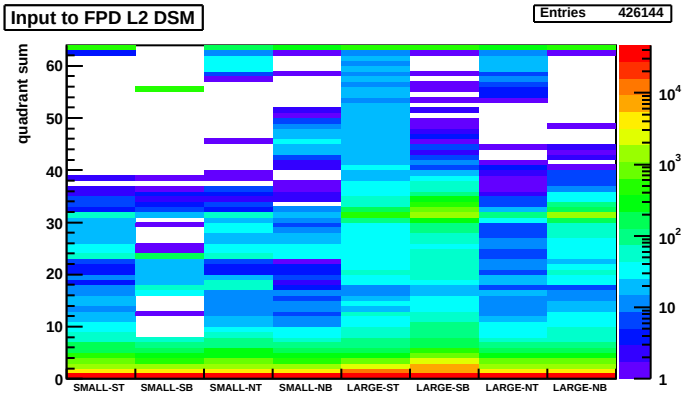

Input to FMS L1 DSM
Entries 872
sumAB - simulated
DSM board

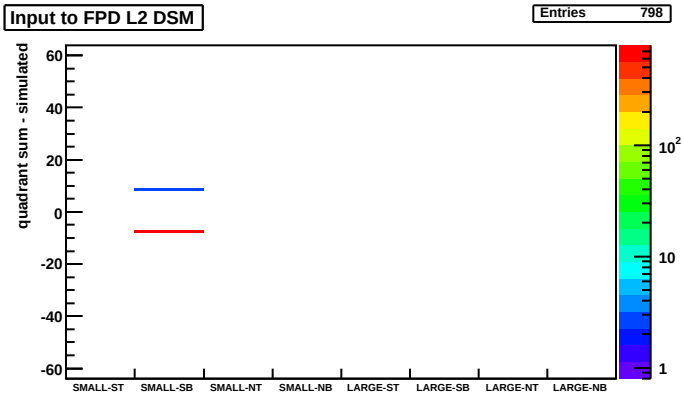

Input to FMS L1 DSM
Entries 639216
sumA
DSM board

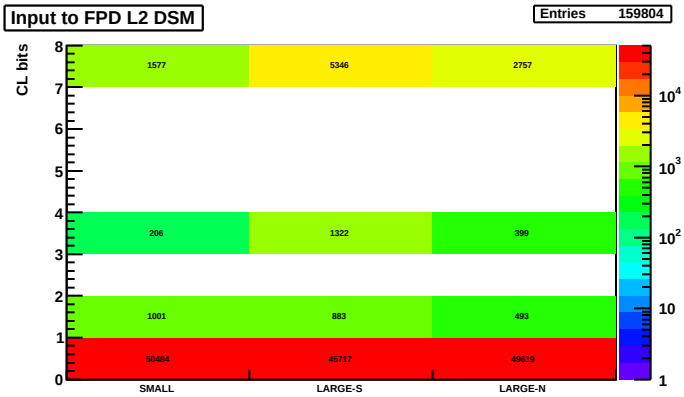

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

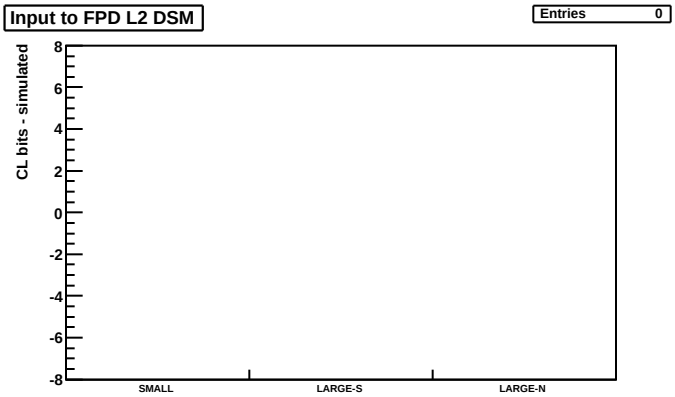

Input to FMS L1 DSM
Entries 639216
FMS L1 HT bits
DSM board

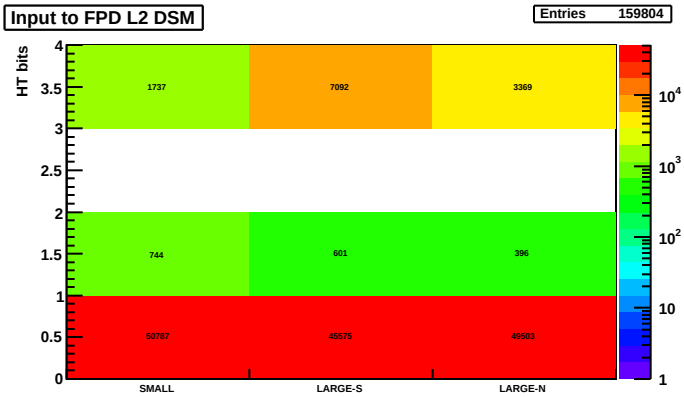

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

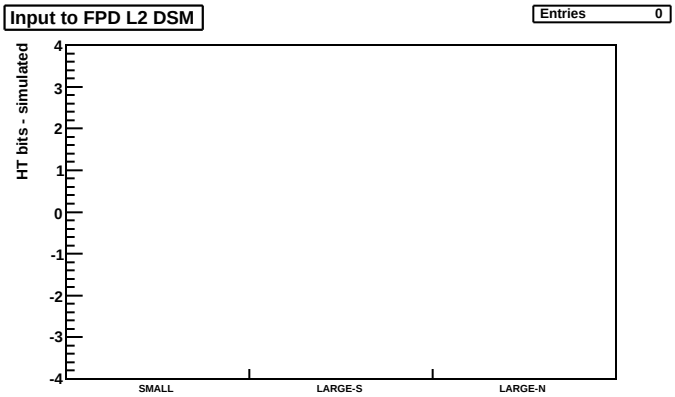

Input to FPD L2 DSM
Entries 426144
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

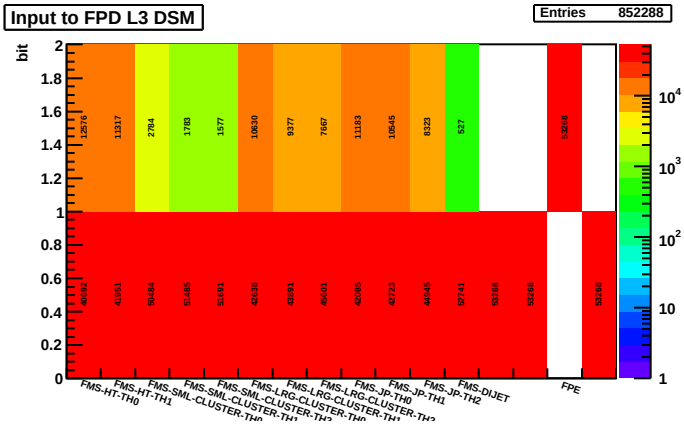

Input to FPD L2 DSM
Entries 798
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

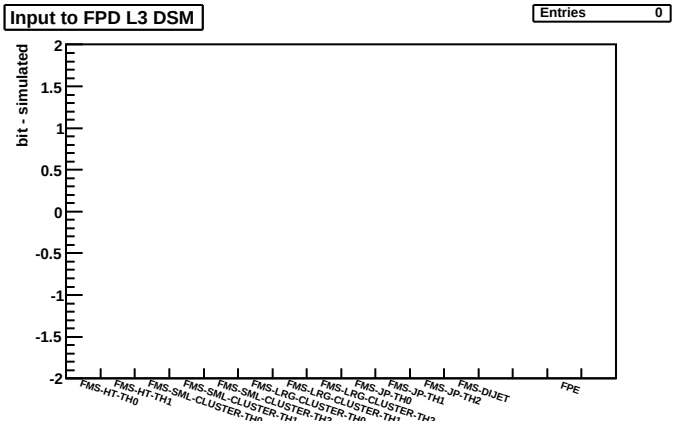

Input to FPD L2 DSM
Entries 159804
CL bits
1577
5346
2757
206
1322
399
1001
883
493
50484
45717
49619
SMALL
LARGE-S
LARGE-N

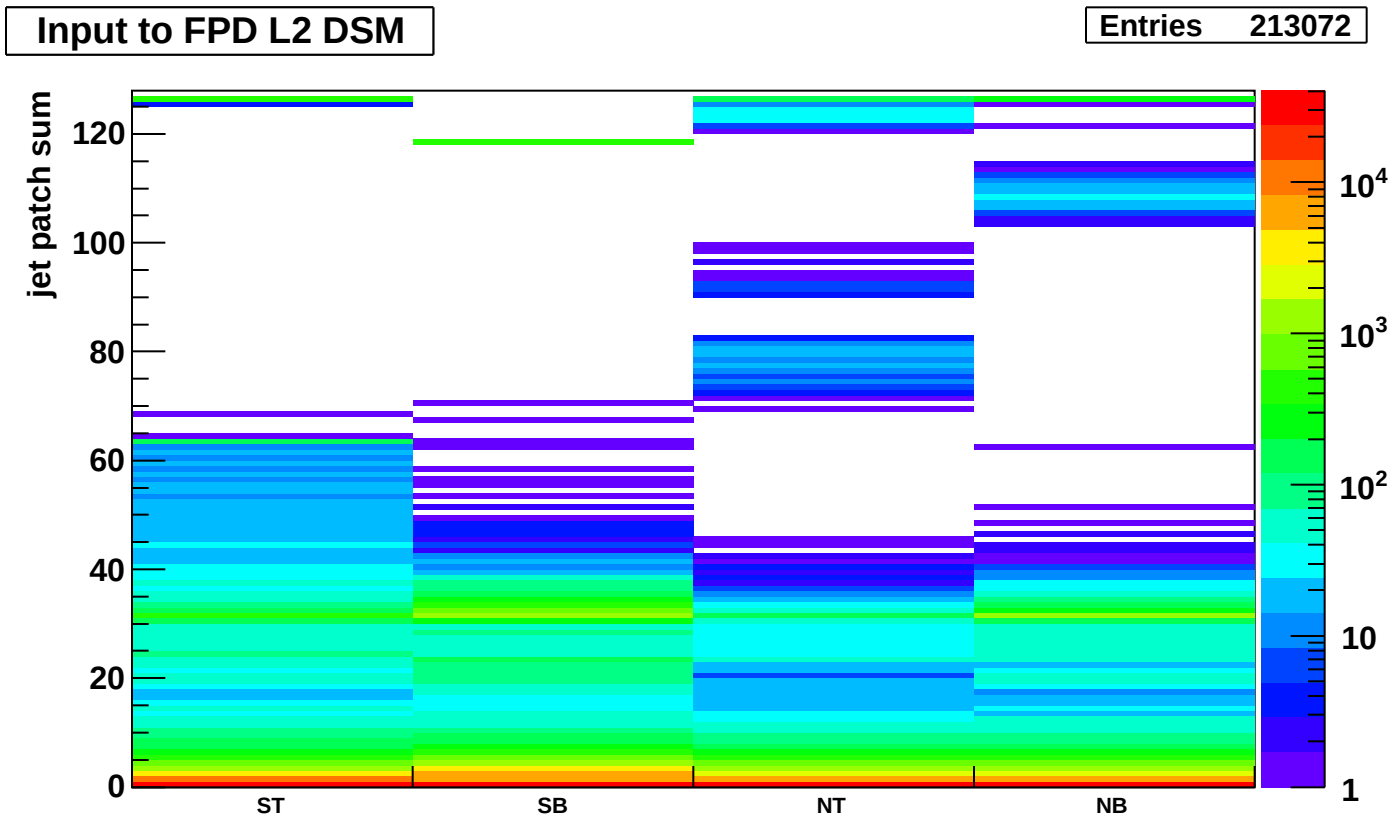

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

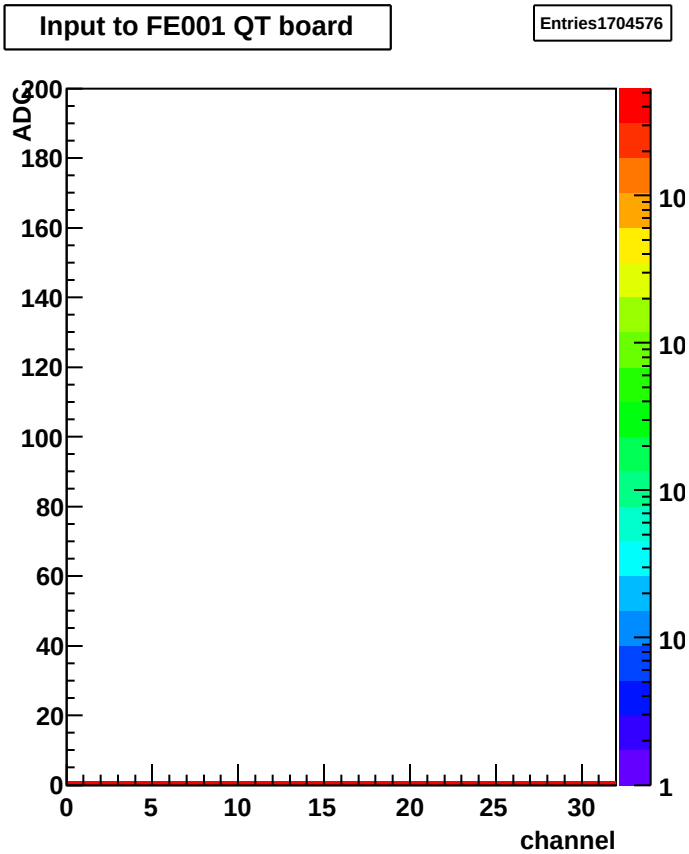

Input to FPD L2 DSM
Entries 159804
HT bits
1737
7092
3369
744
601
396
50787
45575
49503
SMALL
LARGE-S
LARGE-N

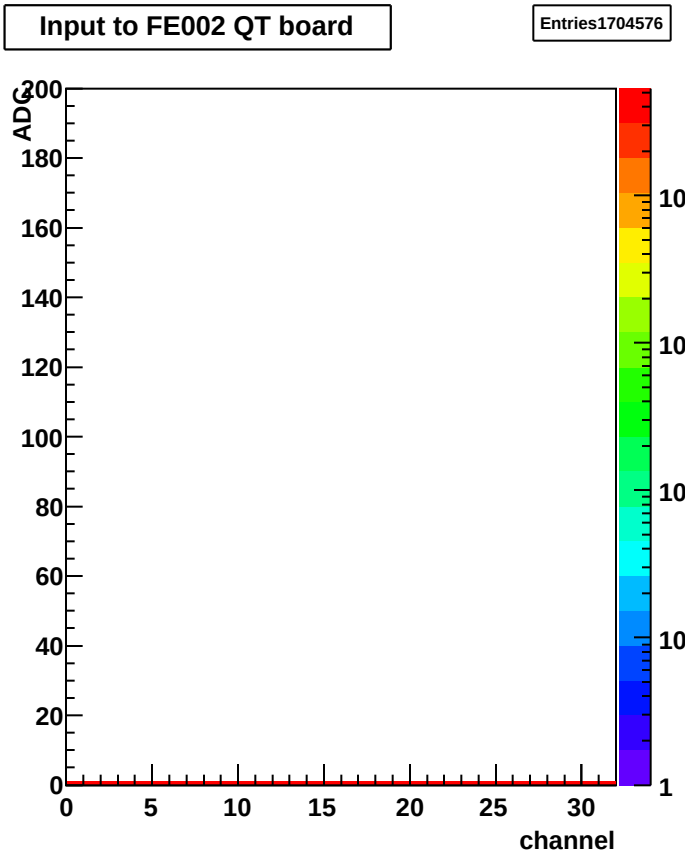

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

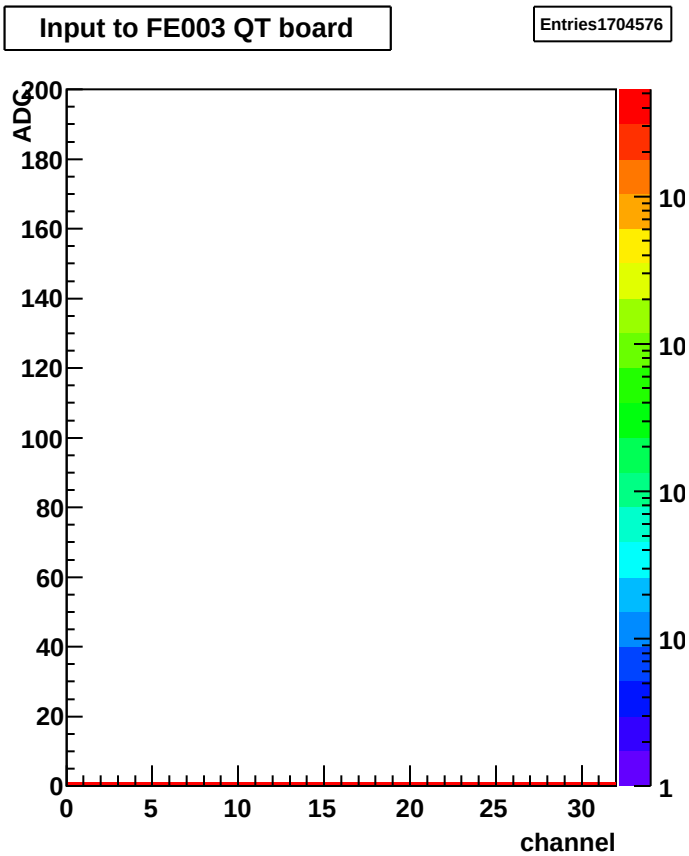

Input to FPD L3 DSM
Entries 852288
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

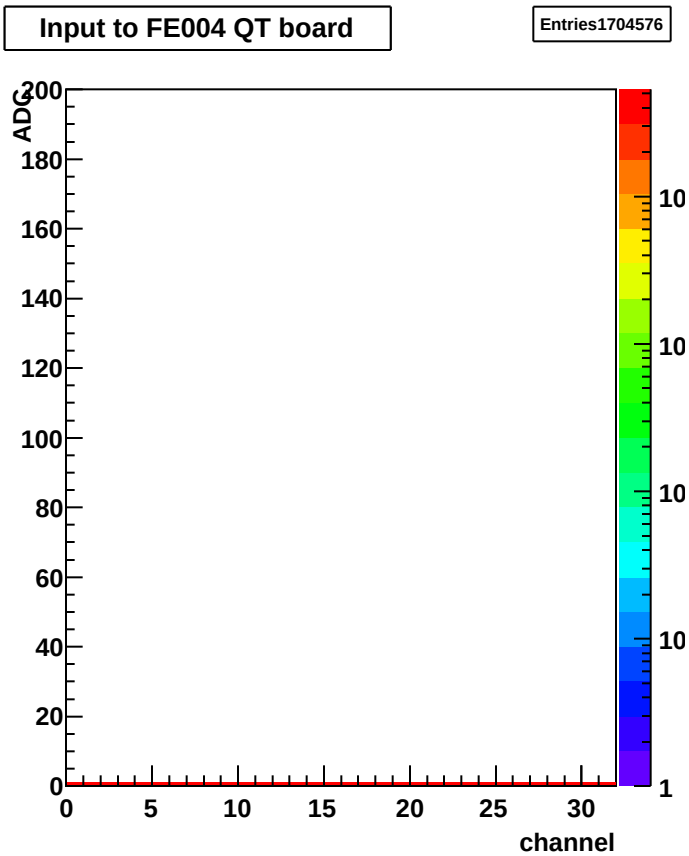

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

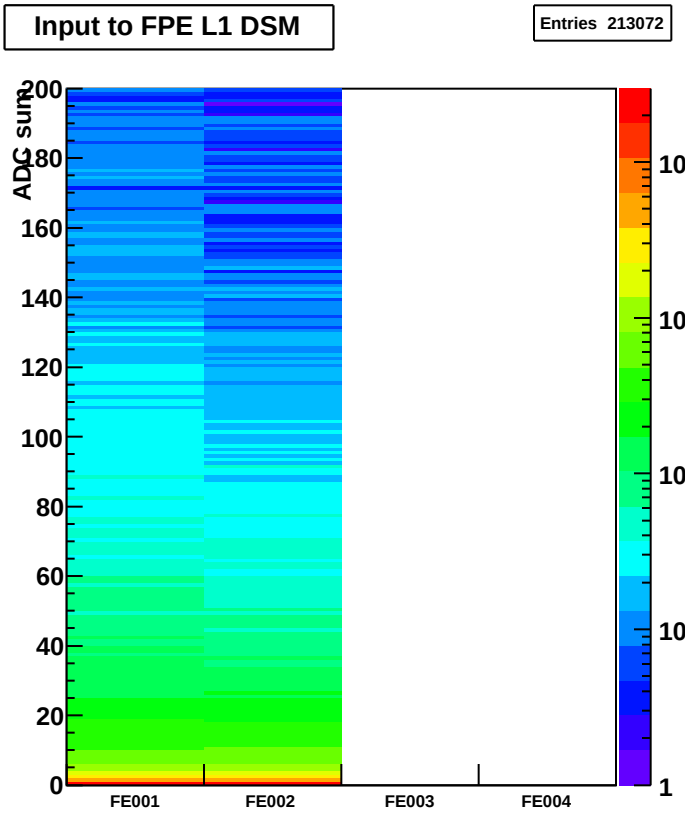

Input to FPD L2 DSM
Entries 213072
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

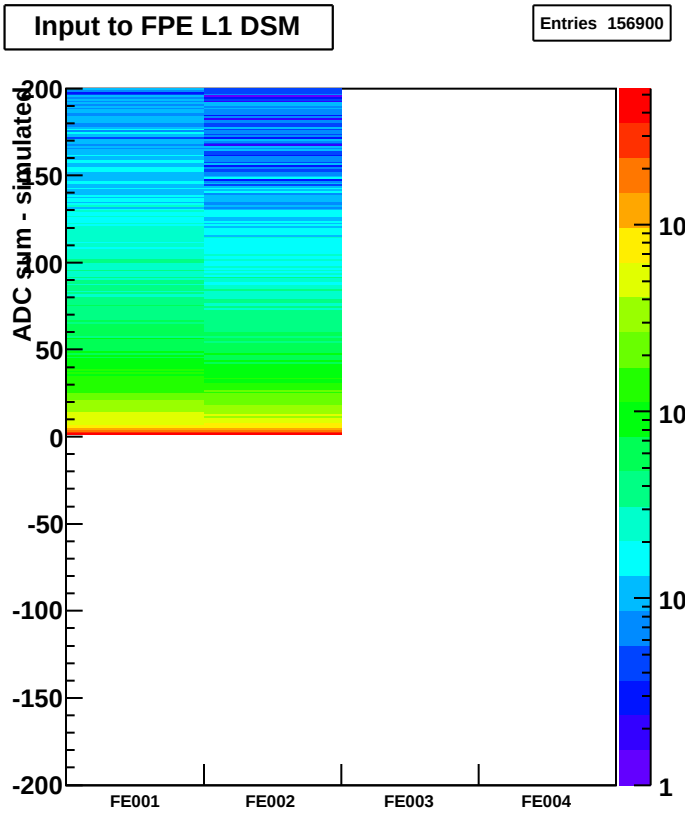

Input to FE001 QT board
Entries1704576
ADC
channel

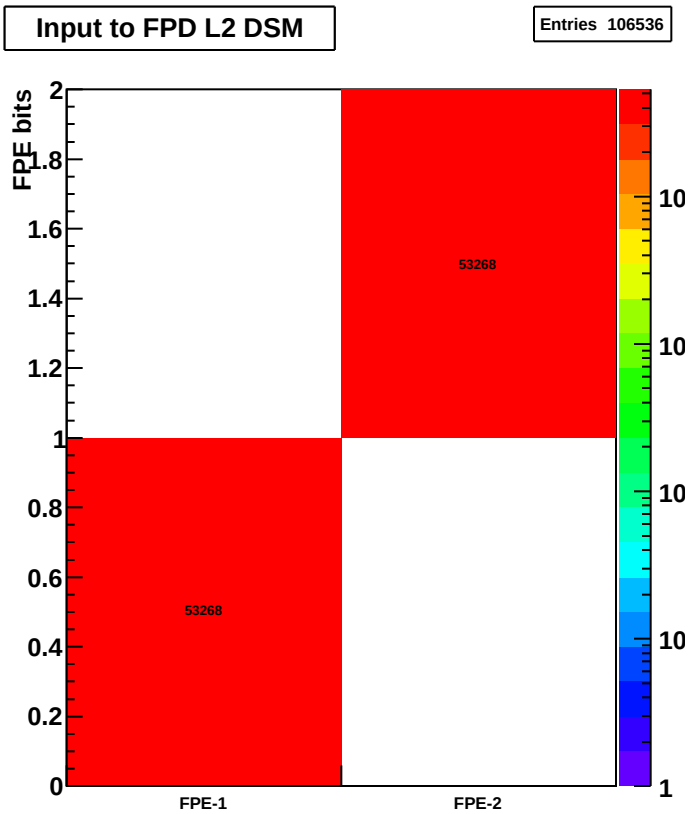

Input to FE002 QT board
Entries1704576
ADC
channel

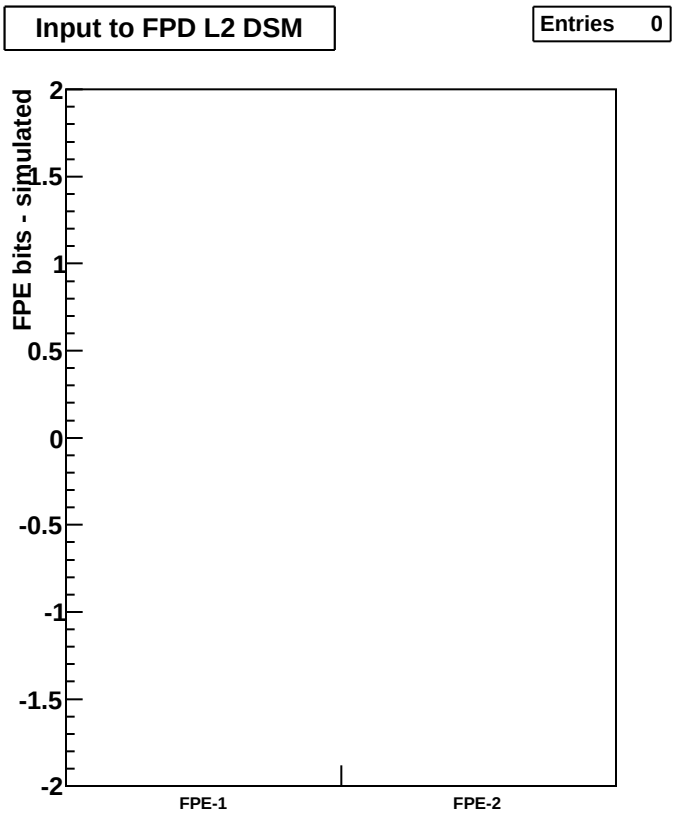

Input to FE003 QT board
Entries1704576
ADC
channel


Input to FE004 QT board
Entries1704576
ADC
channel


Input to FPE L1 DSM
Entries 213072
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 156900
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 106536
FPE bits
53268
53268
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2