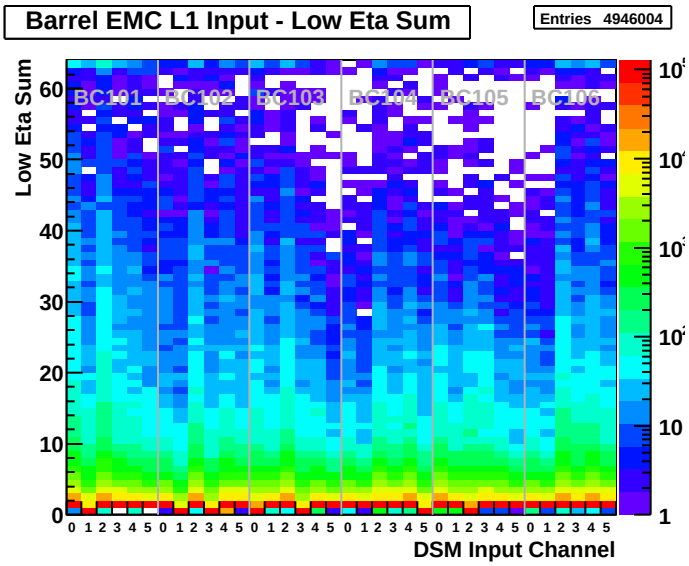
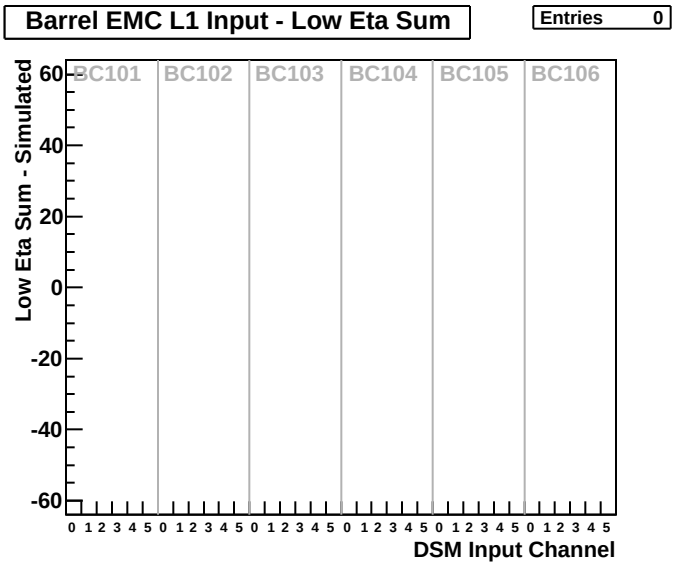
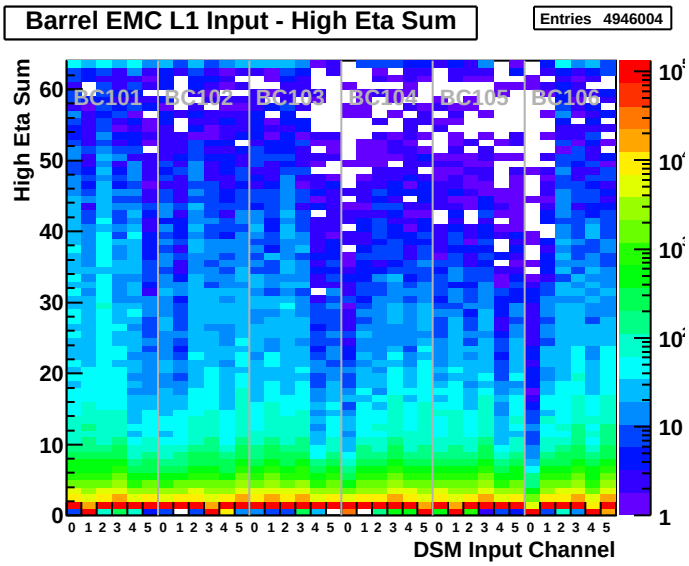
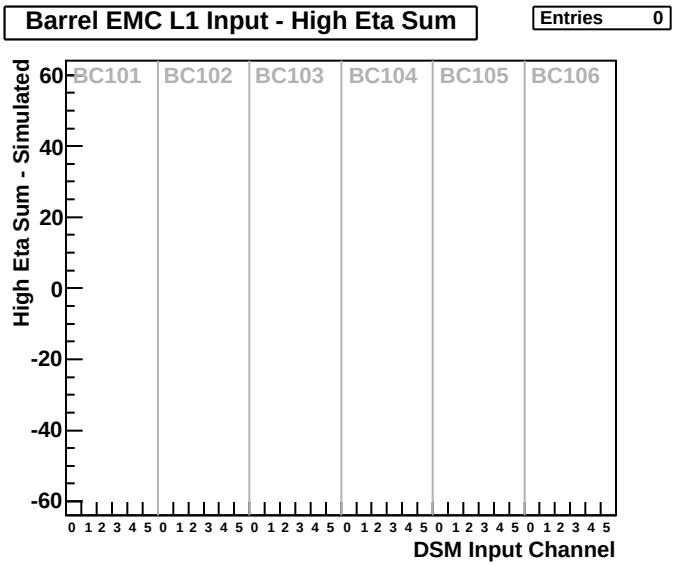
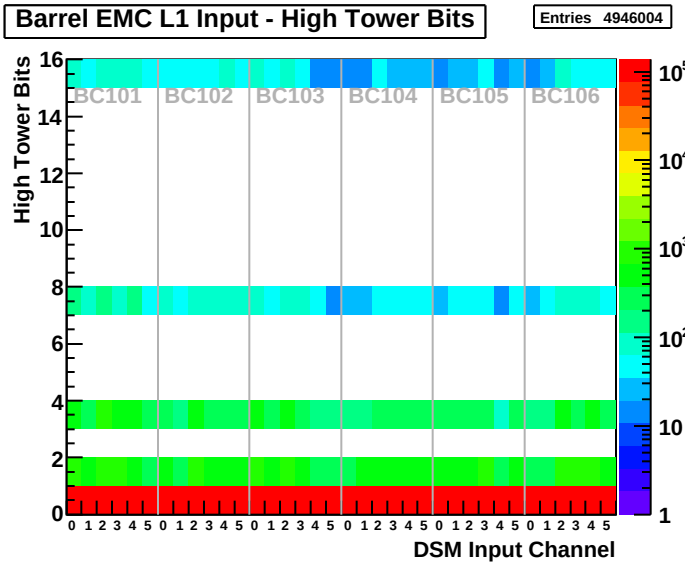
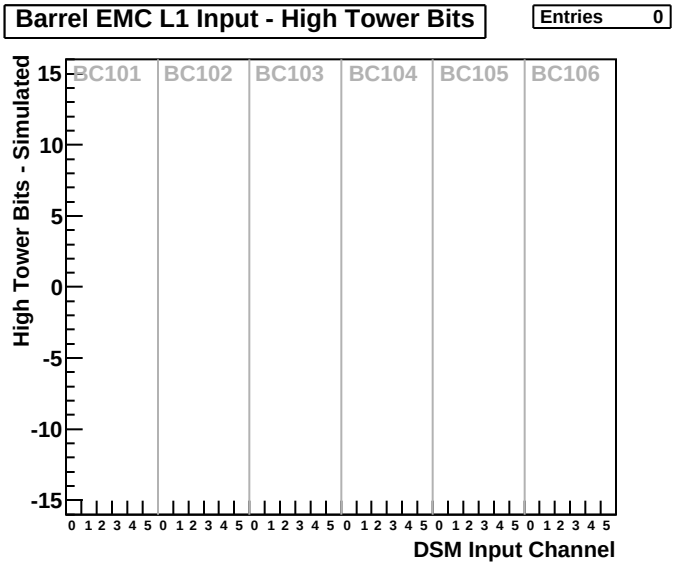

Barrel EMC L1 Input - Low Eta Sum
Entries 4946004
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

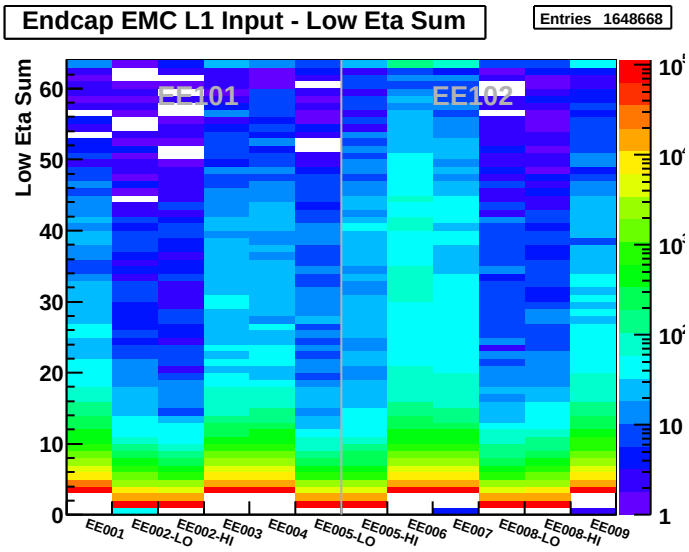

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

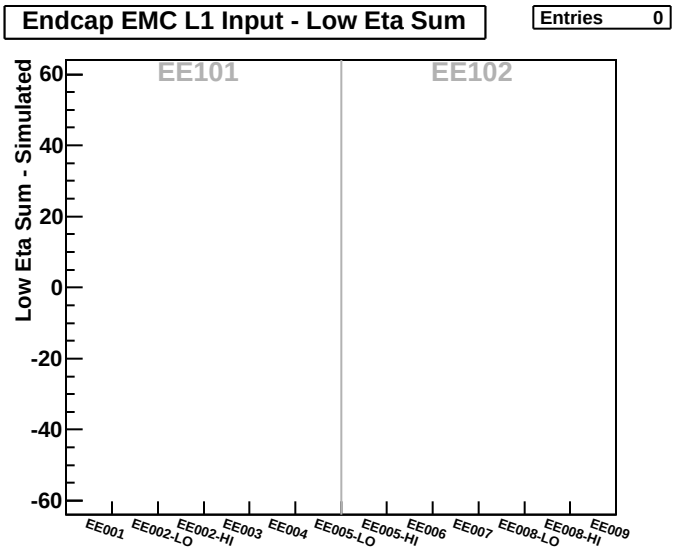

Barrel EMC L1 Input - High Eta Sum
Entries 4946004
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

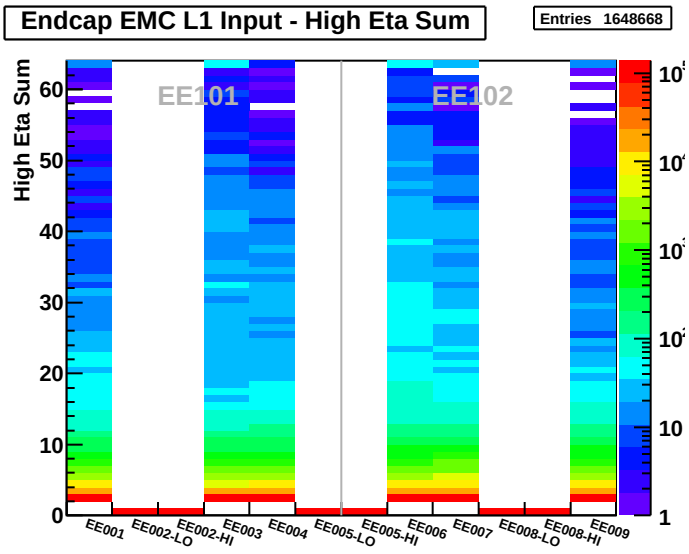

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

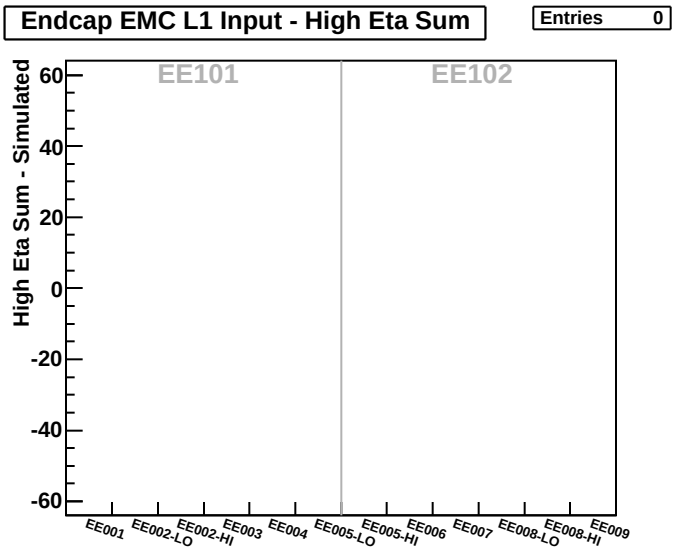

Barrel EMC L1 Input - High Tower Bits
Entries 4946004
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

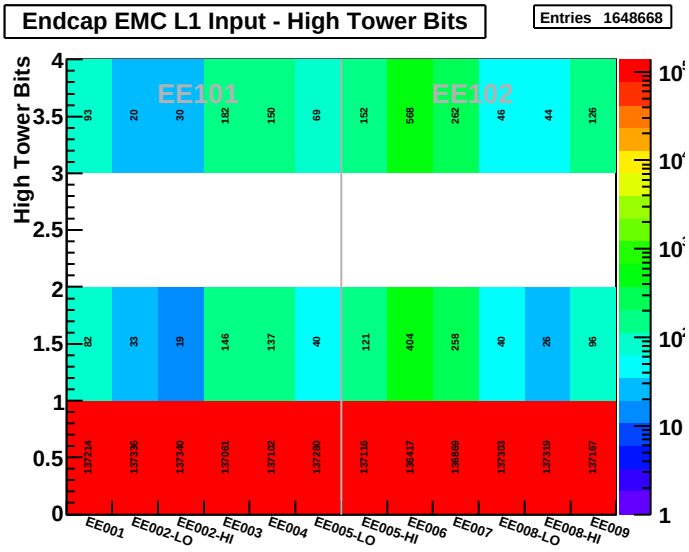

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

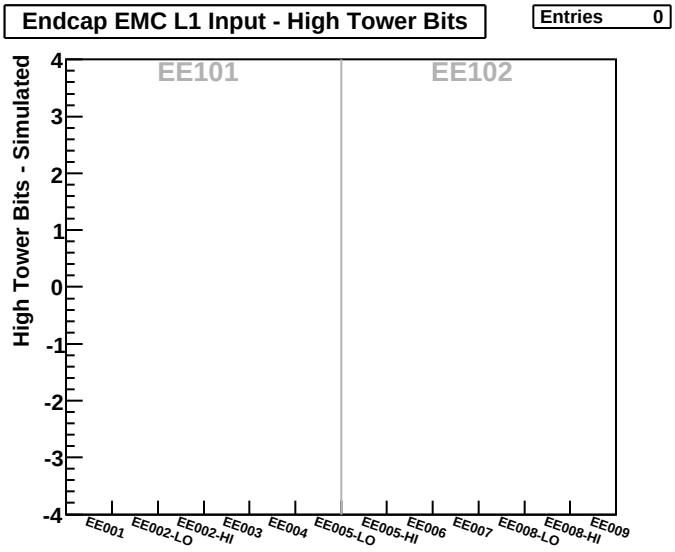

Endcap EMC L1 Input - Low Eta Sum
Entries 1648668
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

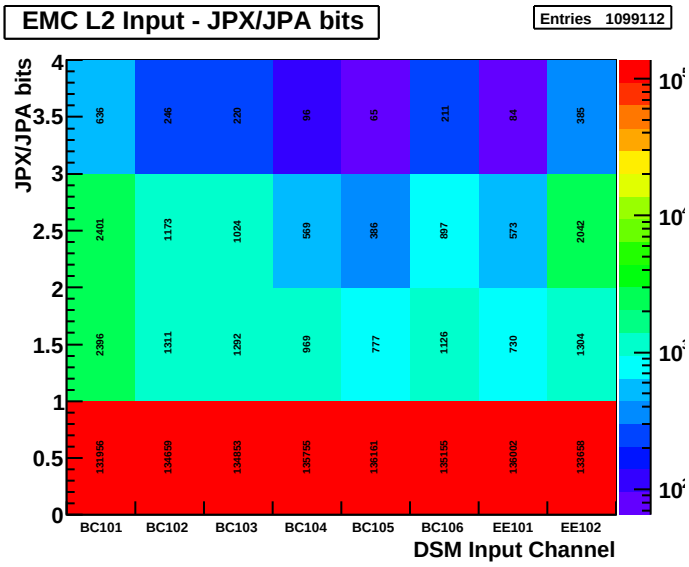

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

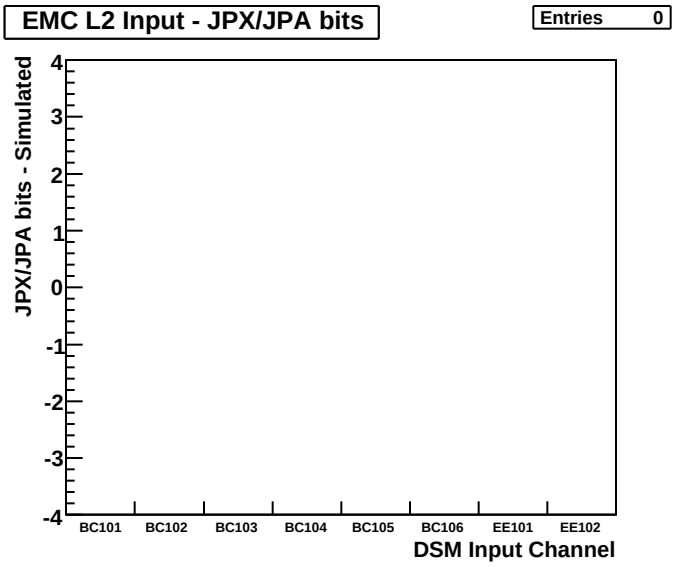

Endcap EMC L1 Input - High Eta Sum
Entries 1648668
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

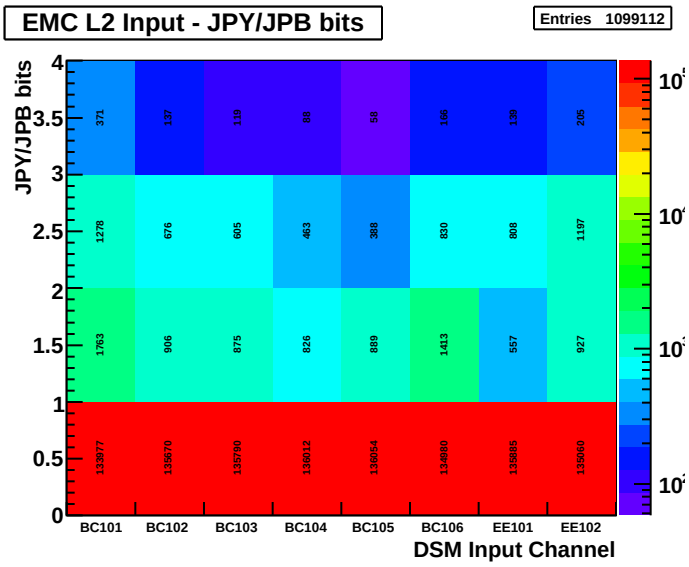

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

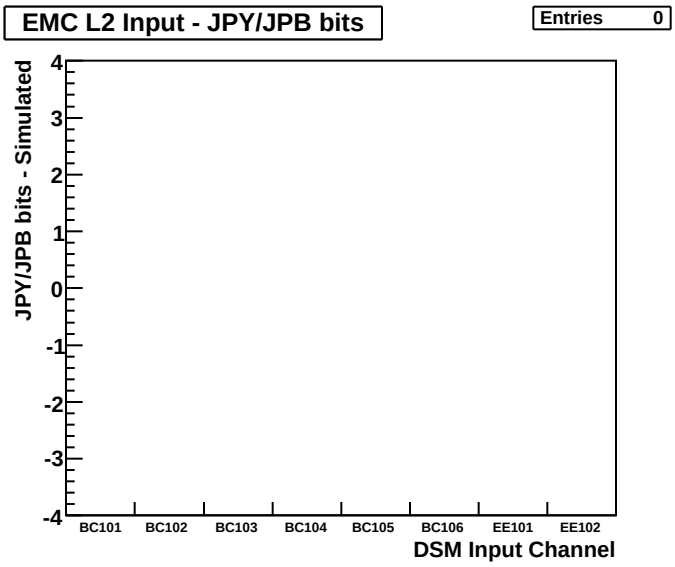

Endcap EMC L1 Input - High Tower Bits
Entries 1648668
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

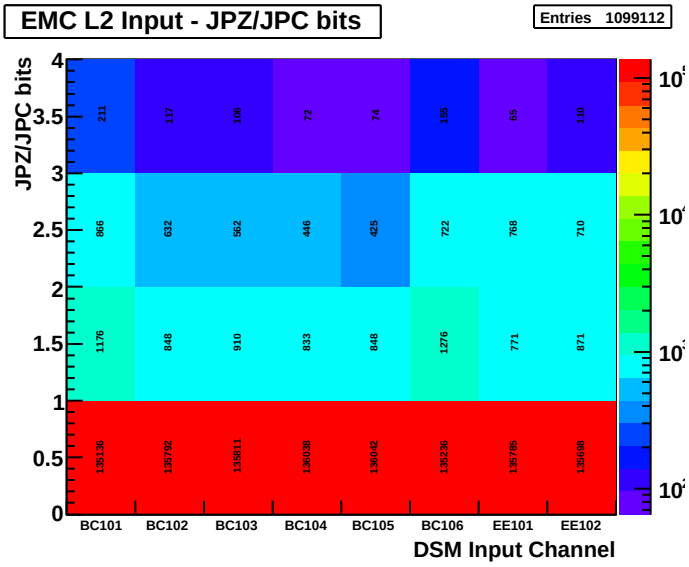

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

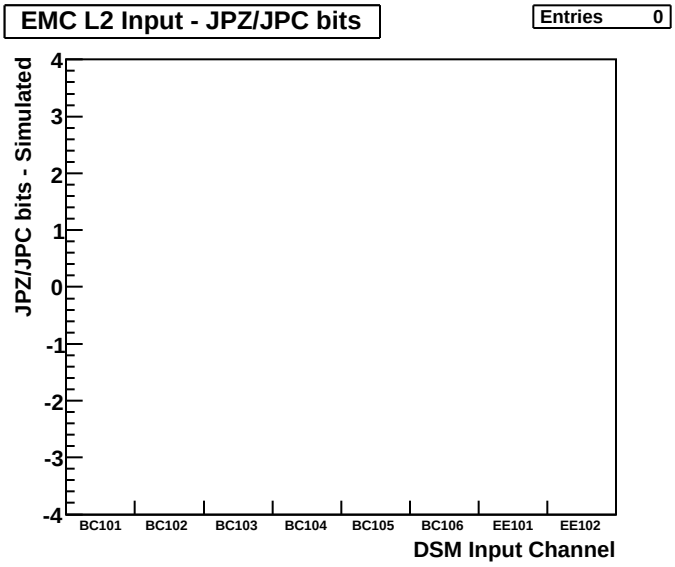

EMC L2 Input - JPX/JPA bits
Entries 1099112
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
636 246 220 96 65 211 84 385
2401 1173 1024 569 386 897 573 2042
2396 1311 1292 969 777 1126 730 1304
131956 134659 134853 135755 136161 135155 136002 133658

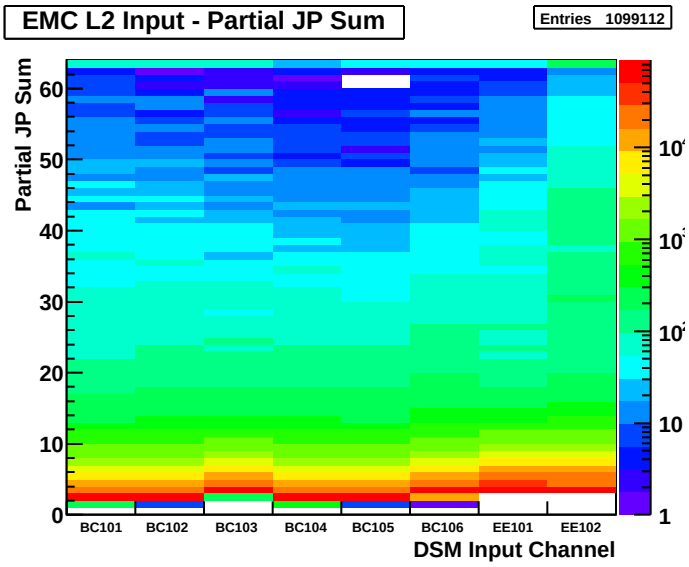

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

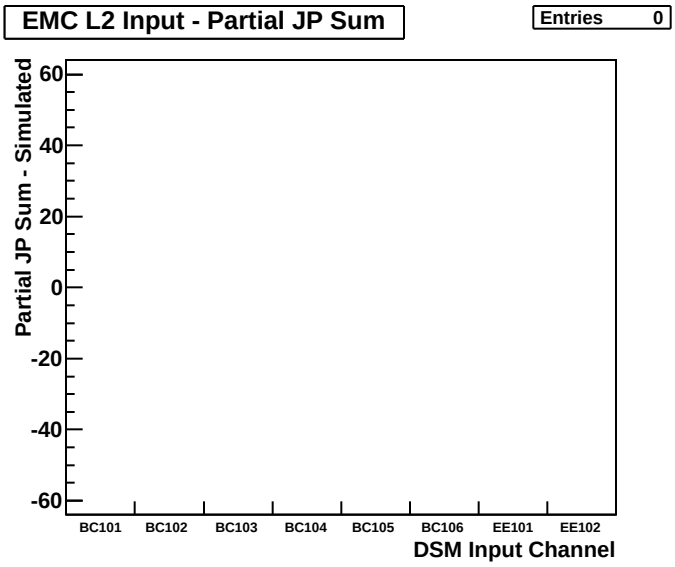

EMC L2 Input - JPY/JPB bits
Entries 1099112
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
371 137 119 88 58 166 139 205
1278 676 605 463 388 830 808 1197
1763 906 875 826 889 1413 557 927
133977 135670 135790 136012 136054 134980 135885 135060

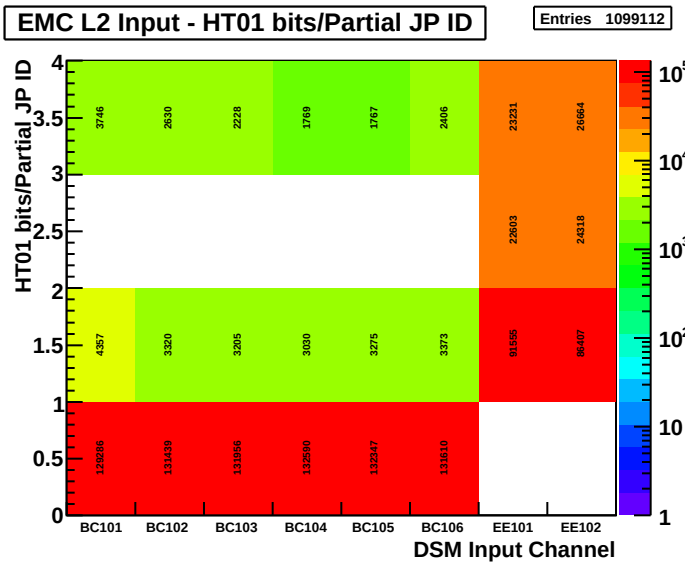

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

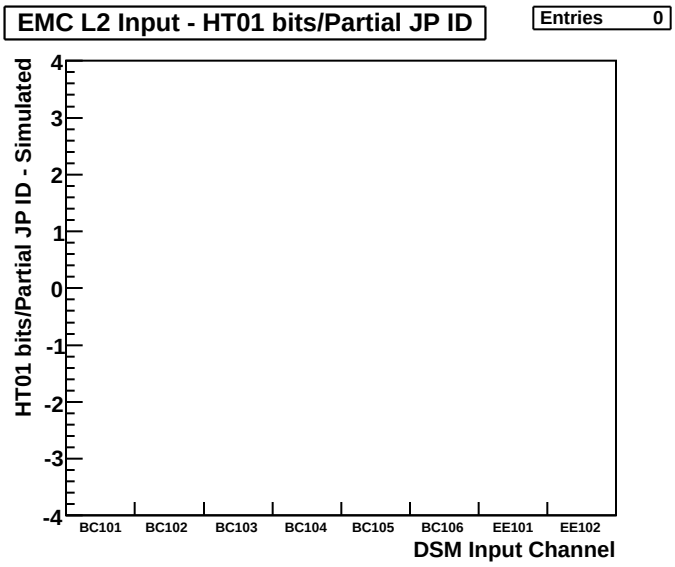

EMC L2 Input - JPZ/JPC bits
Entries 1099112
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
211 117 106 72 74 155 65 110
866 632 562 446 425 722 768 710
1176 848 910 833 848 1276 771 871
135136 135792 135811 136038 136042 135238 135785 135588

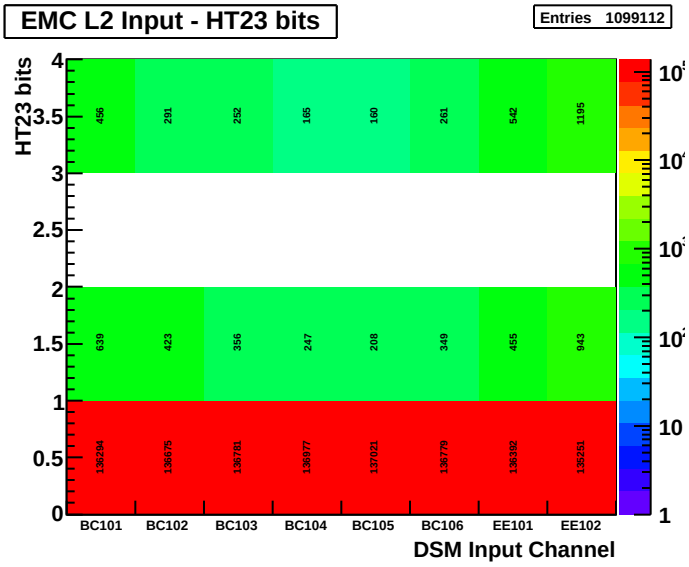

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

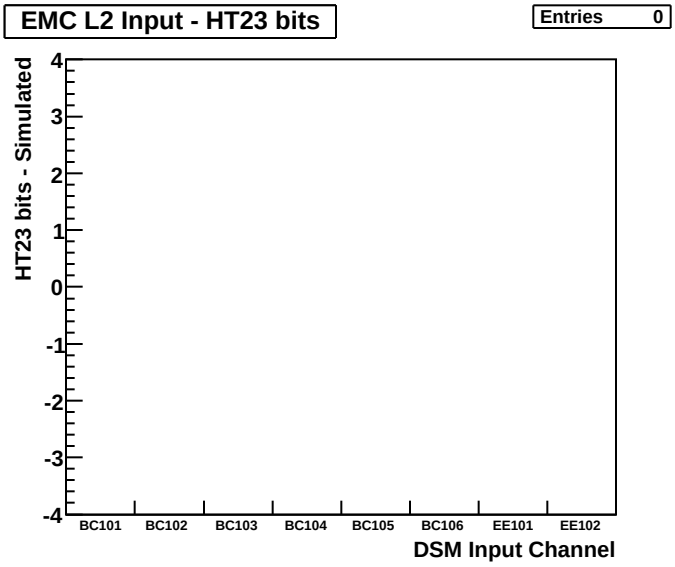

EMC L2 Input - Partial JP Sum
Entries 1099112
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

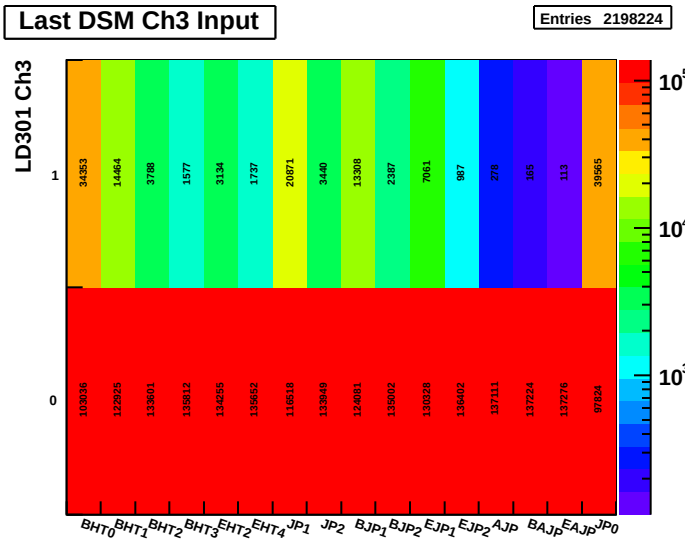

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

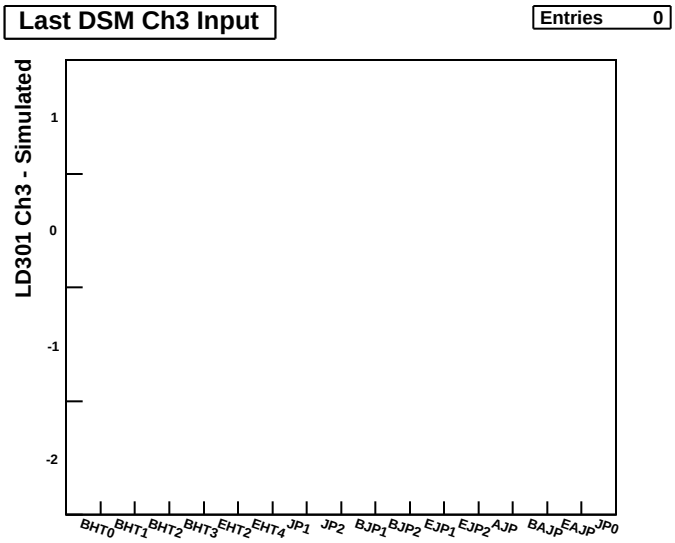

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1099112
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

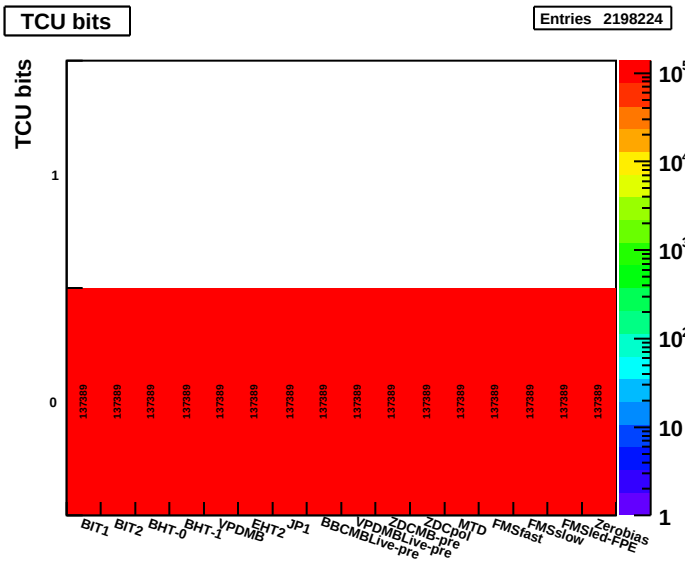

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

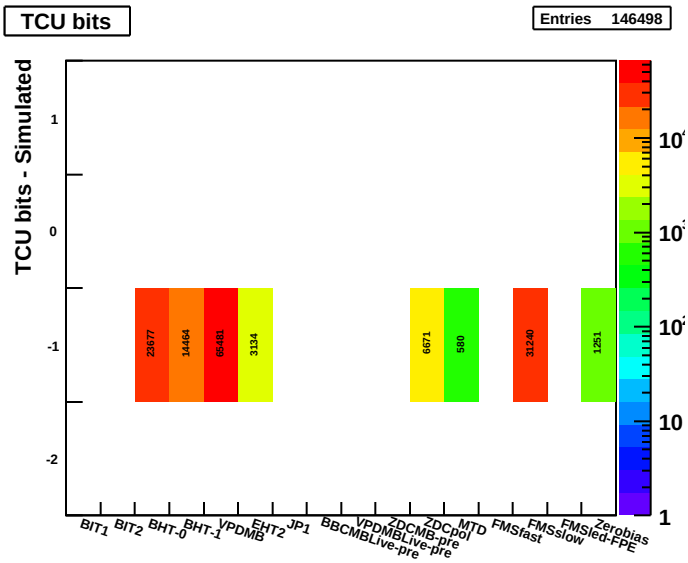

EMC L2 Input - HT23 bits
Entries 1099112
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

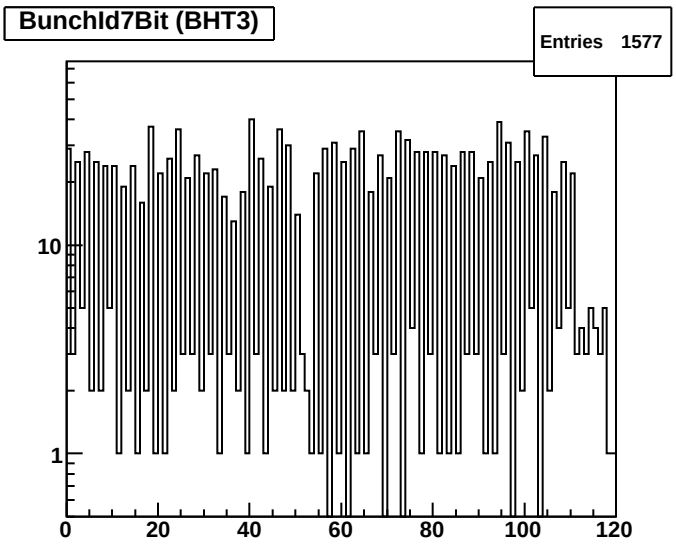

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

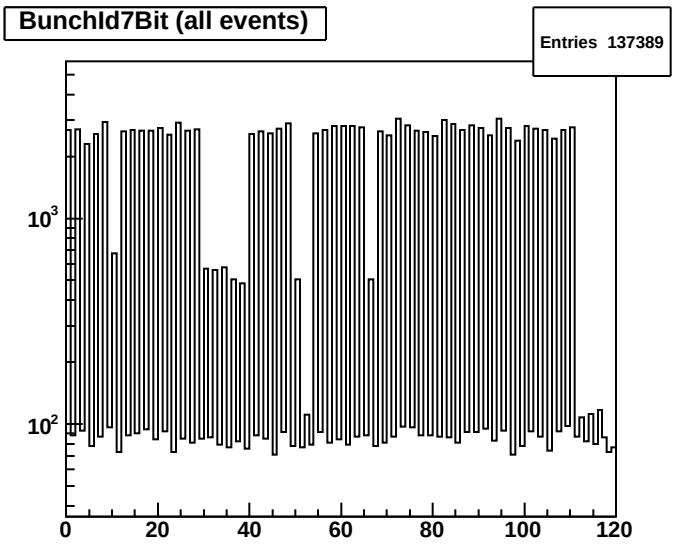

Last DSM Ch3 Input
Entries 2198224
LD301 Ch3
1
0
34353 14464 3788 1577 3134 1737 20871 3440 13308 2367 7061 987 278 165 113 39565
103036 122925 133601 135812 134255 135652 116518 133949 124081 135002 130328 136402 137111 137224 137276 97834
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

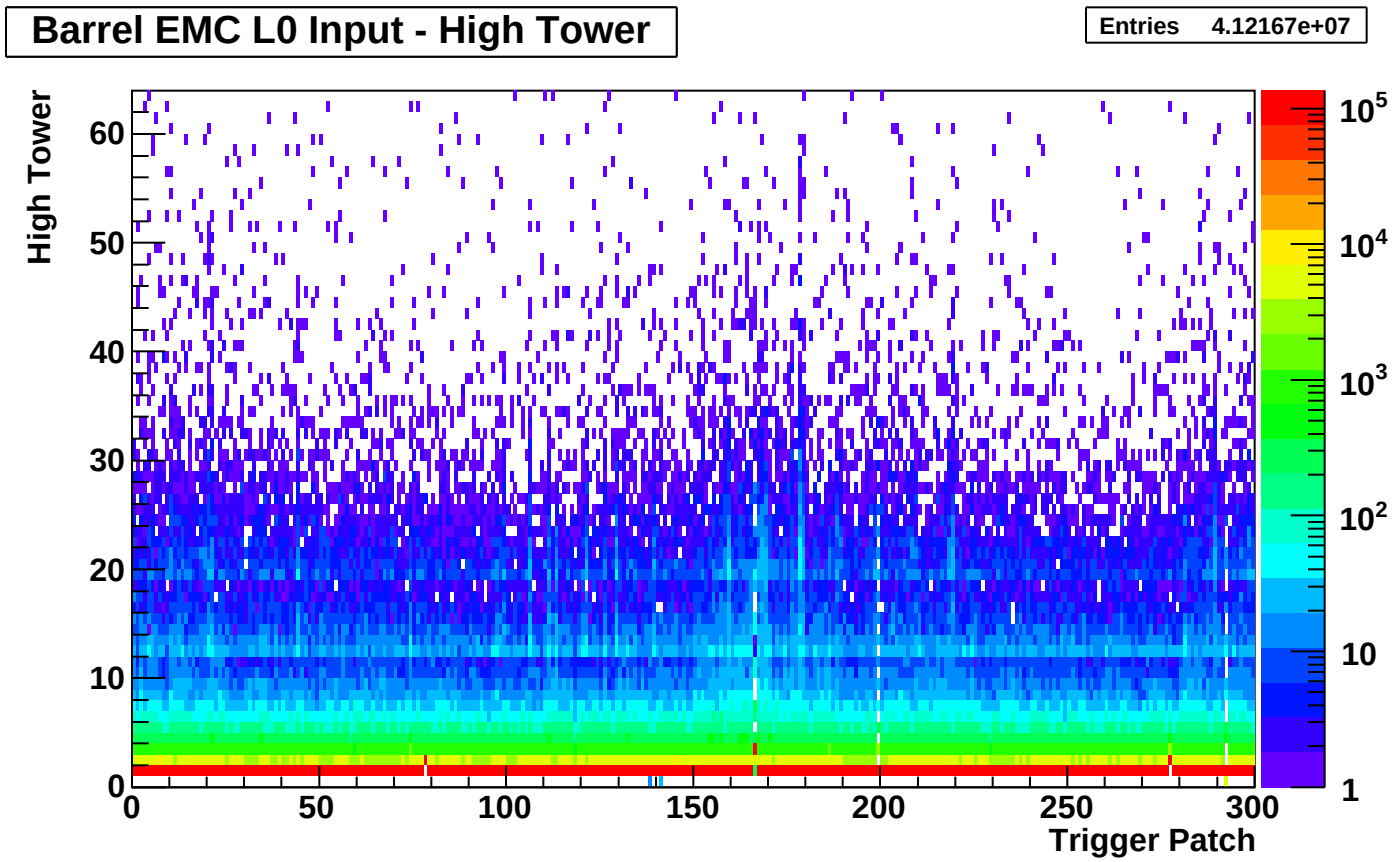

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

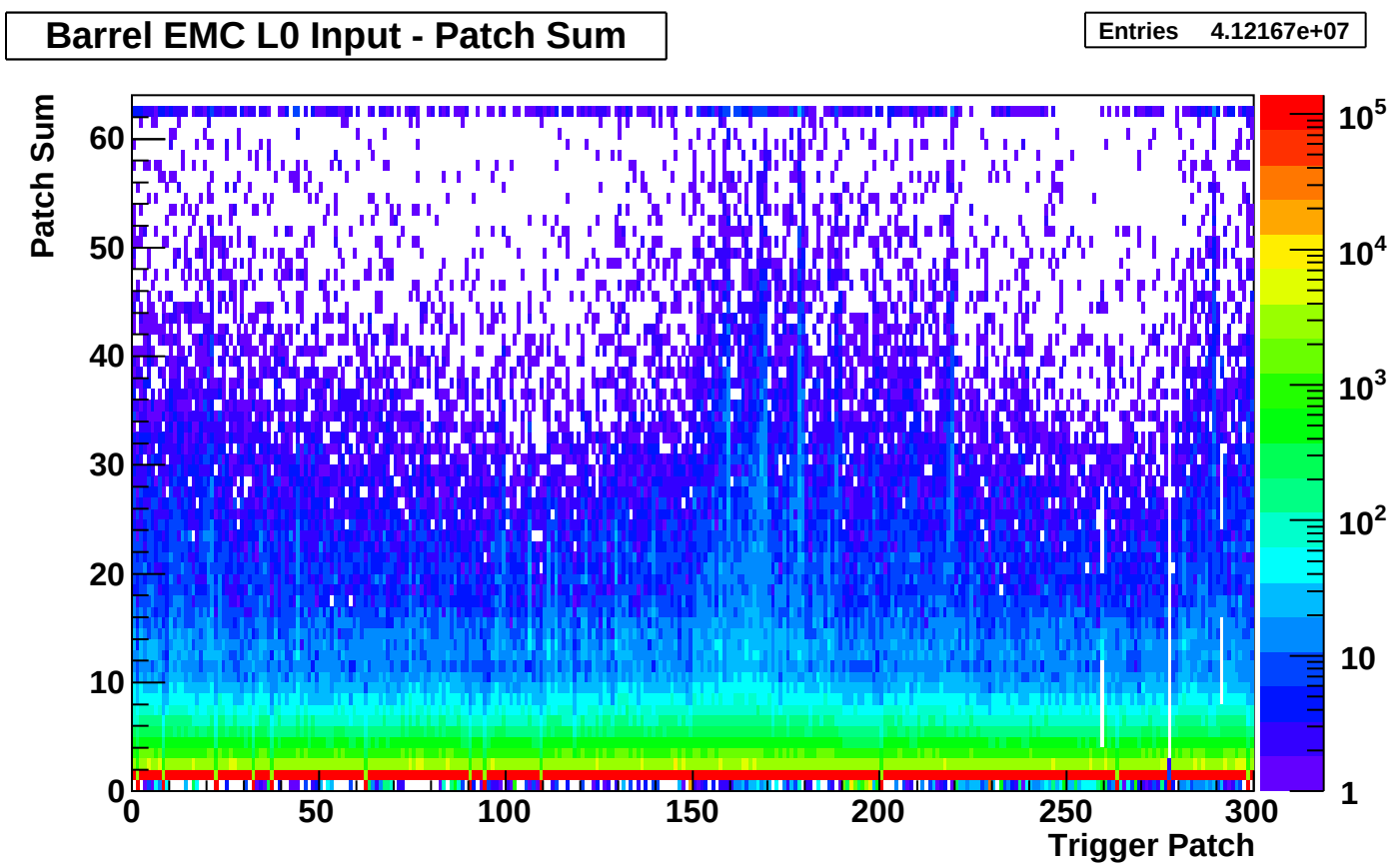

TCU bits
Entries 2198224
TCU bits
1
0
137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389 137389
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

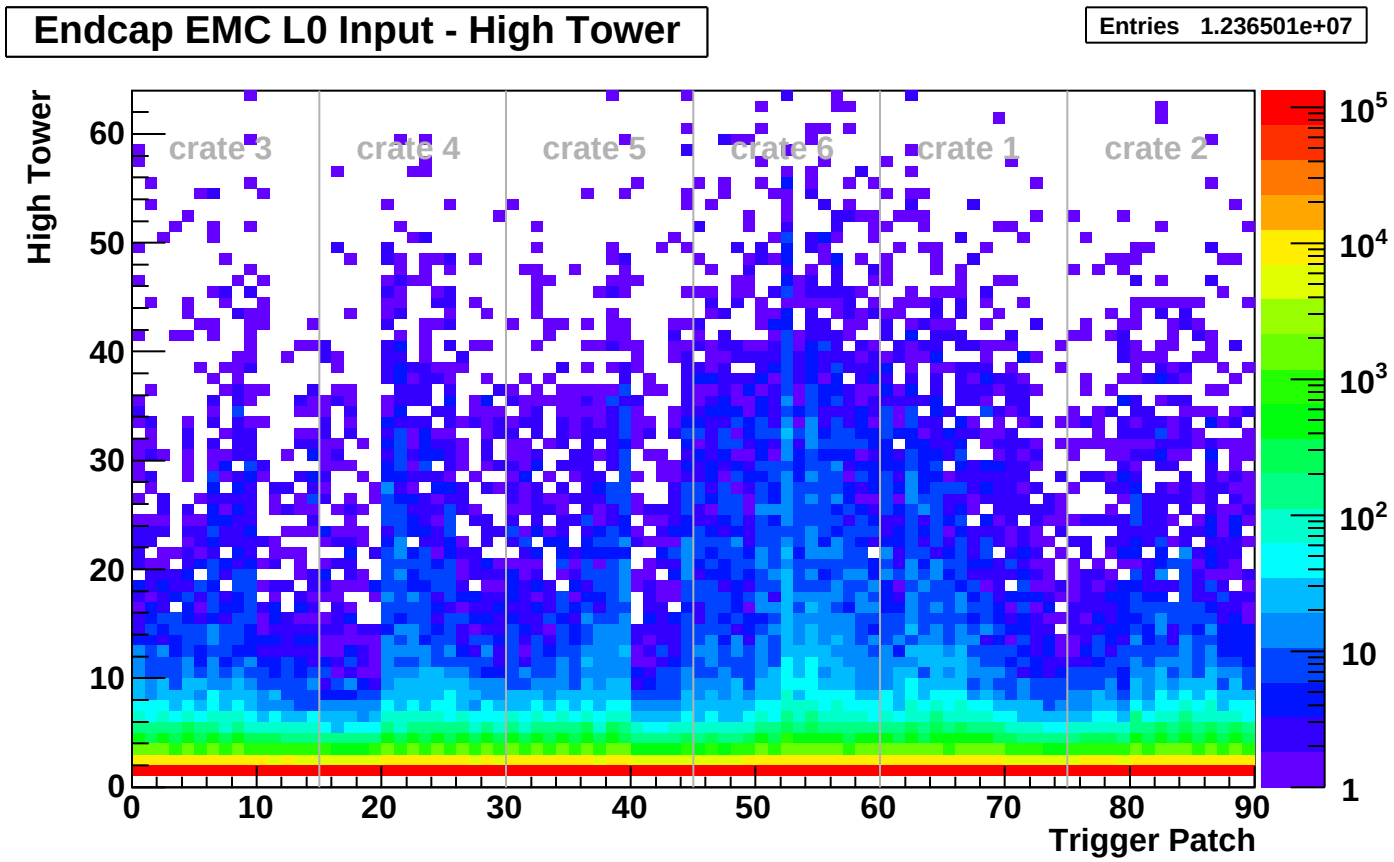

TCU bits
Entries 146498
TCU bits - Simulated
1
0
-1
-2
23677 14464 65481 3134 6671 580 31240 1251
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

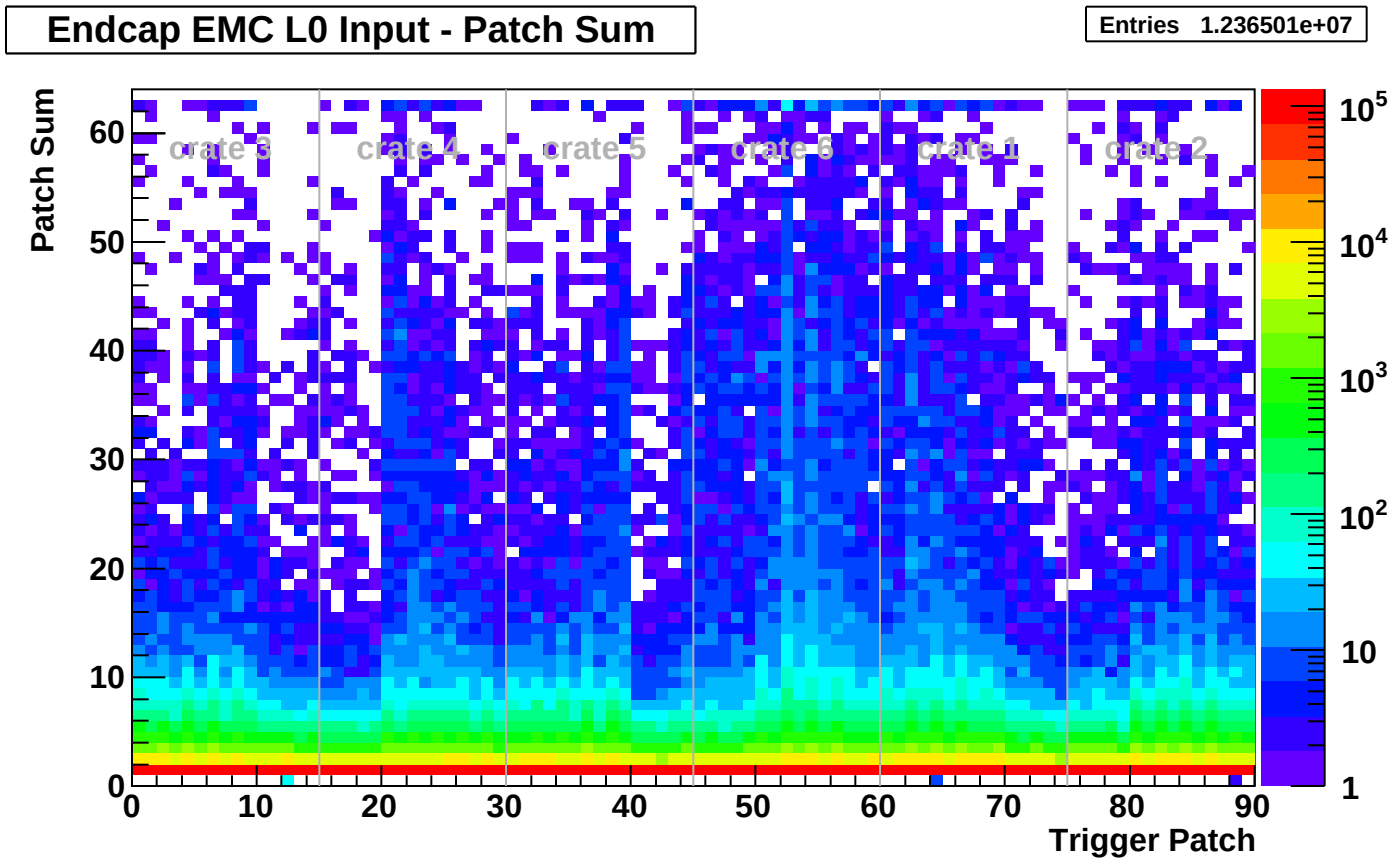

BunchId7Bit (BHT3)
Entries 1577
10
1
0 20 40 60 80 100 120

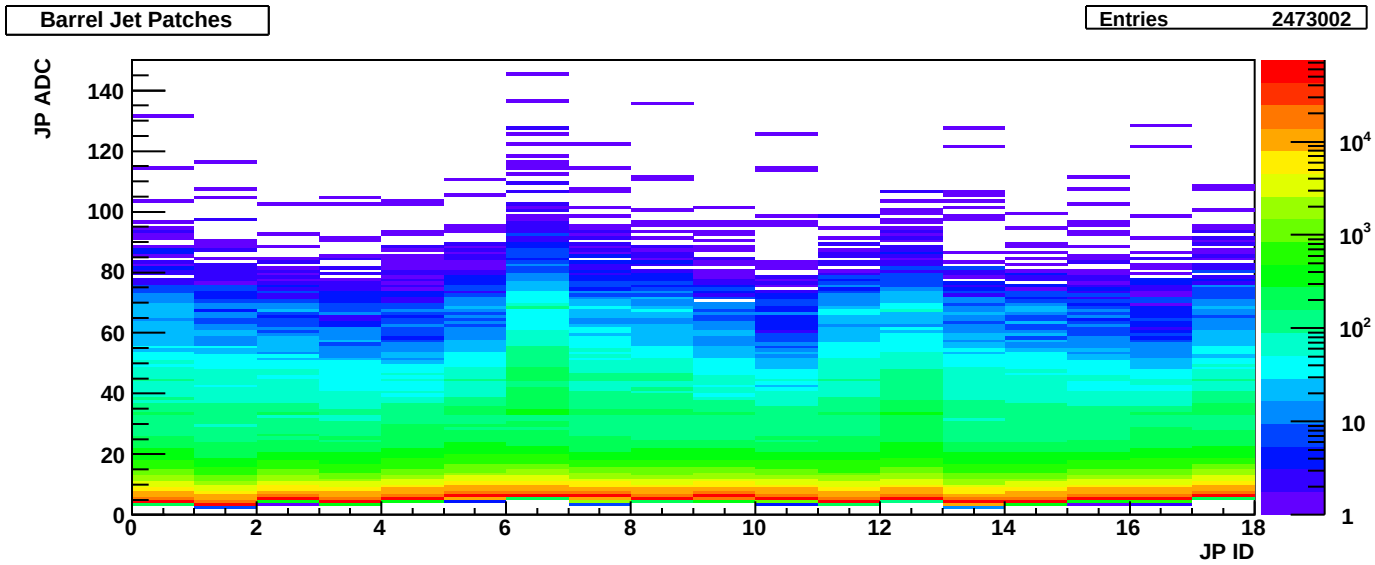

BunchId7Bit (all events)
Entries 137389
10^3
10^2
0 20 40 60 80 100 120

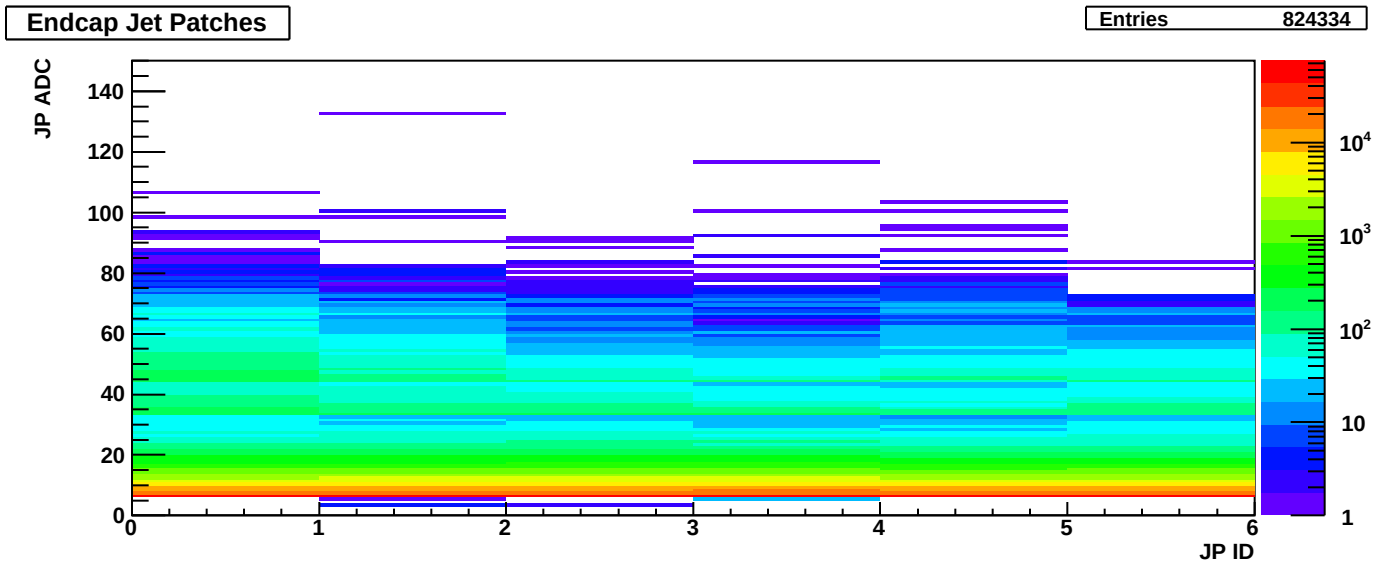

Barrel EMC L0 Input - High Tower
Entries 4.12167e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

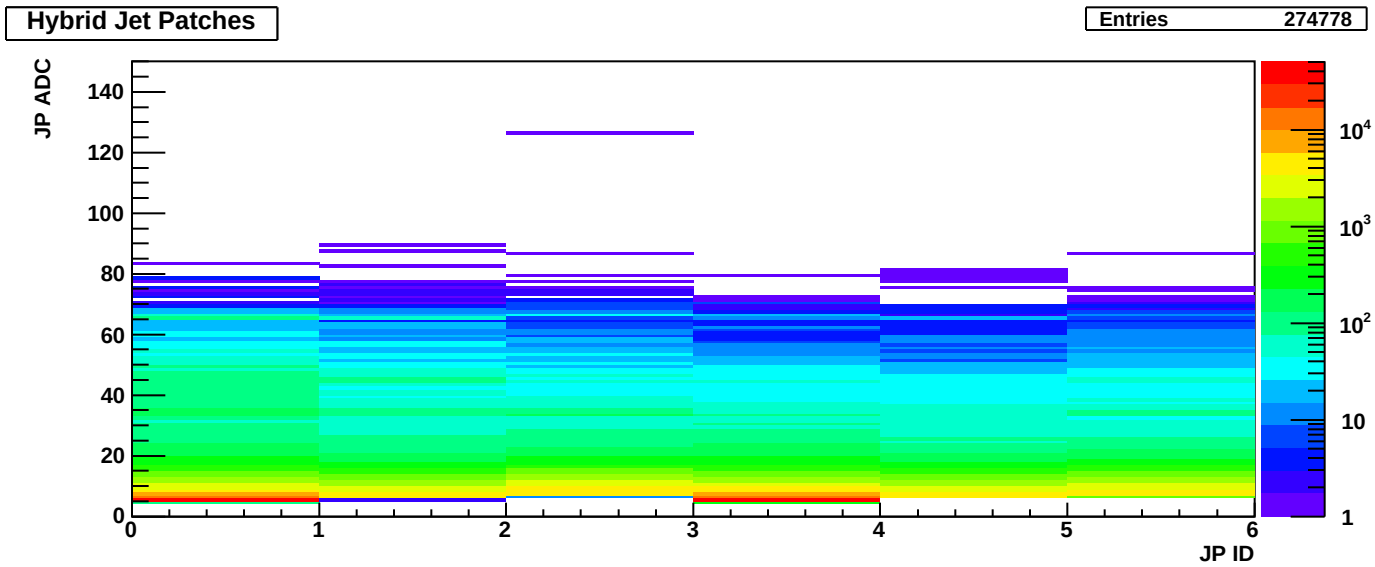

Barrel EMC L0 Input - Patch Sum
Entries 4.12167e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

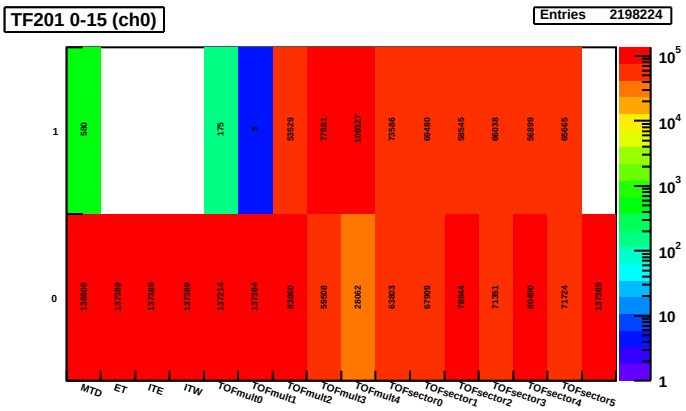

Endcap EMC L0 Input - High Tower
Entries 1.236501e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

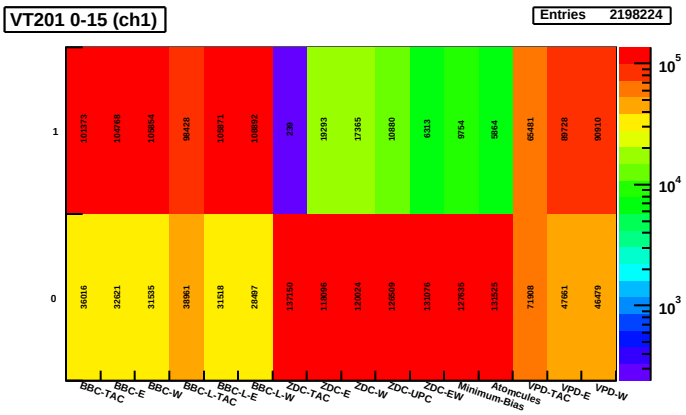

Endcap EMC L0 Input - Patch Sum
Entries 1.236501e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

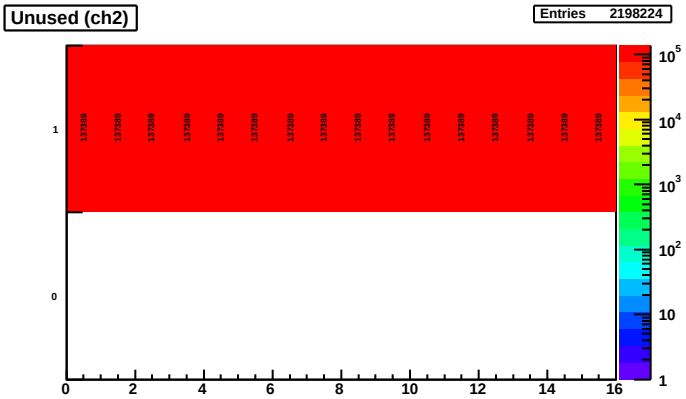

Barrel Jet Patches
Entries 2473002
JP ADC
JP ID

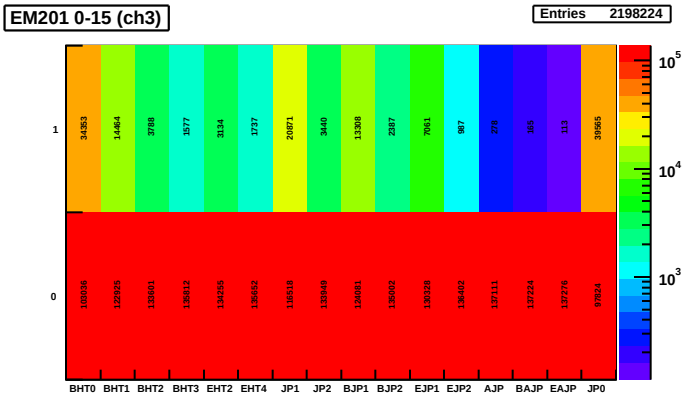

Endcap Jet Patches
Entries 824334
JP ADC
JP ID

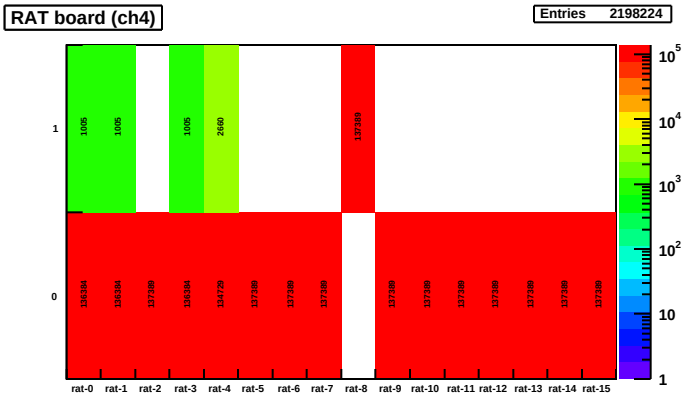

Hybrid Jet Patches
Entries 274778
JP ADC
JP ID

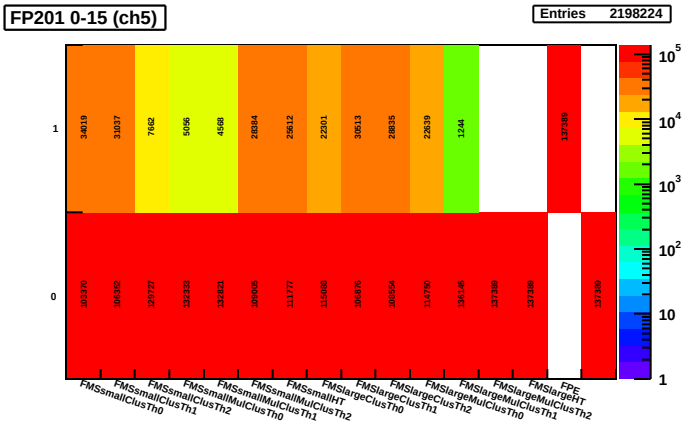

TF201 0-15 (ch0)
Entries 2198224
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

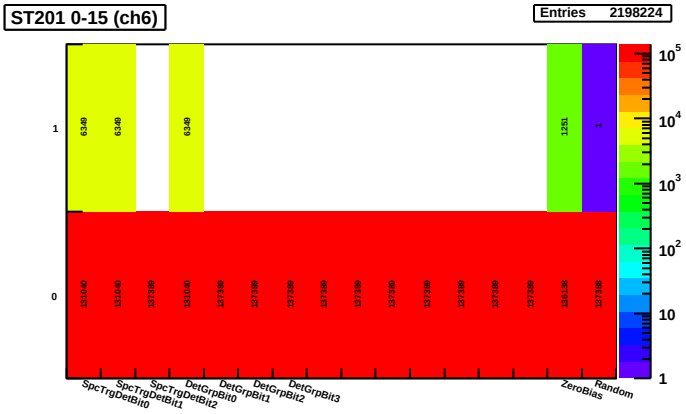

VT201 0-15 (ch1)
Entries 2198224
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

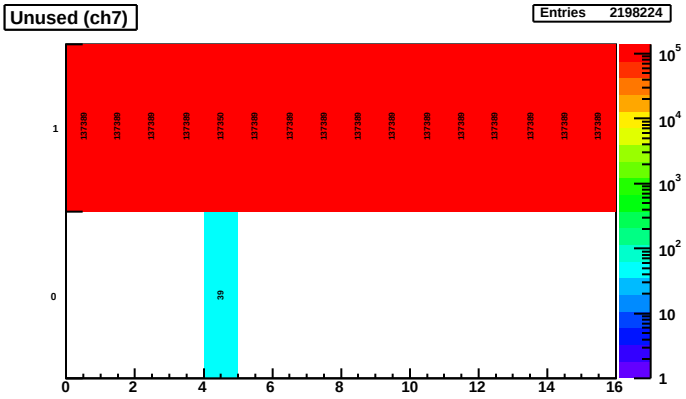

Unused (ch2)
Entries 2198224

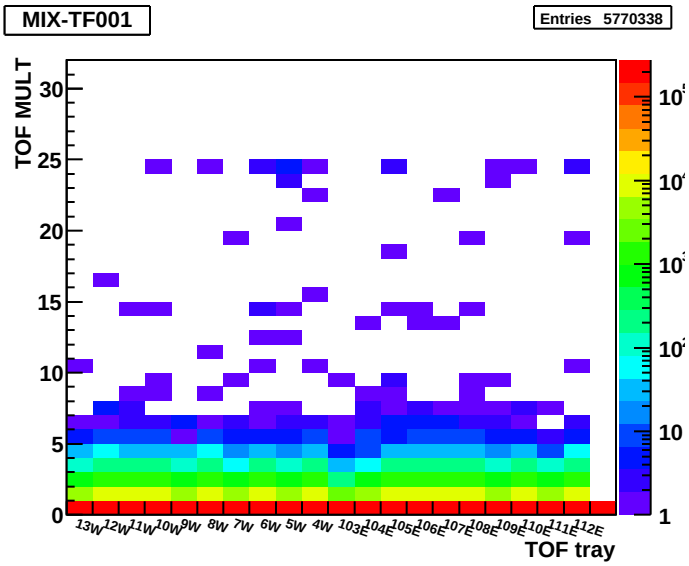

EM201 0-15 (ch3)
Entries 2198224
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

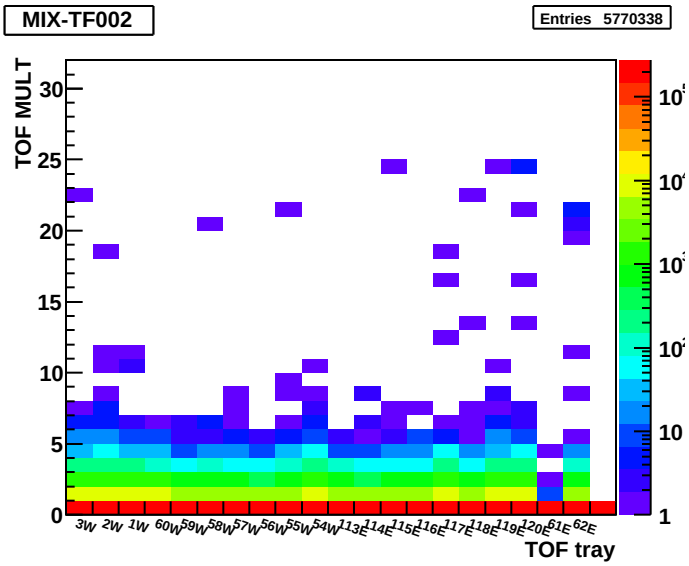

RAT board (ch4)
Entries 2198224
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

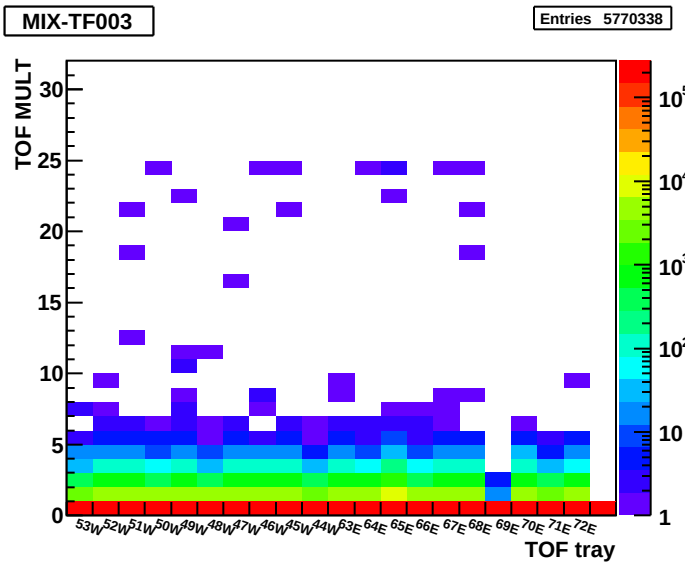

FP201 0-15 (ch5)
Entries 2198224
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

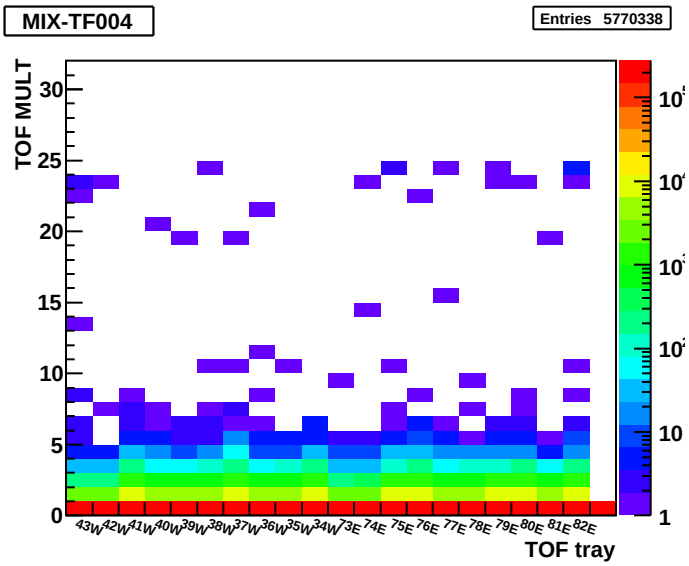

ST201 0-15 (ch6)
Entries 2198224
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

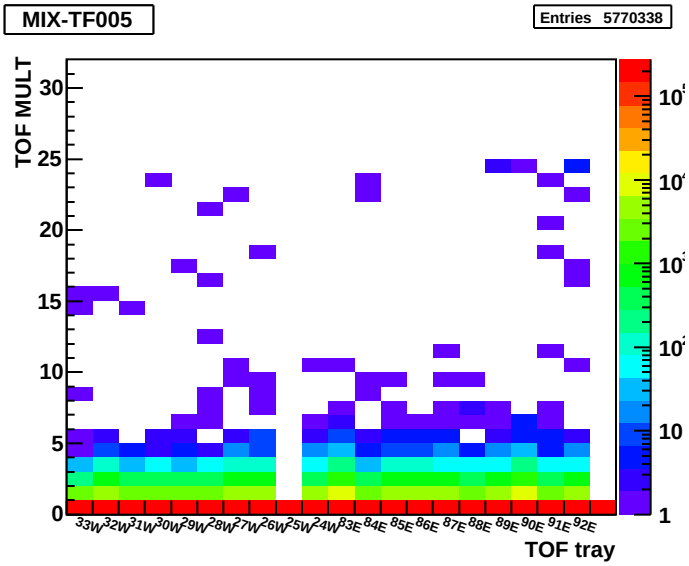

Unused (ch7)
Entries 2198224

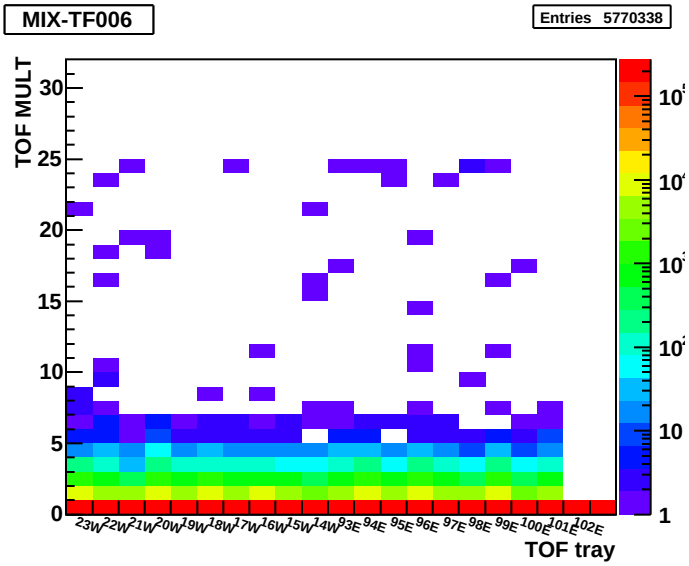

MIX-TF001
Entries 5770338
TOF MULT
TOF tray

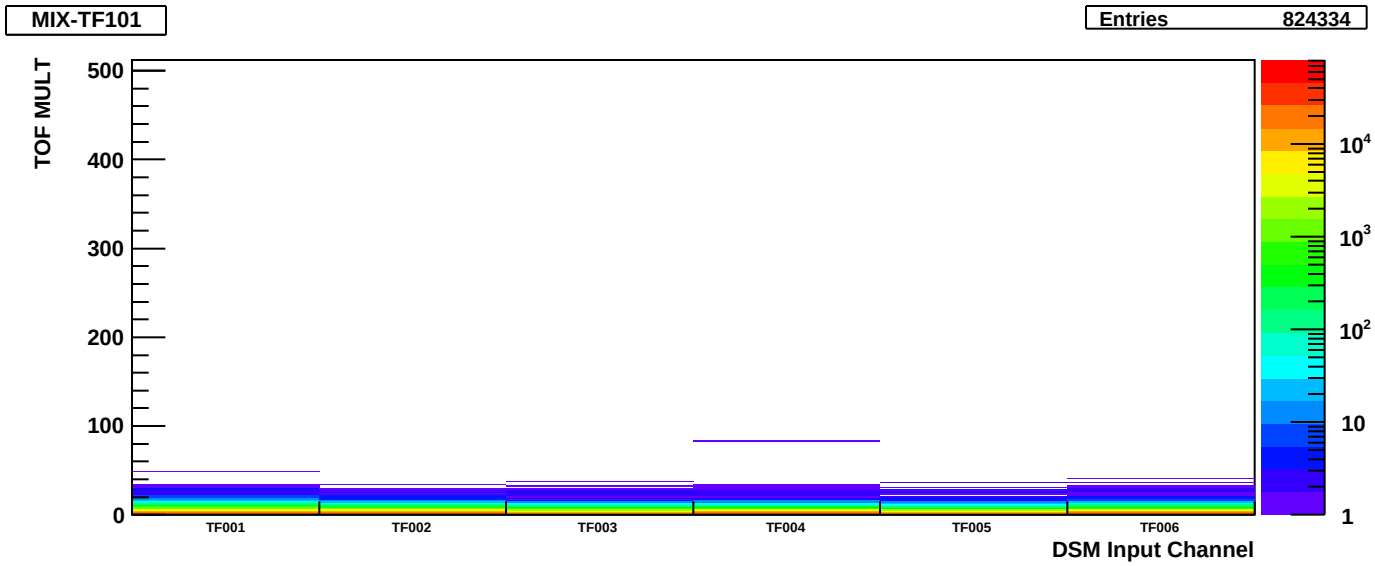

MIX-TF002
Entries 5770338
TOF MULT
TOF tray

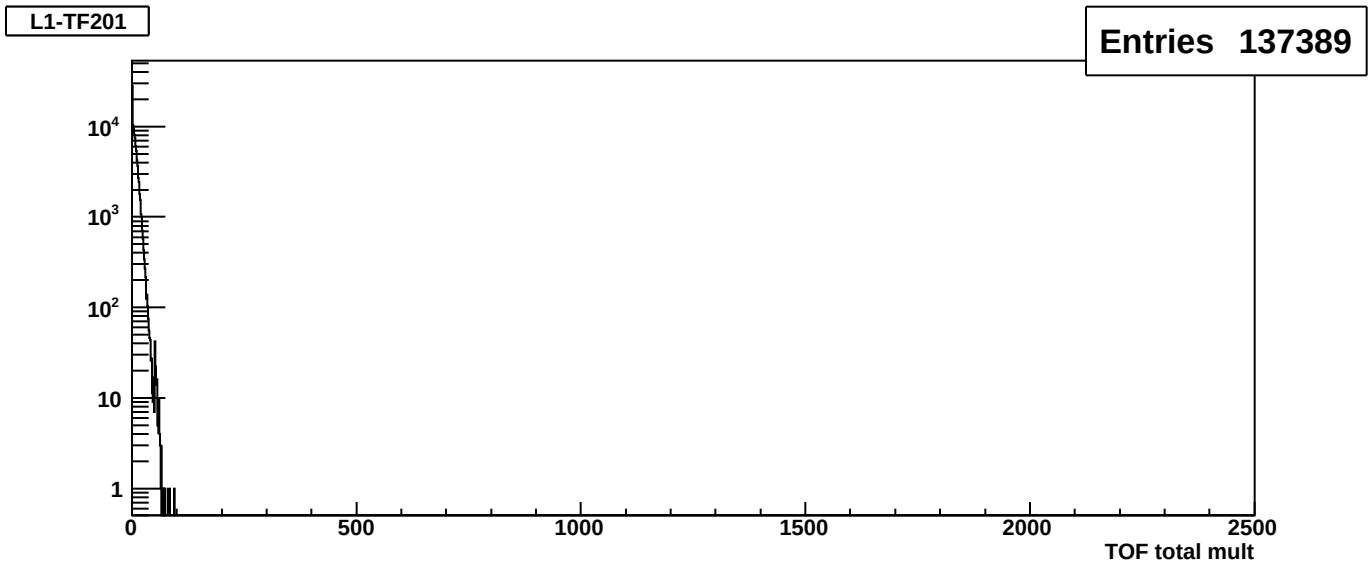

MIX-TF003
Entries 5770338
TOF MULT
TOF tray

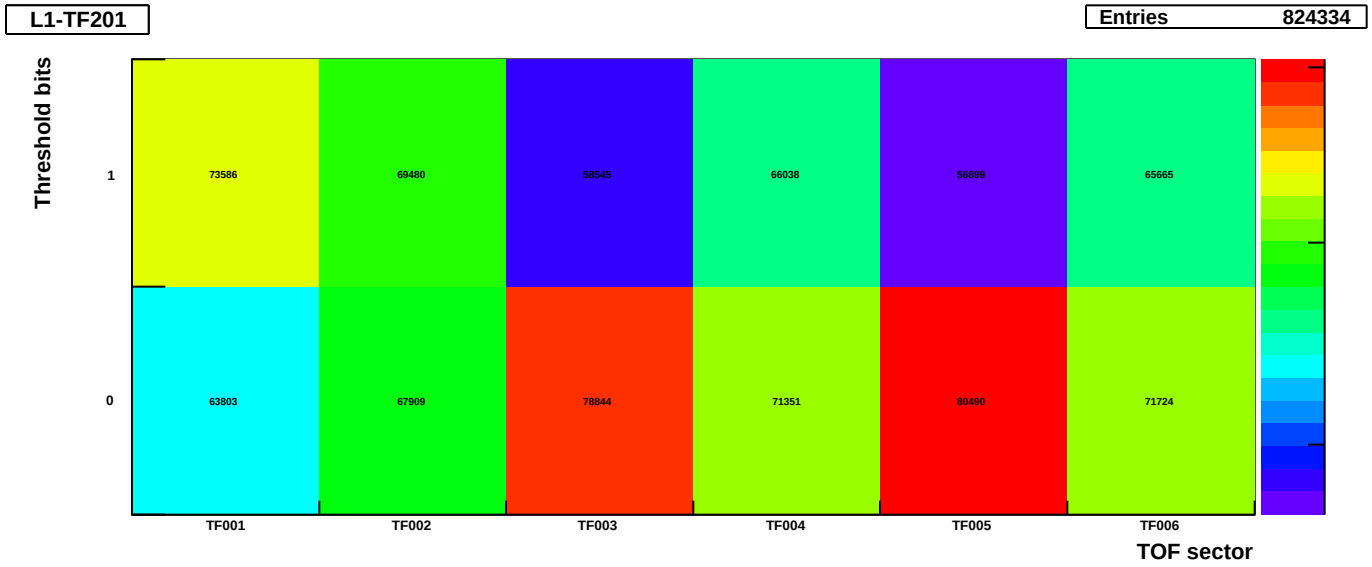

MIX-TF004
Entries 5770338
TOF MULT
TOF tray

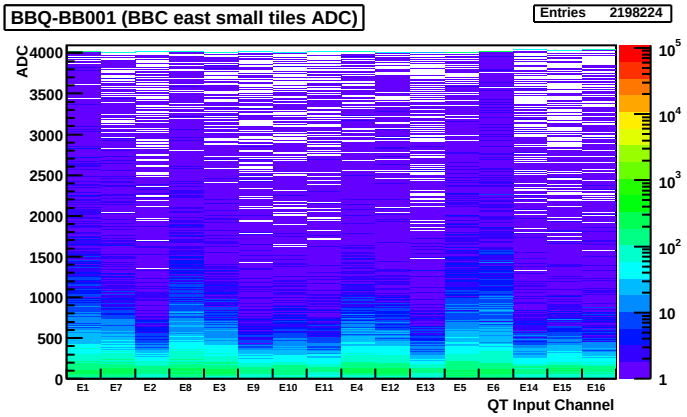

MIX-TF005
Entries 5770338
TOF MULT
TOF tray

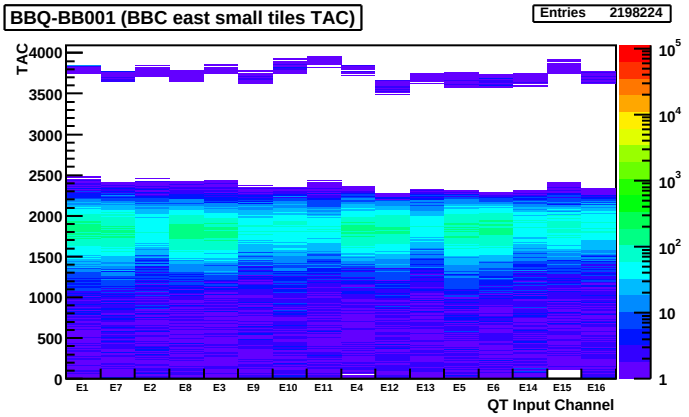

MIX-TF006
Entries 5770338
TOF MULT
TOF tray

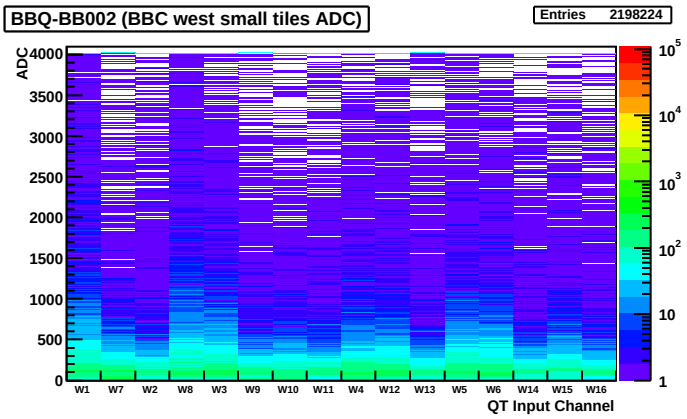

MIX-TF101
Entries 824334
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

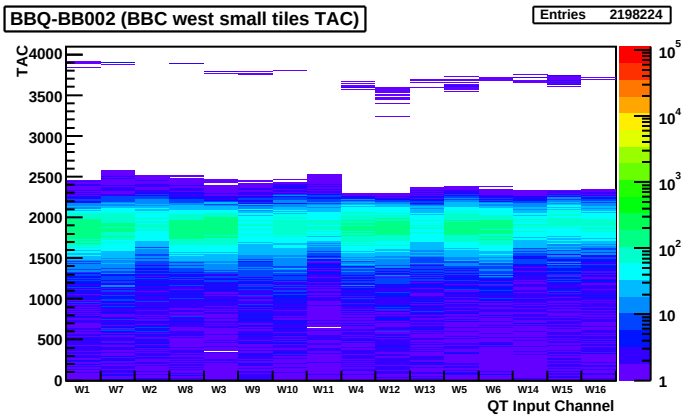

L1-TF201
Entries 137389
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

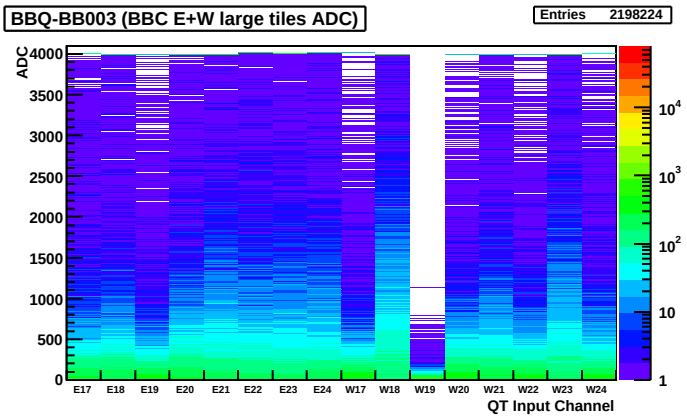

L1-TF201
Entries 824334
Threshold bits
1
0
73586
69480
58545
66038
56899
65665
63803
67909
78844
71351
80490
71724
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

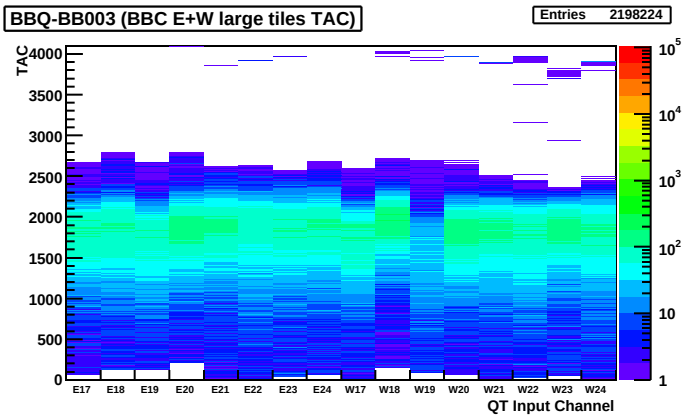

BBQ-BB001 (BBC east small tiles ADC)
Entries 2198224
ADC
QT Input Channel

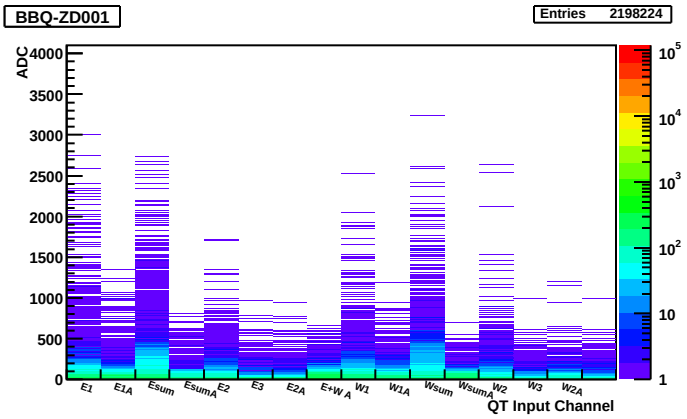

BBQ-BB001 (BBC east small tiles TAC)
Entries 2198224
TAC
QT Input Channel

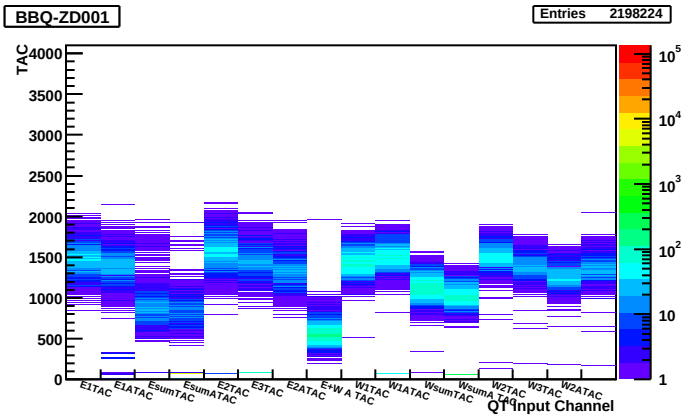

BBQ-BB002 (BBC west small tiles ADC)
Entries 2198224
ADC
QT Input Channel

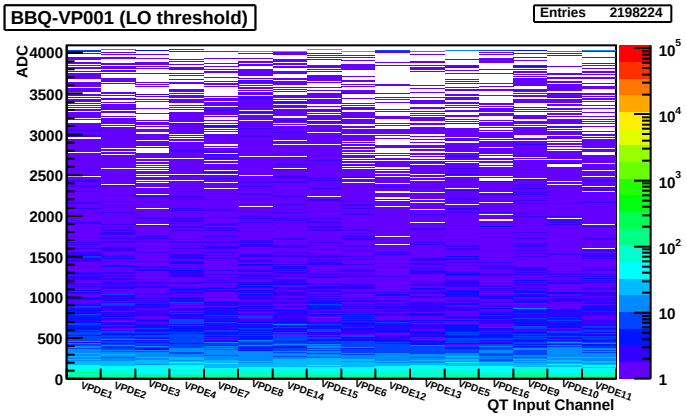

BBQ-BB002 (BBC west small tiles TAC)
Entries 2198224
TAC
QT Input Channel

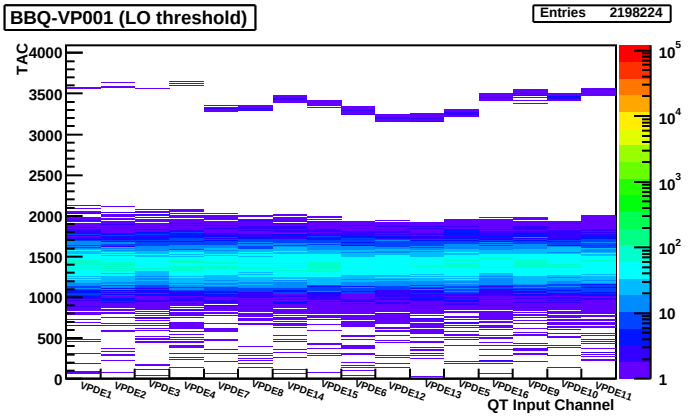

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2198224
ADC
QT Input Channel

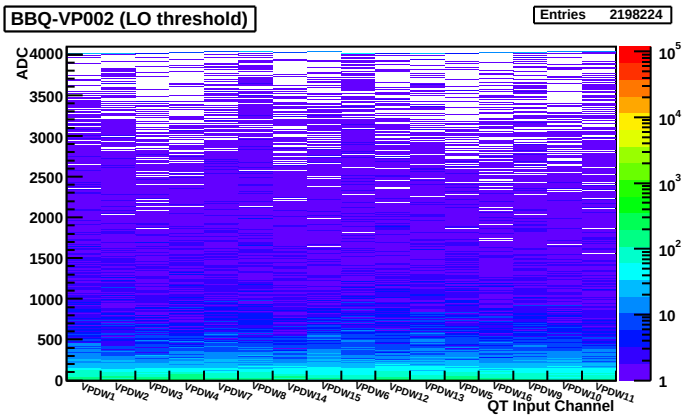

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2198224
TAC
QT Input Channel

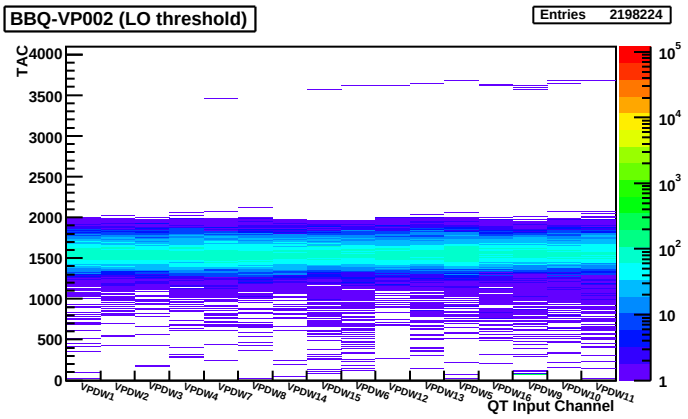

BBQ-ZD001
Entries 2198224
ADC
QT Input Channel

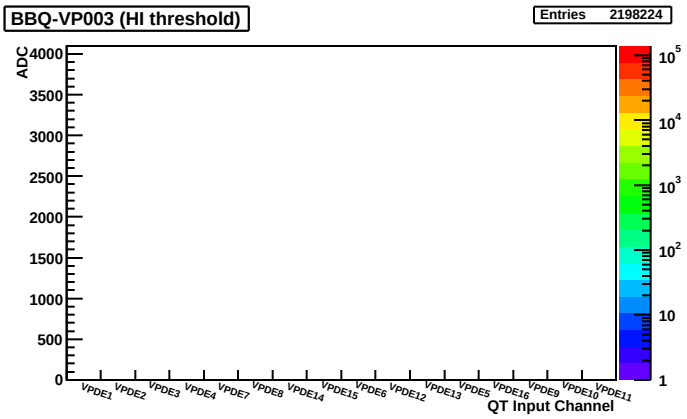

BBQ-ZD001
Entries 2198224
TAC
QT Input Channel

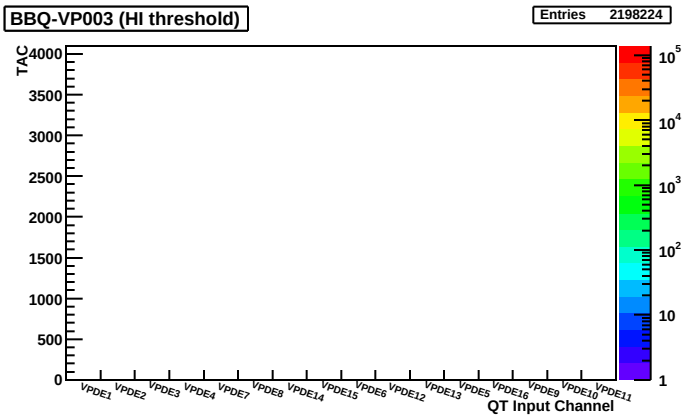

BBQ-VP001 (LO threshold)
Entries 2198224
ADC
QT Input Channel

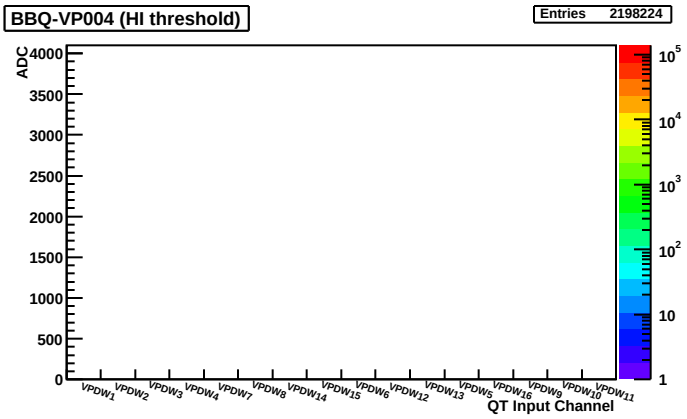

BBQ-VP001 (LO threshold)
Entries 2198224
TAC
QT Input Channel

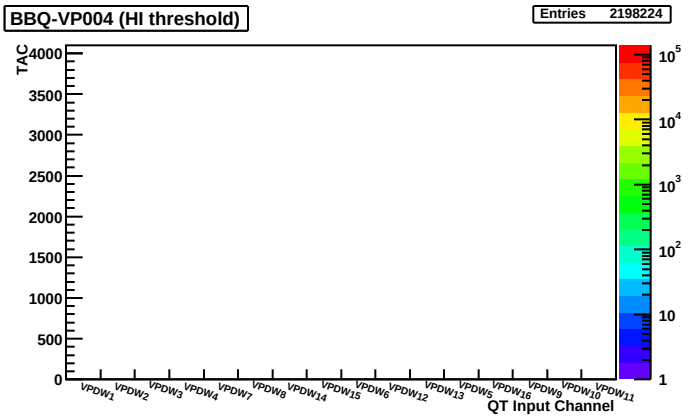

BBQ-VP002 (LO threshold)
Entries 2198224
ADC
QT Input Channel

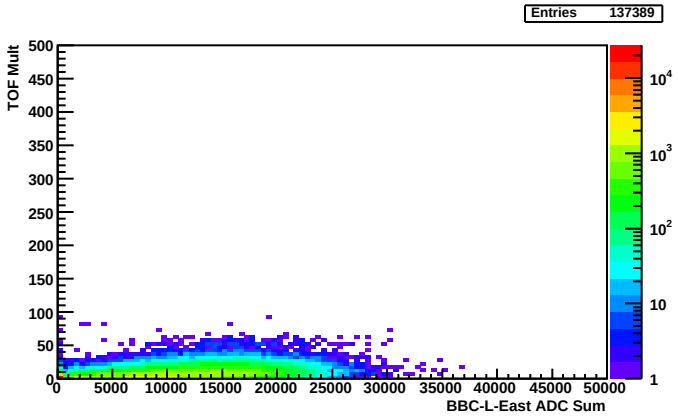

BBQ-VP002 (LO threshold)
Entries 2198224
TAC
QT Input Channel

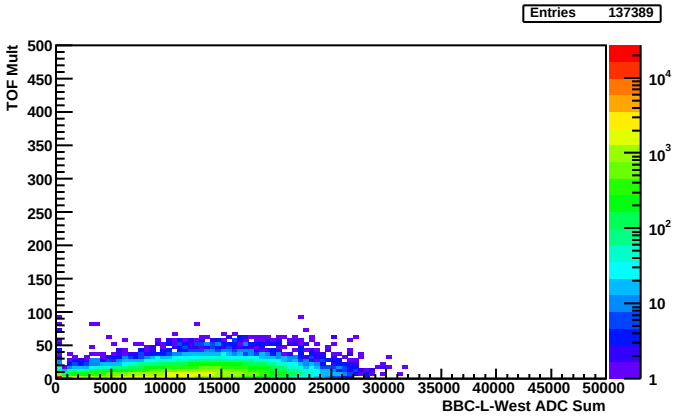

BBQ-VP003 (HI threshold)
Entries 2198224
ADC
QT Input Channel

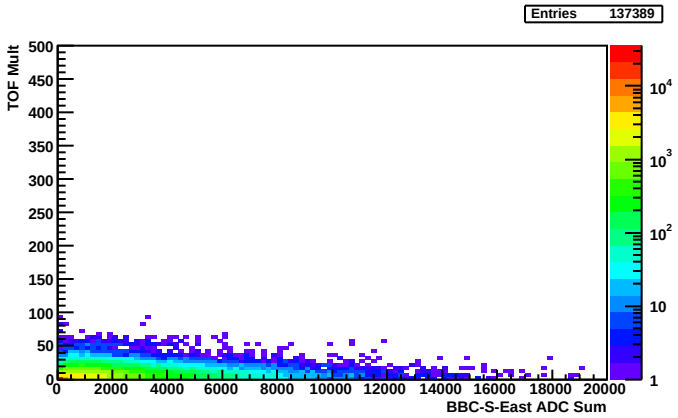

BBQ-VP003 (HI threshold)
Entries 2198224
TAC
QT Input Channel

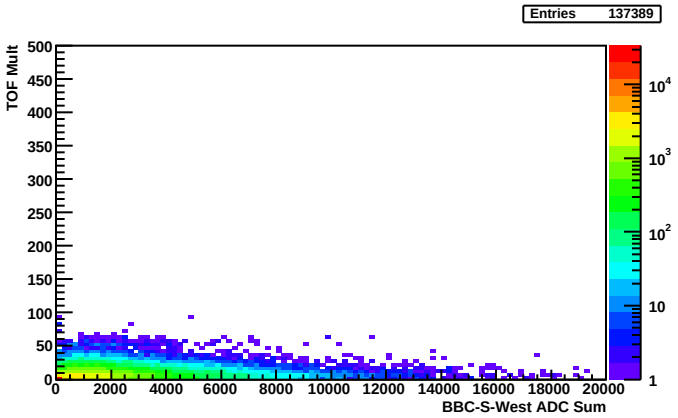

BBQ-VP004 (HI threshold)
Entries 2198224
ADC
QT Input Channel

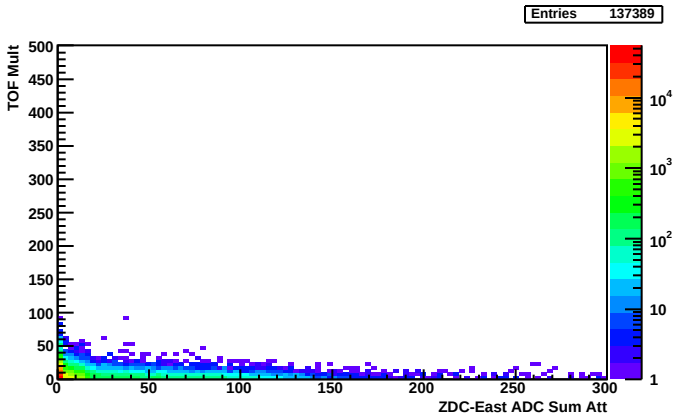

BBQ-VP004 (HI threshold)
Entries 2198224
TAC
QT Input Channel

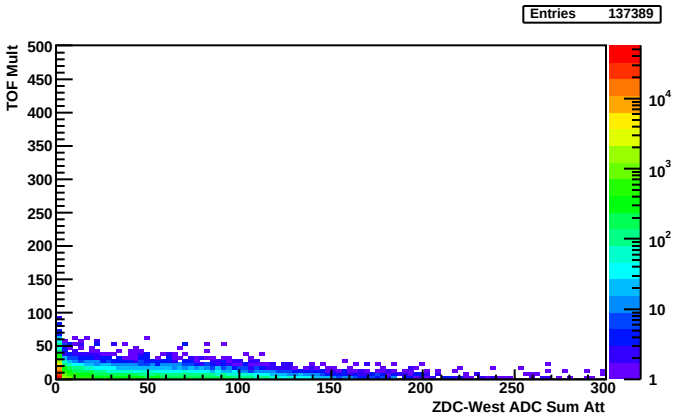

Entries 137389
TOF Mult
BBC-L-East ADC Sum

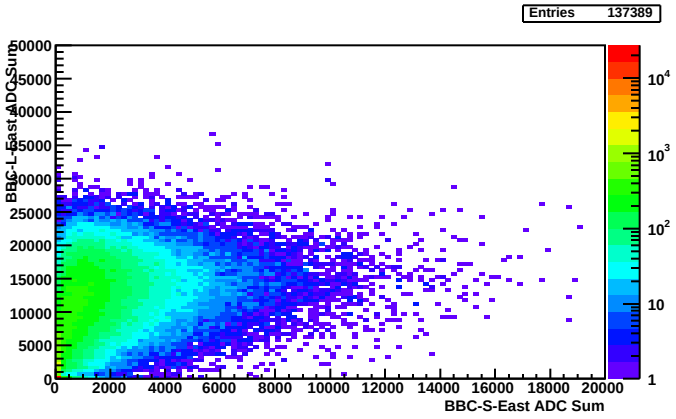

Entries 137389
TOF Mult
BBC-L-West ADC Sum

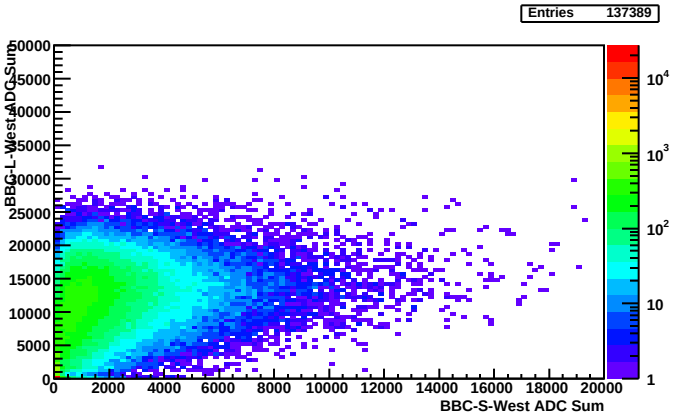

Entries 137389
TOF Mult
BBC-S-East ADC Sum

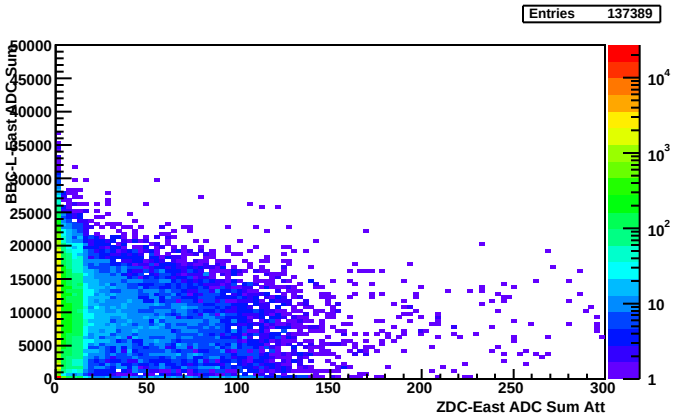

Entries 137389
TOF Mult
BBC-S-West ADC Sum

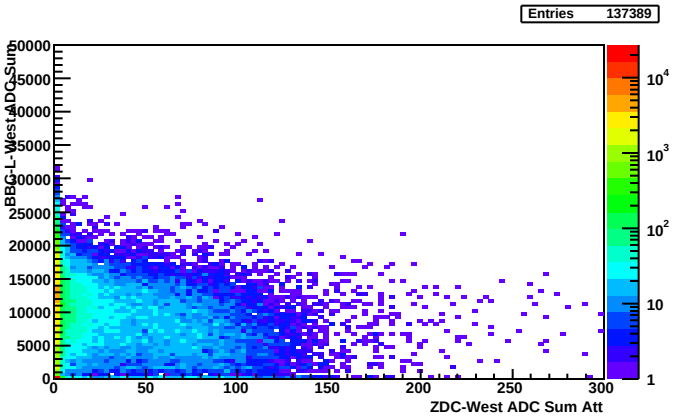

Entries 137389
TOF Mult
ZDC-East ADC Sum Att

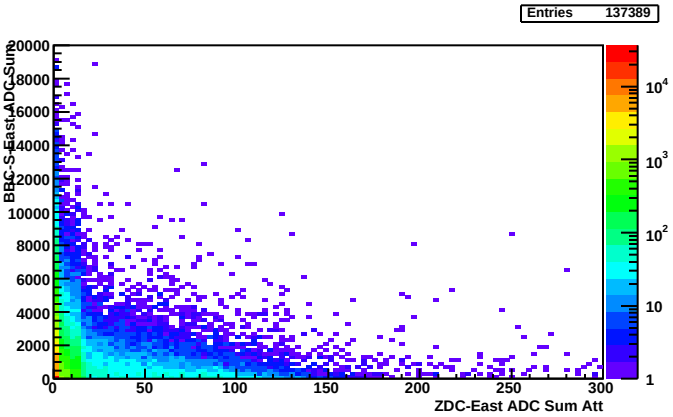

Entries 137389
TOF Mult
ZDC-West ADC Sum Att

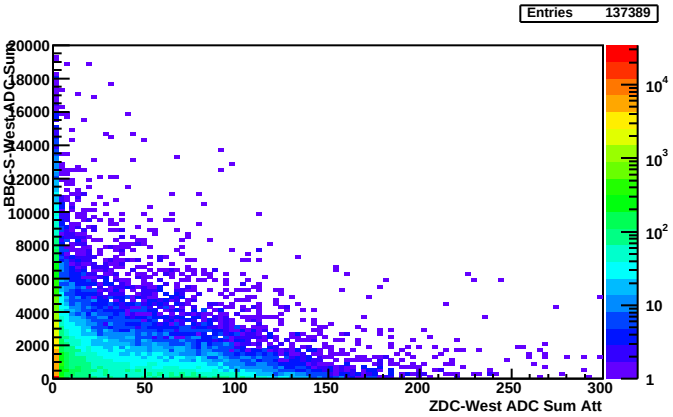

Entries 137389
BBC-L-East ADC Sum
BBC-S-East ADC Sum

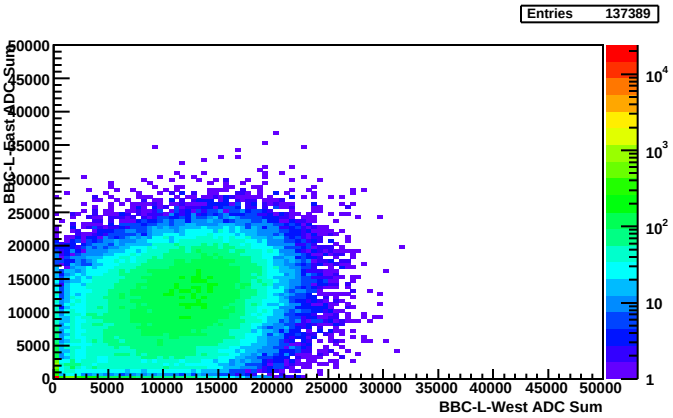

Entries 137389
BBC-L-West ADC Sum
BBC-S-West ADC Sum

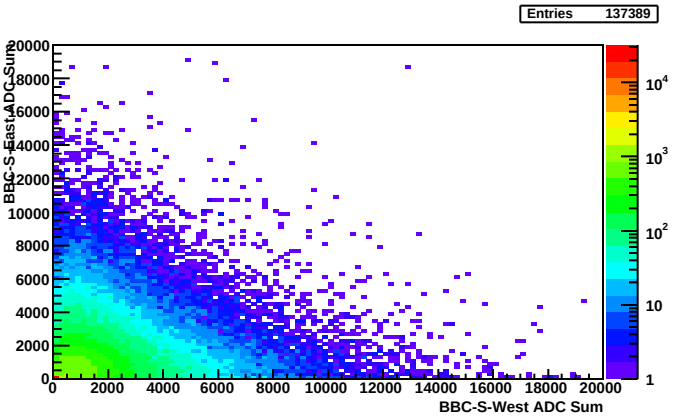

Entries 137389
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

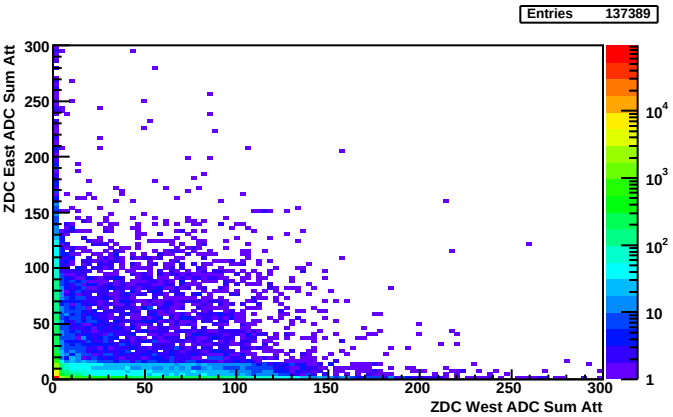

Entries 137389
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

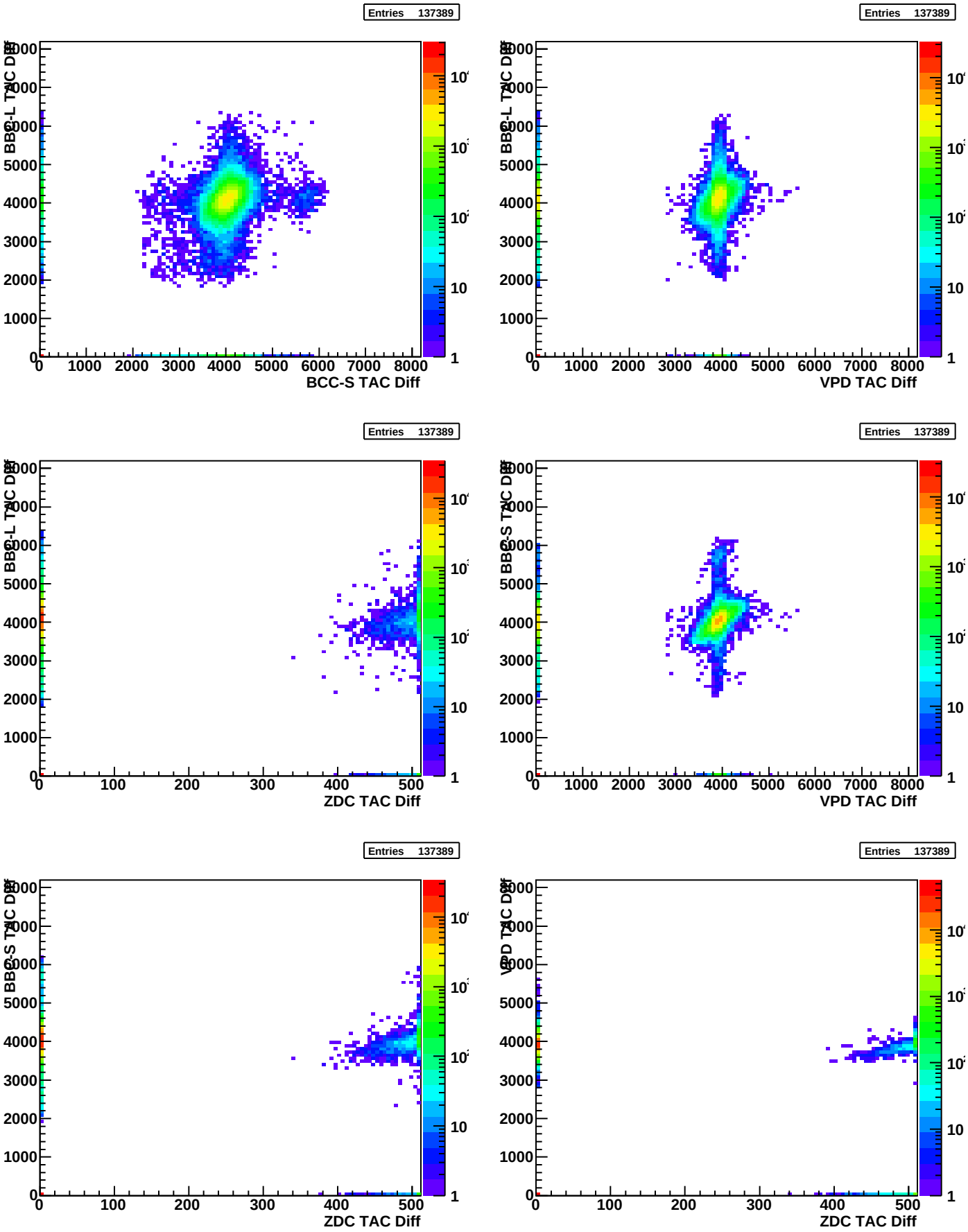

Entries 137389
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

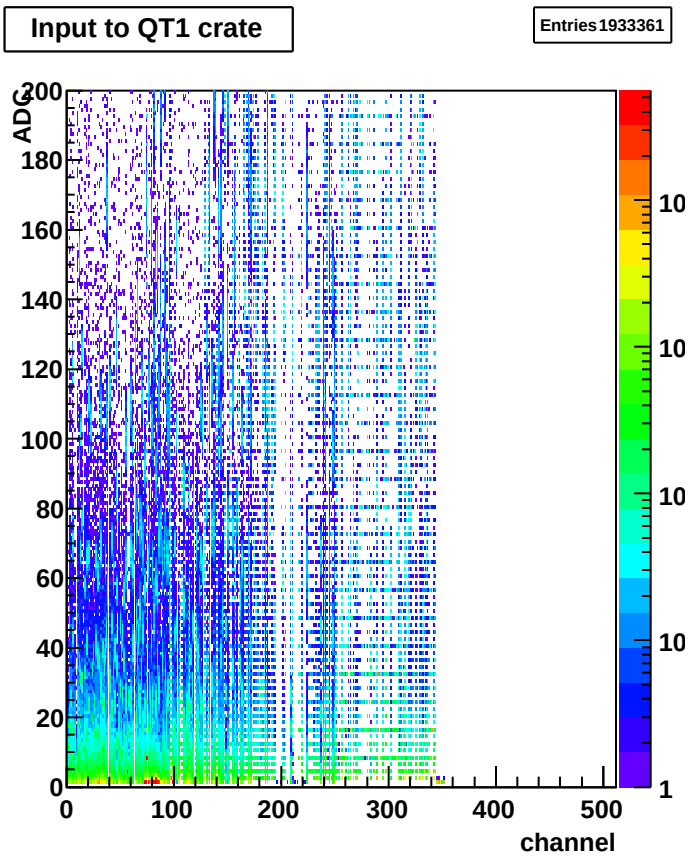

Entries 137389
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

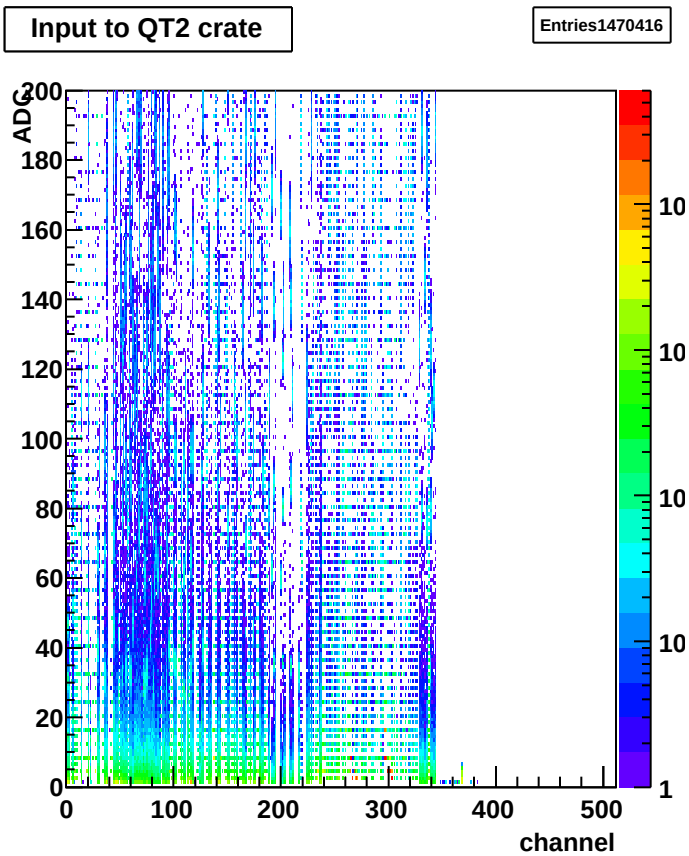

Entries 137389
BBC-L-East ADC Sum
BBC-L-West ADC Sum

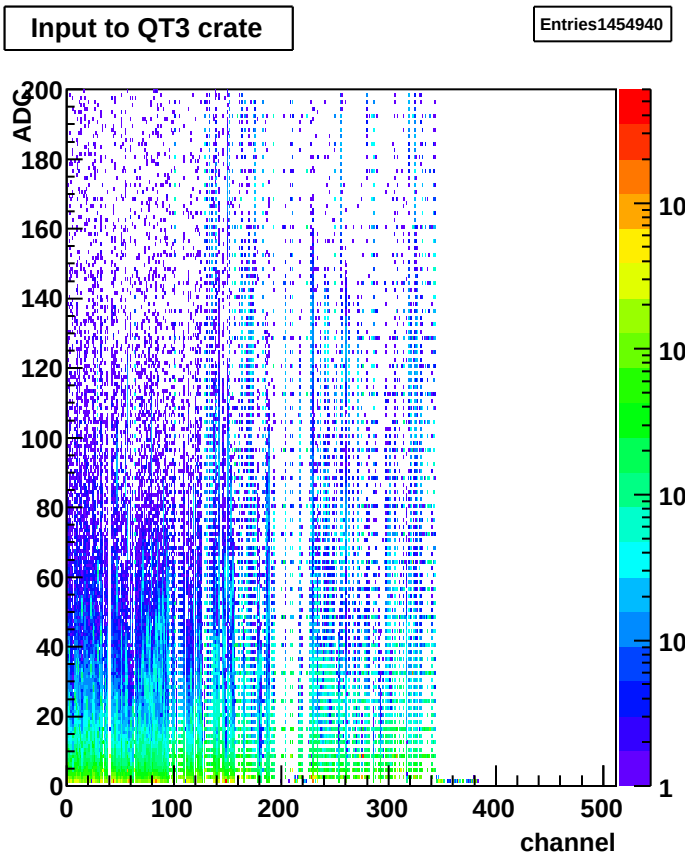

Entries 137389
BBC-S-East ADC Sum
BBC-S-West ADC Sum

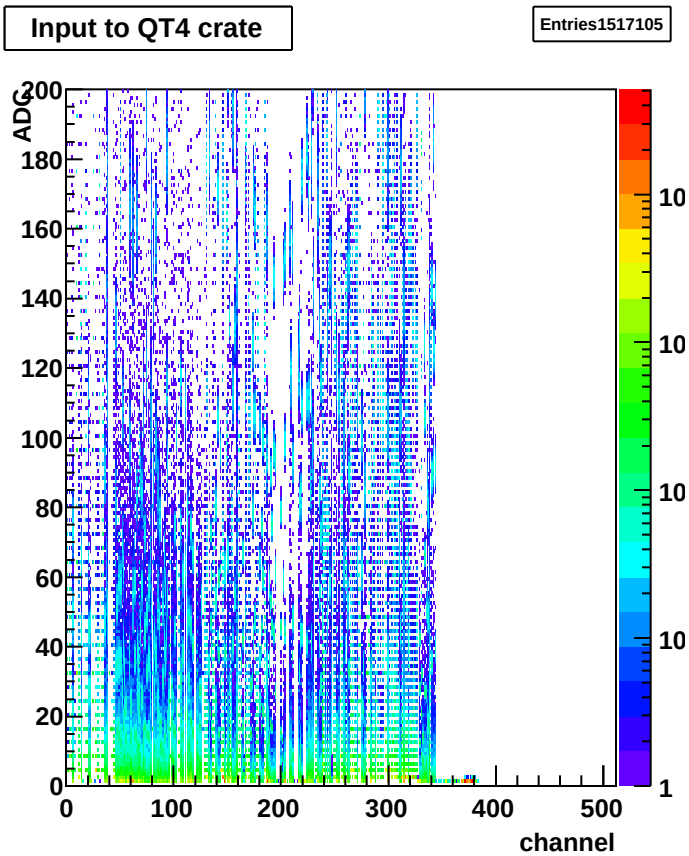

Entries 137389
ZDC East ADC Sum Att
ZDC West ADC Sum Att

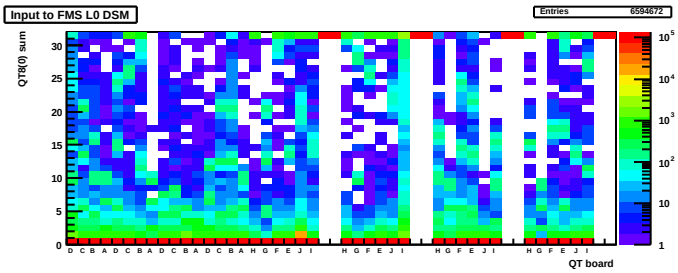

Entries 137389
BBC-L TAC Diff
BCC-S TAC Diff
Entries 137389
BBC-L TAC Diff
VPD TAC Diff
Entries 137389
BBC-L TAC Diff
ZDC TAC Diff
Entries 137389
BBC-S TAC Diff
VPD TAC Diff
Entries 137389
BBC-S TAC Diff
ZDC TAC Diff
Entries 137389
VPD TAC Diff
ZDC TAC Diff

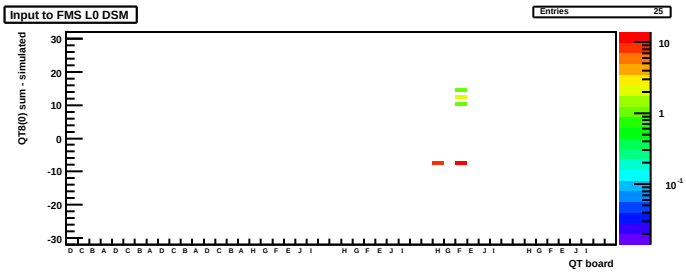

Input to QT1 crate
Entries1933361
ADC
channel

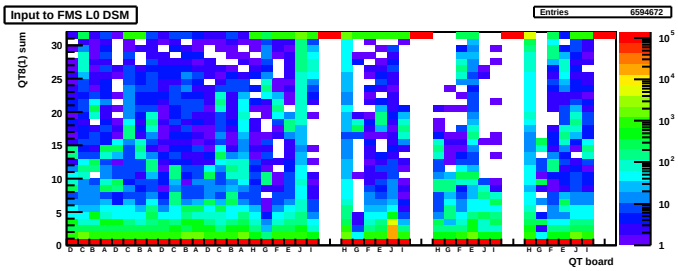

Input to QT2 crate
Entries1470416
ADC
channel

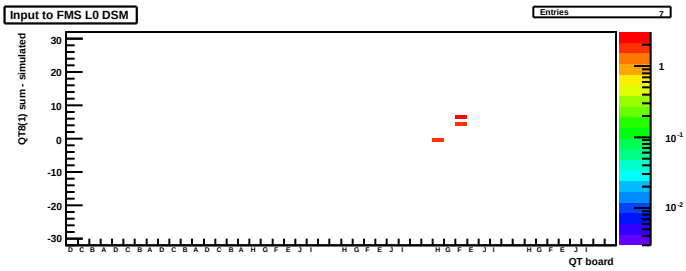

Input to QT3 crate
Entries1454940
ADC
channel

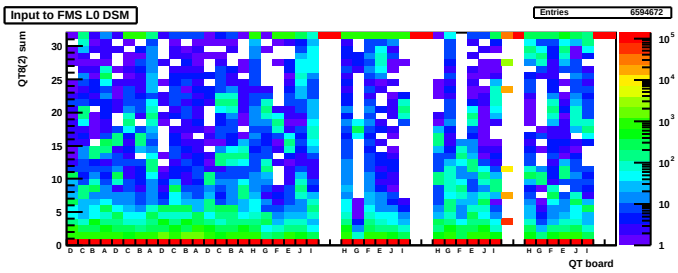

Input to QT4 crate
Entries1517105
ADC
channel

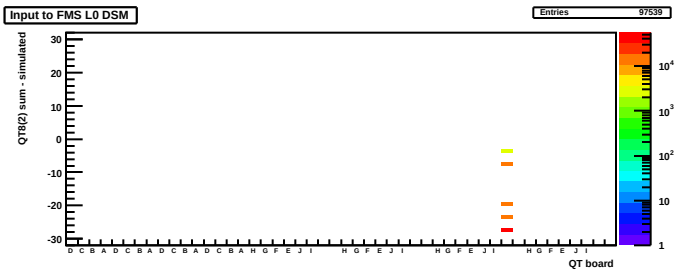

Input to FMS L0 DSM
Entries 6594672
QT8(0) sum
QT board

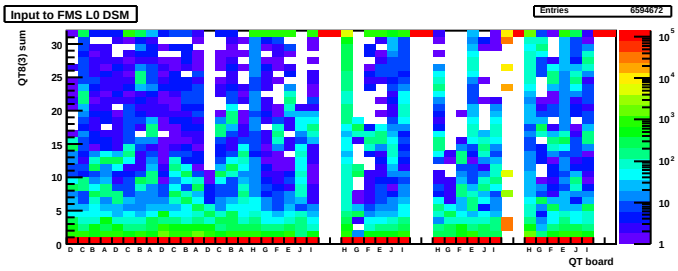

Input to FMS L0 DSM
Entries 25
QT8(0) sum - simulated
QT board

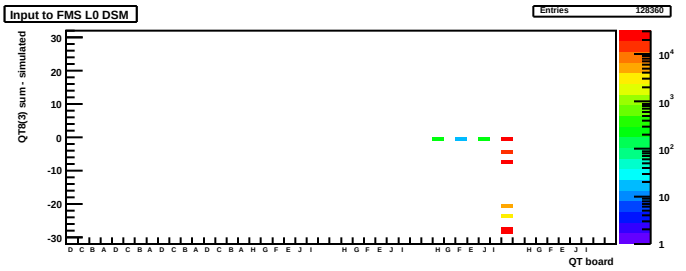

Input to FMS L0 DSM
Entries 6594672
QT8(1) sum
QT board

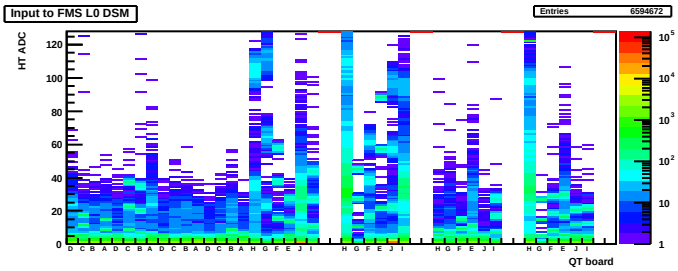

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

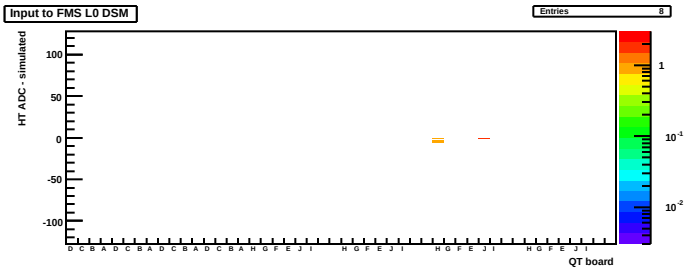

Input to FMS L0 DSM
Entries 6594672
QT8(2) sum
QT board

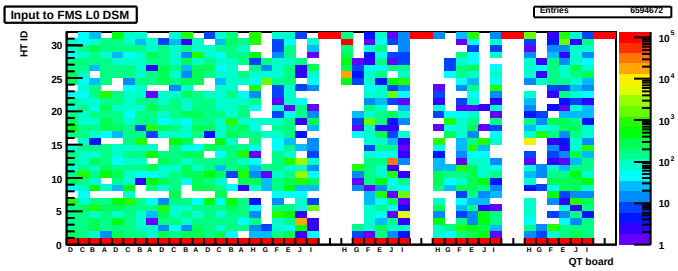

Input to FMS L0 DSM
Entries 97539
QT8(2) sum - simulated
QT board

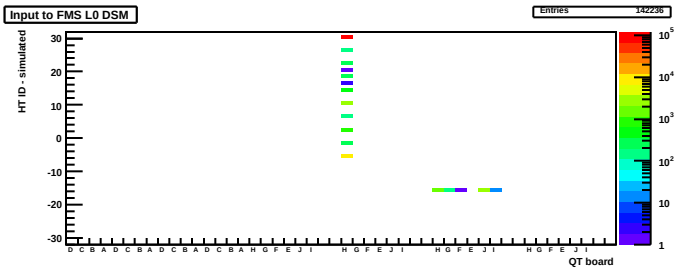

Input to FMS L0 DSM
Entries 6594672
QT8(3) sum
QT board

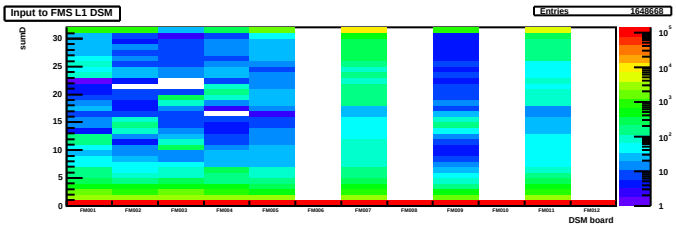

Input to FMS L0 DSM
Entries 128360
QT8(3) sum - simulated
QT board

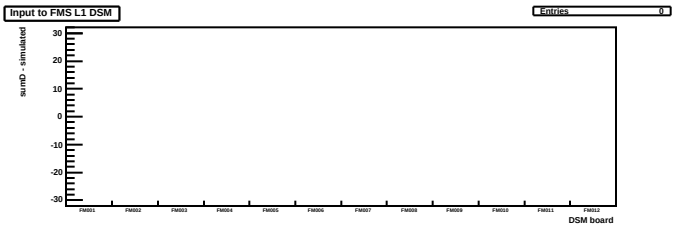

Input to FMS L0 DSM
Entries 6594672
HT ADC
QT board

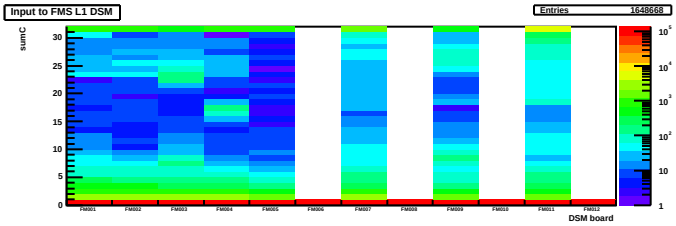

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

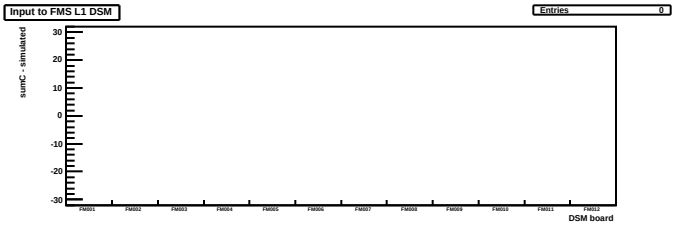

Input to FMS L0 DSM
Entries 6594672
HT ID
QT board

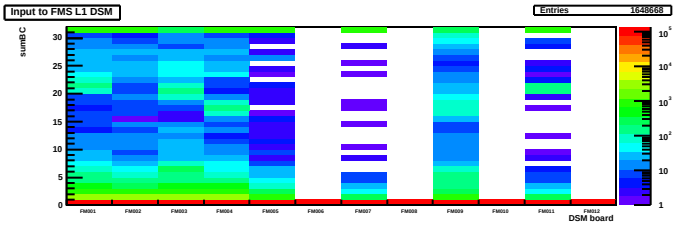

Input to FMS L0 DSM
Entries 142236
HT ID - simulated
QT board

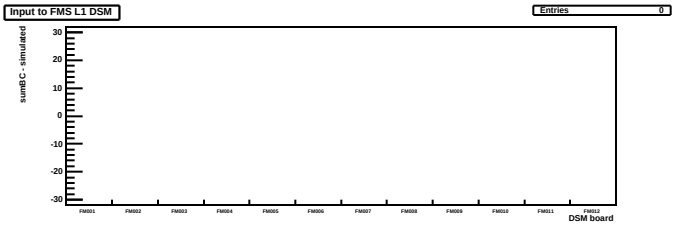

Input to FMS L1 DSM
Entries 1648668
sumD
DSM board

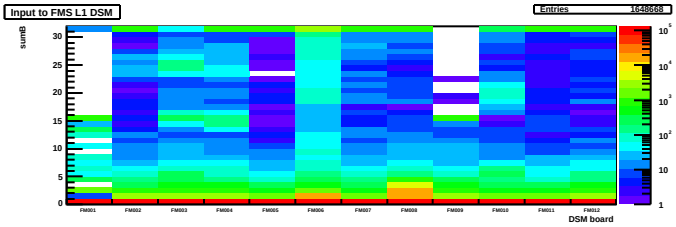

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

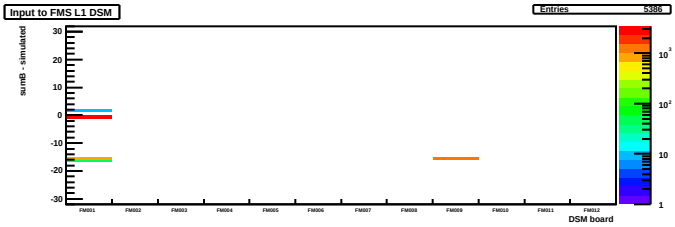

Input to FMS L1 DSM
Entries 1648668
sumC
DSM board

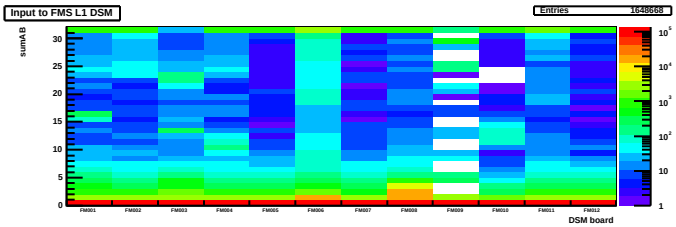

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

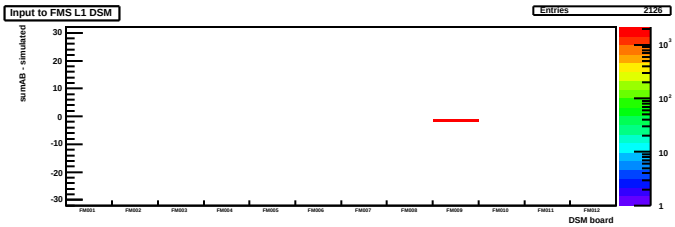

Input to FMS L1 DSM
Entries 1648668
sumBC
DSM board

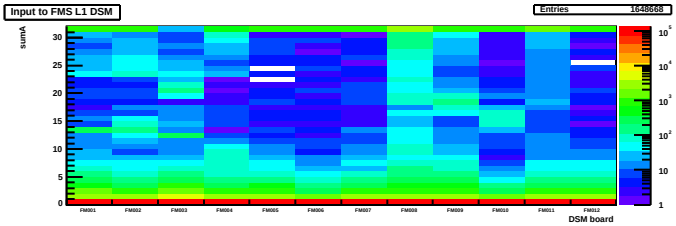

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

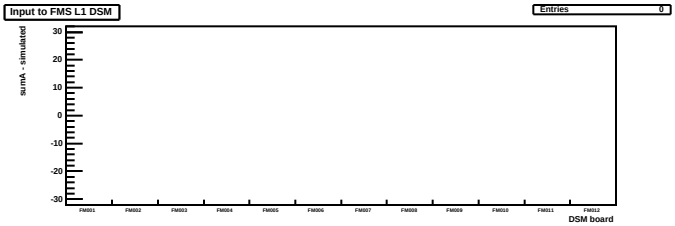

Input to FMS L1 DSM
Entries 1648668
sumB
DSM board

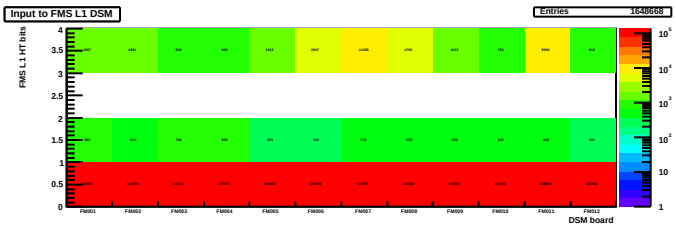

Input to FMS L1 DSM
Entries 5386
sumB - simulated
DSM board

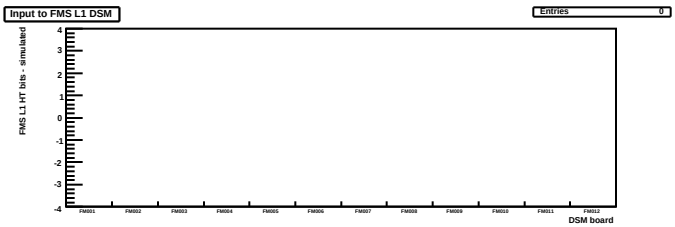

Input to FMS L1 DSM
Entries 1648668
sumAB
DSM board

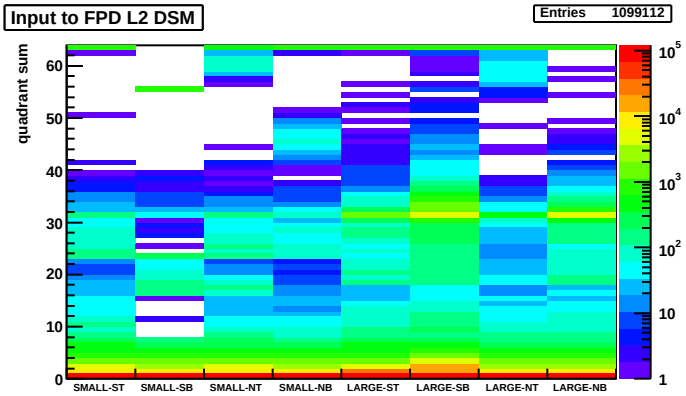

Input to FMS L1 DSM
Entries 2126
sumAB - simulated
DSM board

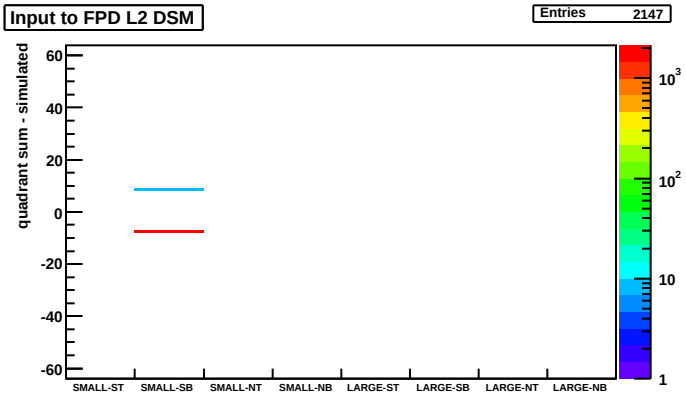

Input to FMS L1 DSM
Entries 1648668
sumA
DSM board

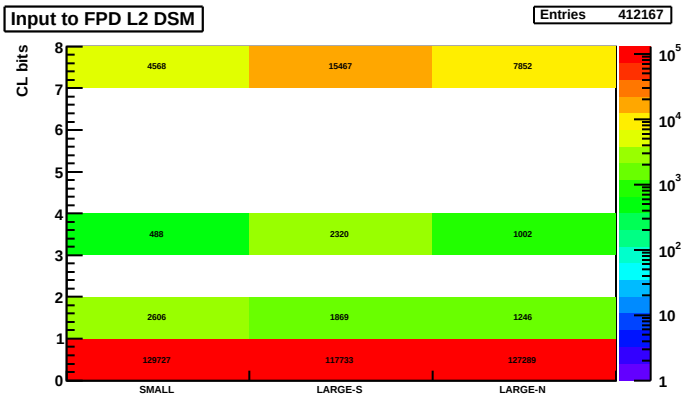

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

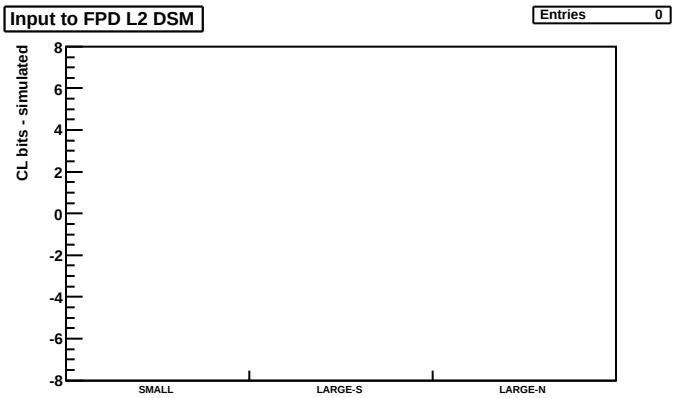

Input to FMS L1 DSM
Entries 1648668
FMS L1 HT bits
DSM board

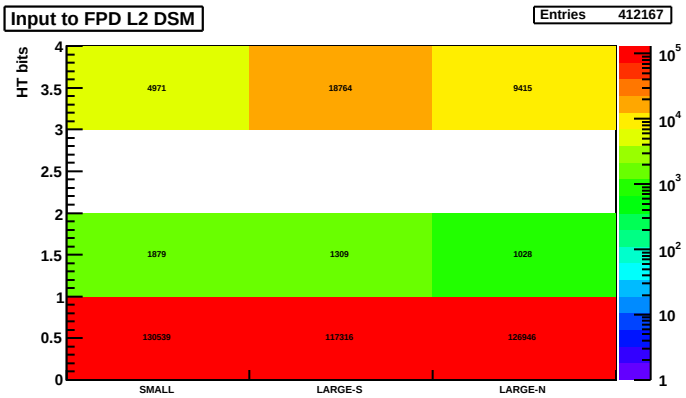

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

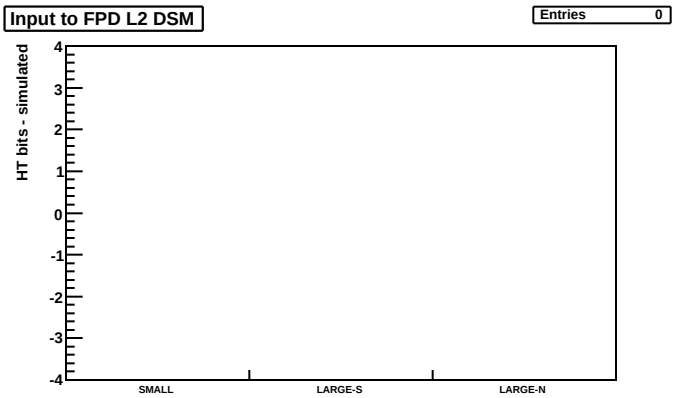

Input to FPD L2 DSM
Entries 1099112
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

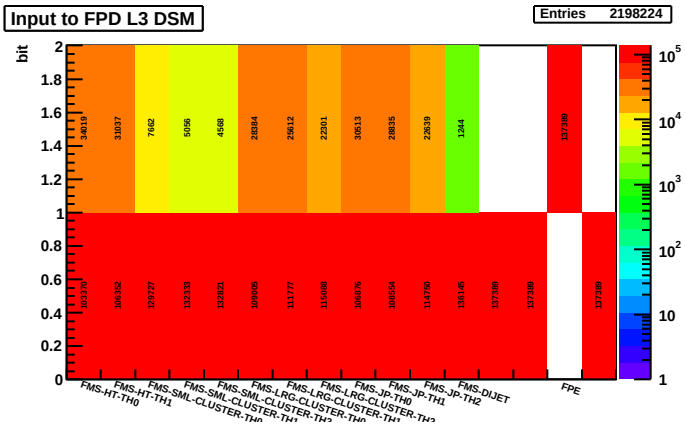

Input to FPD L2 DSM
Entries 2147
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

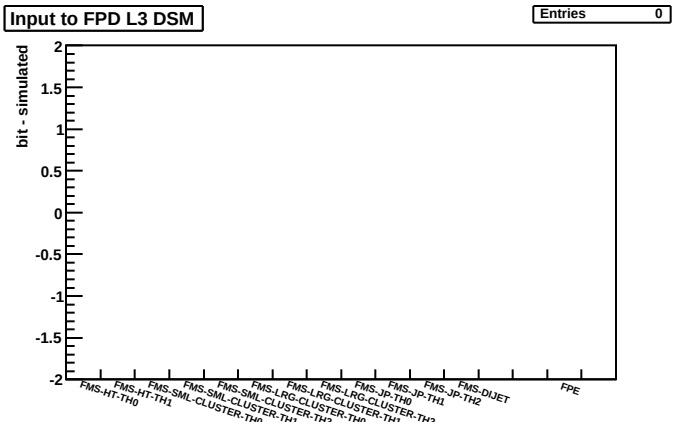

Input to FPD L2 DSM
Entries 412167
CL bits
4568
15467
7852
488
2320
1002
2606
1869
1246
129727
117733
127289
SMALL
LARGE-S
LARGE-N

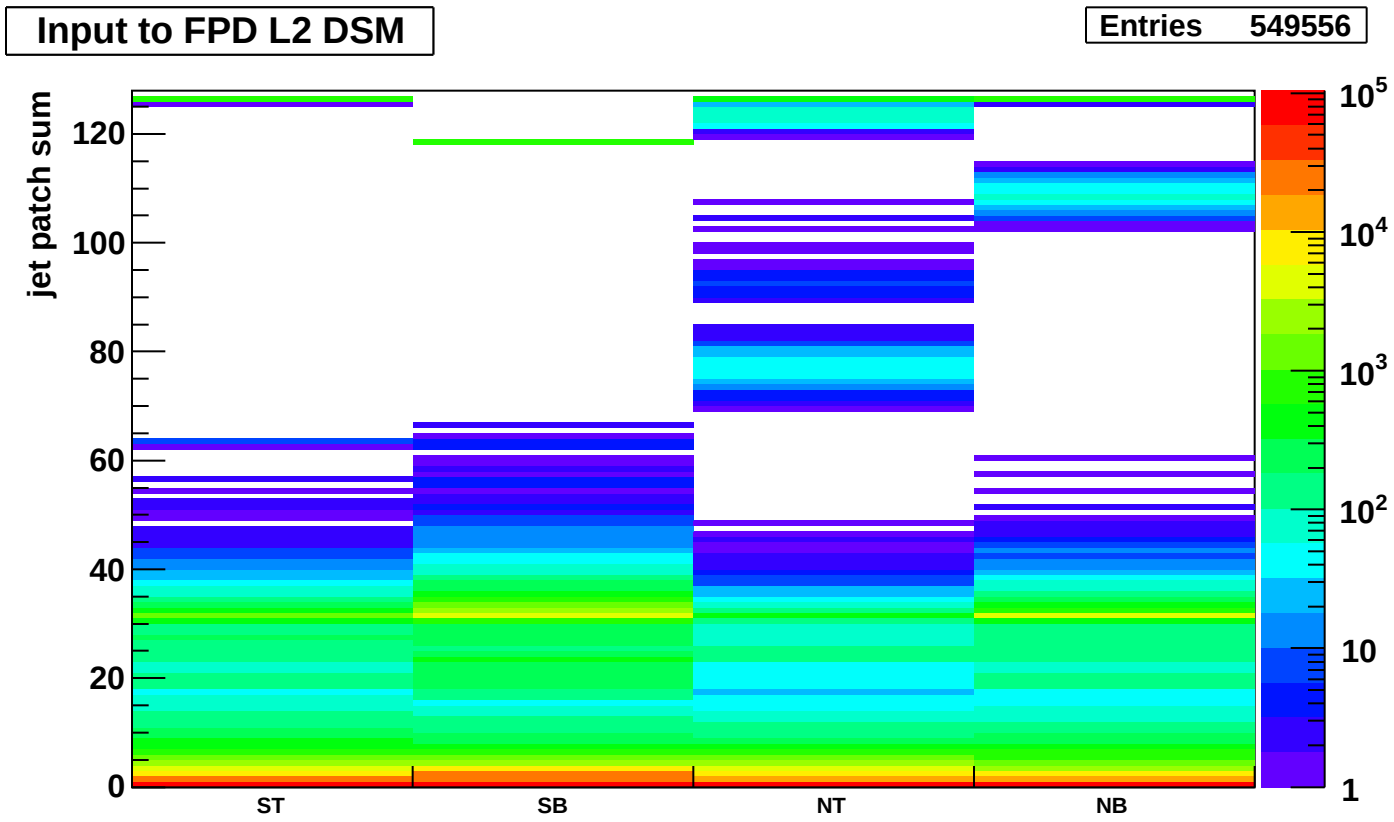

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

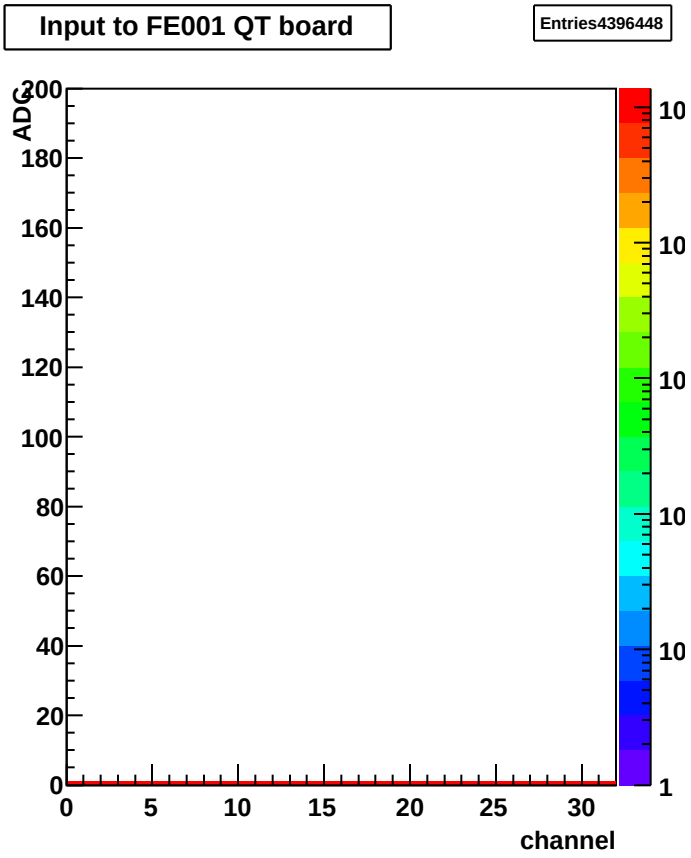

Input to FPD L2 DSM
Entries 412167
HT bits
4971
18764
9415
1879
1309
1028
130539
117316
126946
SMALL
LARGE-S
LARGE-N

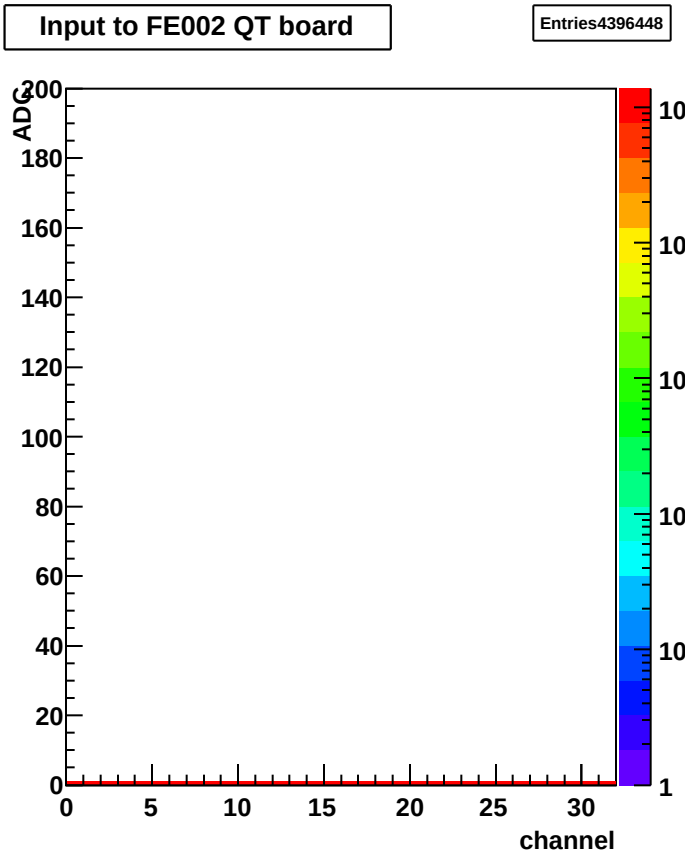

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

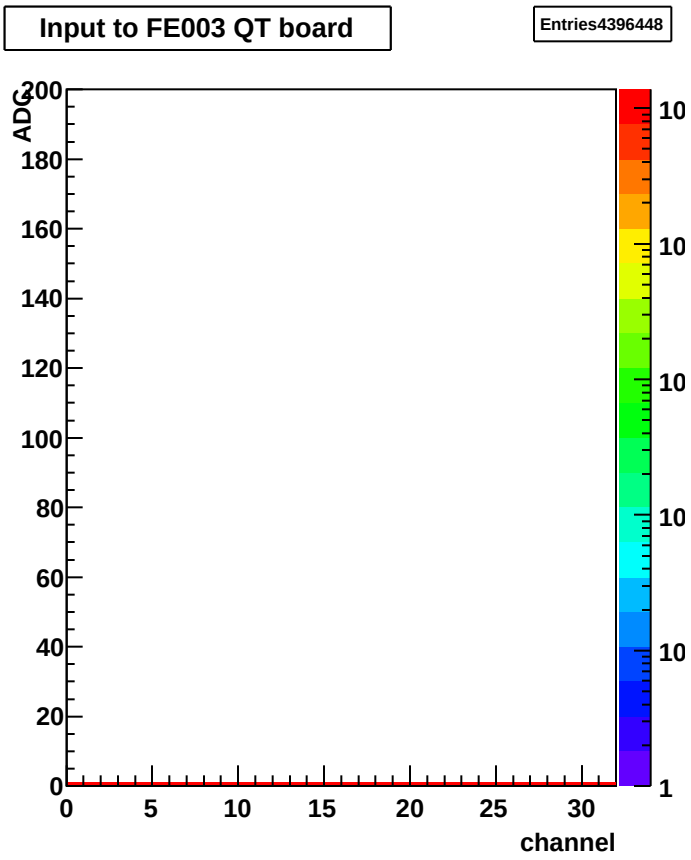

Input to FPD L3 DSM
Entries 2198224
bit
34019
31037
7662
5056
4568
28394
25612
22301
30513
28835
22639
1344
137389
103370
106352
129727
132333
132821
109005
111777
115088
106876
108554
114750
136145
137389
137389
137389
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

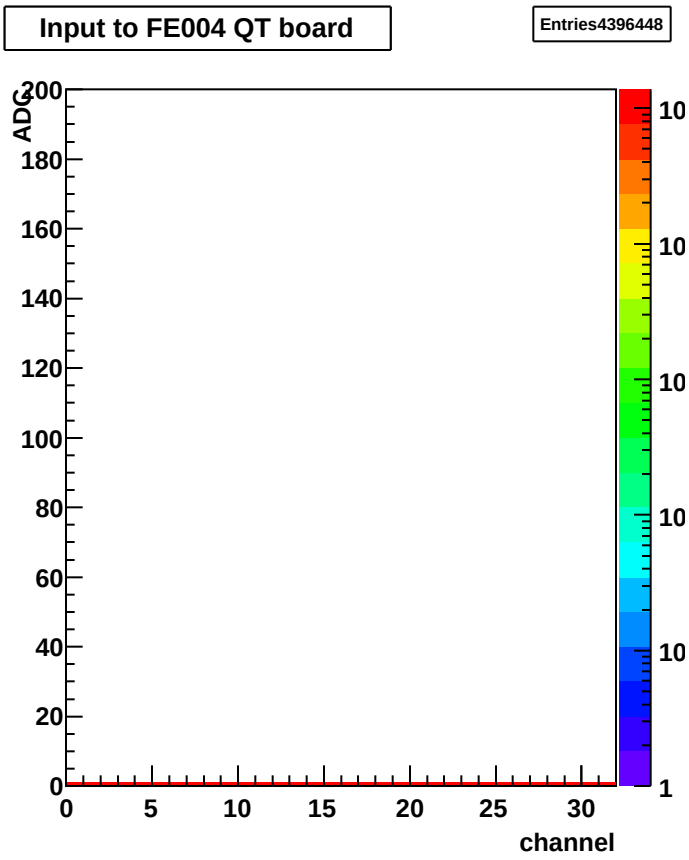

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

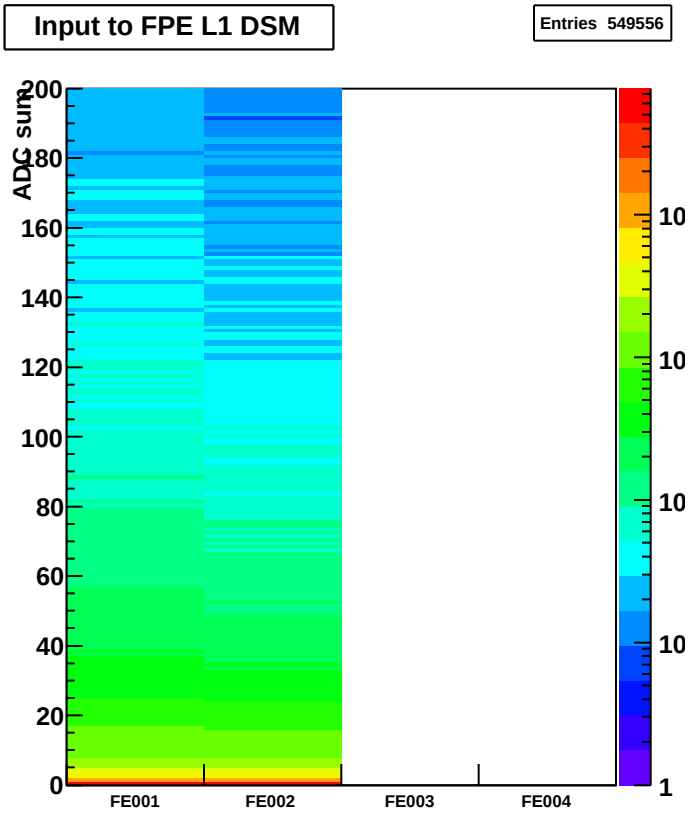

Input to FPD L2 DSM
Entries 549556
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

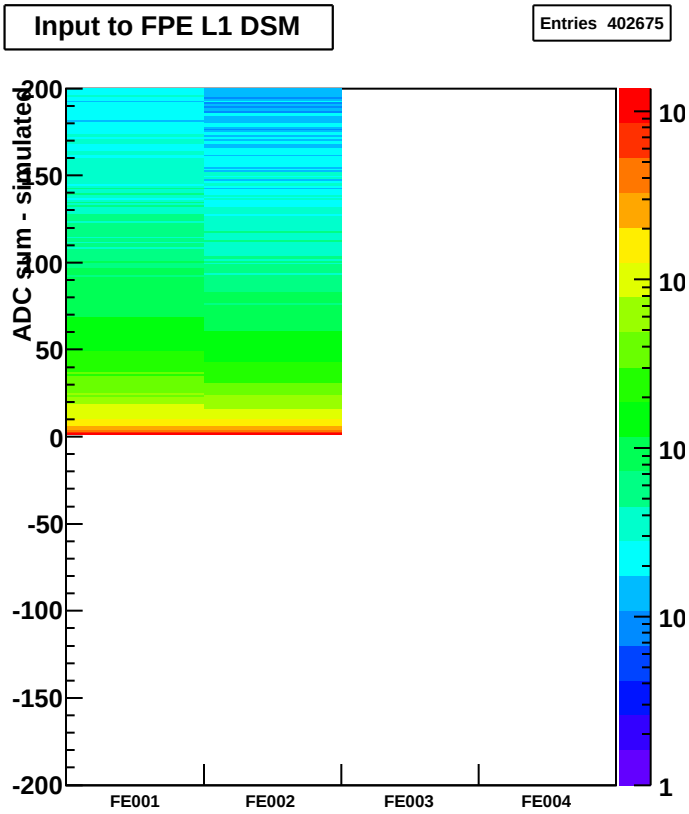

Input to FE001 QT board
Entries4396448
ADC
channel

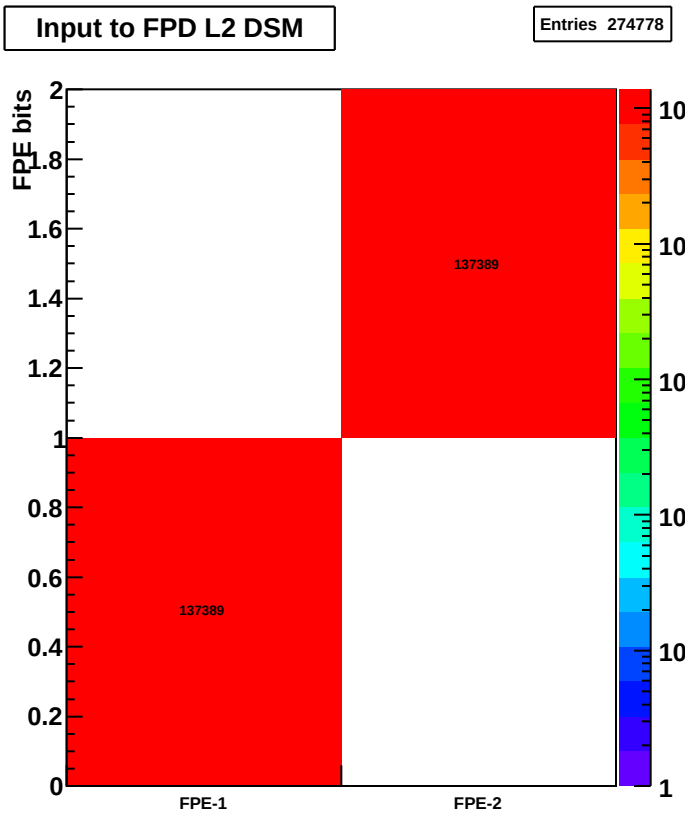

Input to FE002 QT board
Entries4396448
ADC
channel

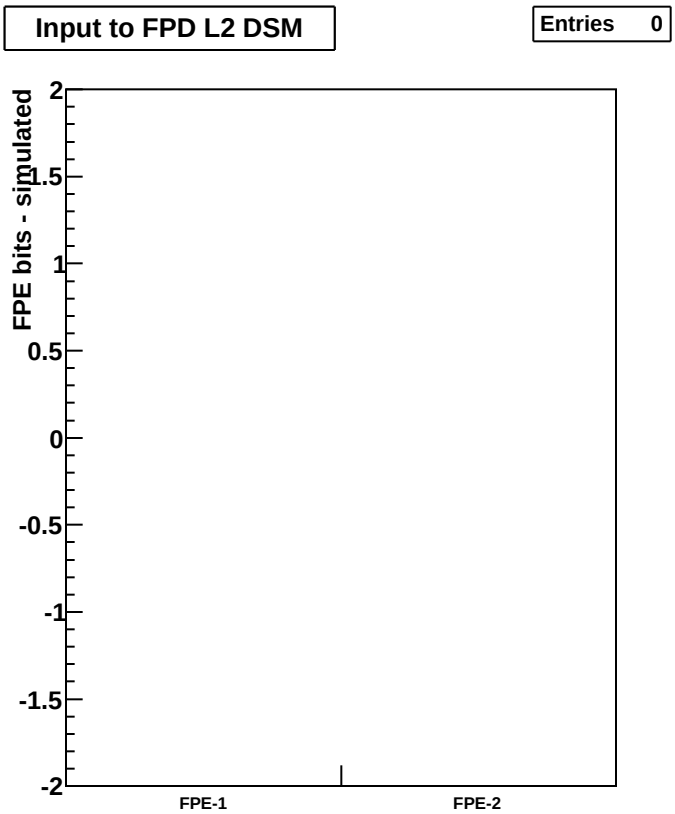

Input to FE003 QT board
Entries4396448
ADC
channel


Input to FE004 QT board
Entries4396448
ADC
channel


Input to FPE L1 DSM
Entries 549556
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 402675
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 274778
FPE bits
137389
137389
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2