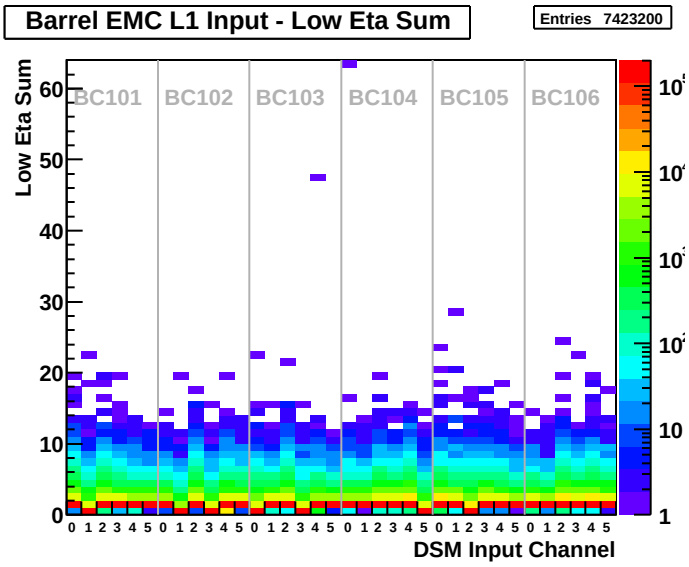
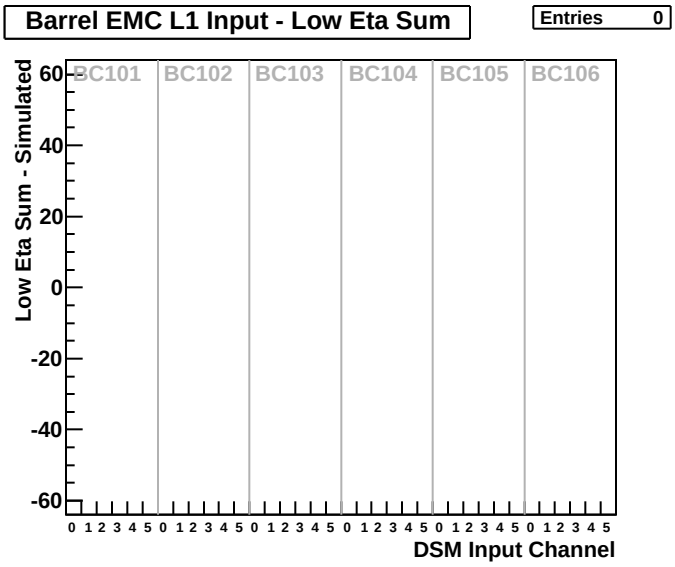
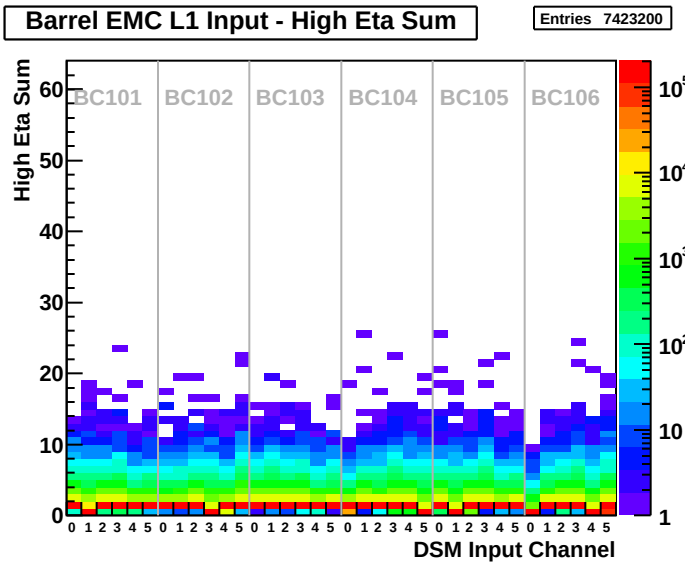
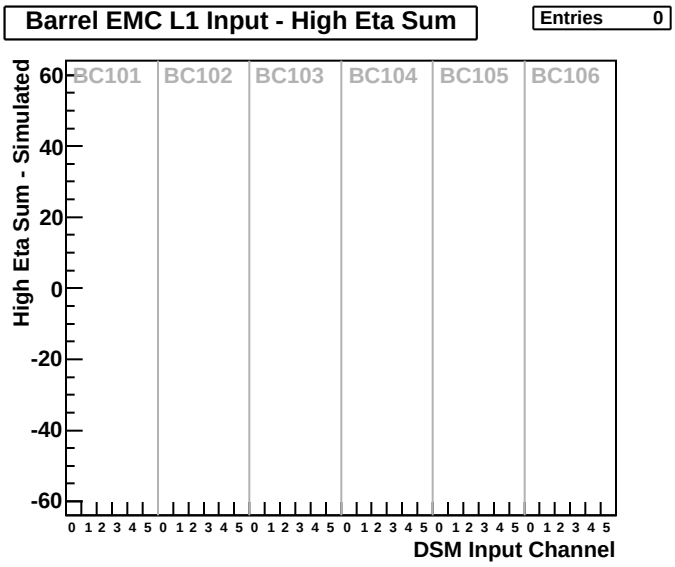
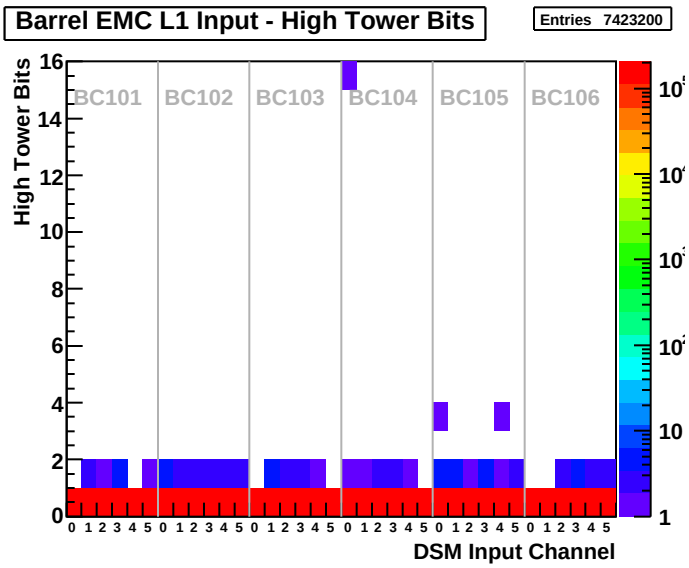
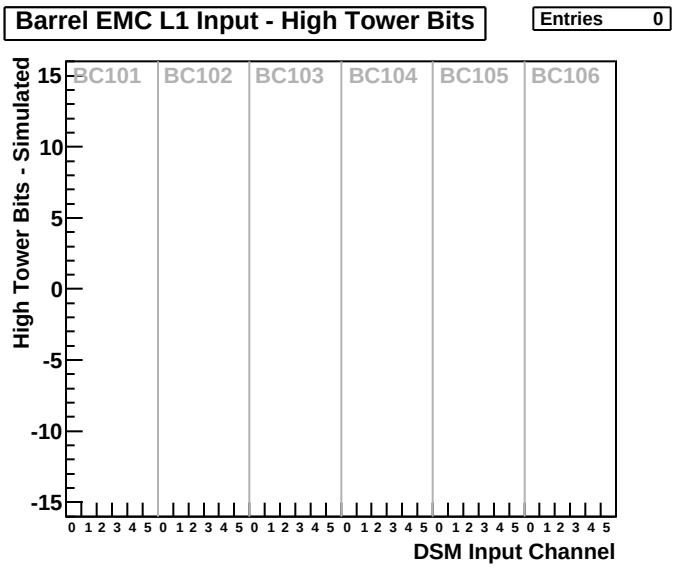

Barrel EMC L1 Input - Low Eta Sum
Entries 7423200
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

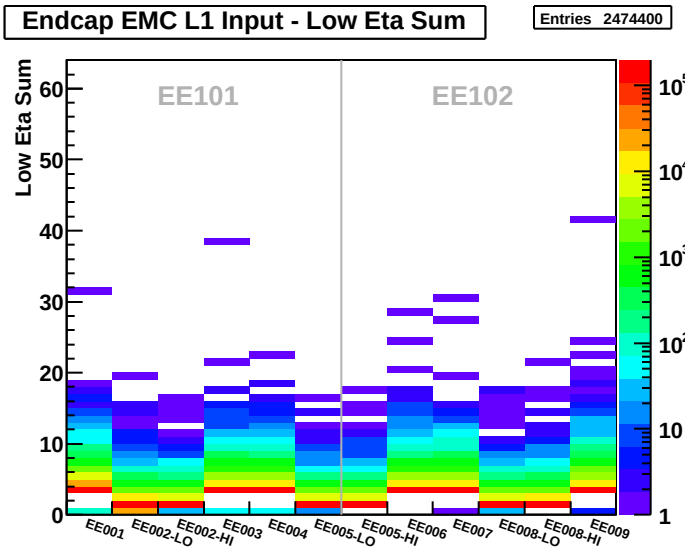

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

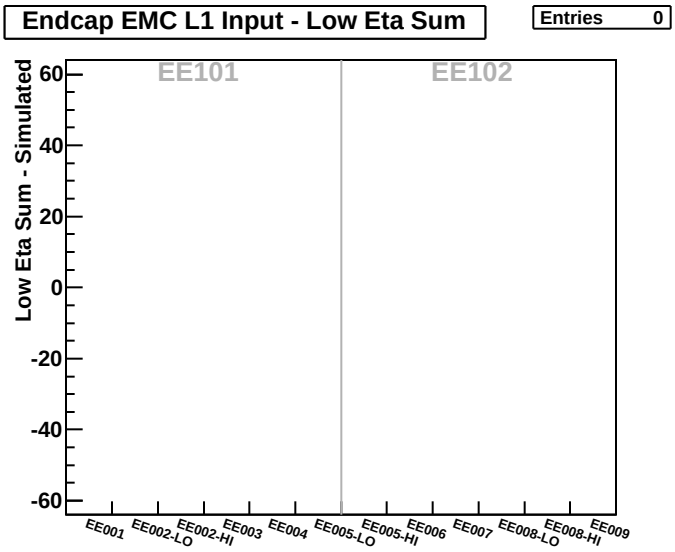

Barrel EMC L1 Input - High Eta Sum
Entries 7423200
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

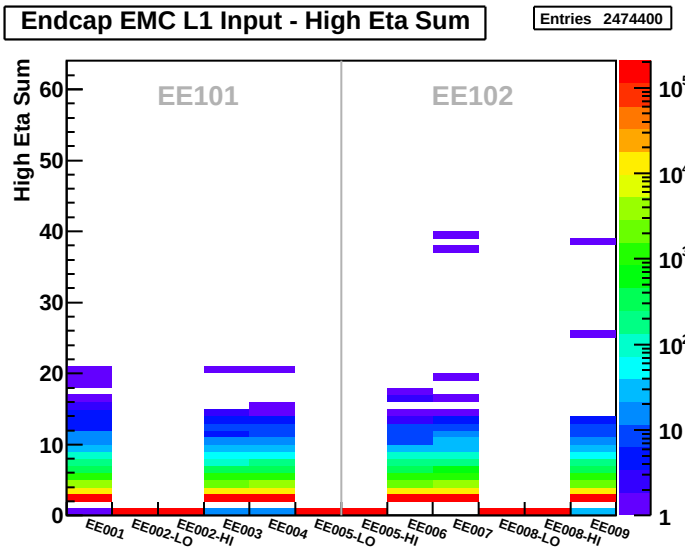

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

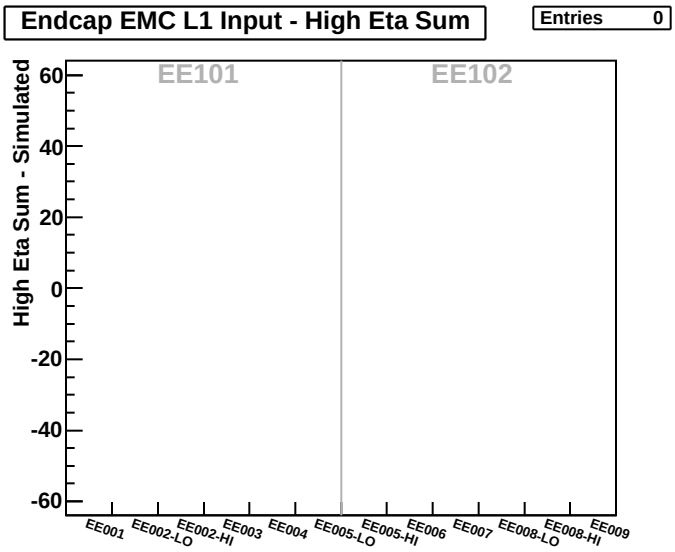

Barrel EMC L1 Input - High Tower Bits
Entries 7423200
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

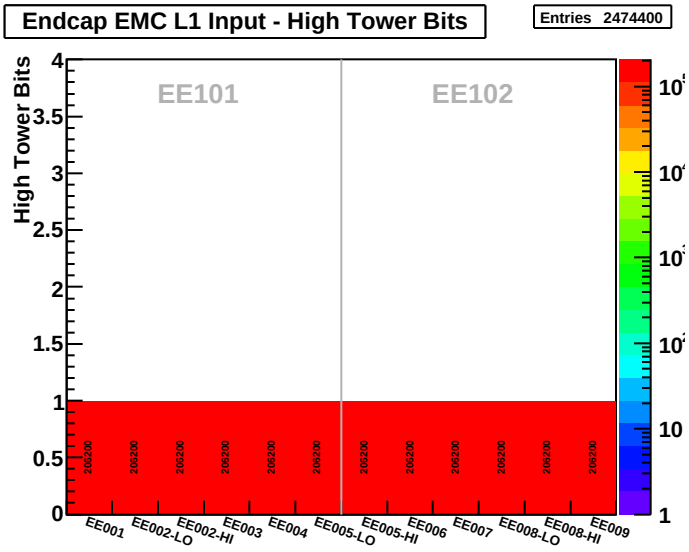

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

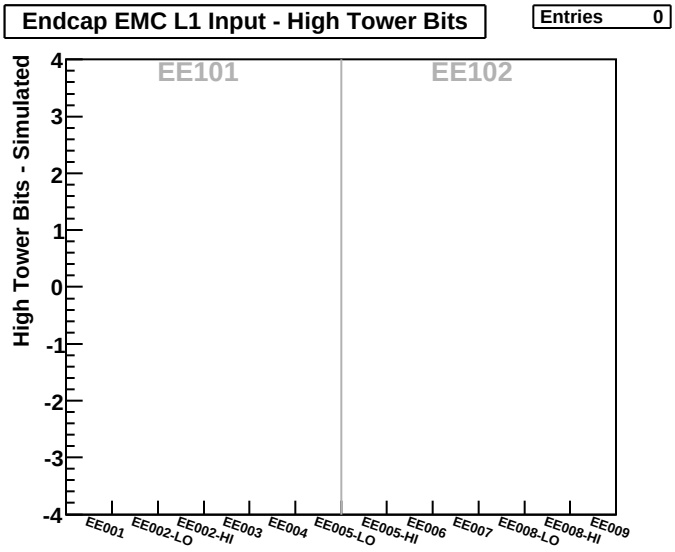

Endcap EMC L1 Input - Low Eta Sum
Entries 2474400
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

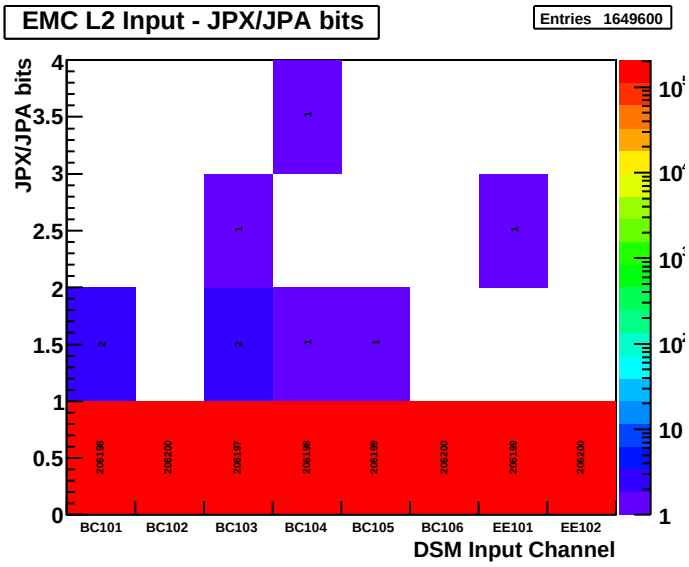

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

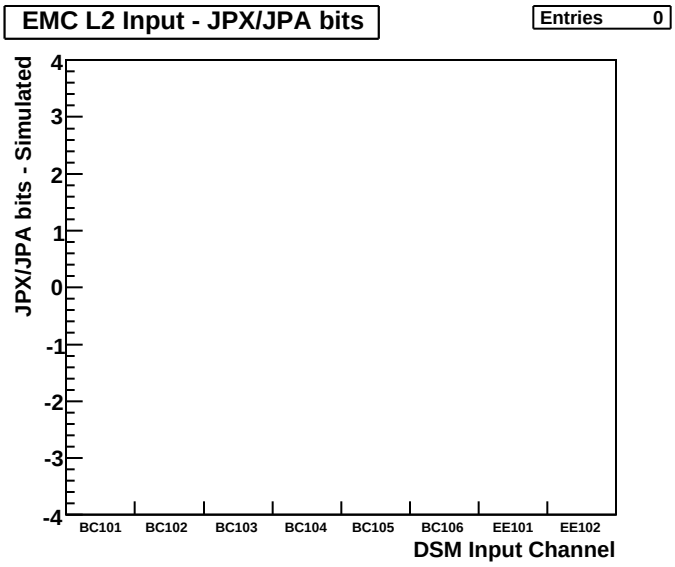

Endcap EMC L1 Input - High Eta Sum
Entries 2474400
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

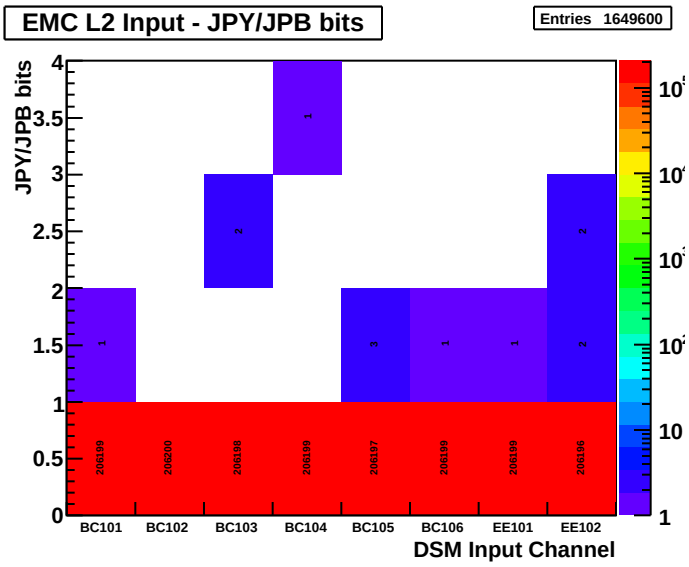

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

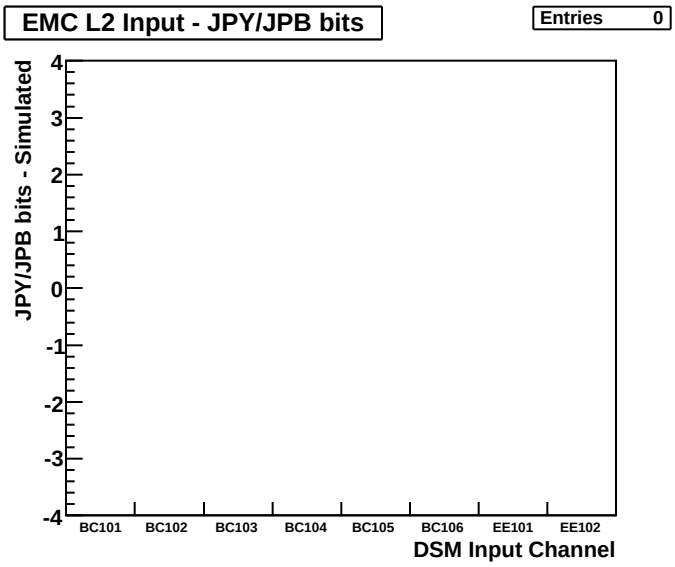

Endcap EMC L1 Input - High Tower Bits
Entries 2474400
High Tower Bits
EE101
EE102
206200
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

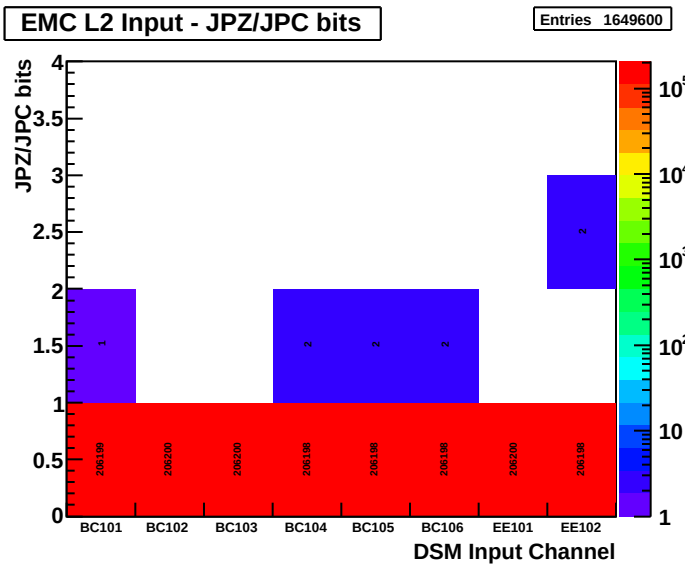

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

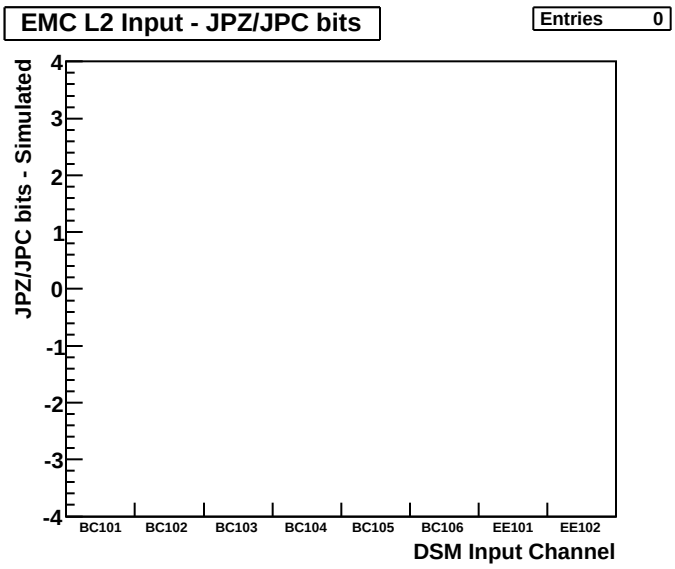

EMC L2 Input - JPX/JPA bits
Entries 1649600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

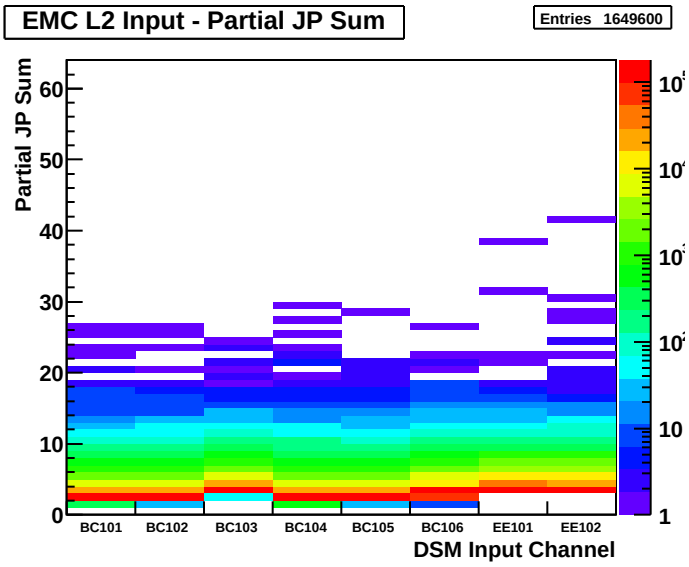

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

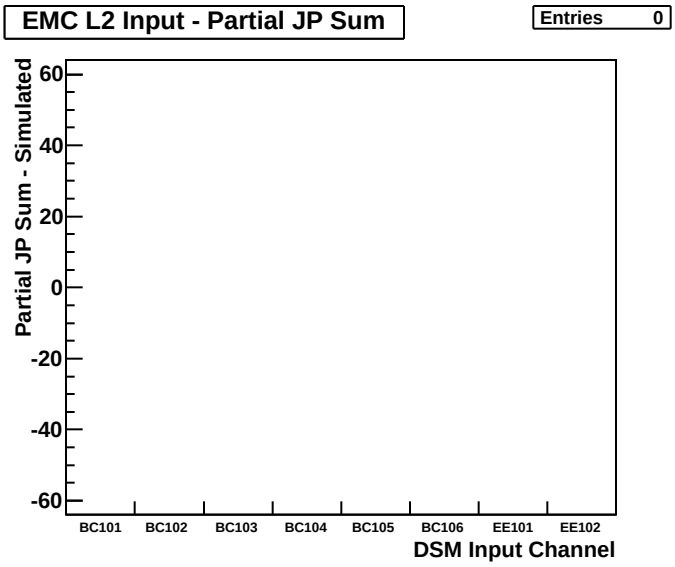

EMC L2 Input - JPY/JPB bits
Entries 1649600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

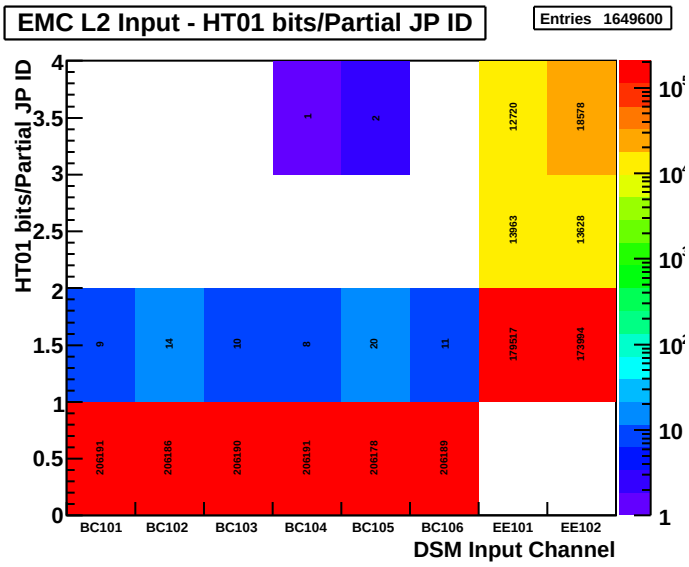

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

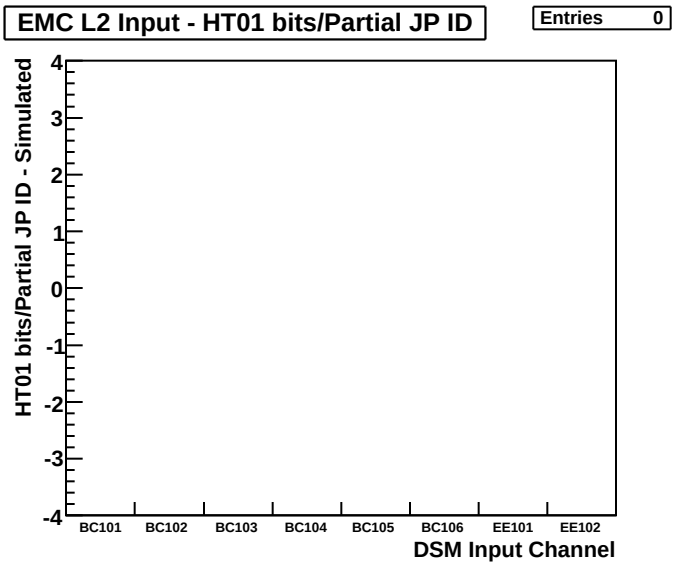

EMC L2 Input - JPZ/JPC bits
Entries 1649600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

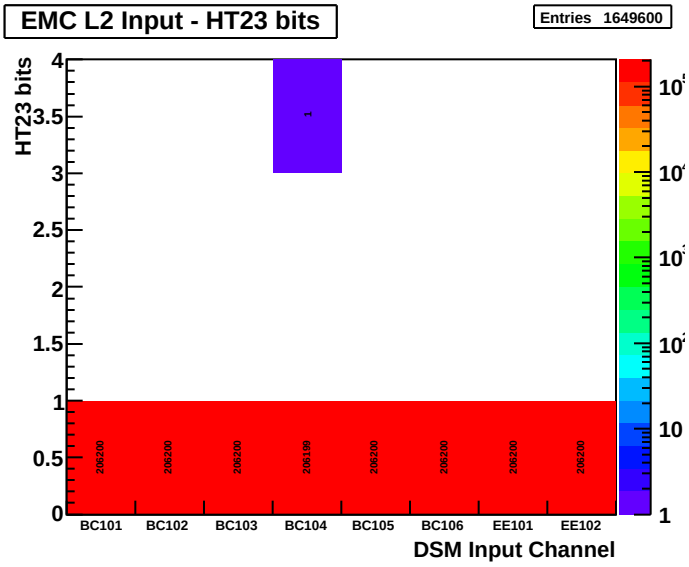

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

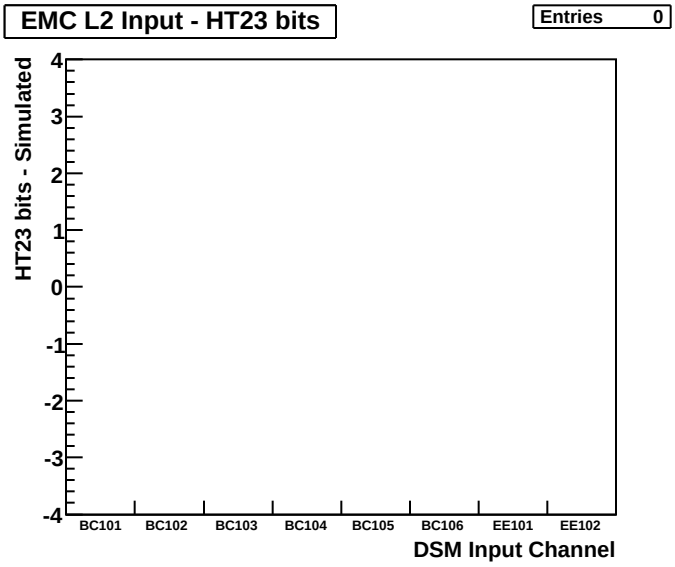

EMC L2 Input - Partial JP Sum
Entries 1649600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

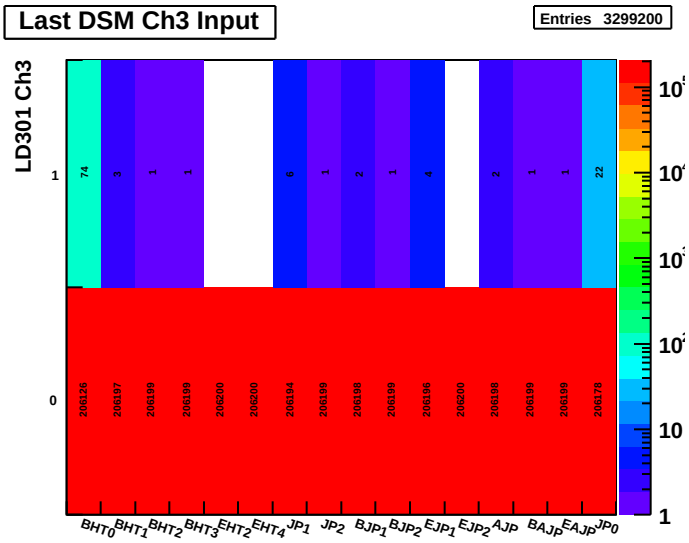

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

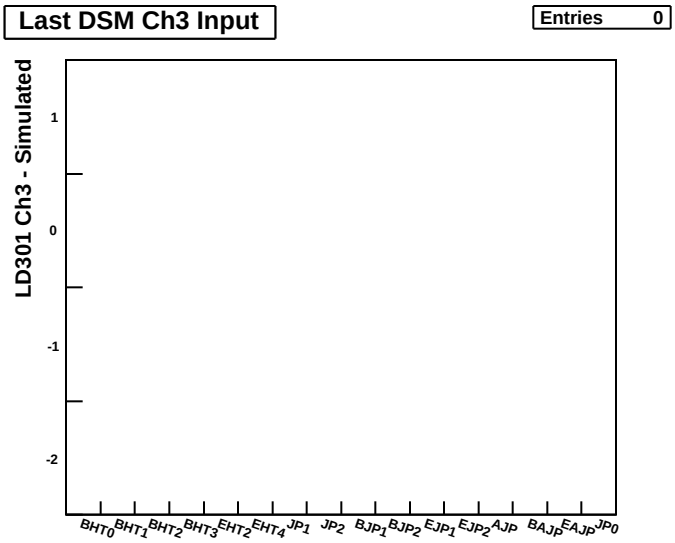

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1649600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

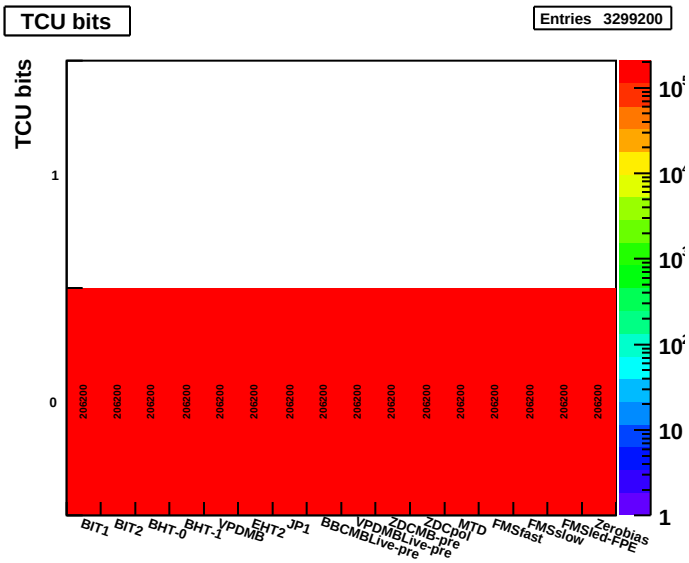

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

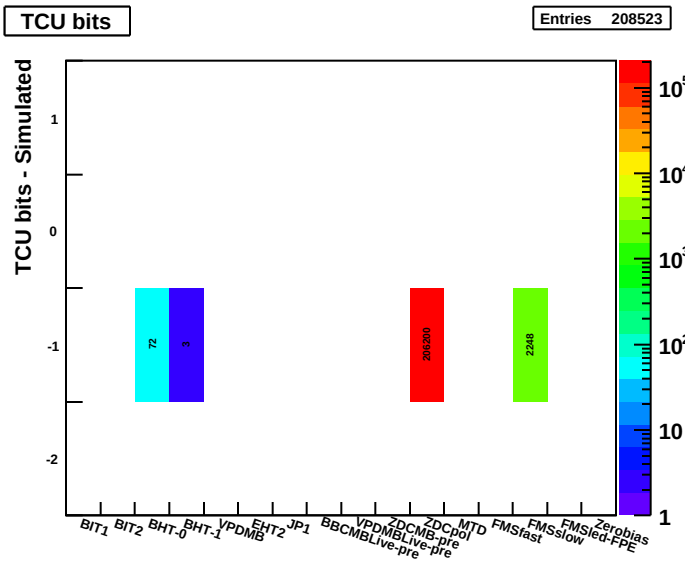

EMC L2 Input - HT23 bits
Entries 1649600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

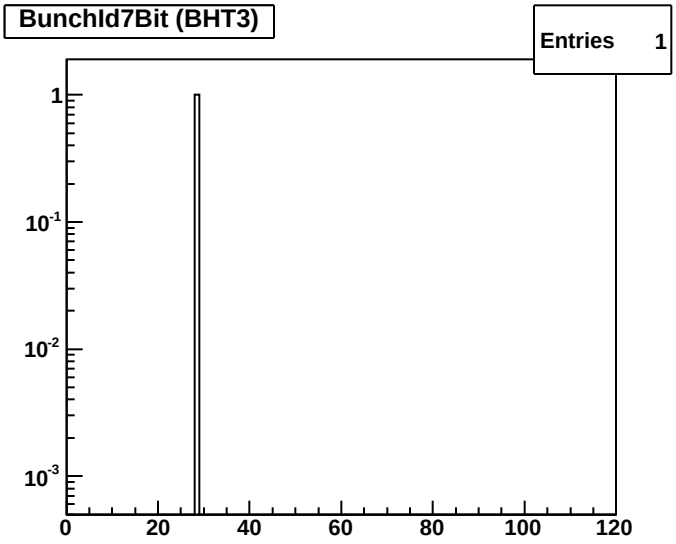

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

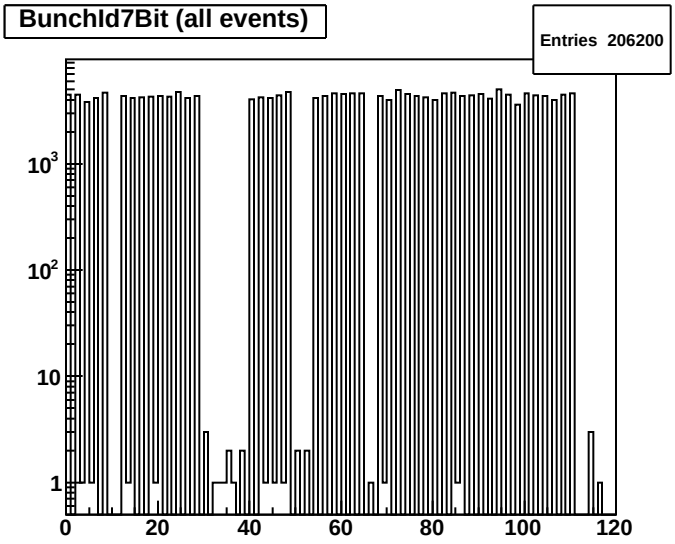

Last DSM Ch3 Input
Entries 3299200
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

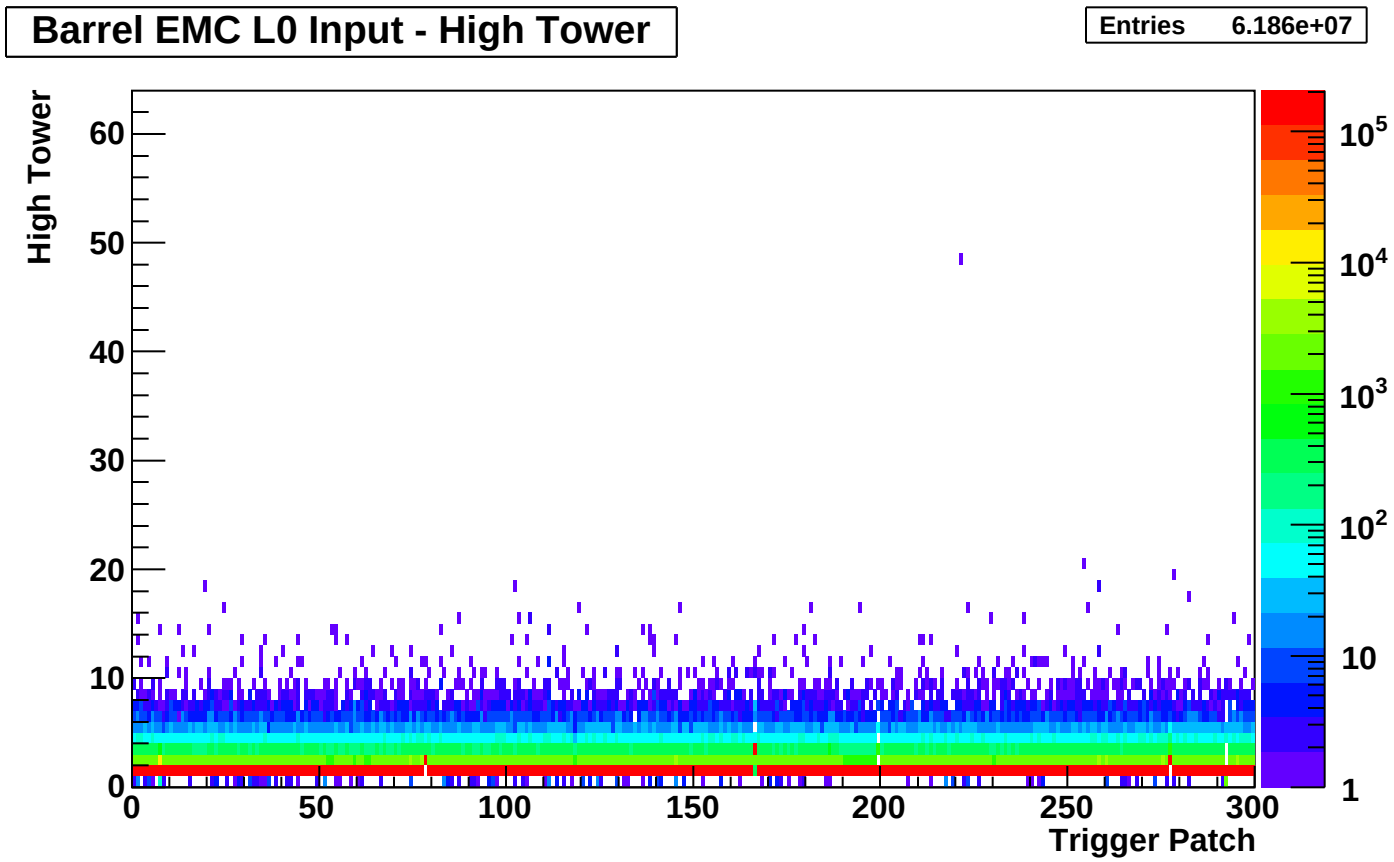

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

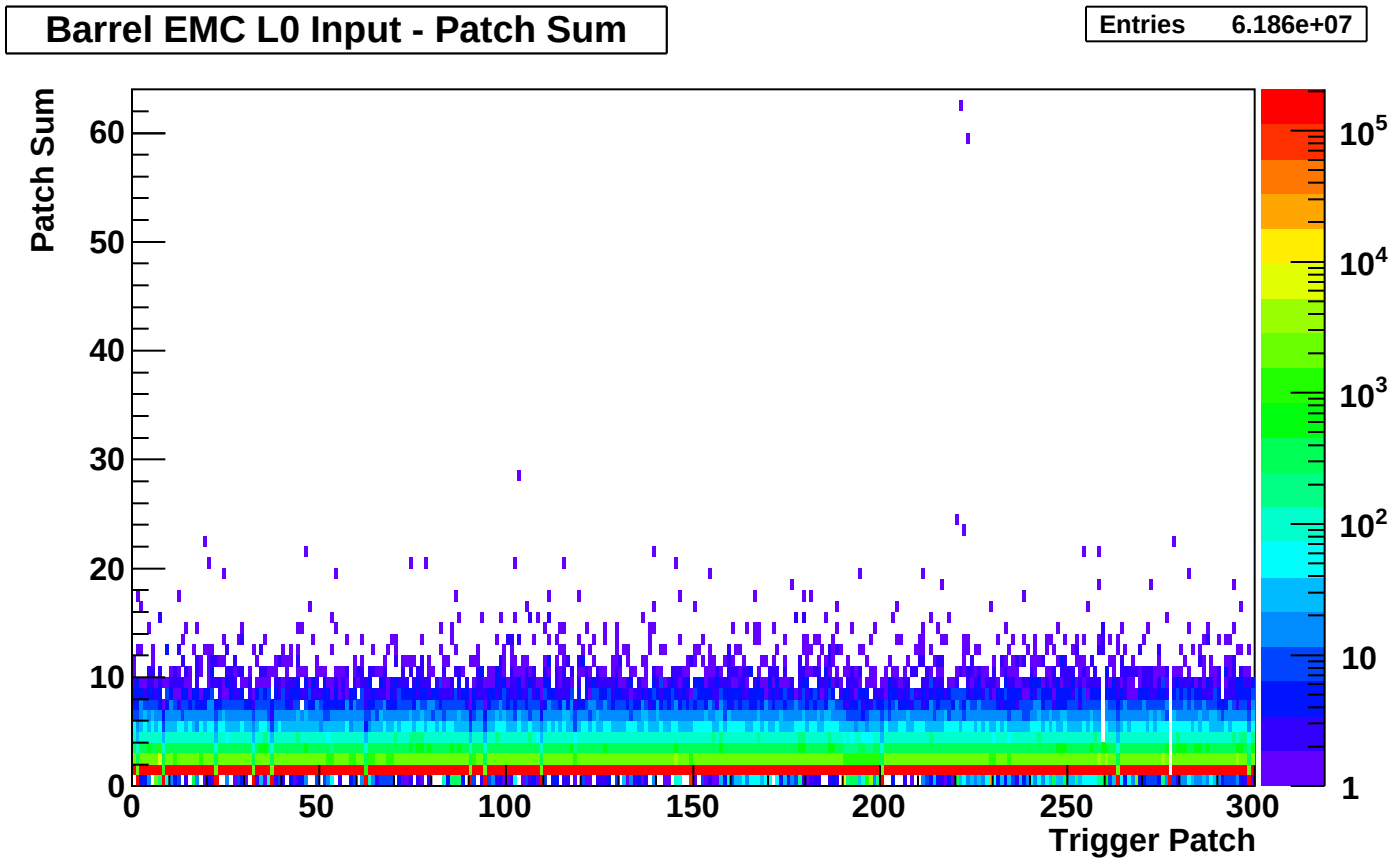

TCU bits
Entries 3299200
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

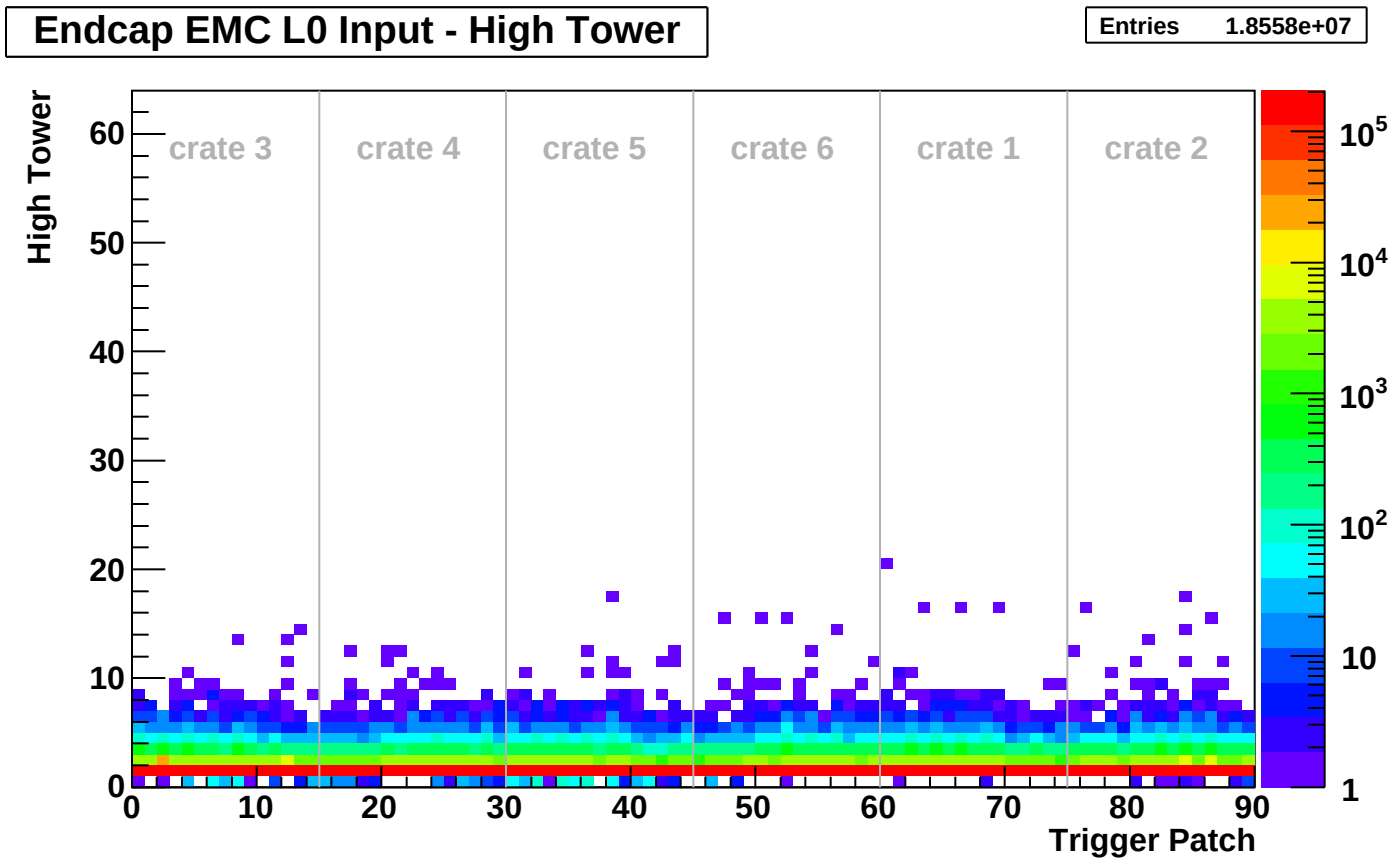

TCU bits
Entries 208523
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

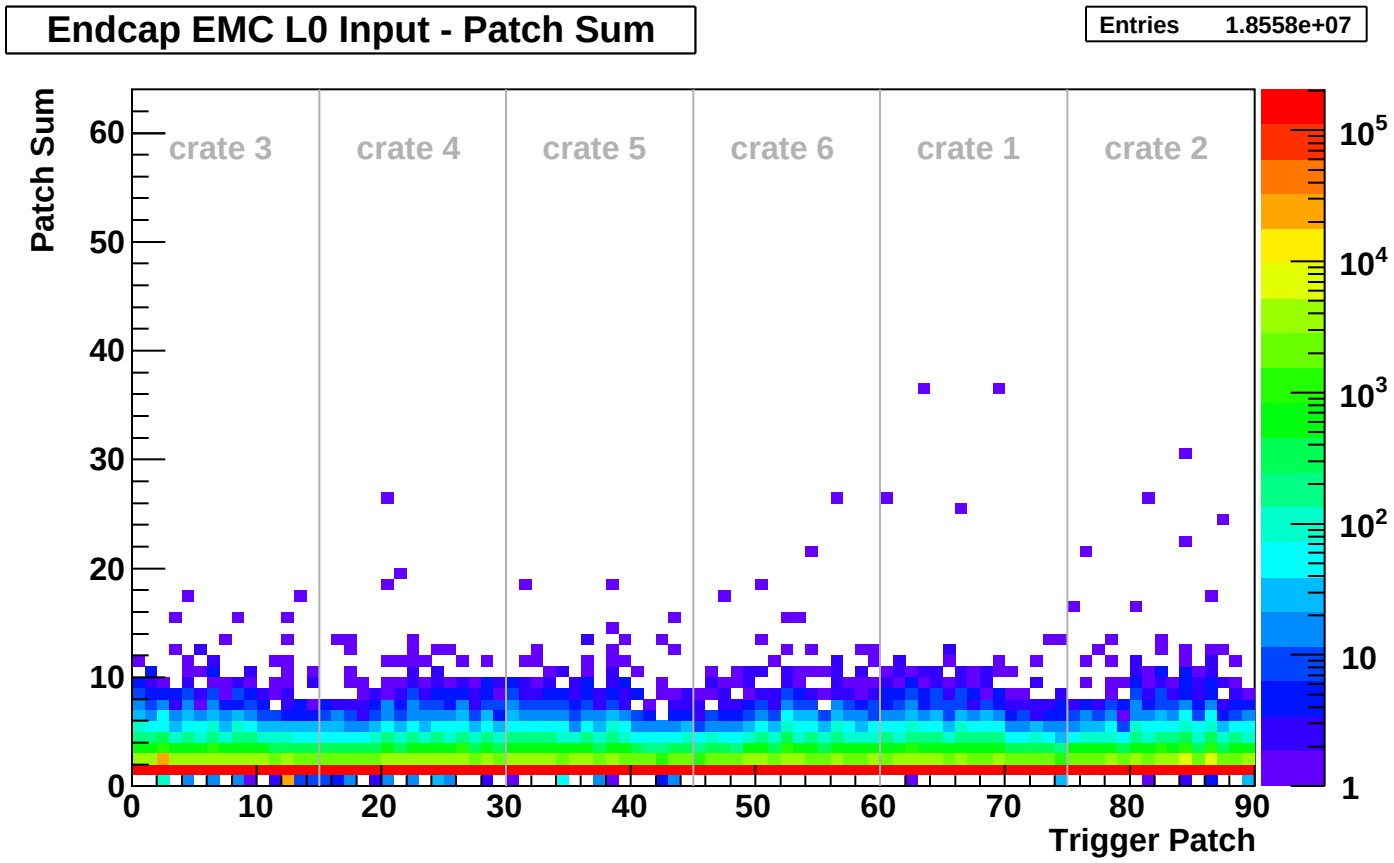

BunchId7Bit (BHT3)
Entries 1

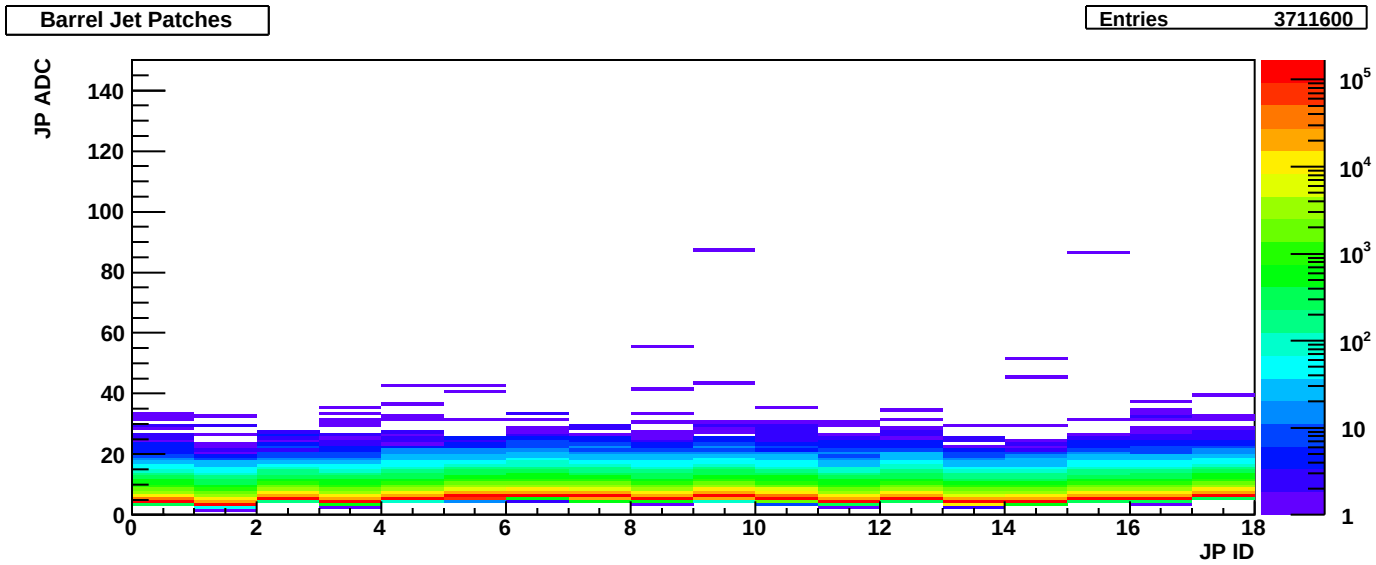

BunchId7Bit (all events)
Entries 206200

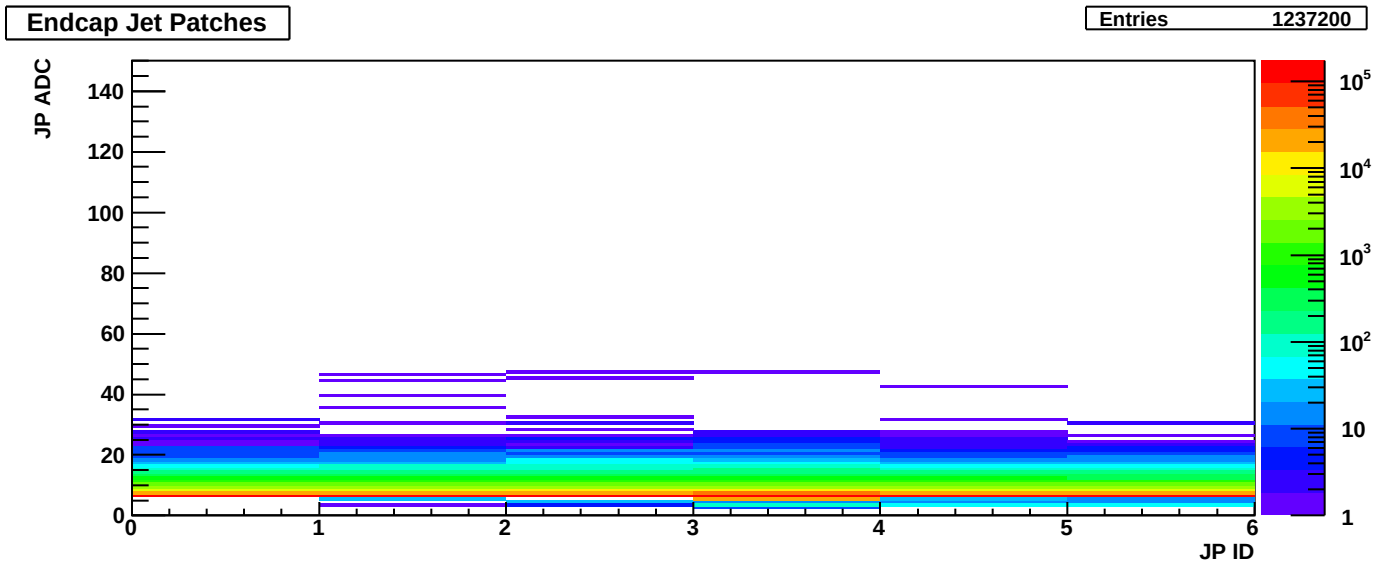

Barrel EMC L0 Input - High Tower
Entries 6.186e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

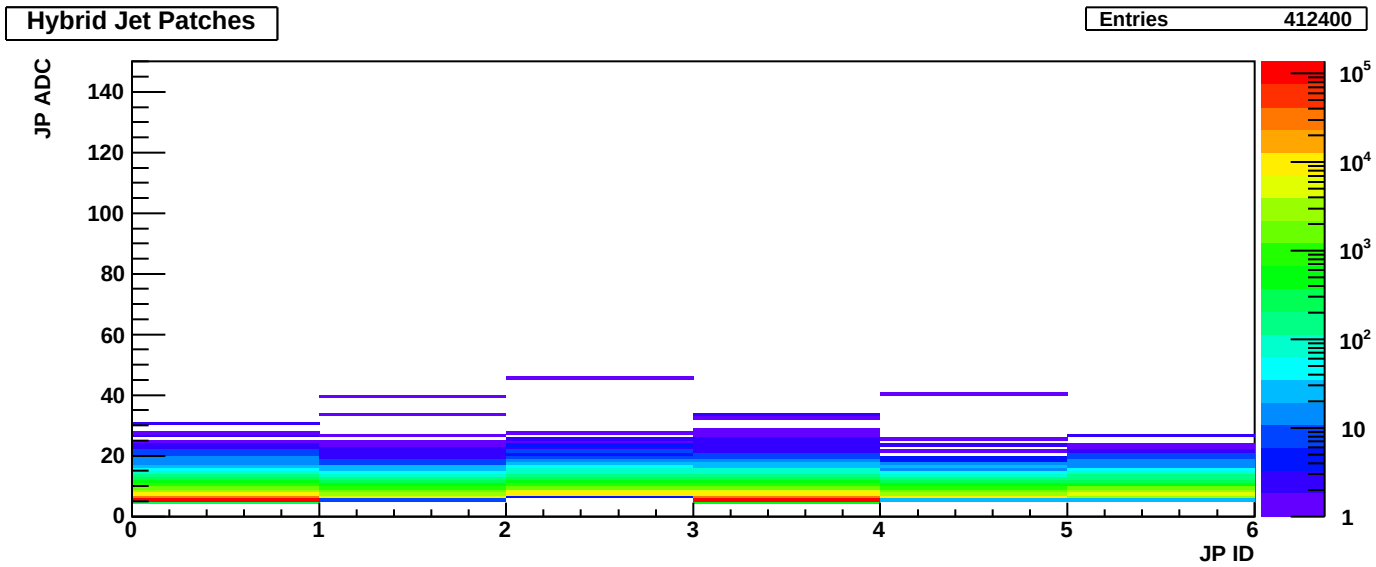

Barrel EMC L0 Input - Patch Sum
Entries 6.186e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

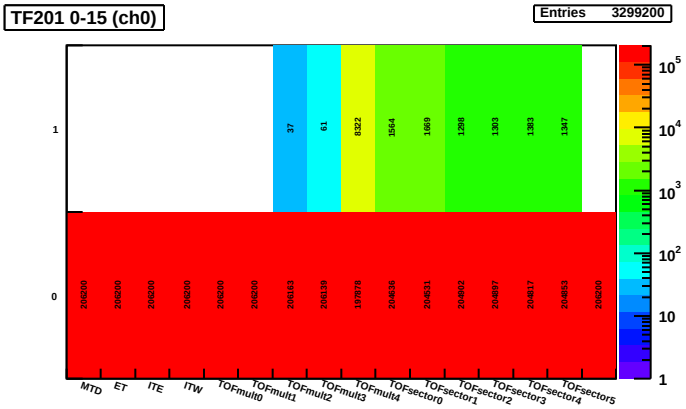

Endcap EMC L0 Input - High Tower
Entries 1.8558e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

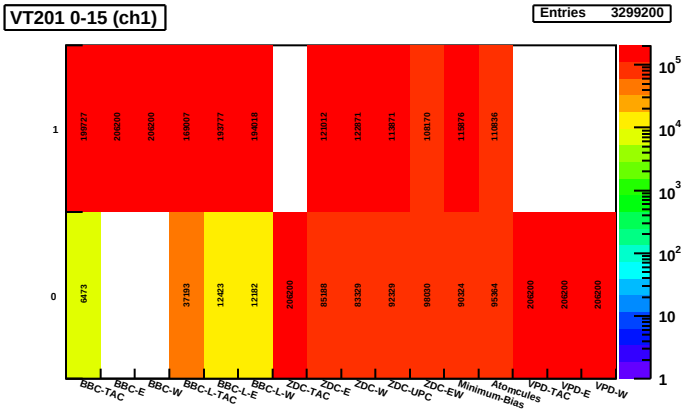

Endcap EMC L0 Input - Patch Sum
Entries 1.8558e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

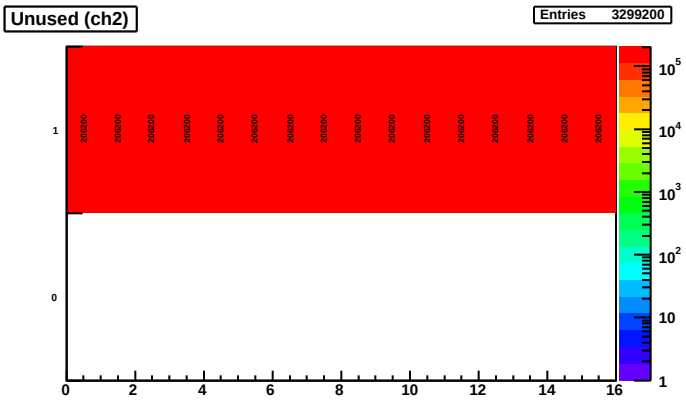

Barrel Jet Patches
Entries 3711600
JP ADC
JP ID

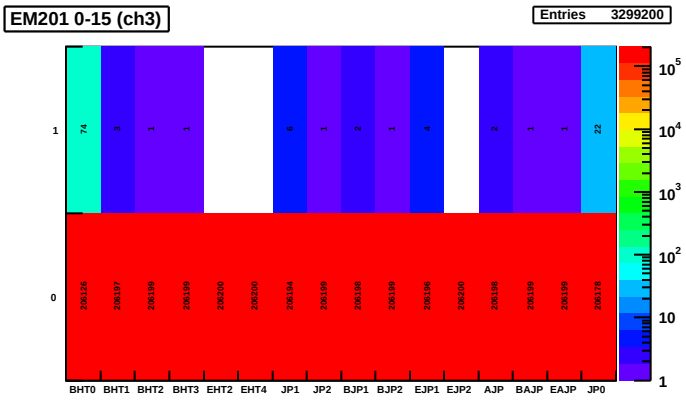

Endcap Jet Patches
Entries 1237200
JP ADC
JP ID

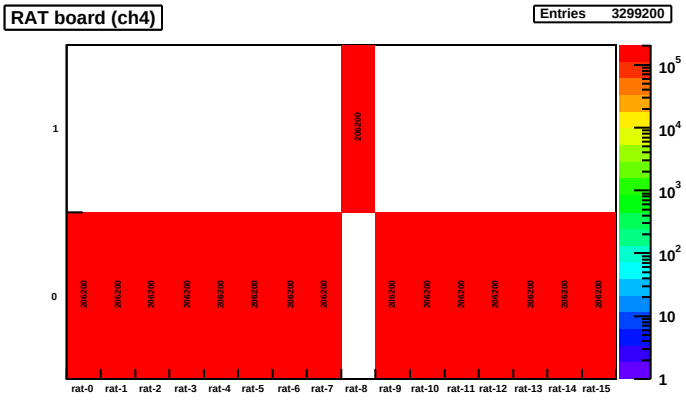

Hybrid Jet Patches
Entries 412400
JP ADC
JP ID

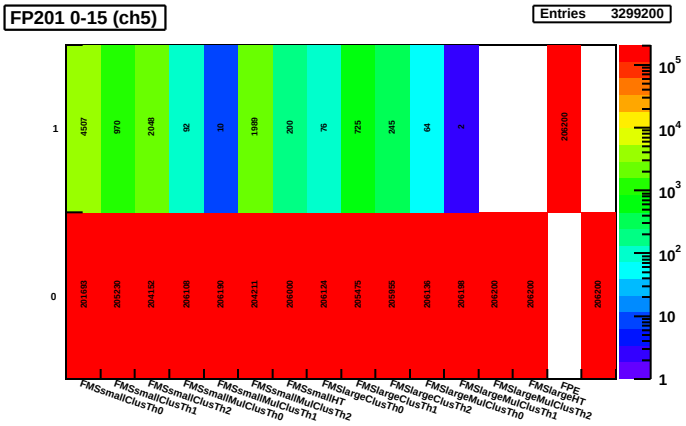

TF201 0-15 (ch0)
Entries 3299200
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

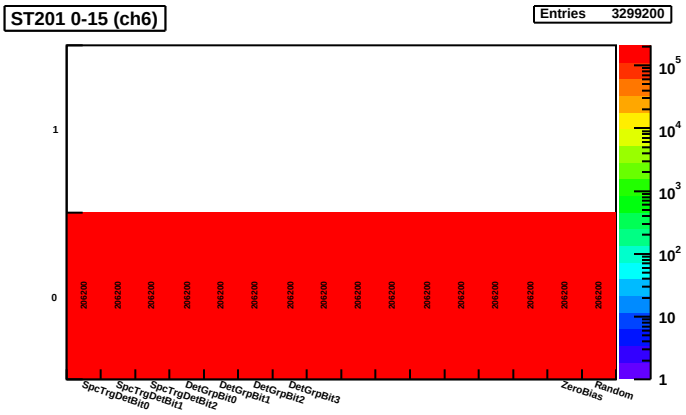

VT201 0-15 (ch1)
Entries 3299200
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

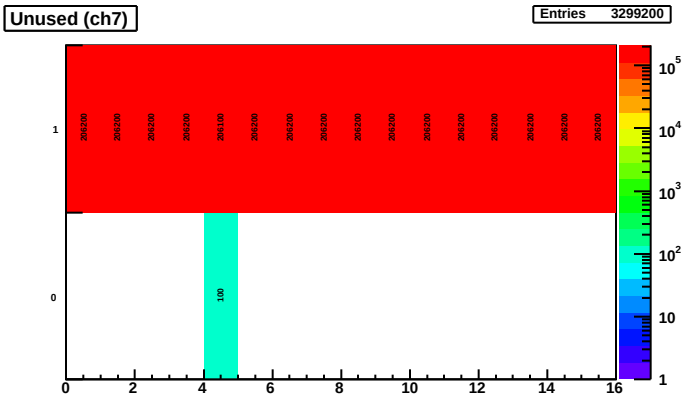

Unused (ch2)
Entries 3299200

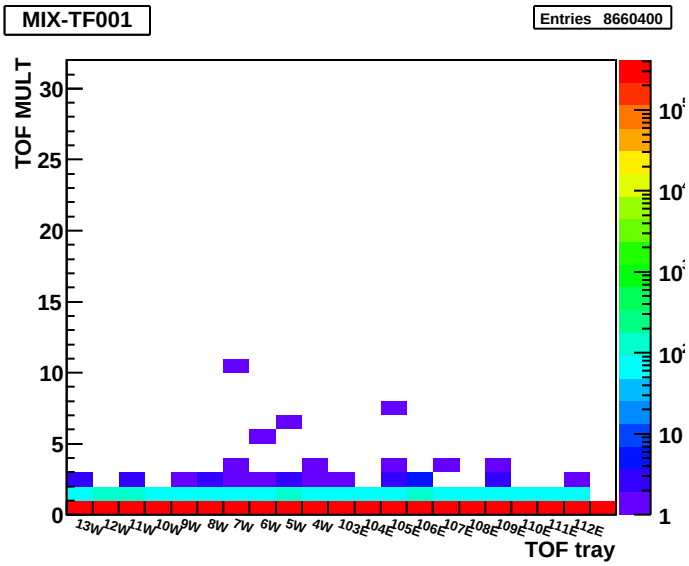

EM201 0-15 (ch3)
Entries 3299200
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

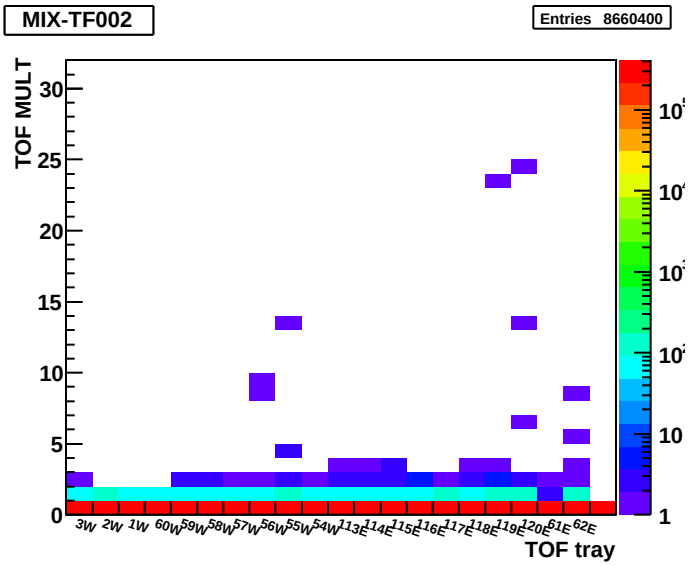

RAT board (ch4)
Entries 3299200
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

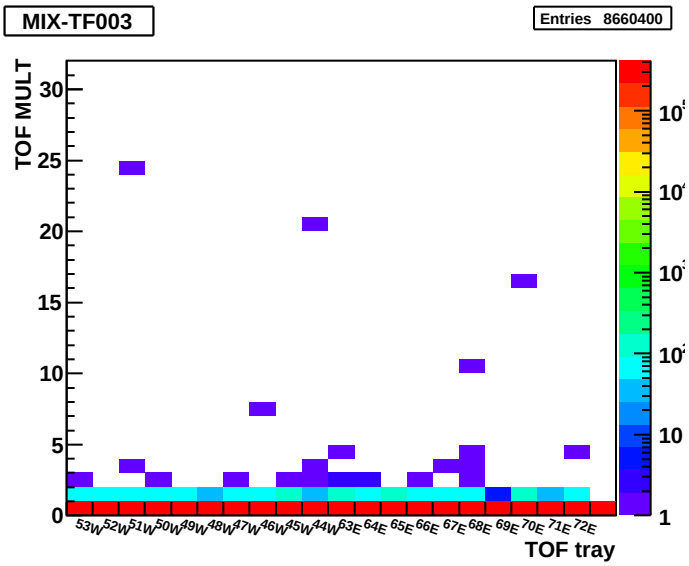

FP201 0-15 (ch5)
Entries 3299200
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

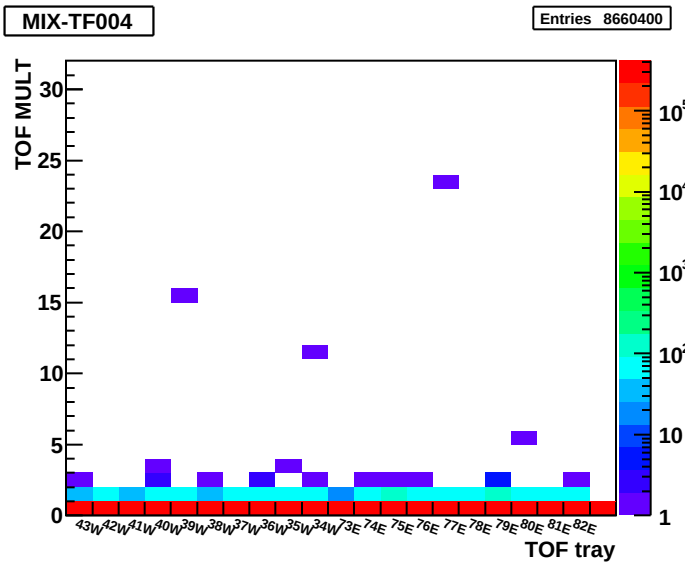

ST201 0-15 (ch6)
Entries 3299200
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

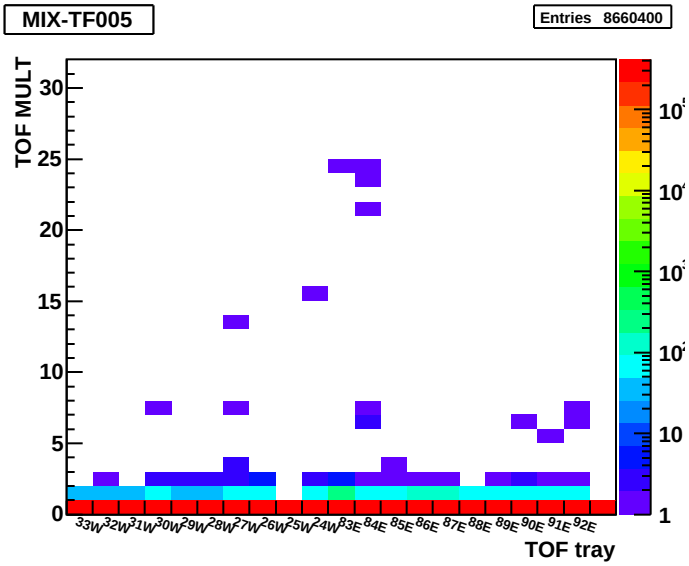

Unused (ch7)
Entries 3299200

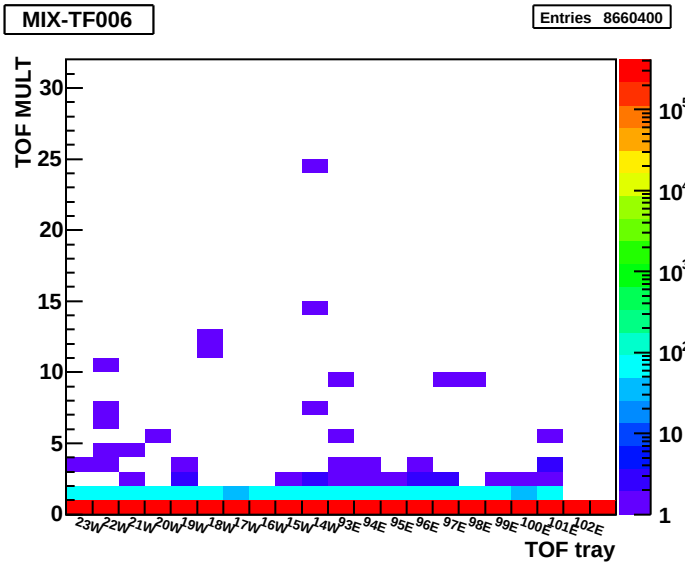

MIX-TF001
Entries 8660400
TOF MULT
TOF tray

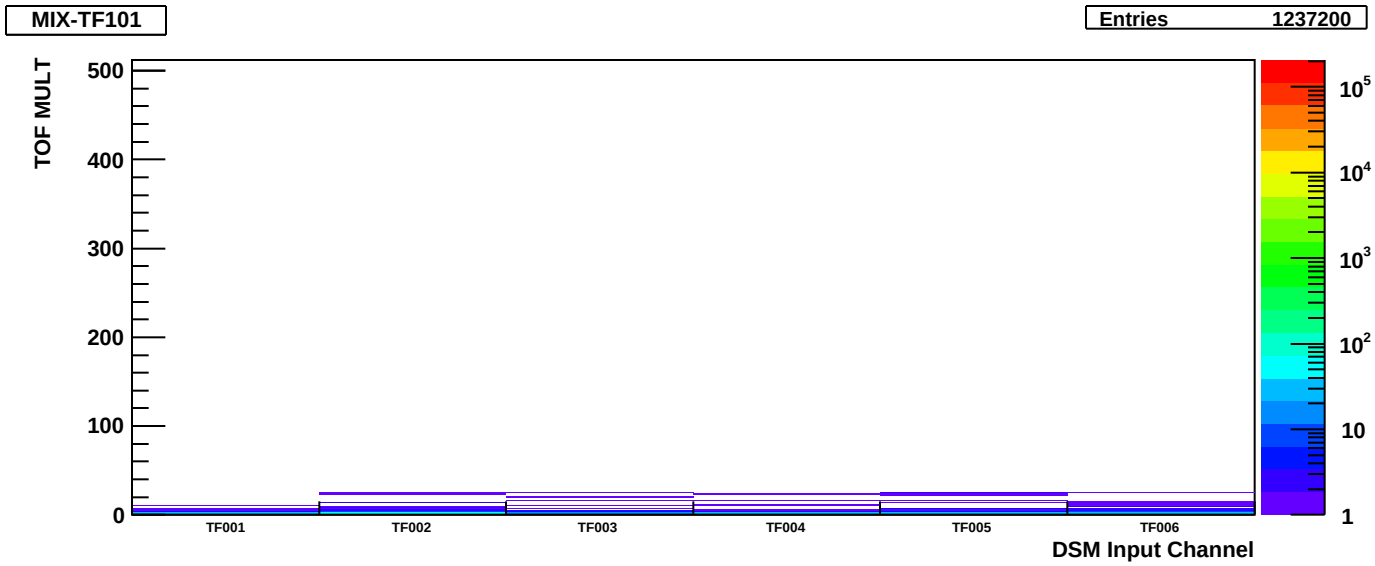

MIX-TF002
Entries 8660400
TOF MULT
TOF tray

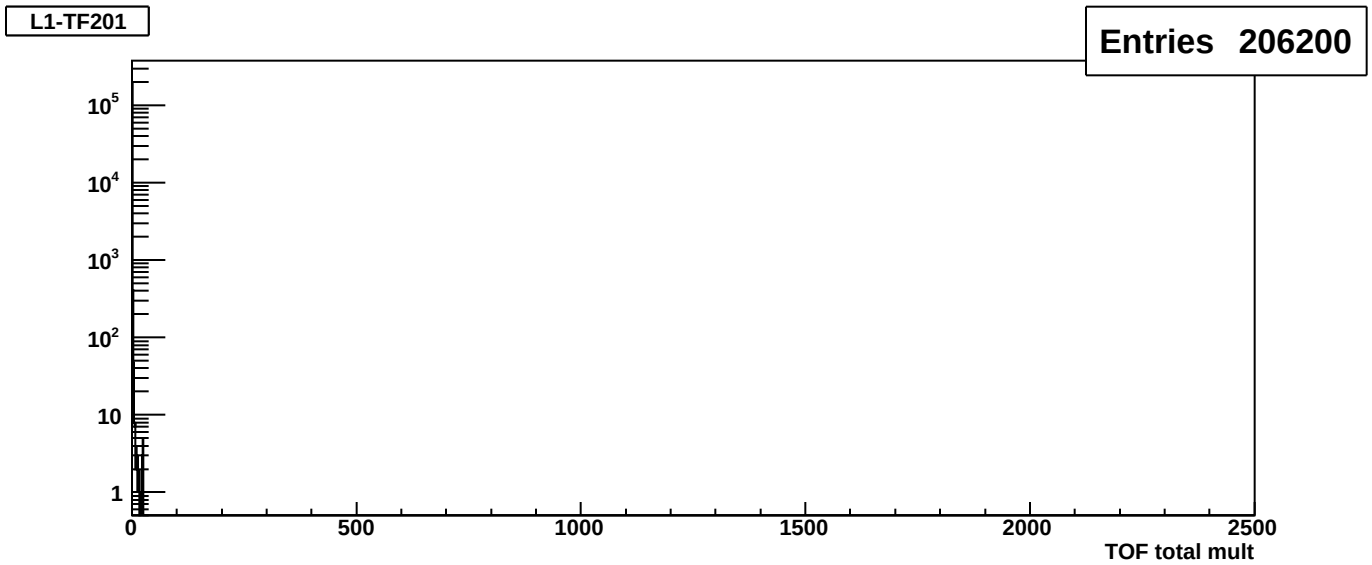

MIX-TF003
Entries 8660400
TOF MULT
TOF tray

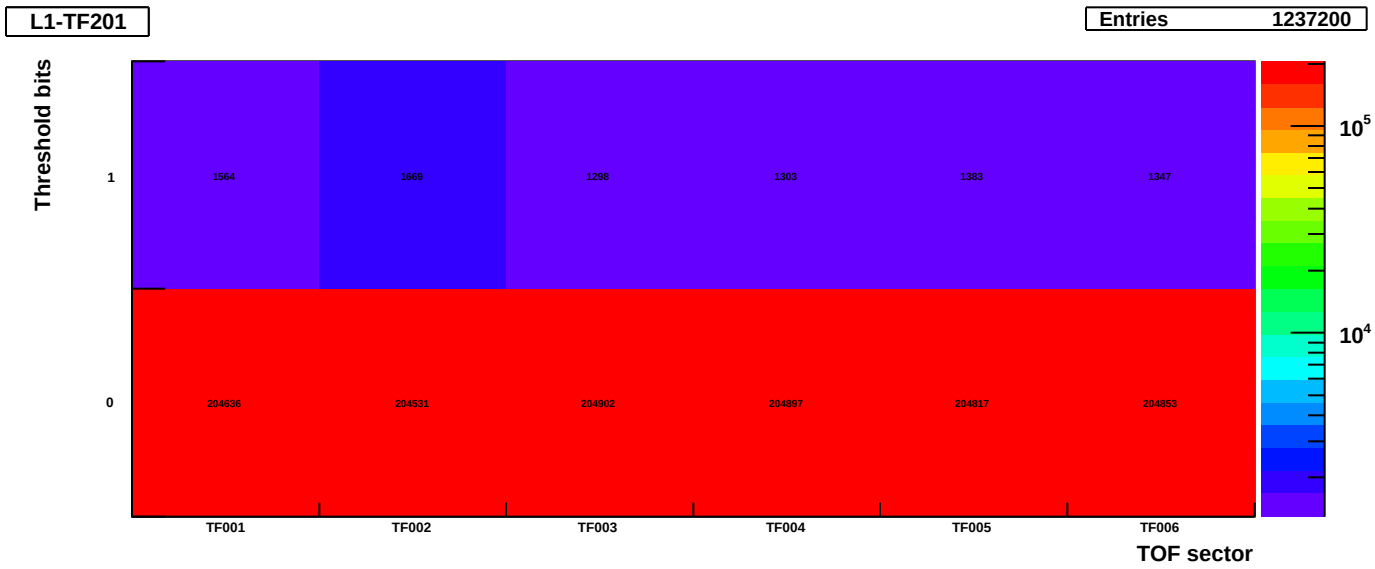

MIX-TF004
Entries 8660400
TOF MULT
TOF tray

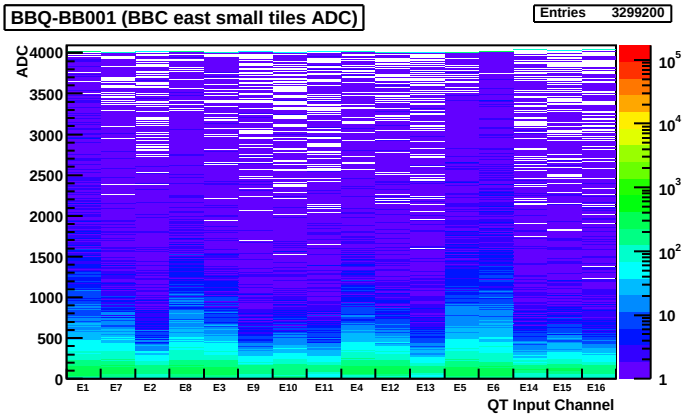

MIX-TF005
Entries 8660400
TOF MULT
TOF tray

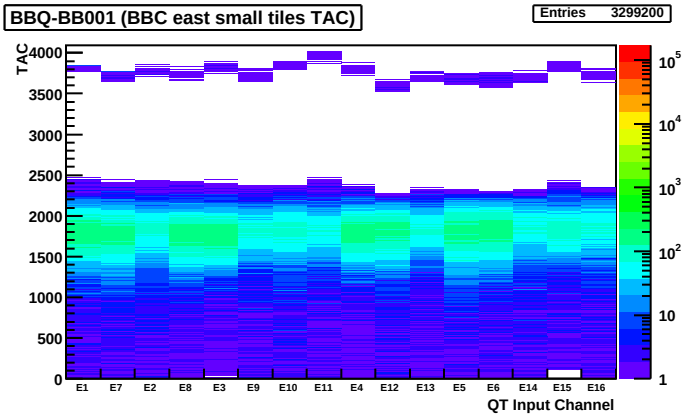

MIX-TF006
Entries 8660400
TOF MULT
TOF tray

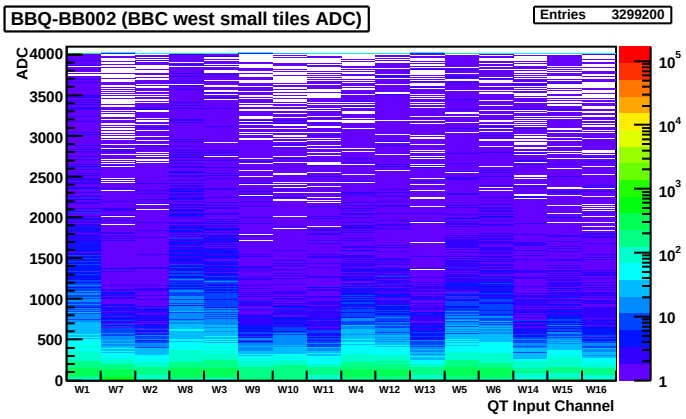

MIX-TF101
Entries 1237200
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

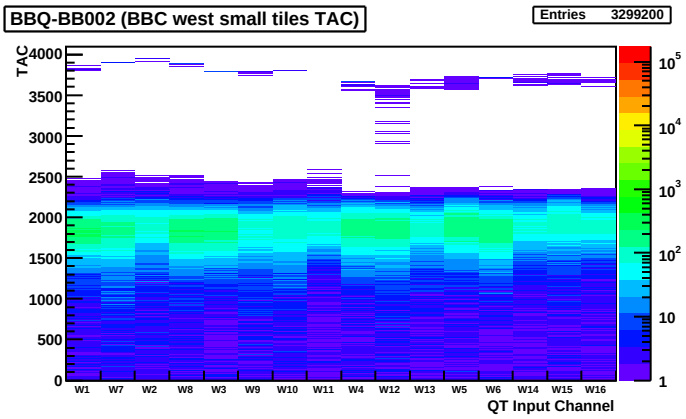

L1-TF201
Entries 206200
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

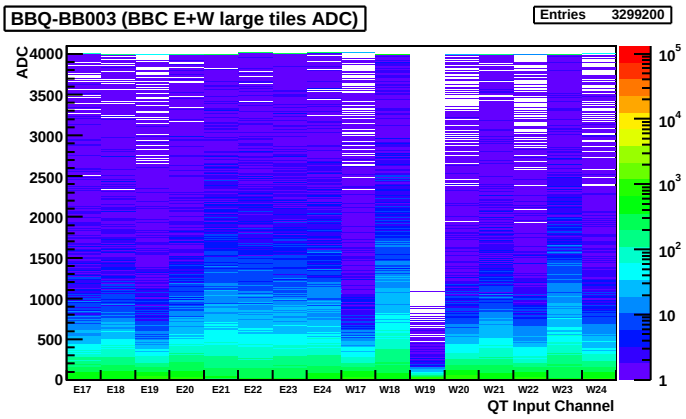

L1-TF201
Entries 1237200
Threshold bits
1
0
1564
1669
1298
1303
1383
1347
204636
204531
204902
204897
204817
204853
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

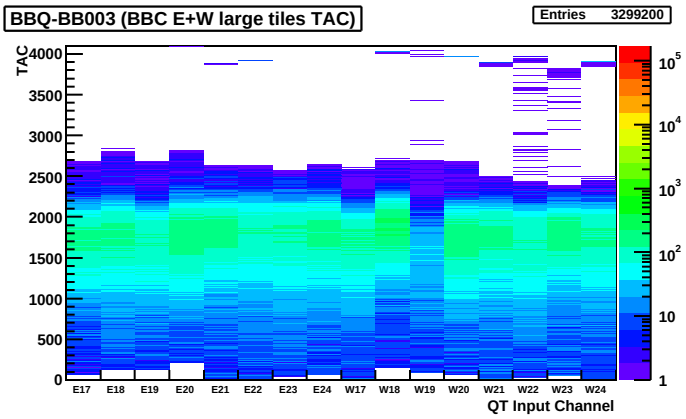

BBQ-BB001 (BBC east small tiles ADC)
Entries 3299200
ADC
QT Input Channel

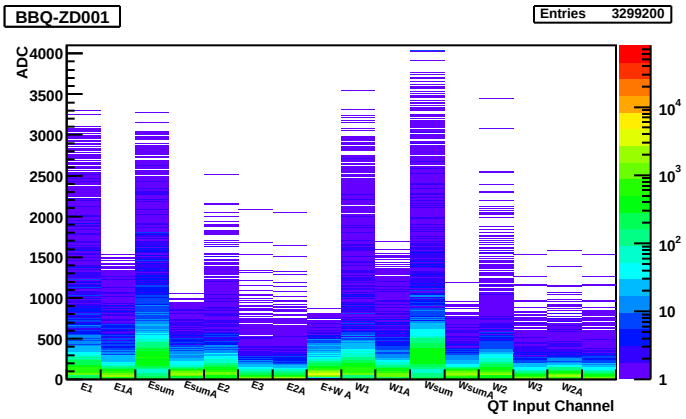

BBQ-BB001 (BBC east small tiles TAC)
Entries 3299200
TAC
QT Input Channel

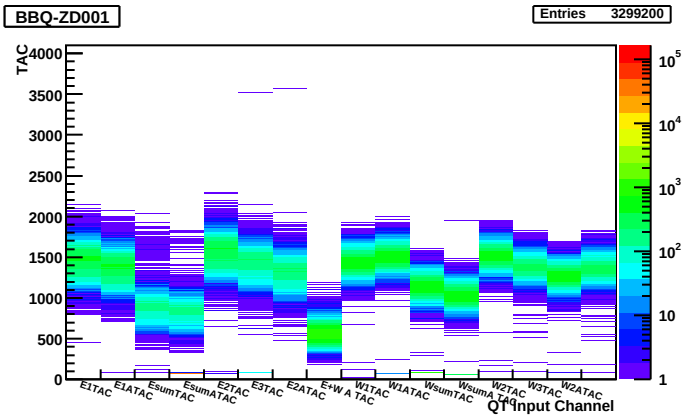

BBQ-BB002 (BBC west small tiles ADC)
Entries 3299200
ADC
QT Input Channel

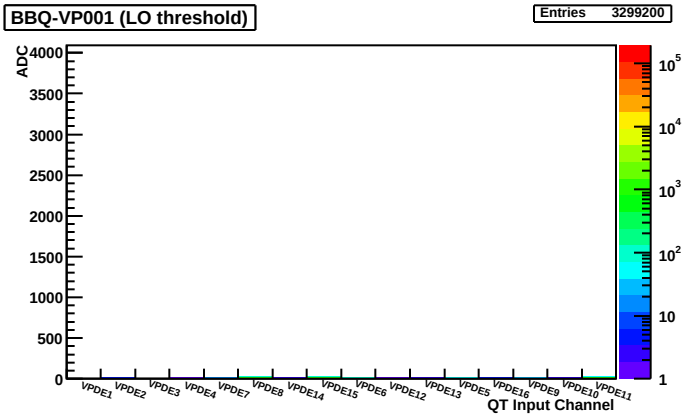

BBQ-BB002 (BBC west small tiles TAC)
Entries 3299200
TAC
QT Input Channel

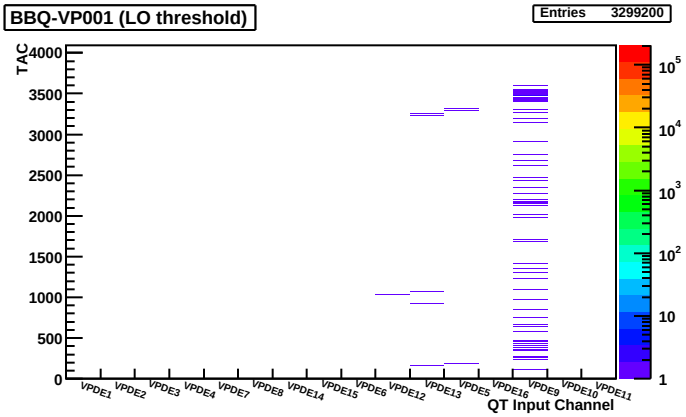

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3299200
ADC
QT Input Channel

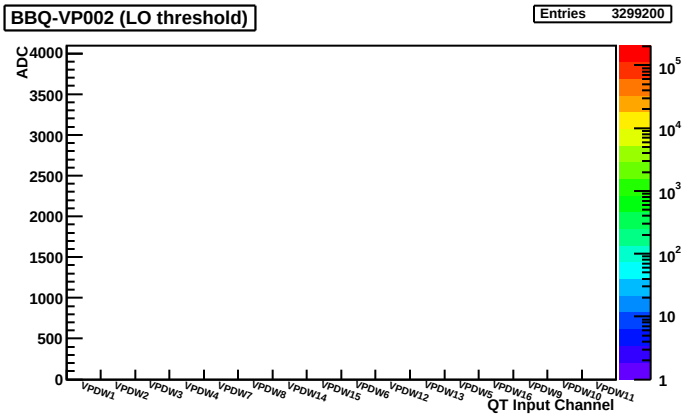

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3299200
TAC
QT Input Channel

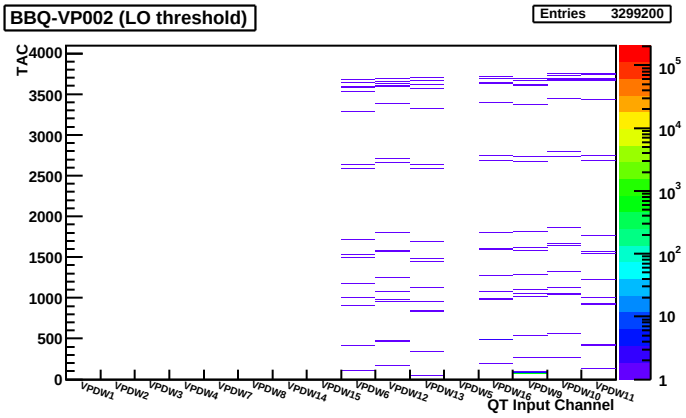

BBQ-ZD001
Entries 3299200
ADC
QT Input Channel

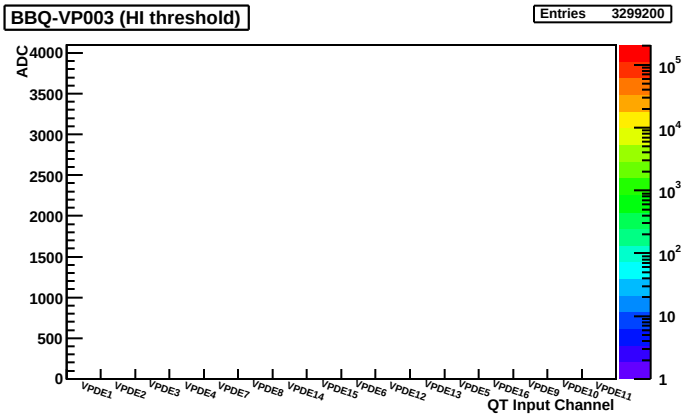

BBQ-ZD001
Entries 3299200
TAC
QT Input Channel

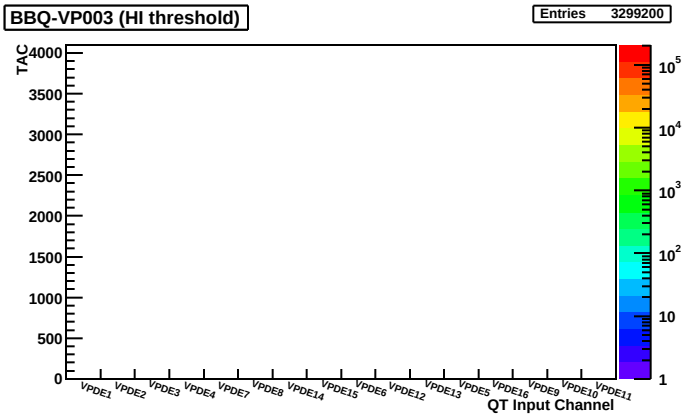

BBQ-VP001 (LO threshold)
Entries 3299200
ADC
QT Input Channel

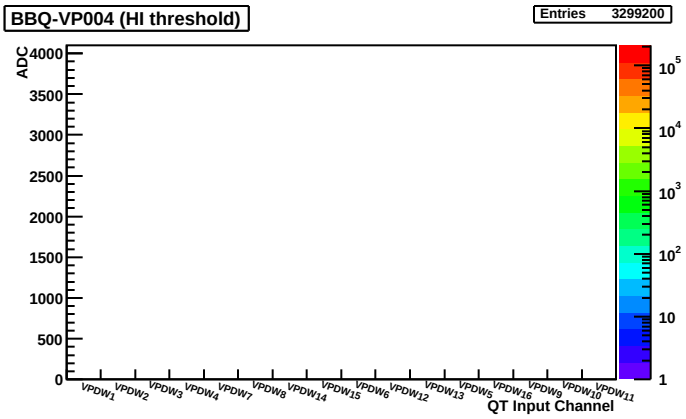

BBQ-VP001 (LO threshold)
Entries 3299200
TAC
QT Input Channel

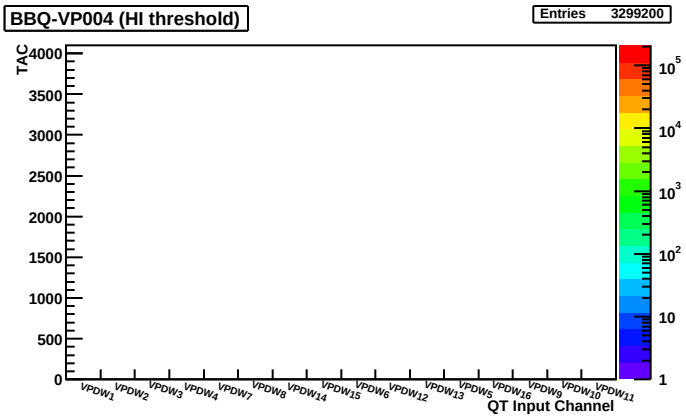

BBQ-VP002 (LO threshold)
Entries 3299200
ADC
QT Input Channel

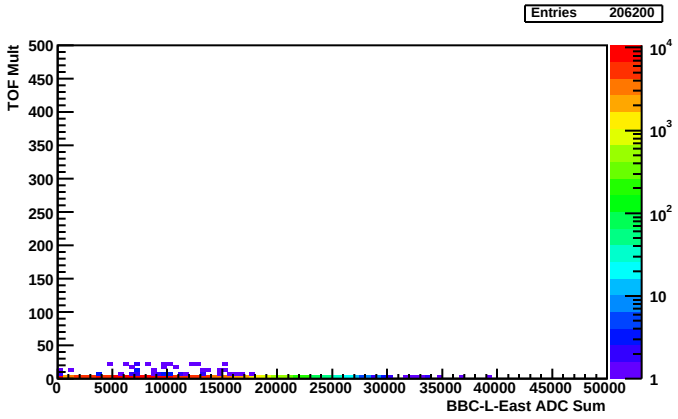

BBQ-VP002 (LO threshold)
Entries 3299200
TAC
QT Input Channel

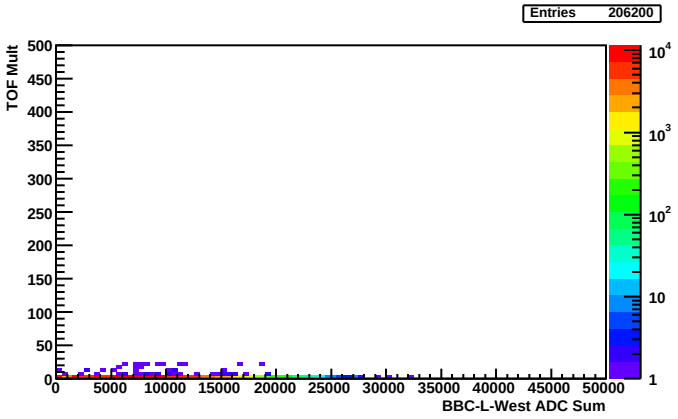

BBQ-VP003 (HI threshold)
Entries 3299200
ADC
QT Input Channel

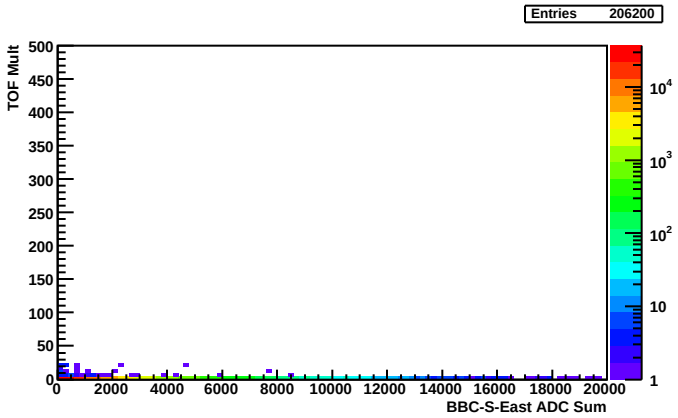

BBQ-VP003 (HI threshold)
Entries 3299200
TAC
QT Input Channel

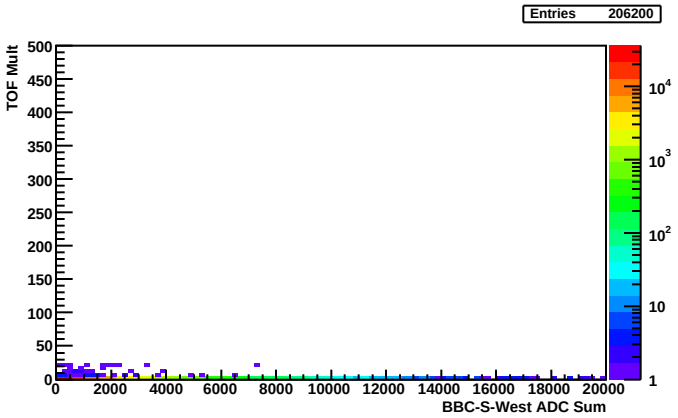

BBQ-VP004 (HI threshold)
Entries 3299200
ADC
QT Input Channel

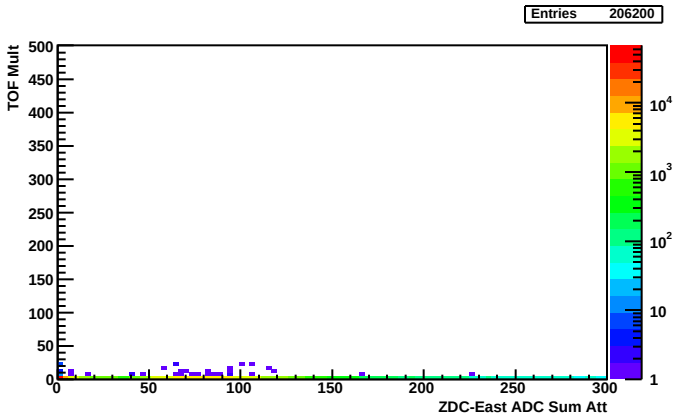

BBQ-VP004 (HI threshold)
Entries 3299200
TAC
QT Input Channel

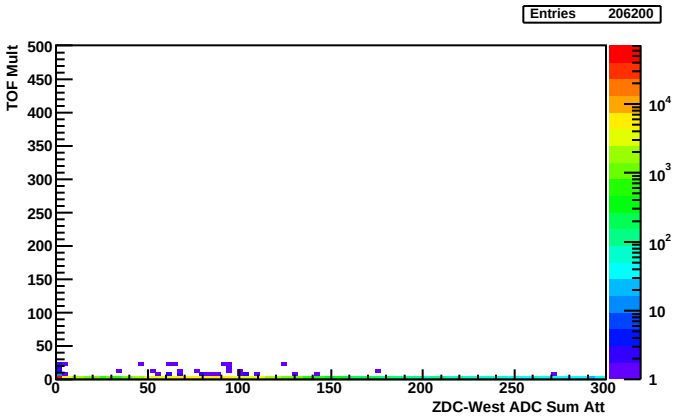

Entries 206200
TOF Mult
BBC-L-East ADC Sum

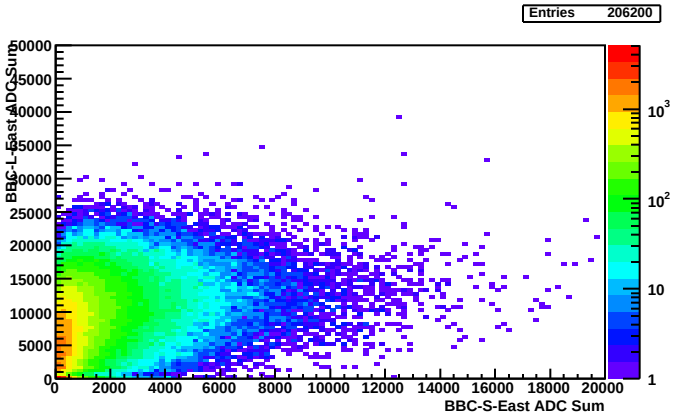

Entries 206200
TOF Mult
BBC-L-West ADC Sum

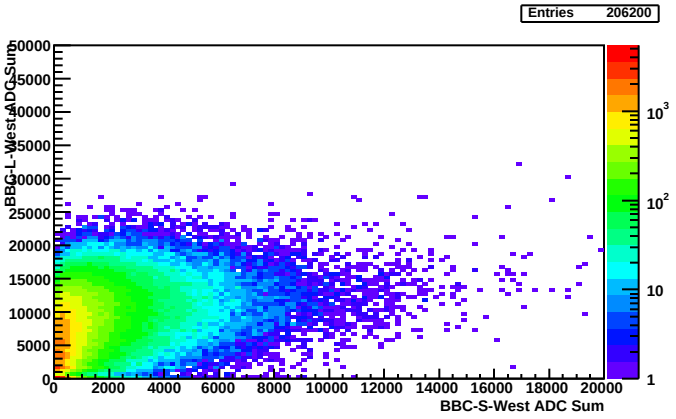

Entries 206200
TOF Mult
BBC-S-East ADC Sum

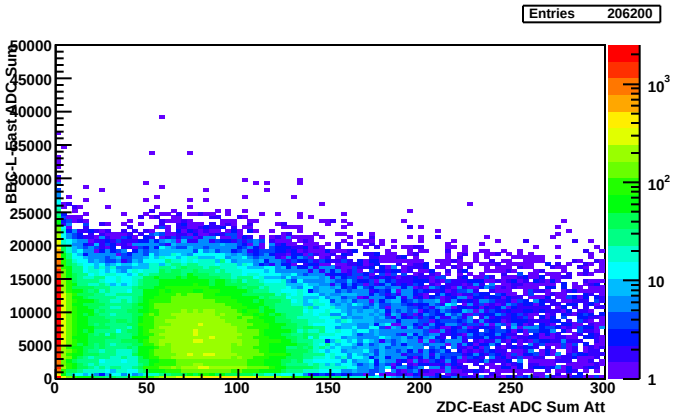

Entries 206200
TOF Mult
BBC-S-West ADC Sum

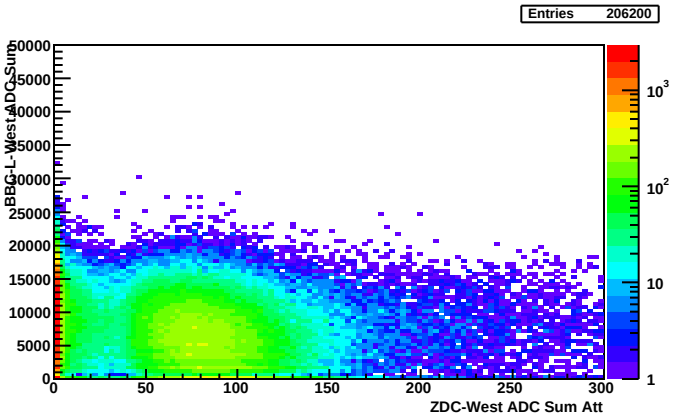

Entries 206200
TOF Mult
ZDC-East ADC Sum Att

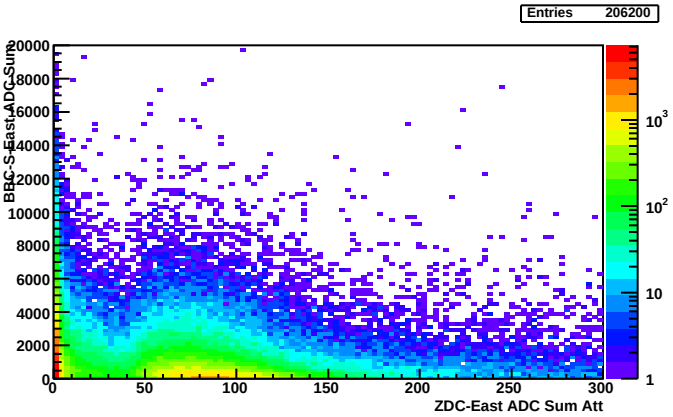

Entries 206200
TOF Mult
ZDC-West ADC Sum Att

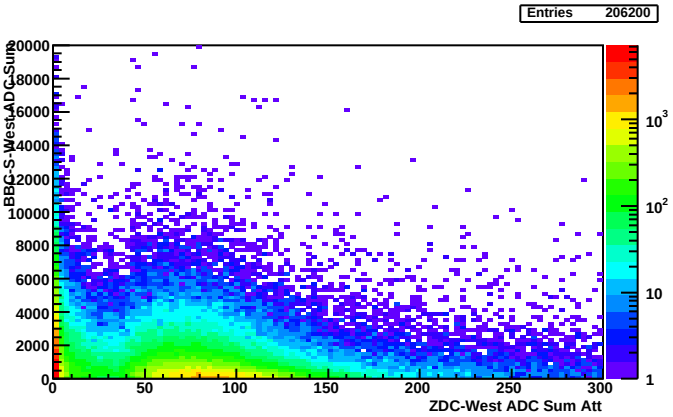

Entries 206200
BBC-L-East ADC Sum
BBC-S-East ADC Sum

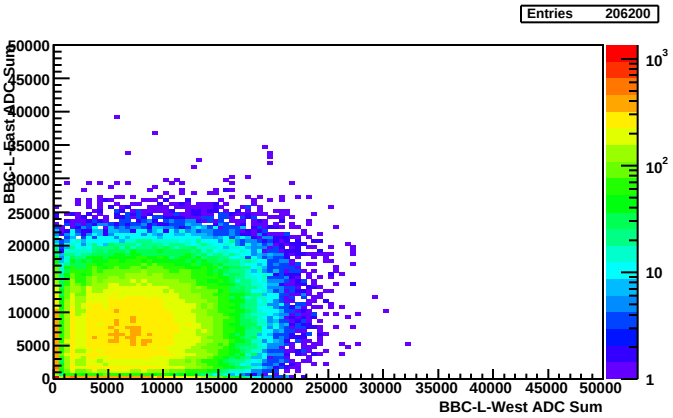

Entries 206200
BBC-L-West ADC Sum
BBC-S-West ADC Sum

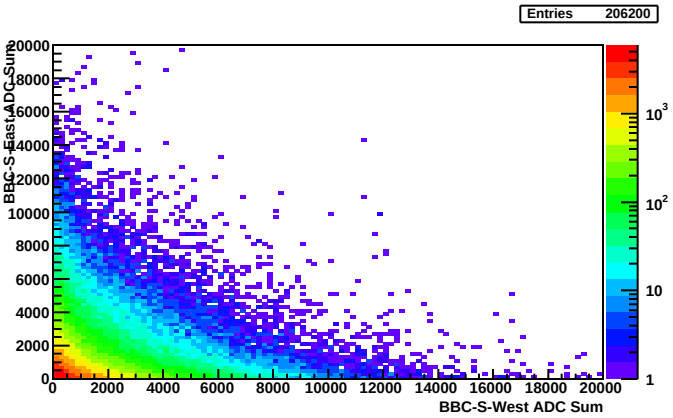

Entries 206200
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

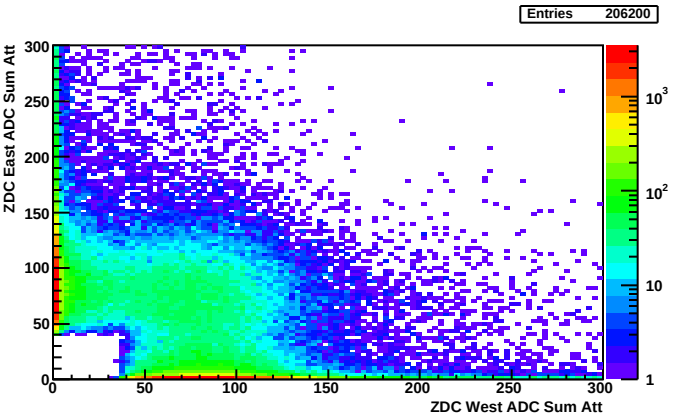

Entries 206200
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

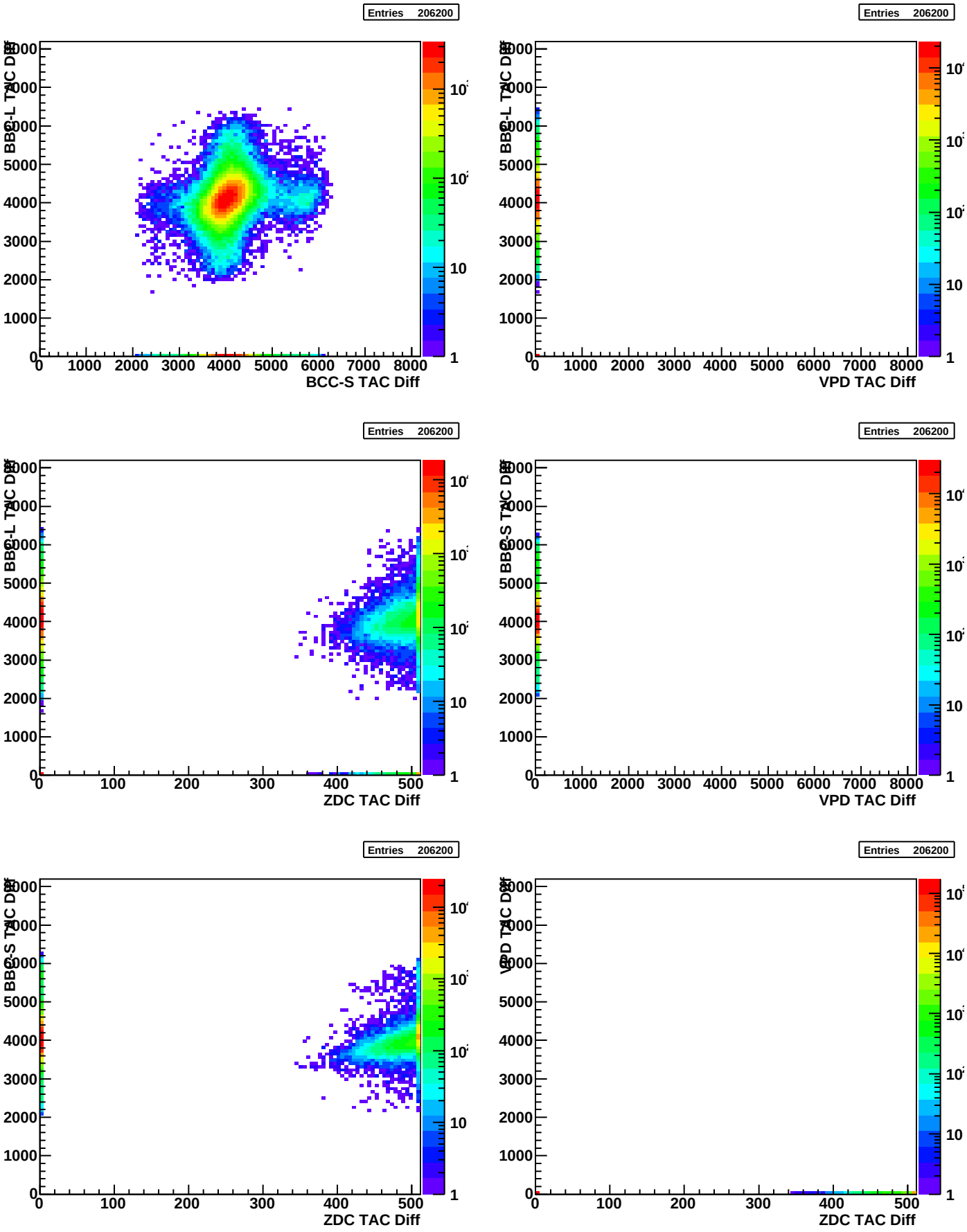

Entries 206200
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

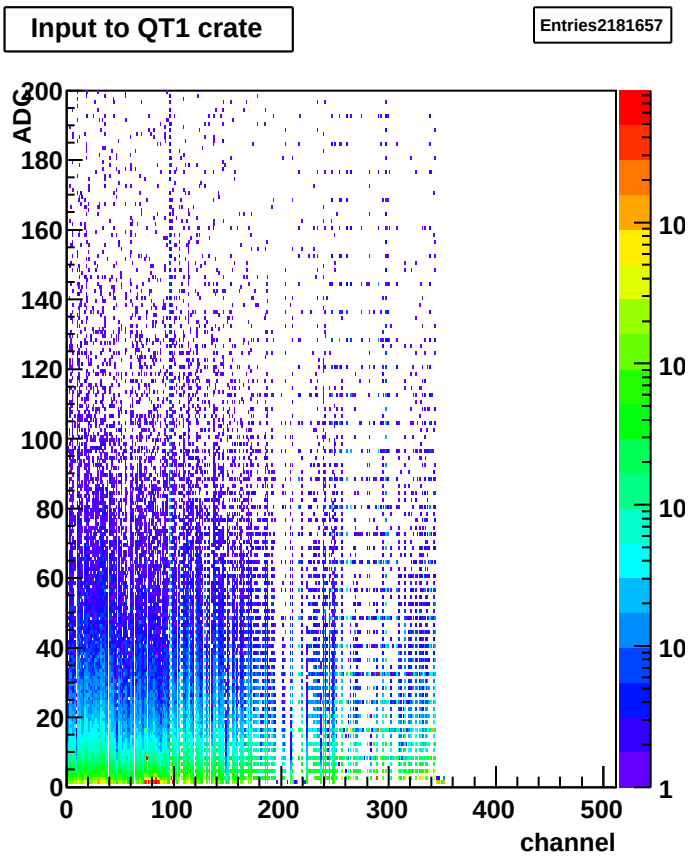

Entries 206200
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

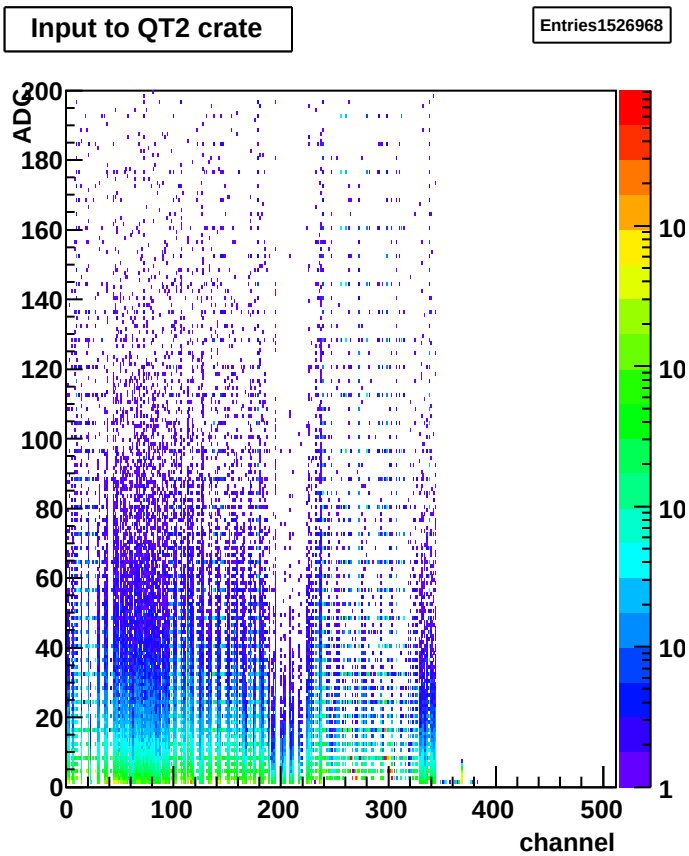

Entries 206200
BBC-L-East ADC Sum
BBC-L-West ADC Sum

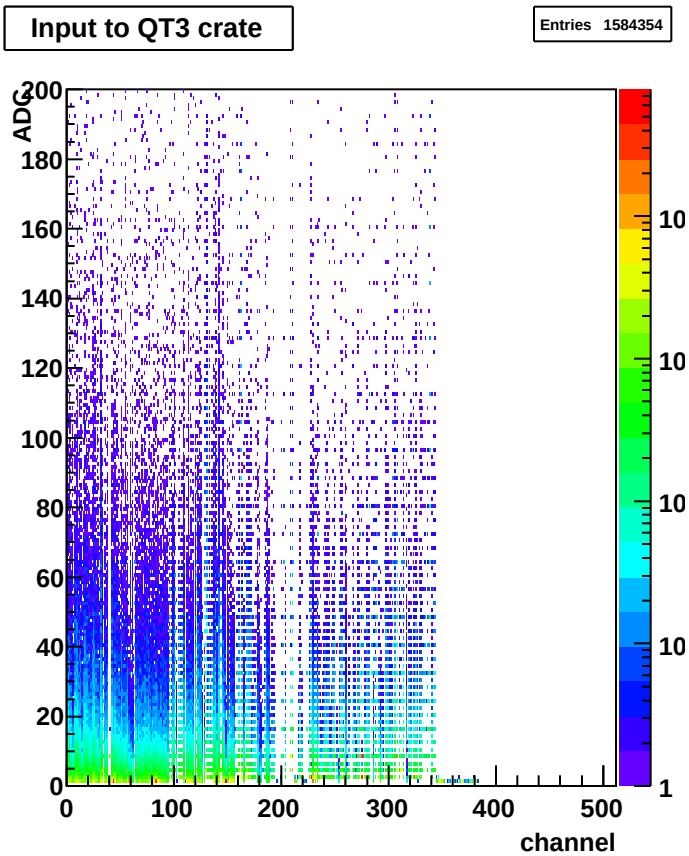

Entries 206200
BBC-S-East ADC Sum
BBC-S-West ADC Sum

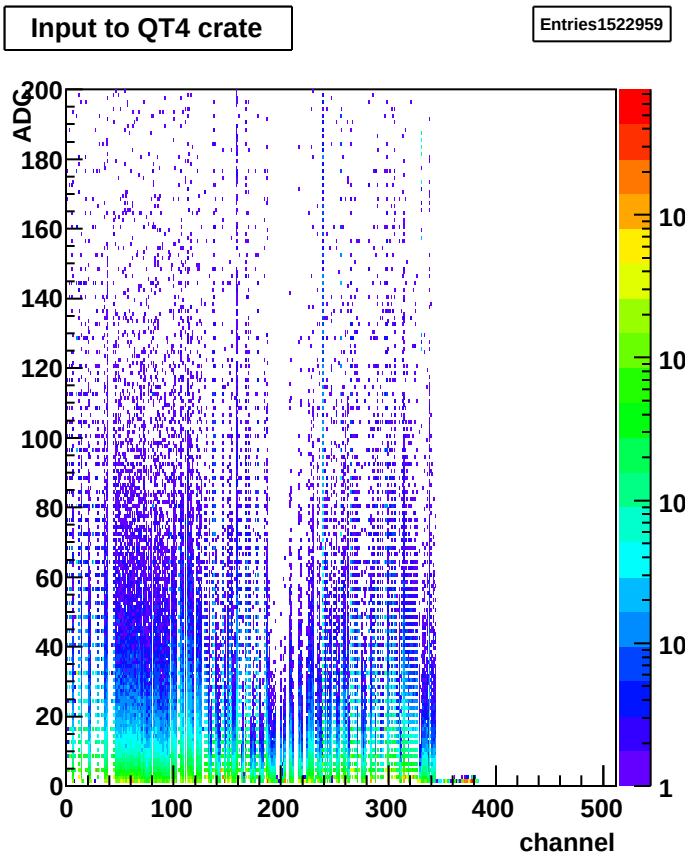

Entries 206200
ZDC East ADC Sum Att
ZDC West ADC Sum Att

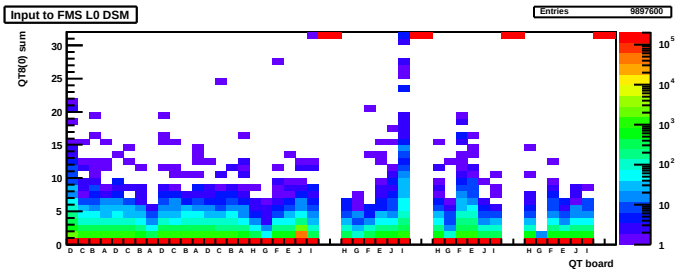

Entries 206200
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff

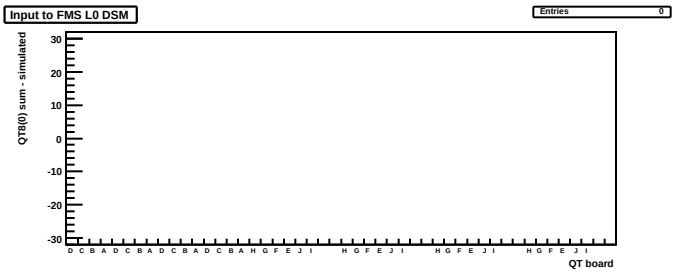

Input to QT1 crate
Entries2181657
ADC
channel

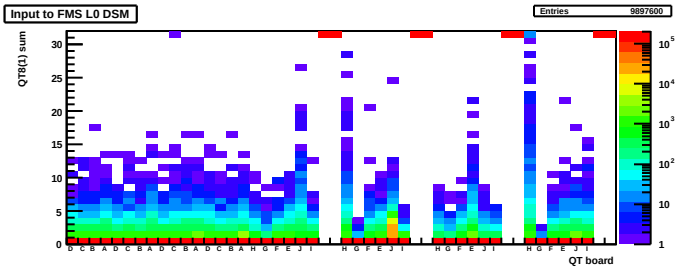

Input to QT2 crate
Entries1526968
ADC
channel

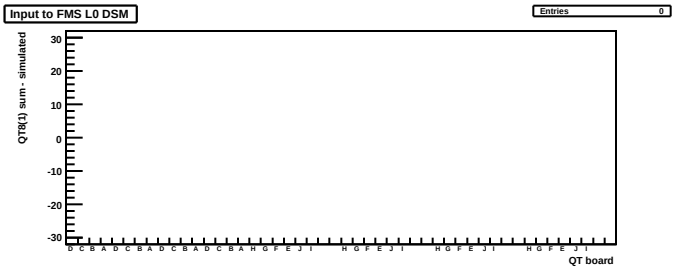

Input to QT3 crate
Entries 1584354
ADC
channel

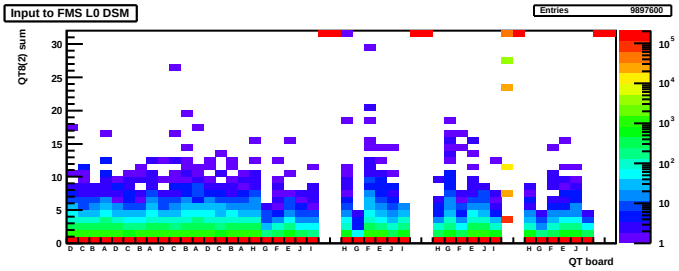

Input to QT4 crate
Entries1522959
ADC
channel

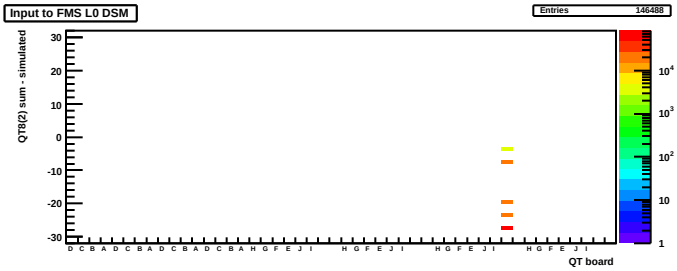

Input to FMS L0 DSM
Entries 9897600
QT8(0) sum
QT board

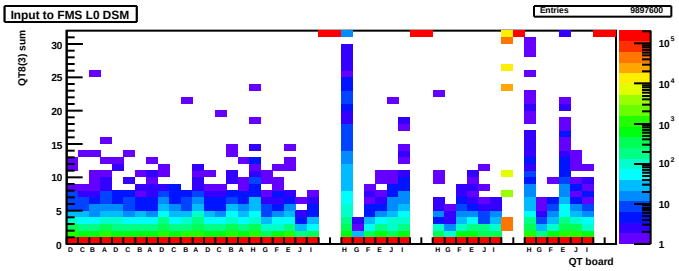

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

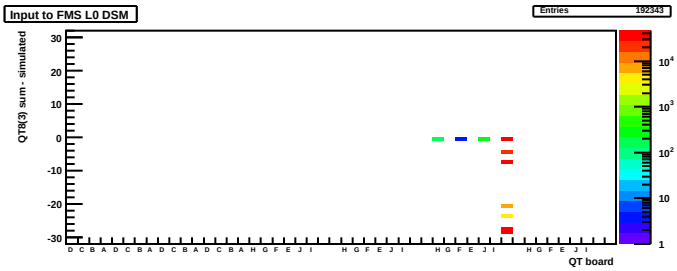

Input to FMS L0 DSM
Entries 9897600
QT8(1) sum
QT board

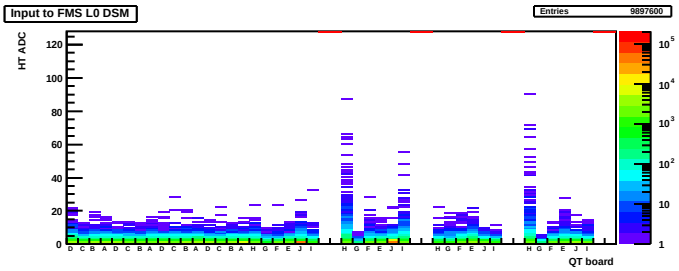

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

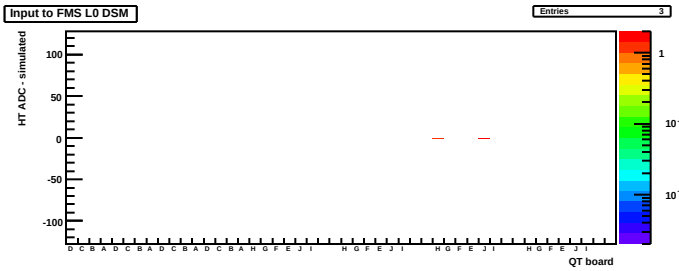

Input to FMS L0 DSM
Entries 9897600
QT8(2) sum
QT board

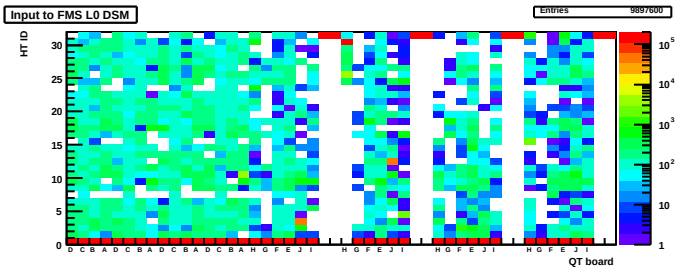

Input to FMS L0 DSM
Entries 146488
QT8(2) sum - simulated
QT board

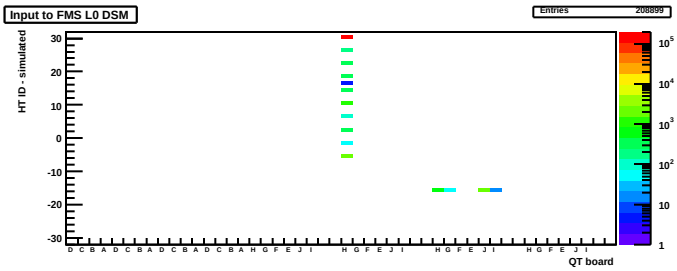

Input to FMS L0 DSM
Entries 9897600
QT8(3) sum
QT board

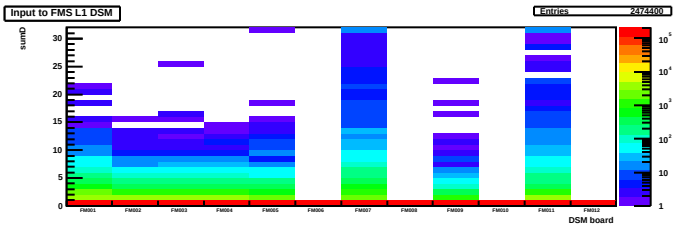

Input to FMS L0 DSM
Entries 192343
QT8(3) sum - simulated
QT board

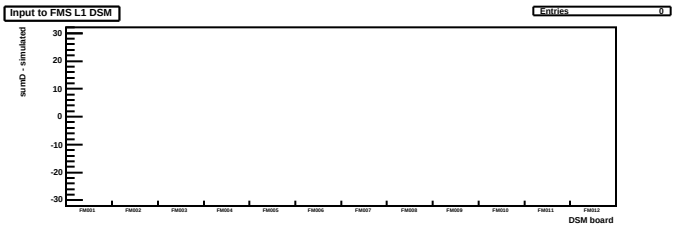

Input to FMS L0 DSM
Entries 9897600
HT ADC
QT board

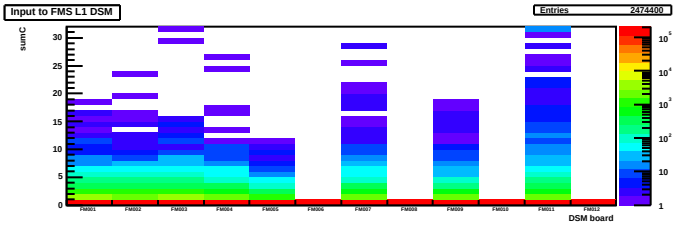

Input to FMS L0 DSM
Entries 3
HT ADC - simulated
QT board

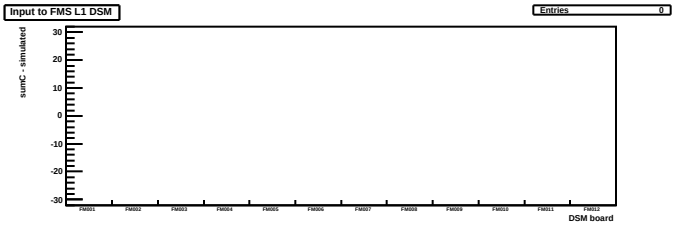

Input to FMS L0 DSM
Entries 9897600
HT ID
QT board

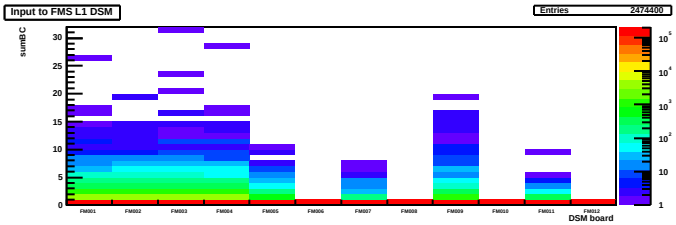

Input to FMS L0 DSM
Entries 208899
HT ID - simulated
QT board

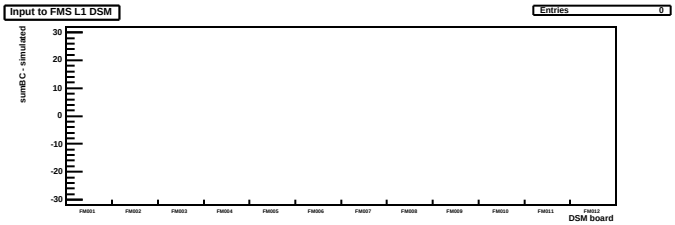

Input to FMS L1 DSM
Entries 2474400
sumD
DSM board

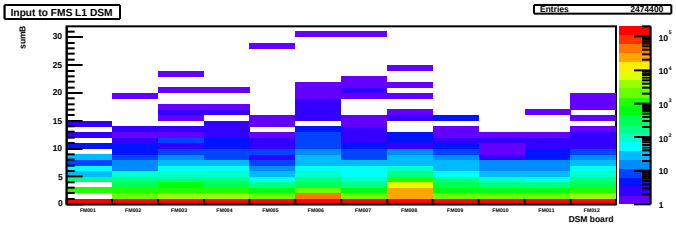

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

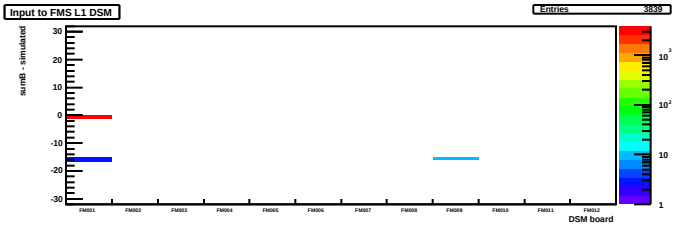

Input to FMS L1 DSM
Entries 2474400
sumC
DSM board

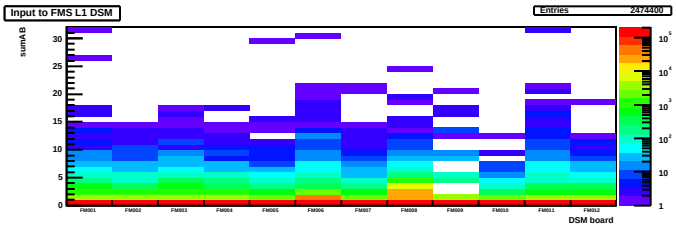

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

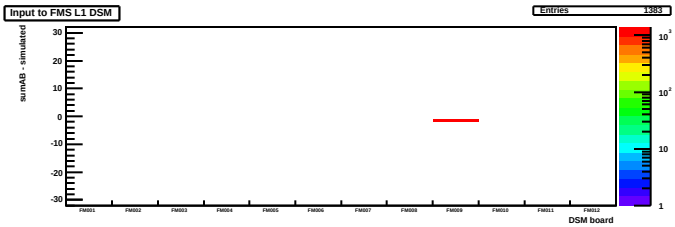

Input to FMS L1 DSM
Entries 2474400
sumBC
DSM board

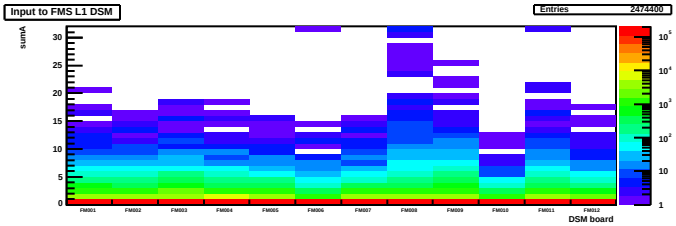

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

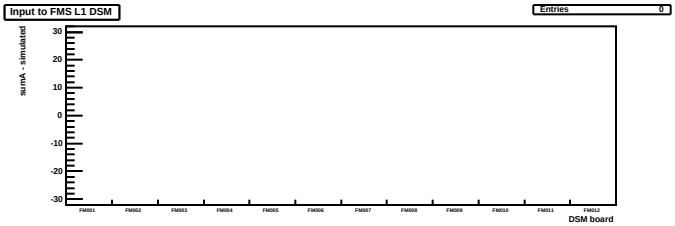

Input to FMS L1 DSM
Entries 2474400
sumB
DSM board

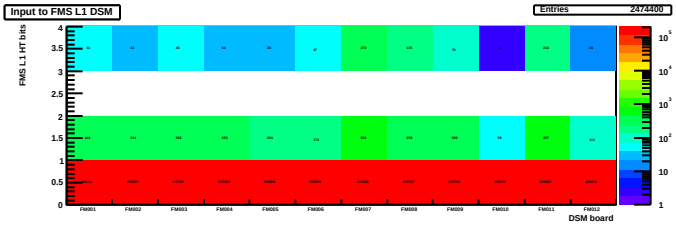

Input to FMS L1 DSM
Entries 3839
sumB - simulated
DSM board

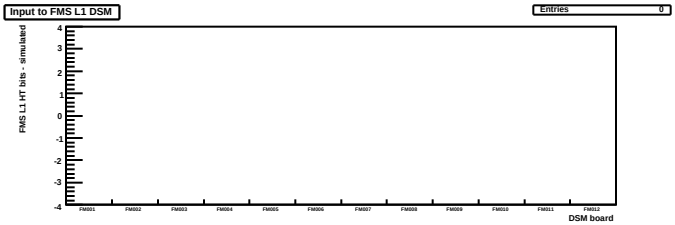

Input to FMS L1 DSM
Entries 2474400
sumAB
DSM board

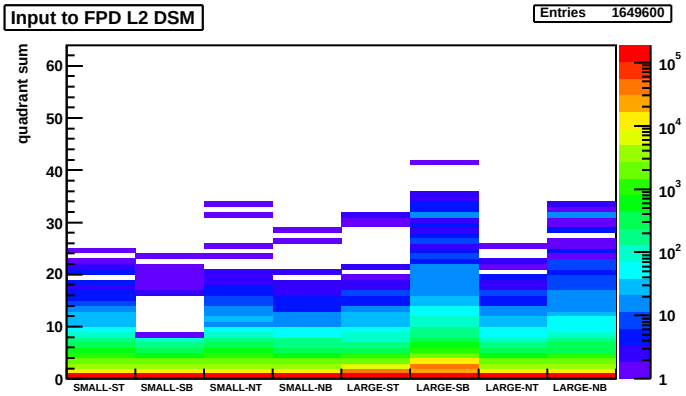

Input to FMS L1 DSM
Entries 1383
sumAB - simulated
DSM board

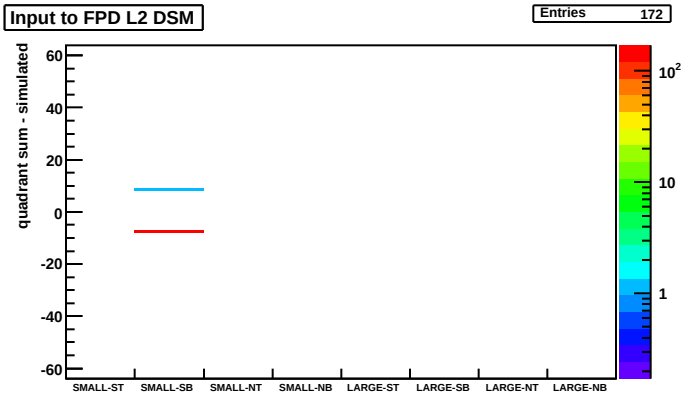

Input to FMS L1 DSM
Entries 2474400
sumA
DSM board

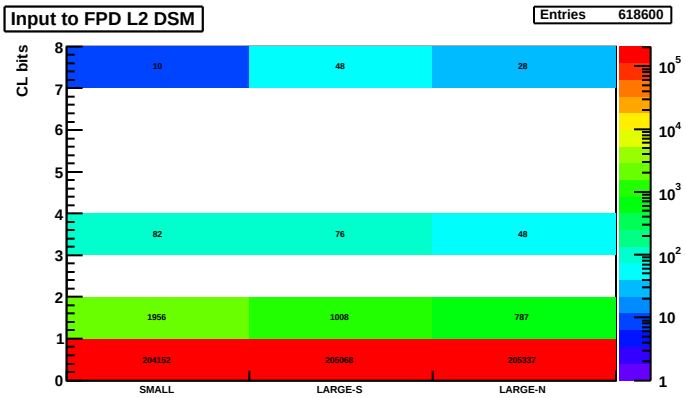

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

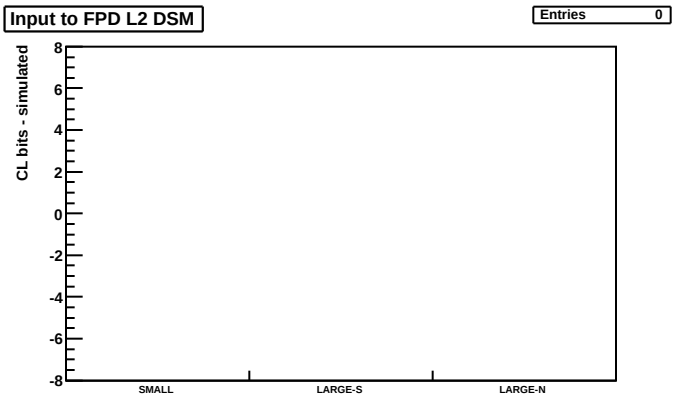

Input to FMS L1 DSM
Entries 2474400
FMS L1 HT bits
DSM board

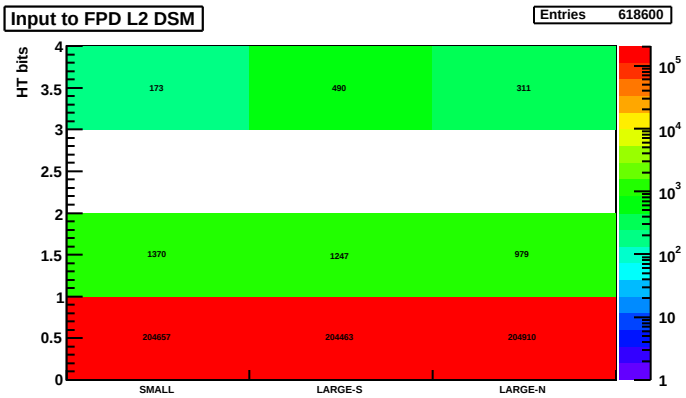

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

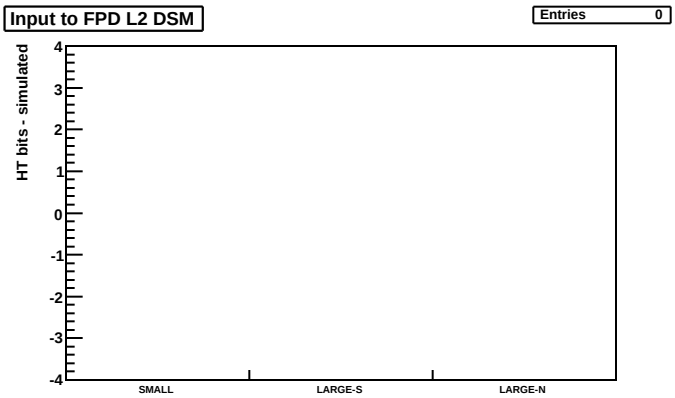

Input to FPD L2 DSM
Entries 1649600
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

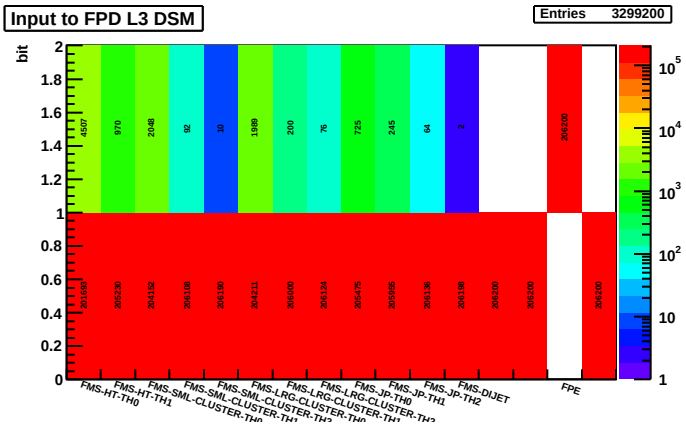

Input to FPD L2 DSM
Entries 172
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

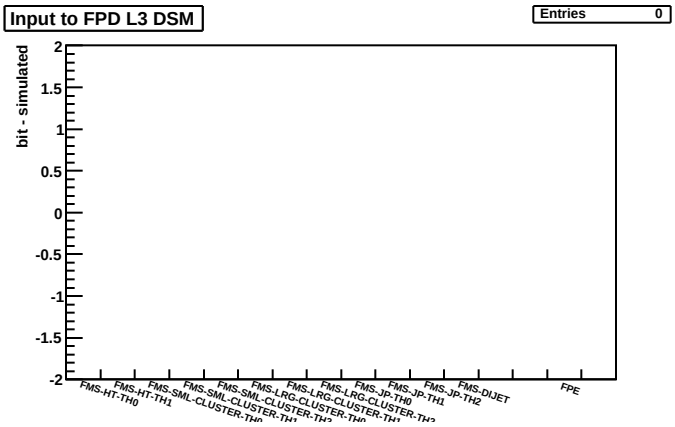

Input to FPD L2 DSM
Entries 618600
CL bits
10
48
28
82
76
48
1956
1008
787
204152
205068
205337
SMALL
LARGE-S
LARGE-N

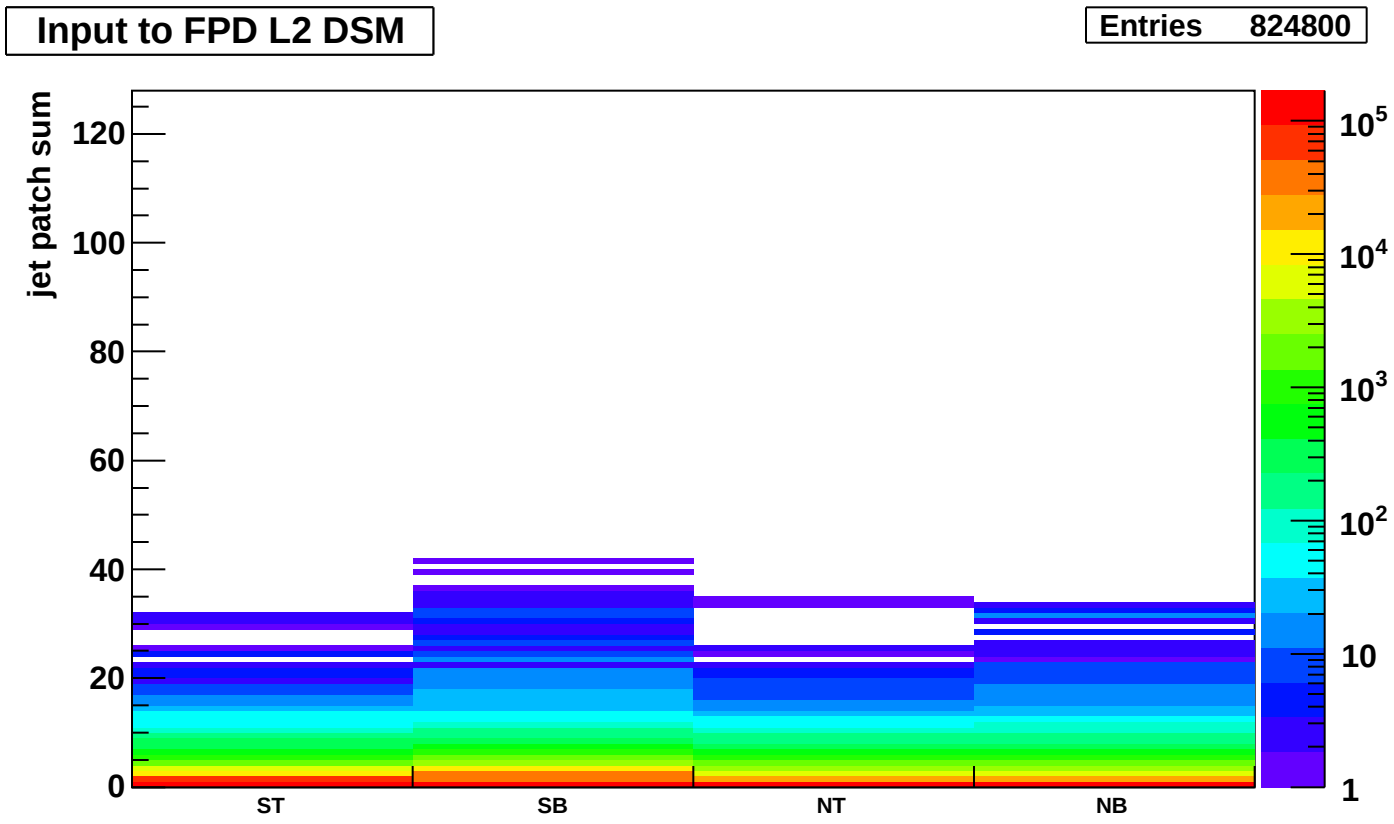

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

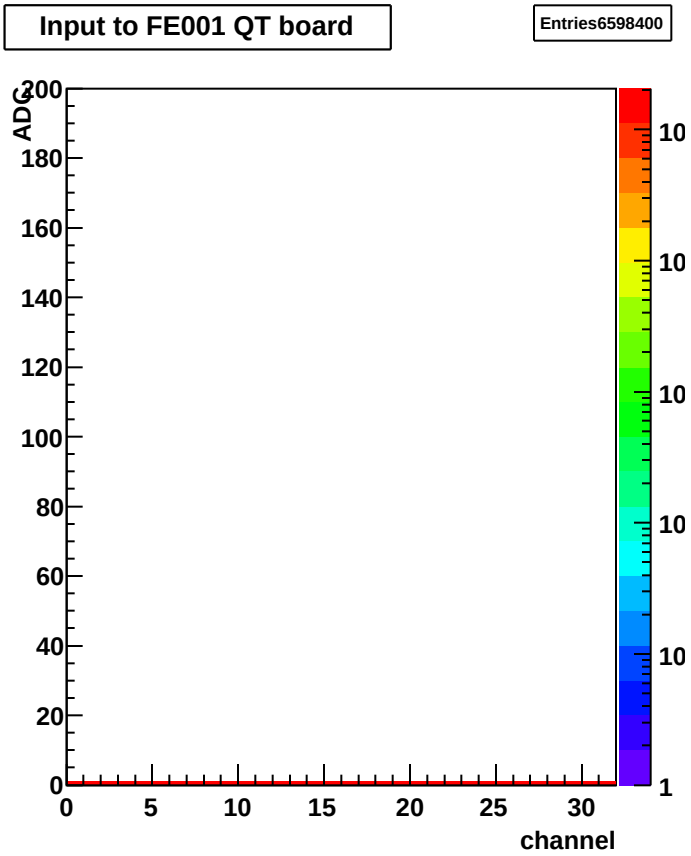

Input to FPD L2 DSM
Entries 618600
HT bits
173
490
311
1370
1247
979
204657
204463
204910
SMALL
LARGE-S
LARGE-N

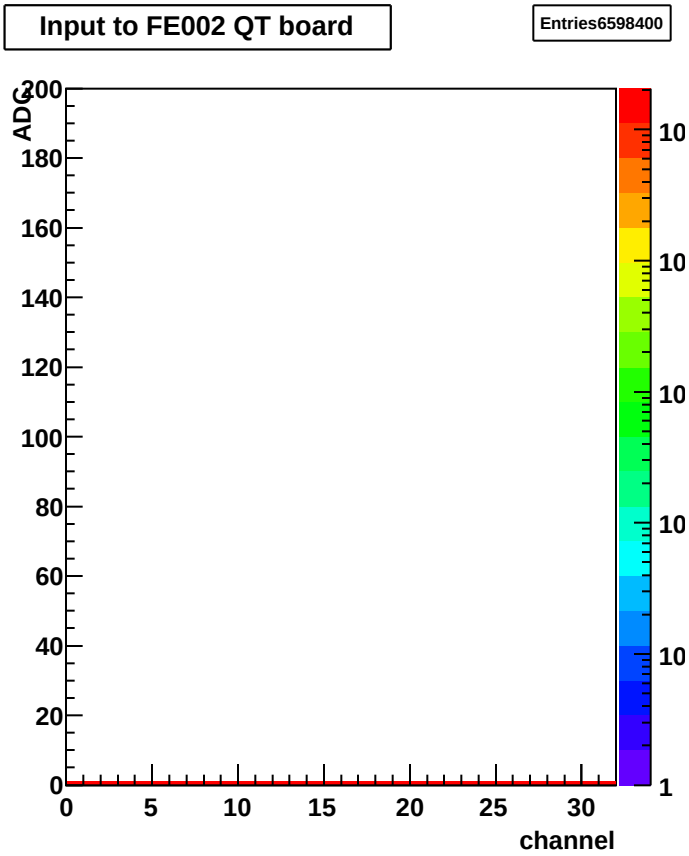

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

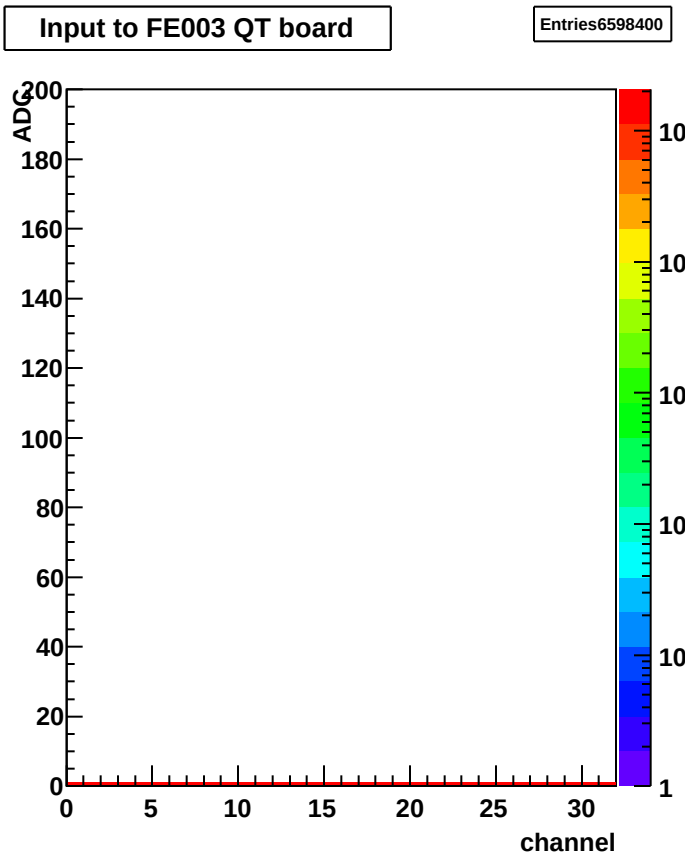

Input to FPD L3 DSM
Entries 3299200
bit
4507
970
2048
92
10
1989
200
76
725
245
64
2
206200
206200
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

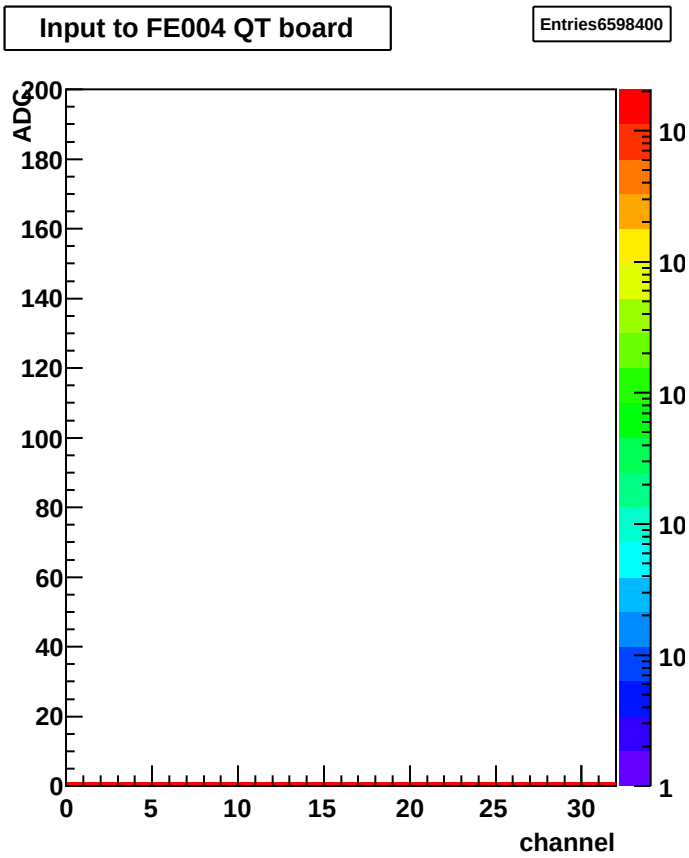

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

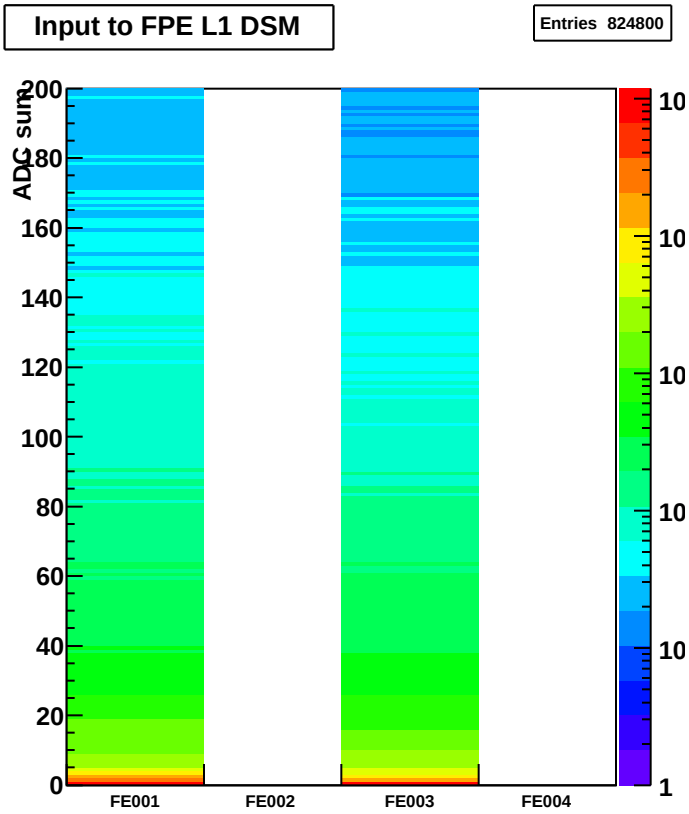

Input to FPD L2 DSM
Entries 824800
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

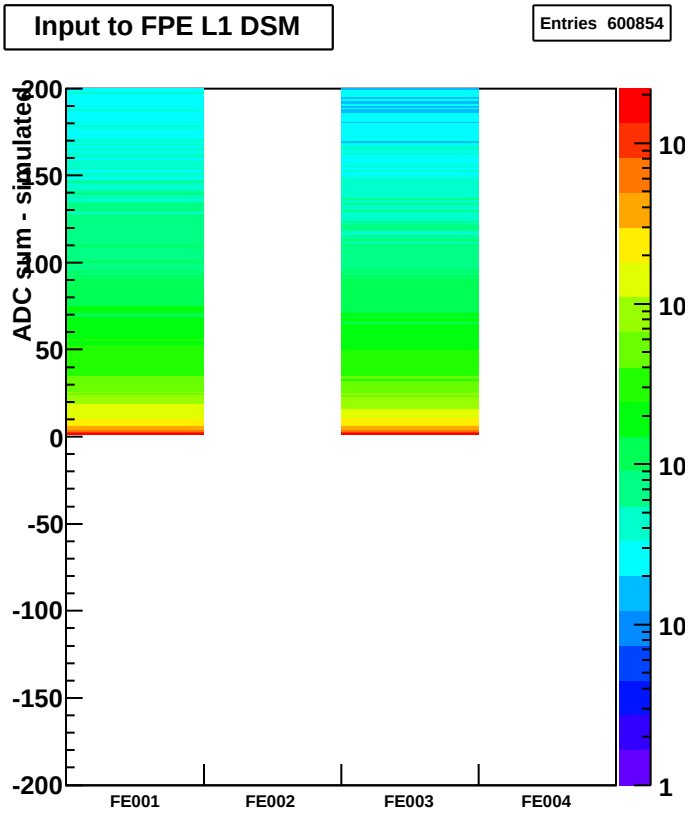

Input to FE001 QT board
Entries6598400
ADC
channel

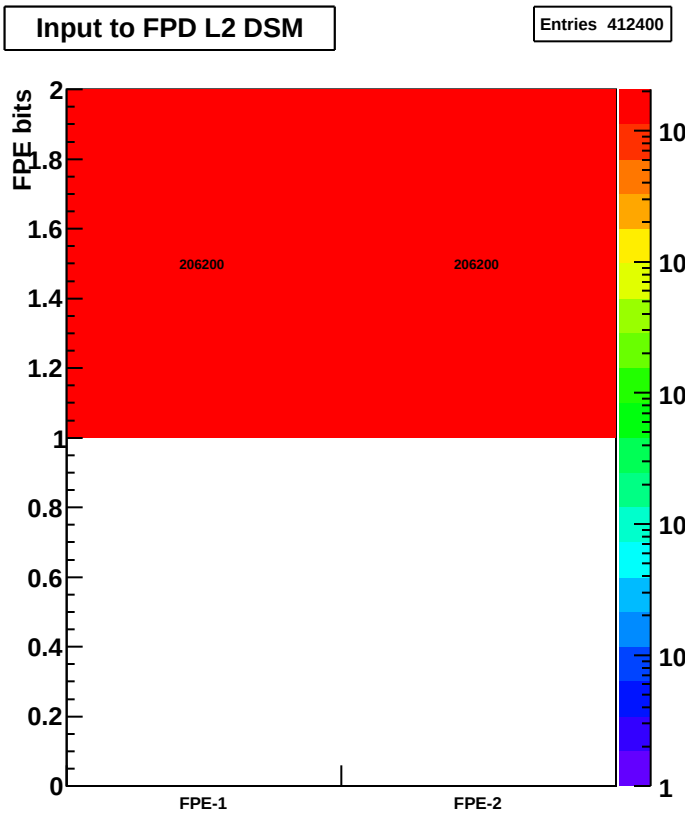

Input to FE002 QT board
Entries6598400
ADC
channel

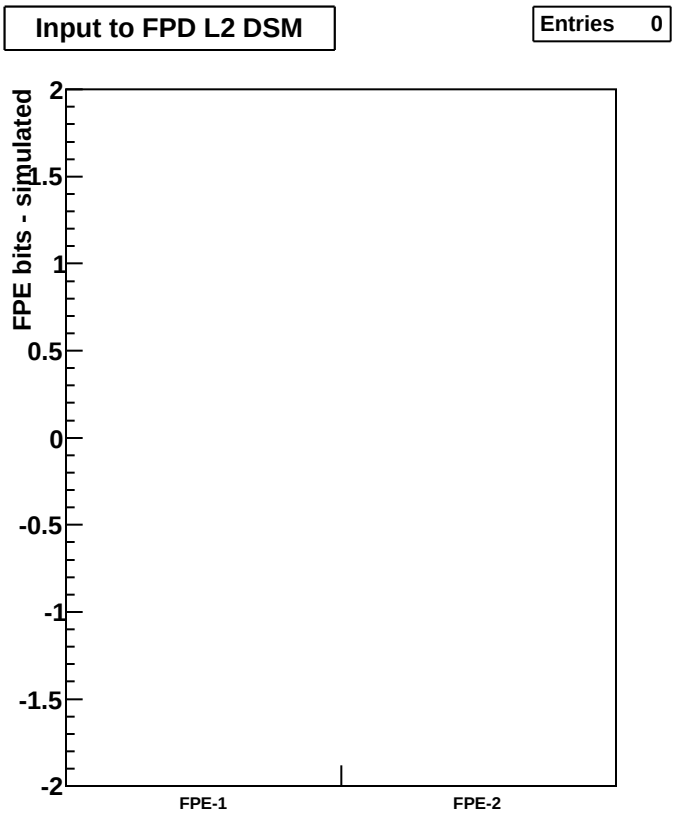

Input to FE003 QT board
Entries6598400
ADC
channel


Input to FE004 QT board
Entries6598400
ADC
channel


Input to FPE L1 DSM
Entries 824800
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 600854
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 412400
FPE bits
206200
206200
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2