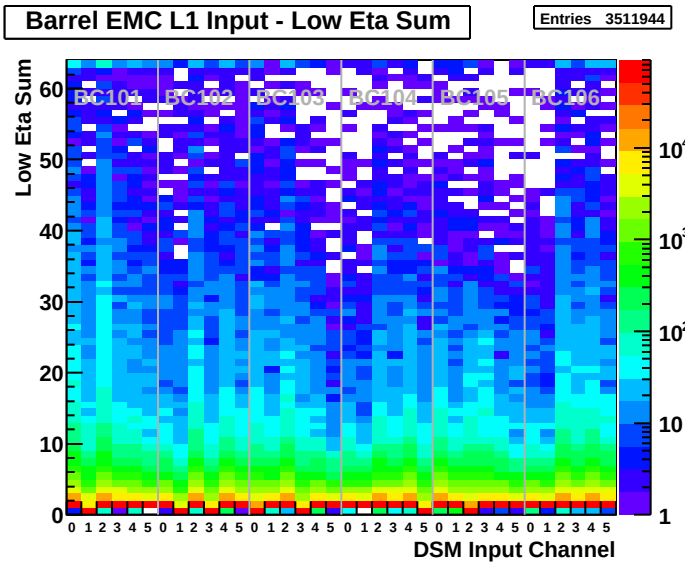
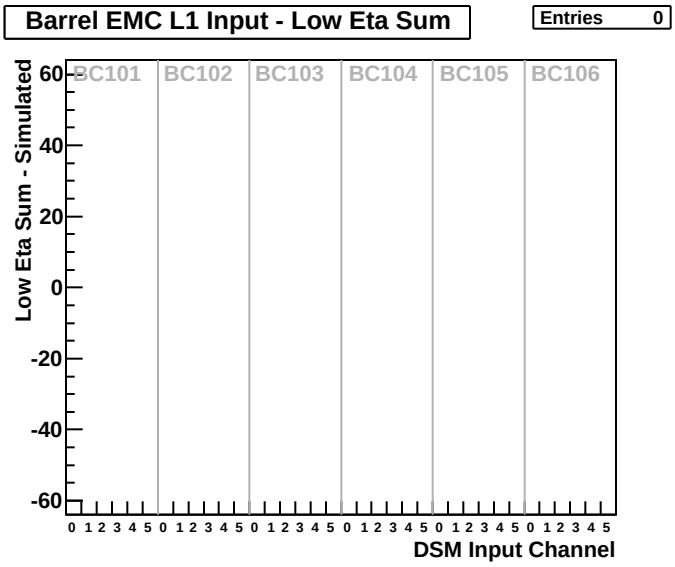
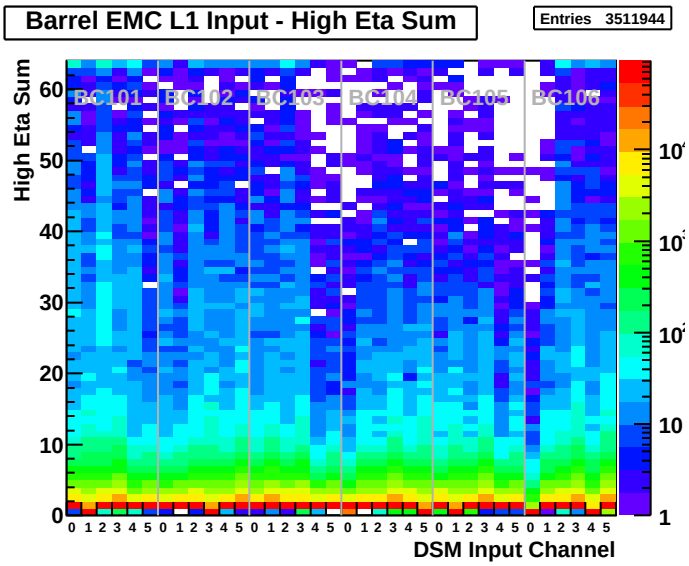
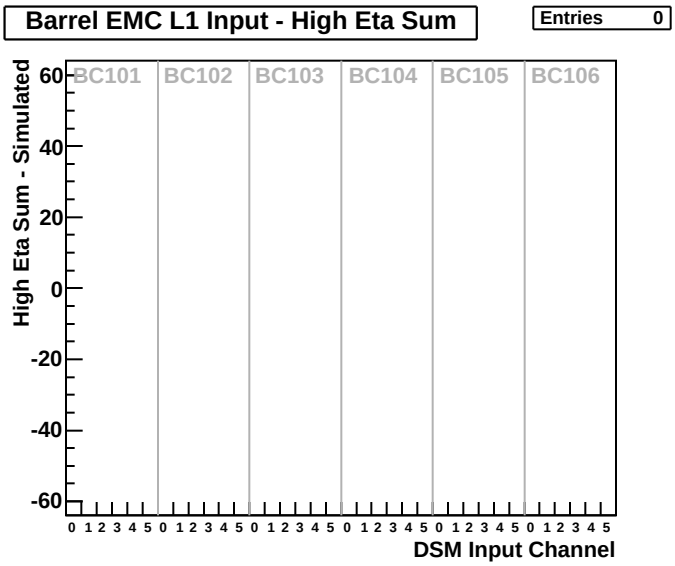
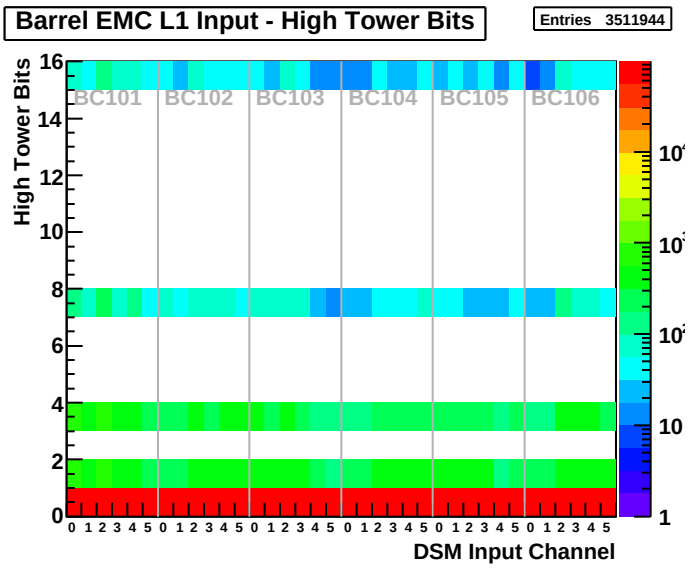
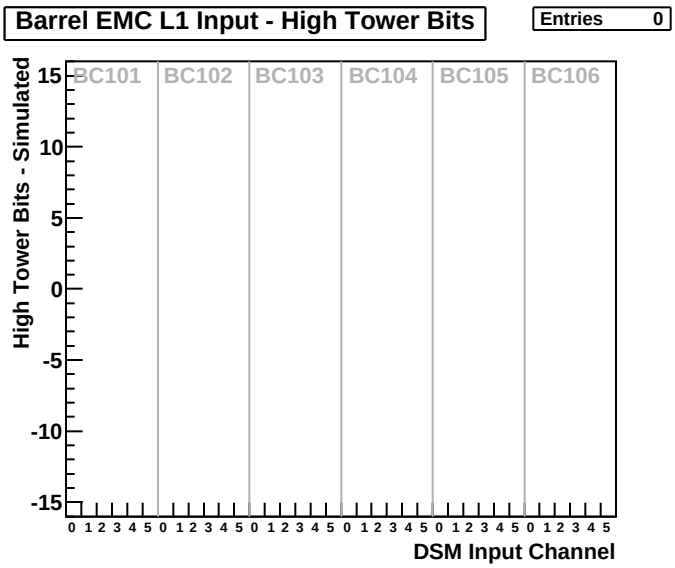

Barrel EMC L1 Input - Low Eta Sum
Entries 3511944
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

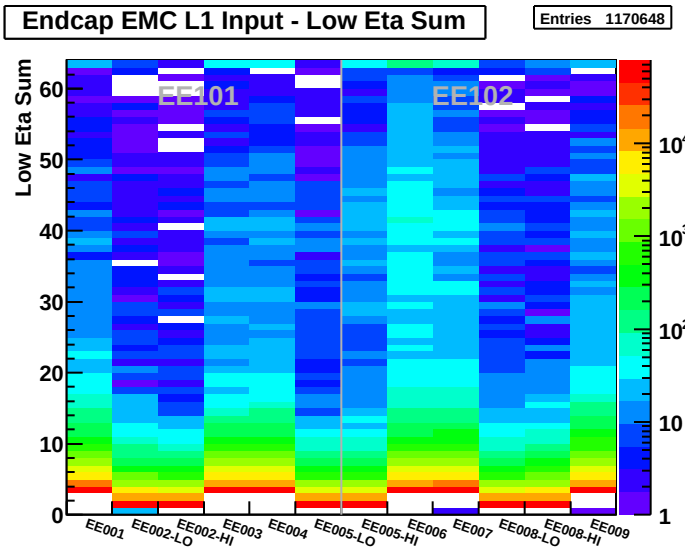

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

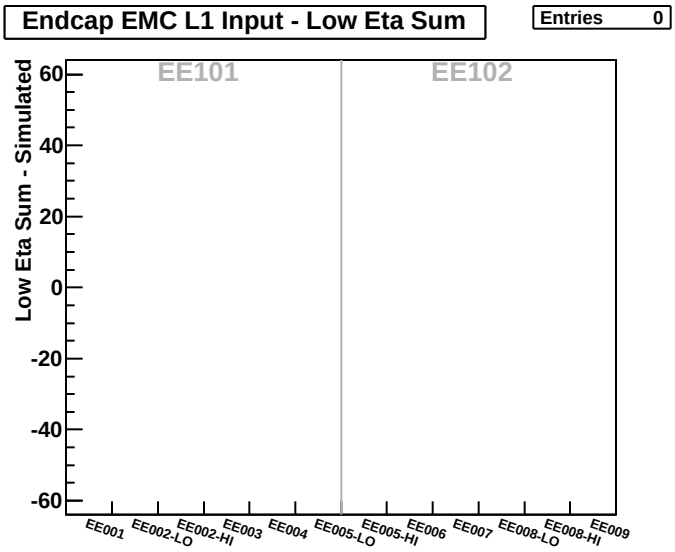

Barrel EMC L1 Input - High Eta Sum
Entries 3511944
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

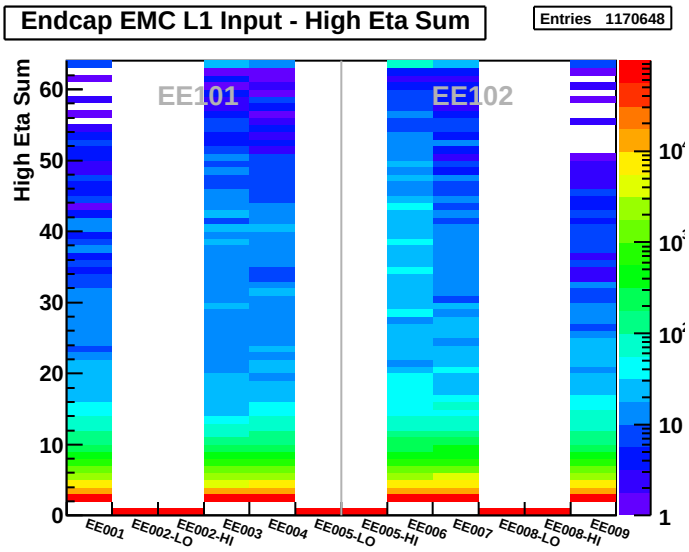

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

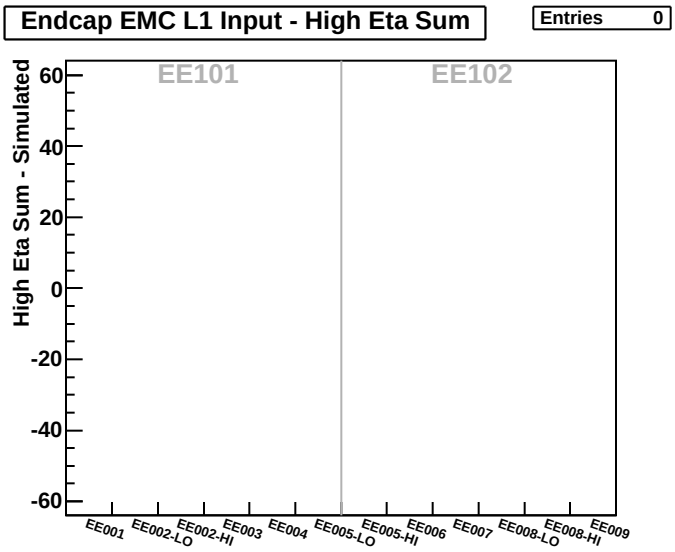

Barrel EMC L1 Input - High Tower Bits
Entries 3511944
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

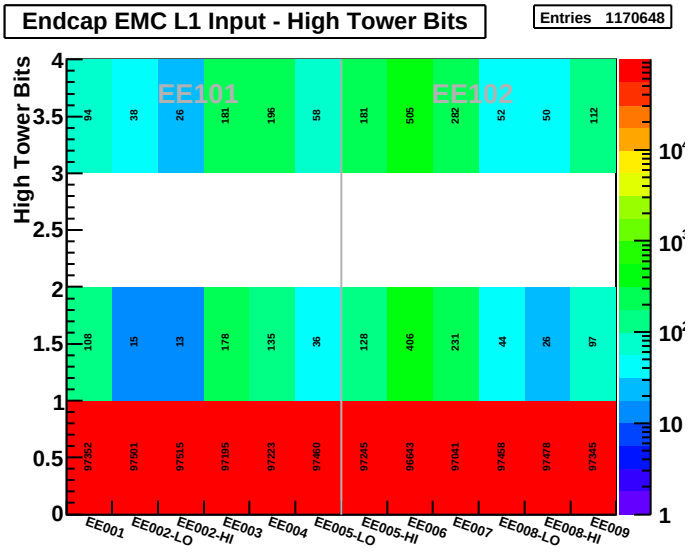

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

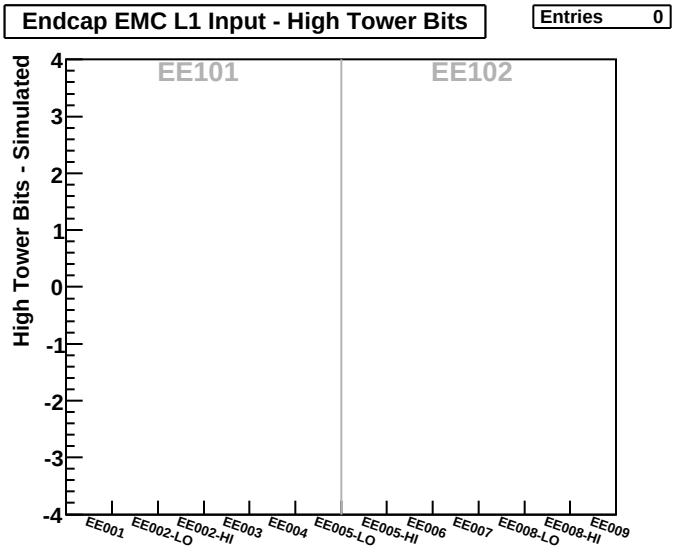

Endcap EMC L1 Input - Low Eta Sum
Entries 1170648
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

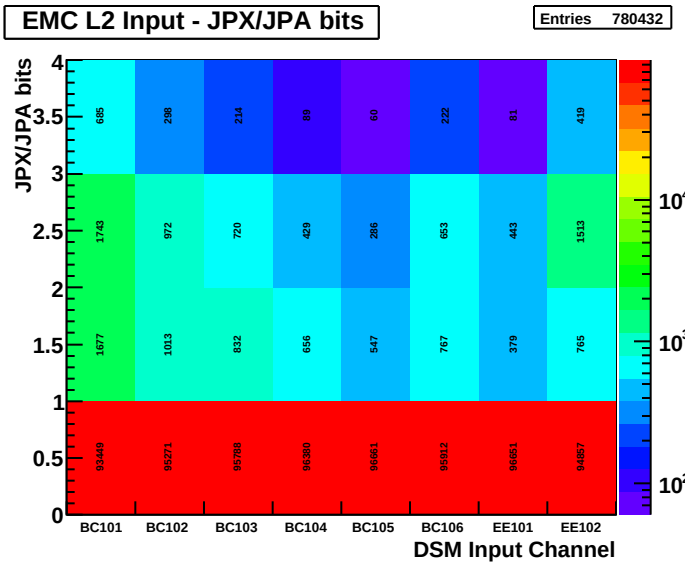

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

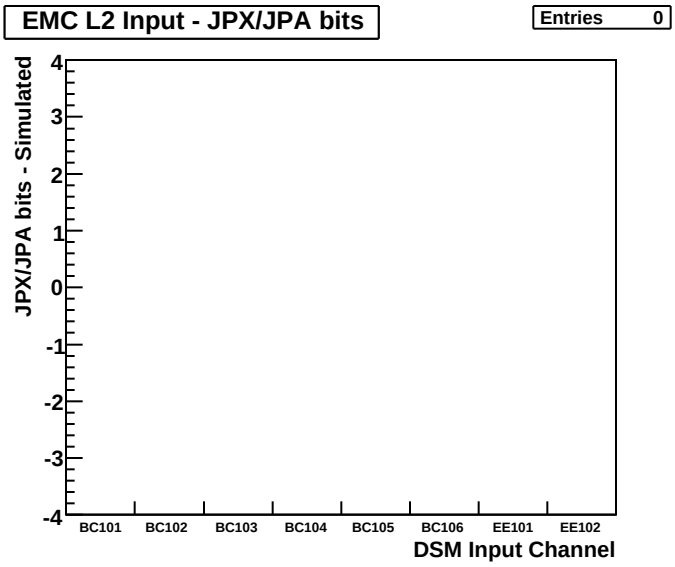

Endcap EMC L1 Input - High Eta Sum
Entries 1170648
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

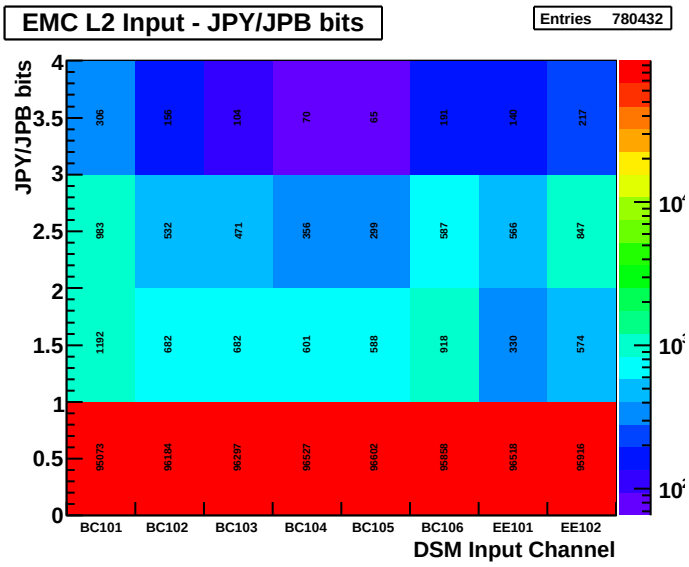

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

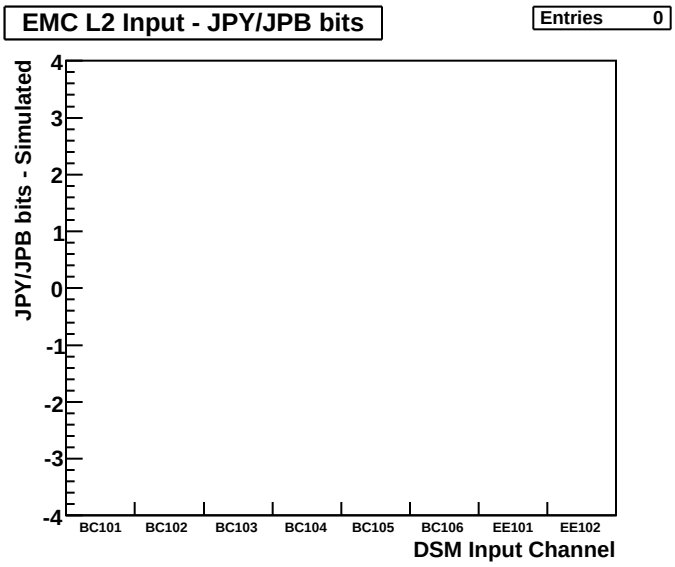

Endcap EMC L1 Input - High Tower Bits
Entries 1170648
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

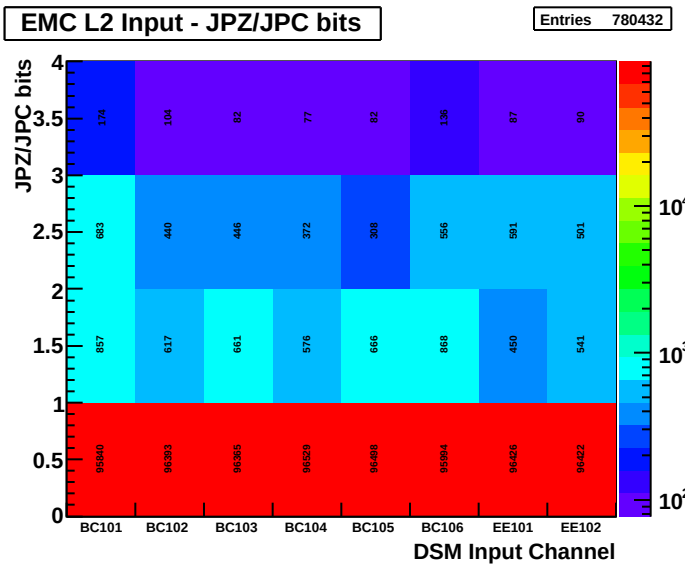

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

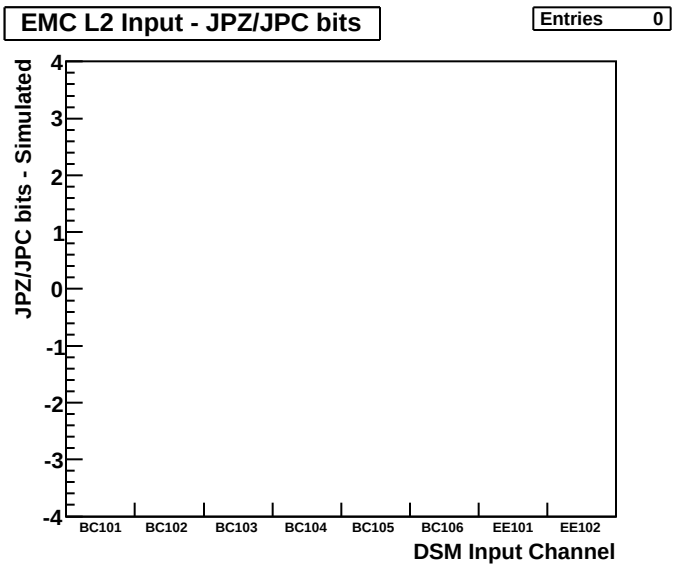

EMC L2 Input - JPX/JPA bits
Entries 780432
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
685 298 214 89 60 222 81 419
1743 972 720 429 286 653 443 1513
1677 1013 832 656 547 767 379 765
93449 95271 95788 96380 96661 95912 96651 94857

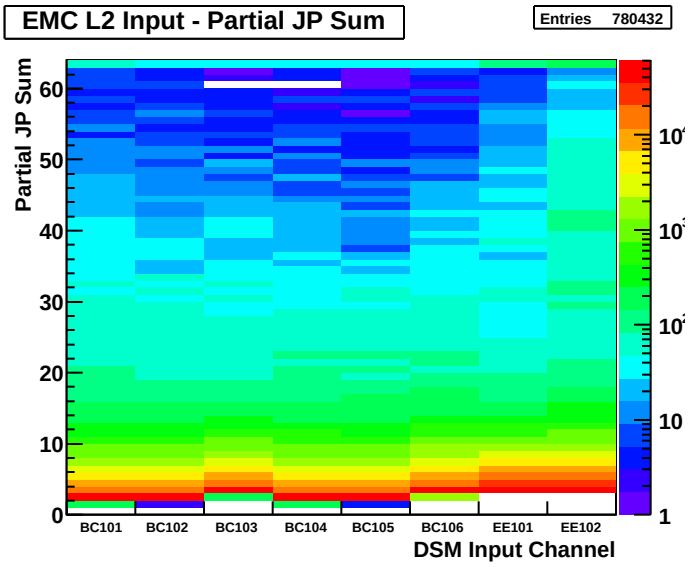

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

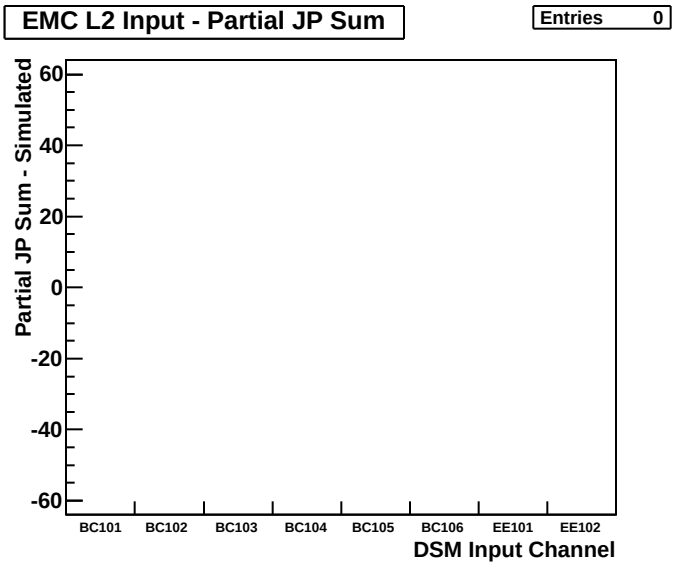

EMC L2 Input - JPY/JPB bits
Entries 780432
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
306 156 104 70 65 101 140 217
983 532 471 356 299 587 566 847
1192 682 682 601 588 918 330 574
95073 96184 96297 96527 96602 95898 96518 95916

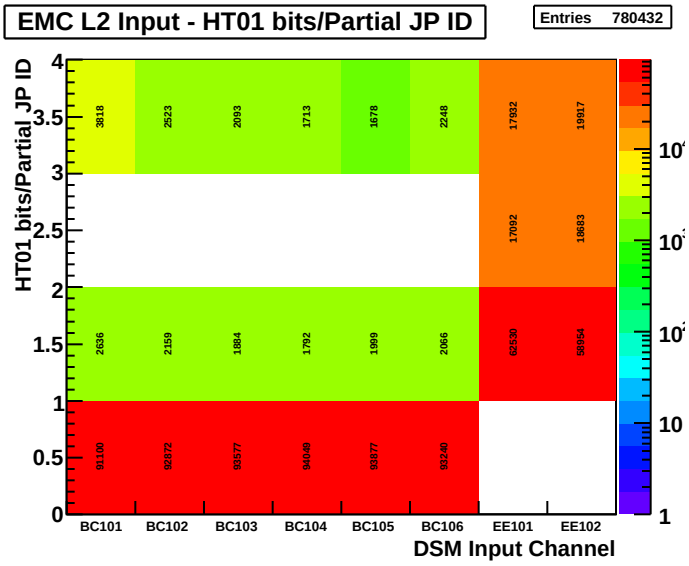

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

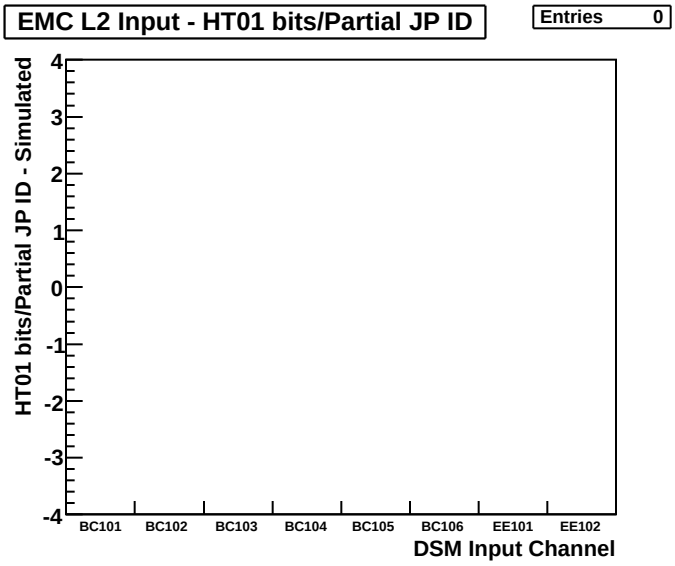

EMC L2 Input - JPZ/JPC bits
Entries 780432
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
174 104 82 77 82 136 87 90
683 440 446 372 308 556 591 501
857 617 661 576 666 868 450 541
95940 96393 96365 96529 96498 95994 96426 96422

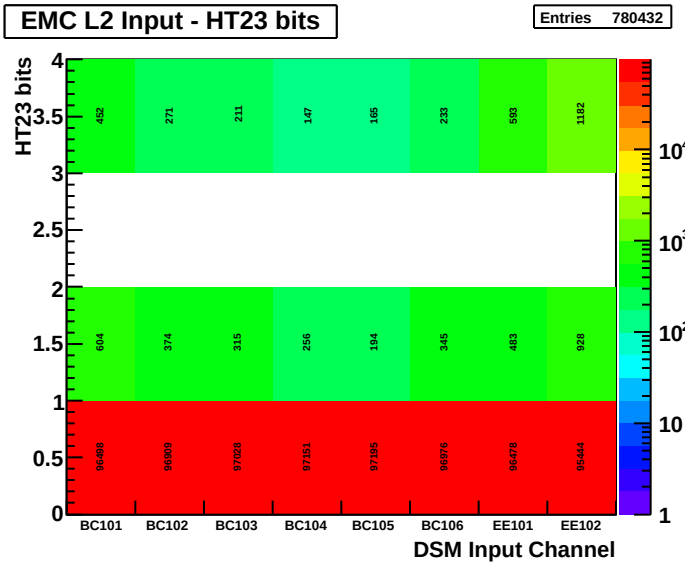

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

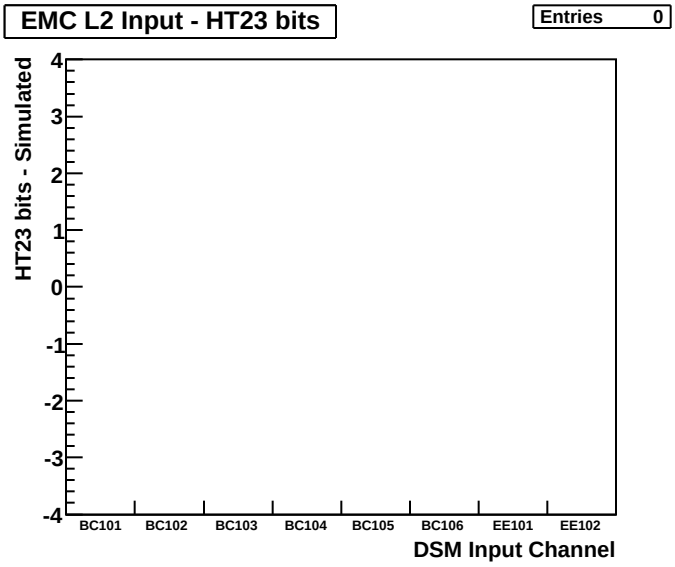

EMC L2 Input - Partial JP Sum
Entries 780432
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

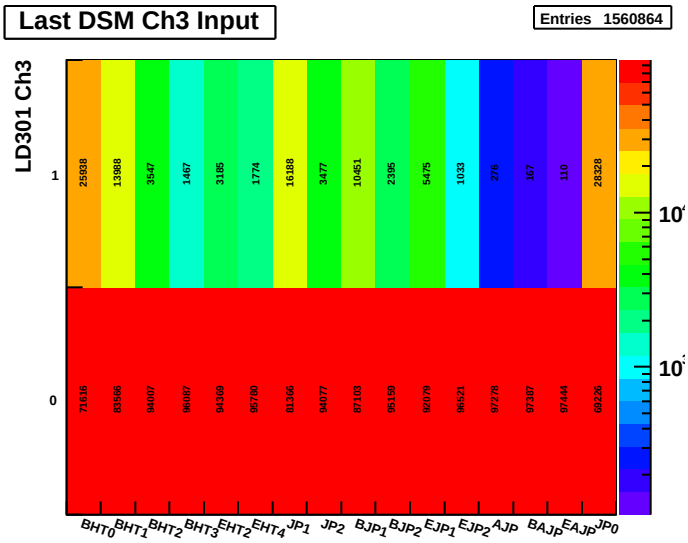

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

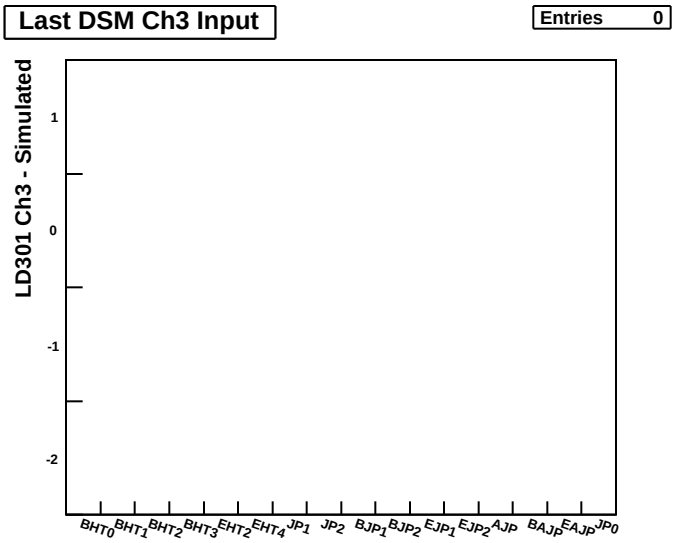

EMC L2 Input - HT01 bits/Partial JP ID
Entries 780432
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

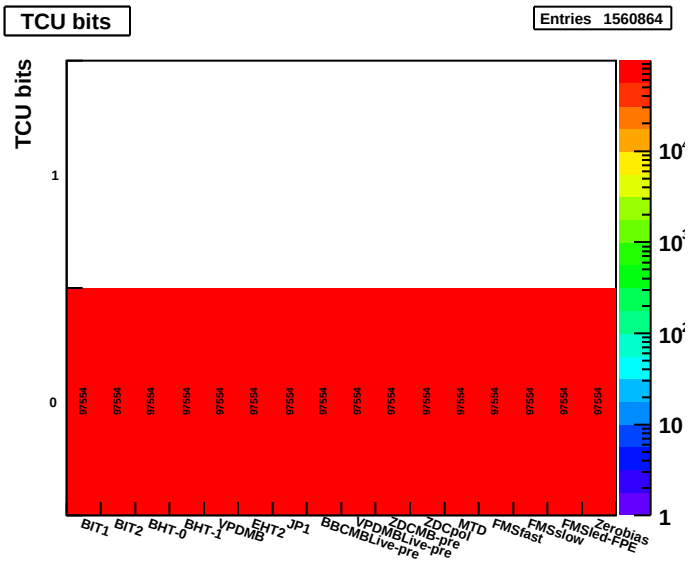

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

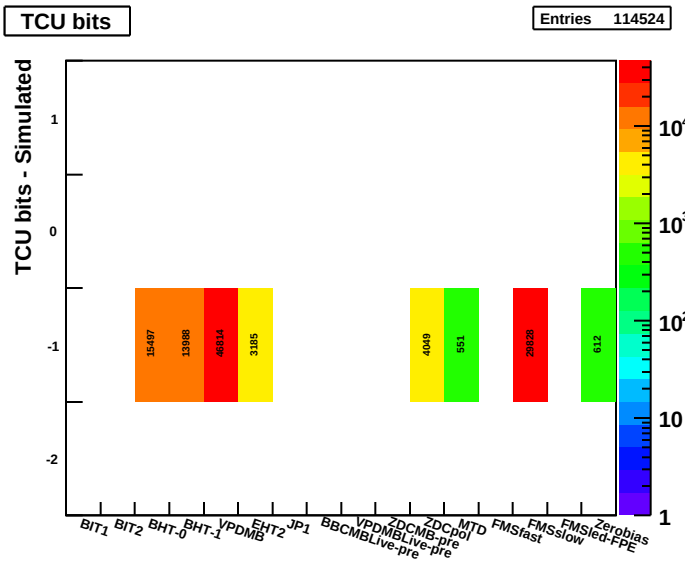

EMC L2 Input - HT23 bits
Entries 780432
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

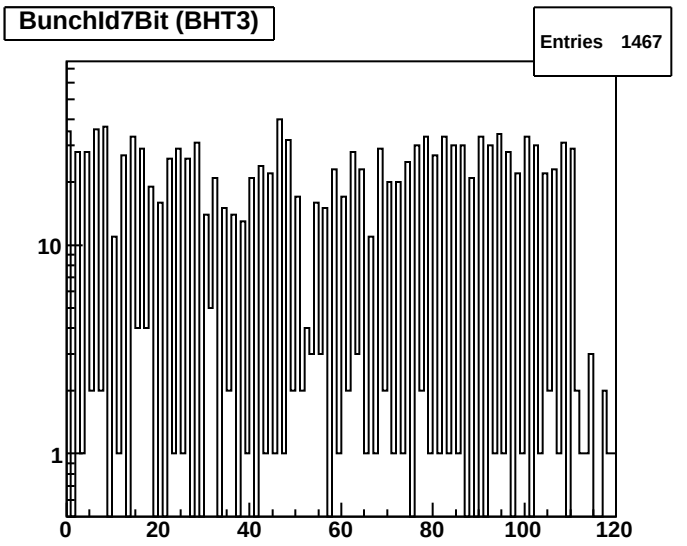

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

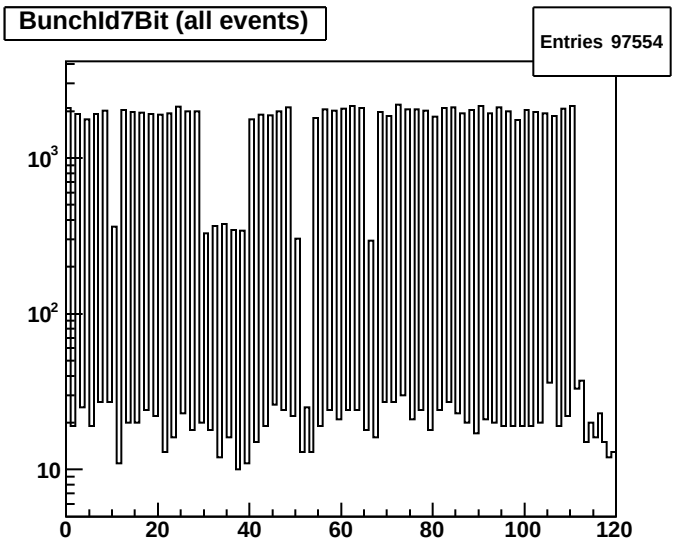

Last DSM Ch3 Input
Entries 1560864
LD301 Ch3
25938 13988 3547 1467 3185 1774 16188 3477 10451 2395 5475 1033 276 167 110 28328
71616 83566 94007 96097 94369 95760 81366 94077 87103 95159 92079 96521 97278 97387 97444 69226
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

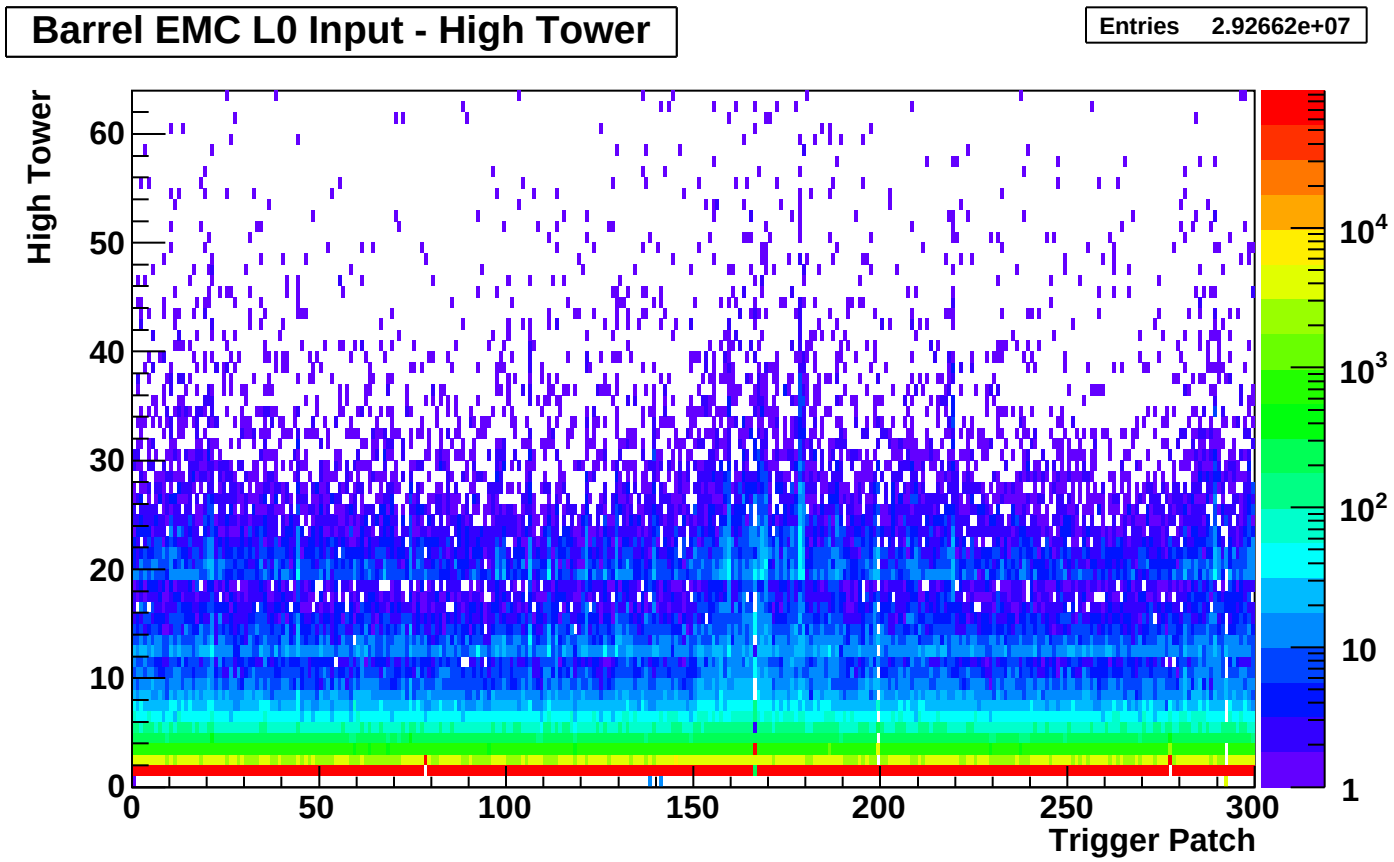

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

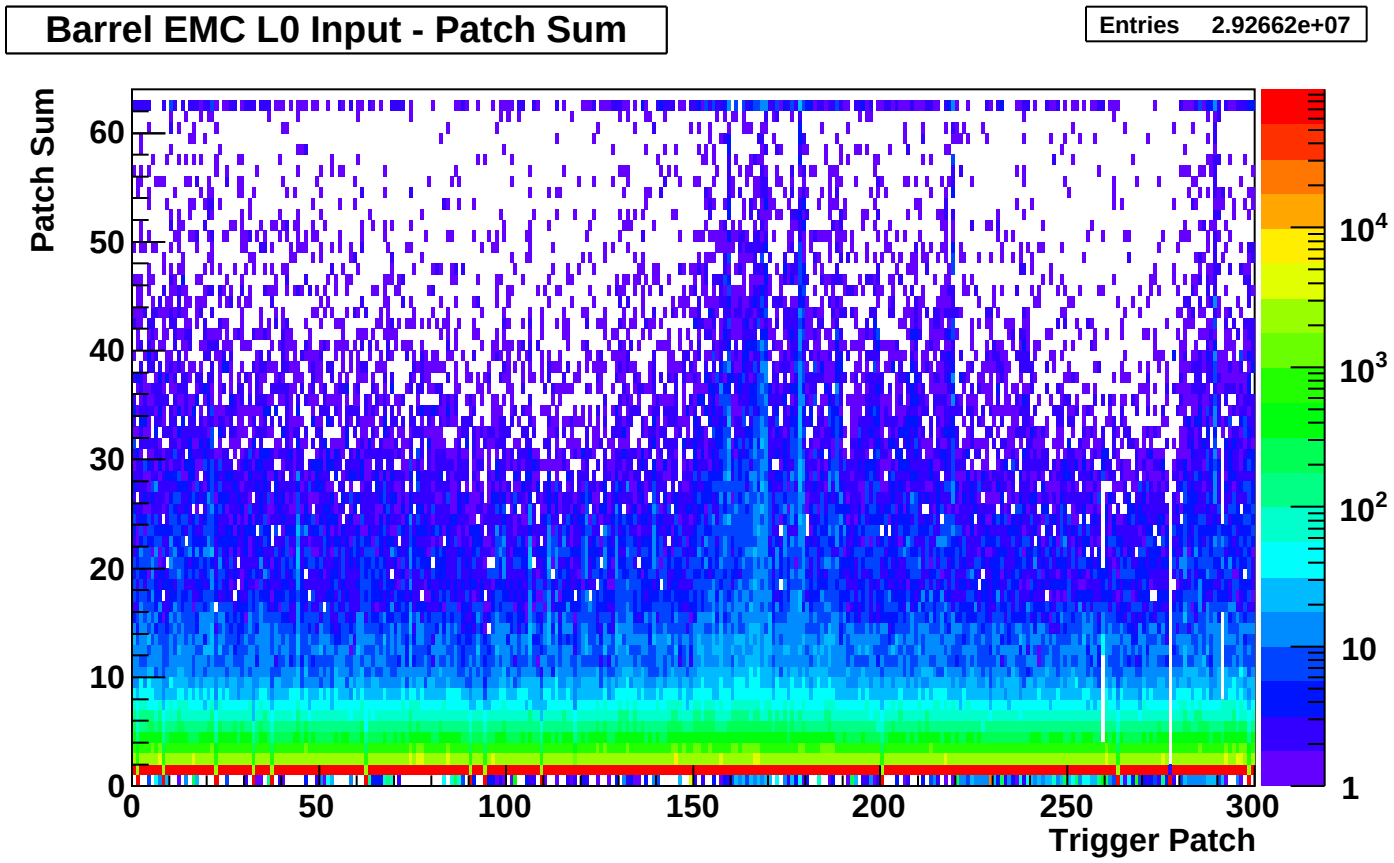

TCU bits
Entries 1560864
TCU bits
97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554 97554
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

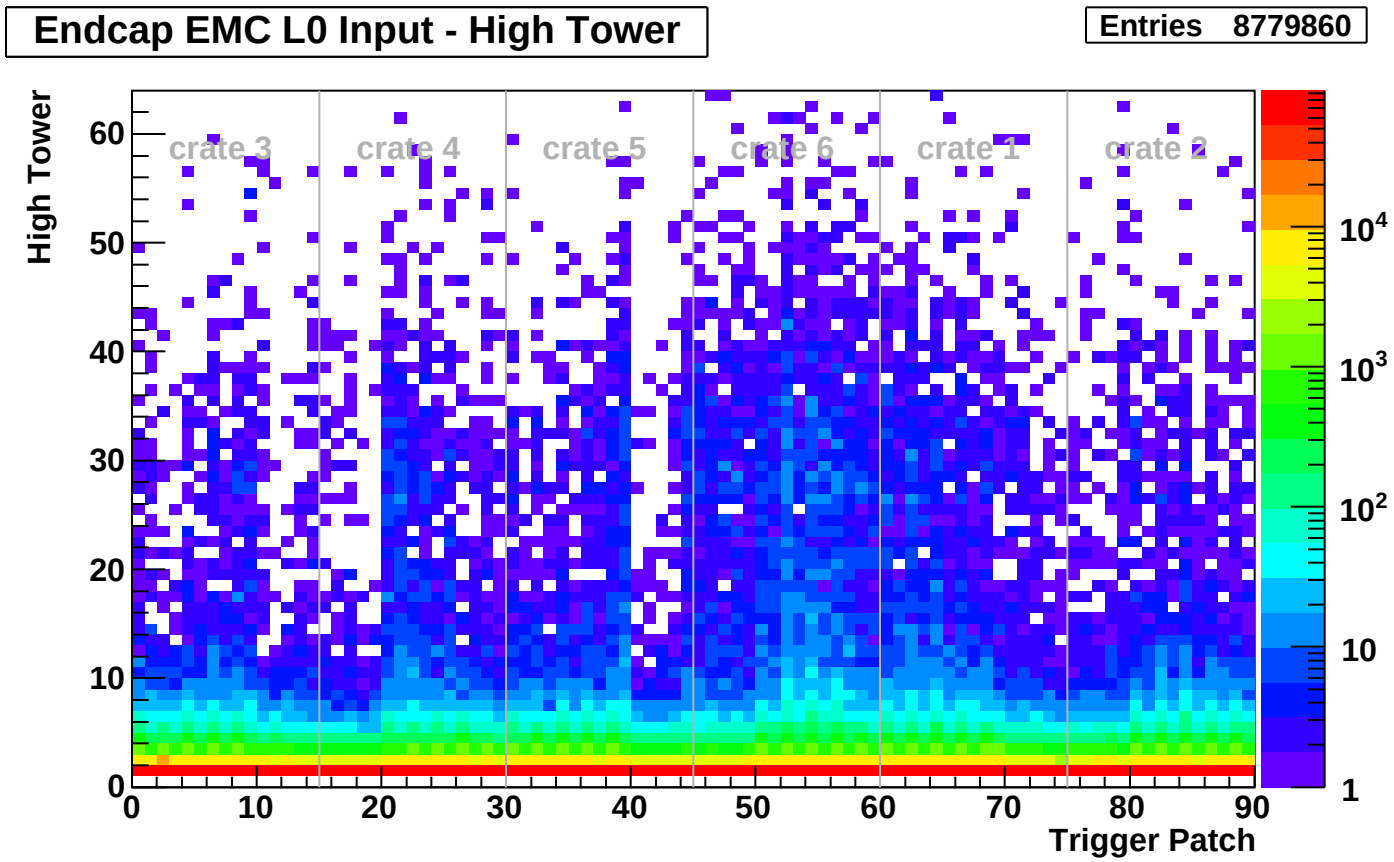

TCU bits
Entries 114524
TCU bits - Simulated
15497 13988 46814 3185 4049 551 29828 612
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

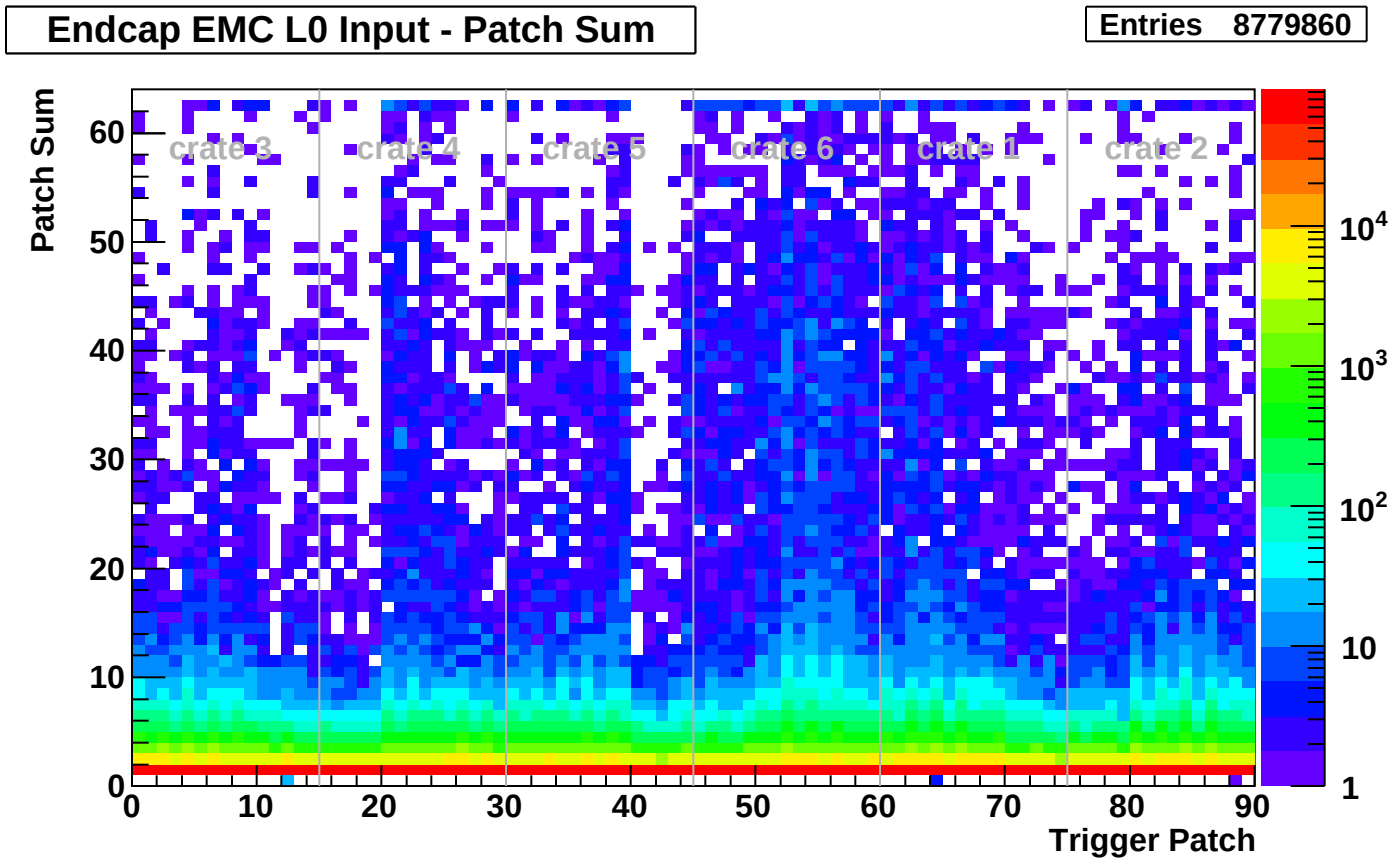

BunchId7Bit (BHT3)
Entries 1467

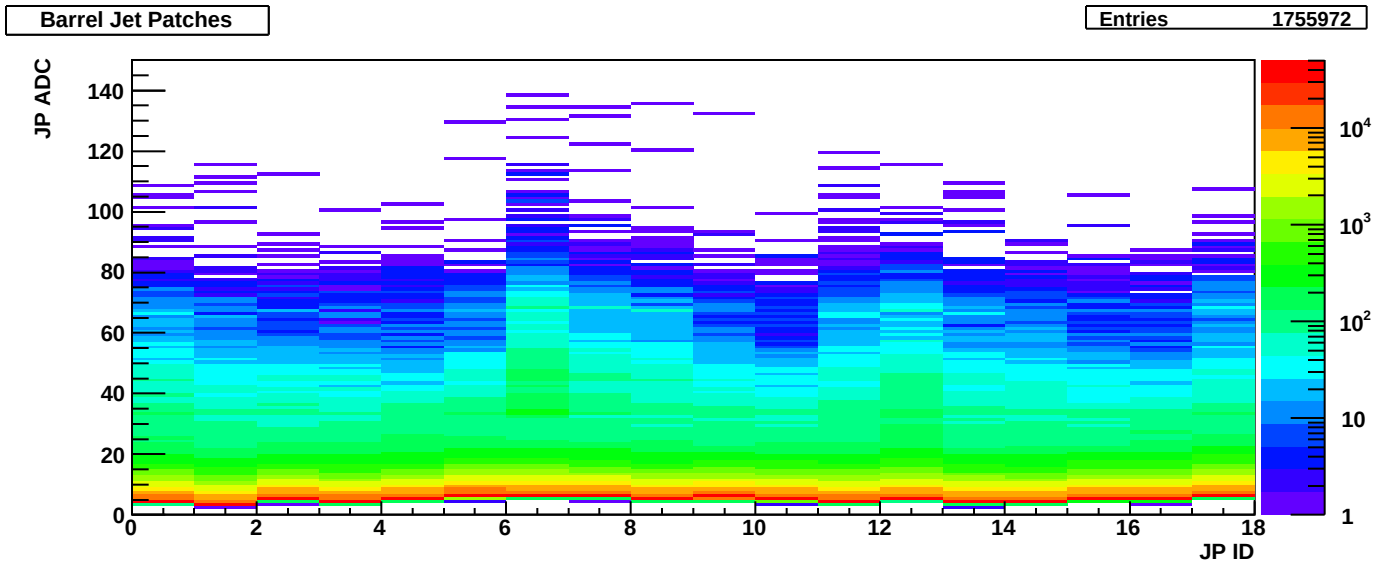

BunchId7Bit (all events)
Entries 97554

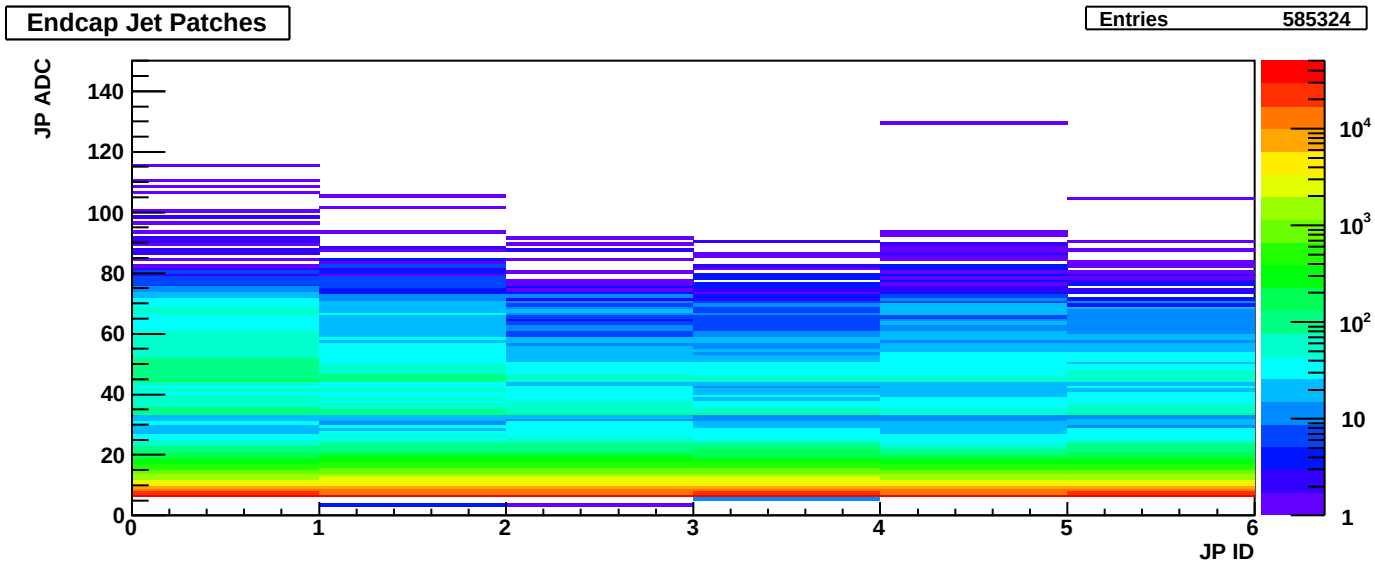

Barrel EMC L0 Input - High Tower
Entries 2.92662e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

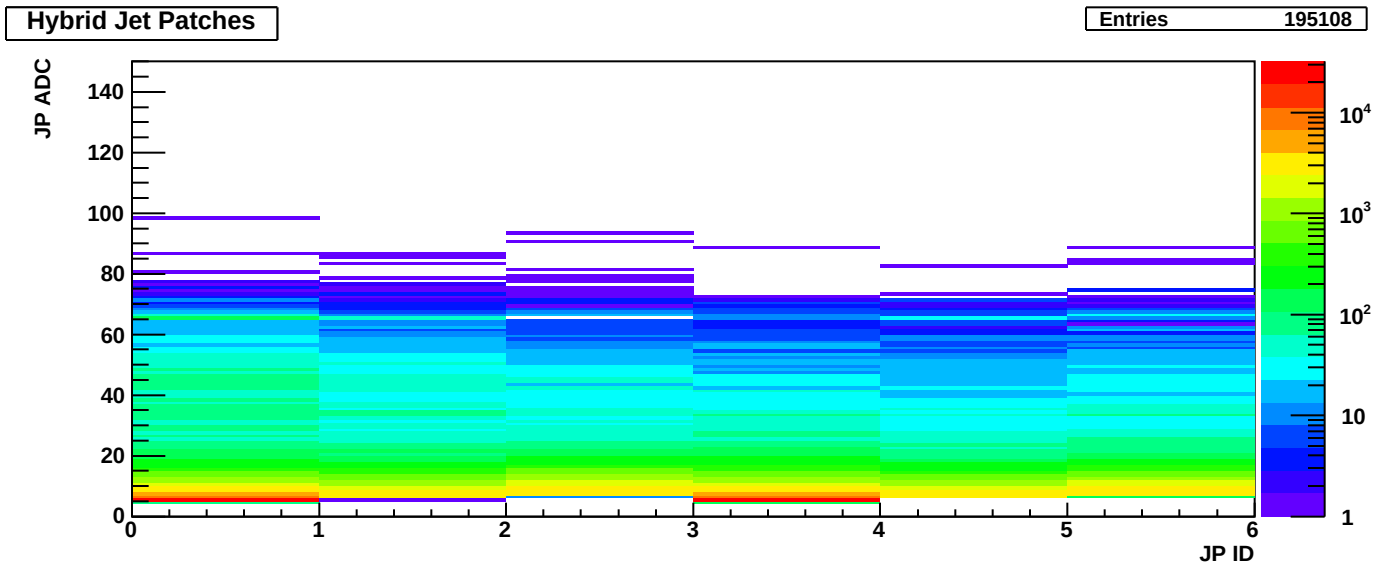

Barrel EMC L0 Input - Patch Sum
Entries 2.92662e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

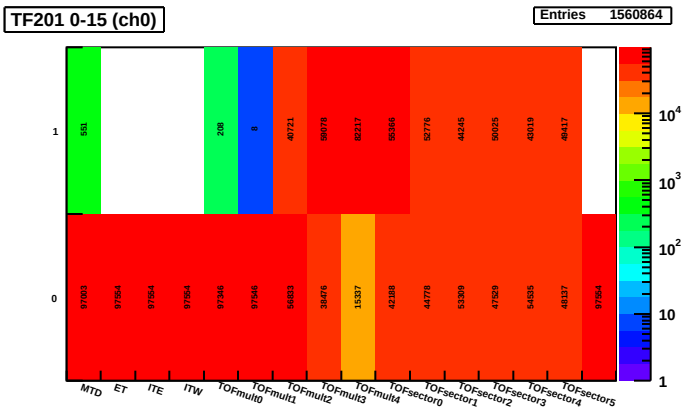

Endcap EMC L0 Input - High Tower
Entries 8779860
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

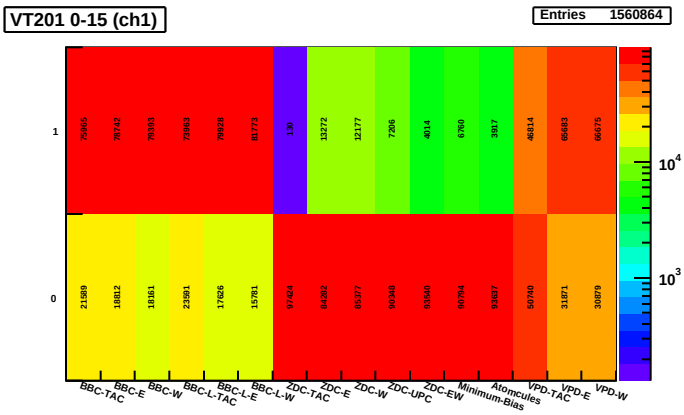

Endcap EMC L0 Input - Patch Sum
Entries 8779860
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

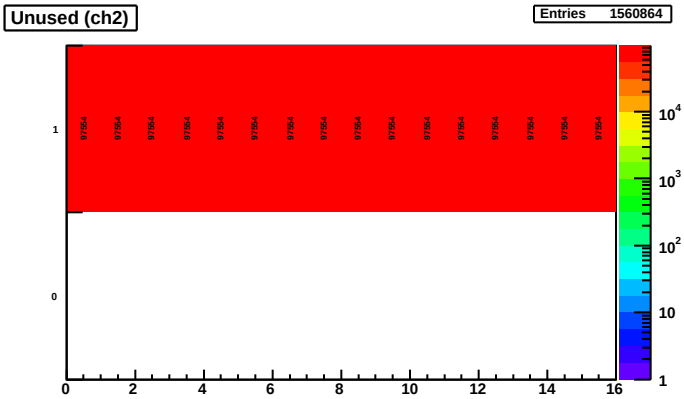

Barrel Jet Patches
Entries 1755972
JP ADC
JP ID

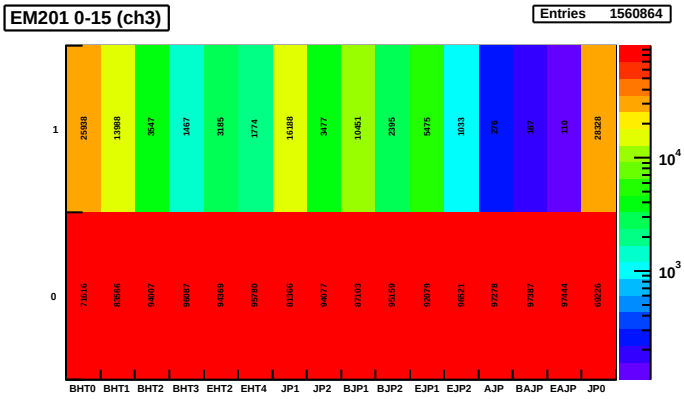

Endcap Jet Patches
Entries 585324
JP ADC
JP ID

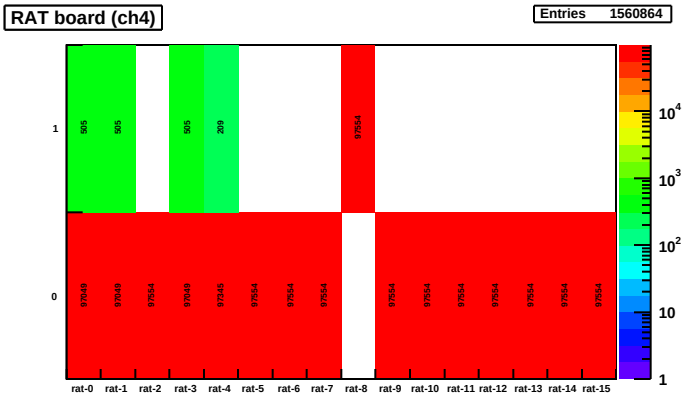

Hybrid Jet Patches
Entries 195108
JP ADC
JP ID

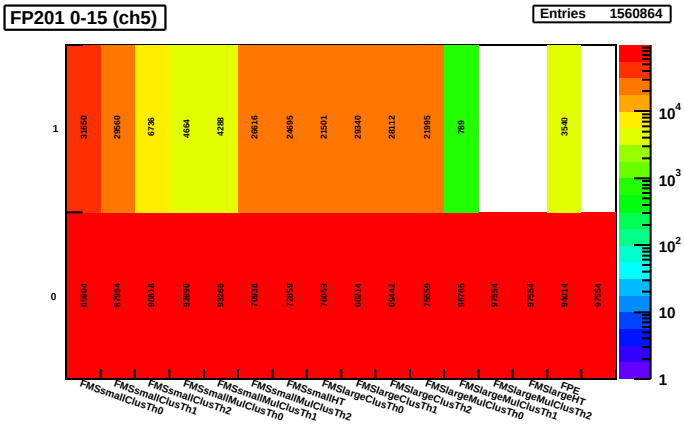

TF201 0-15 (ch0)
Entries 1560864
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

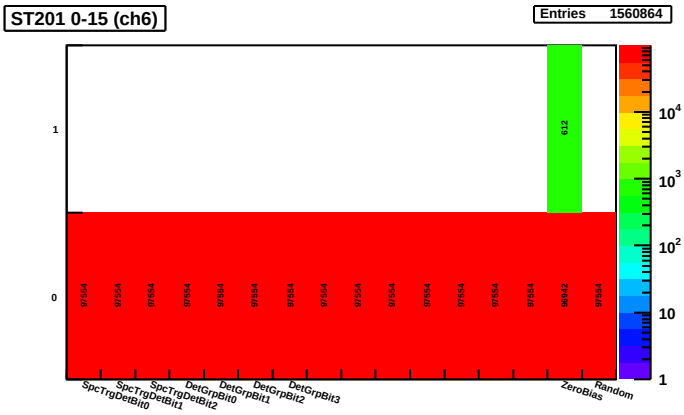

VT201 0-15 (ch1)
Entries 1560864
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

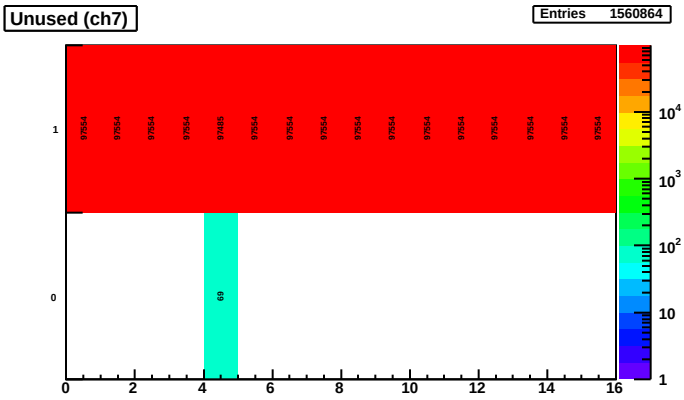

Unused (ch2)
Entries 1560864

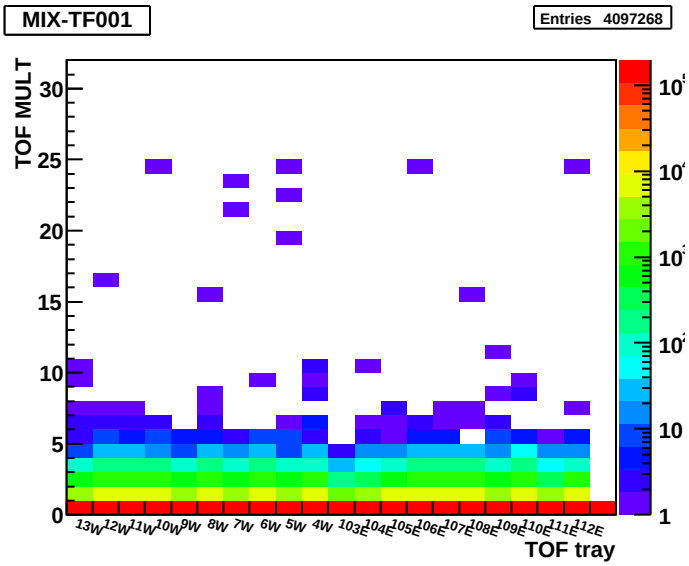

EM201 0-15 (ch3)
Entries 1560864
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

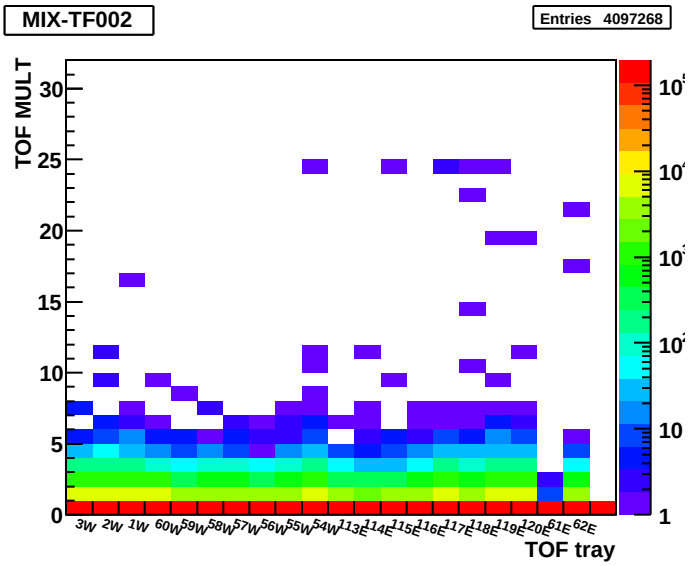

RAT board (ch4)
Entries 1560864
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

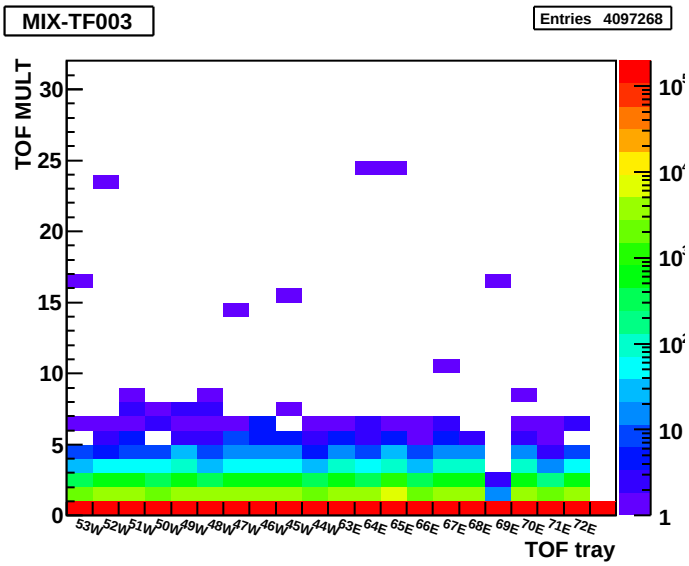

FP201 0-15 (ch5)
Entries 1560864
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

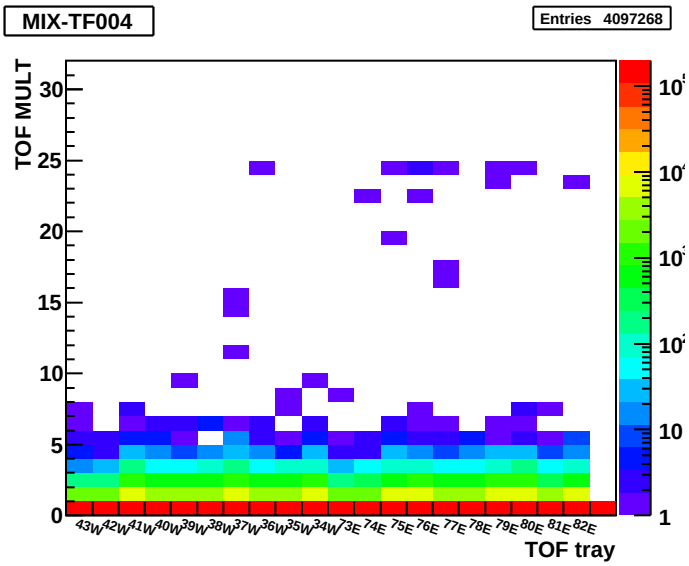

ST201 0-15 (ch6)
Entries 1560864
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

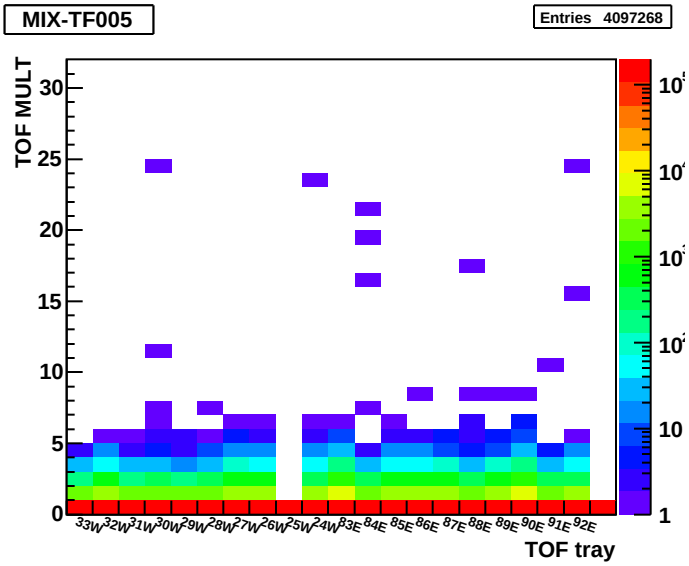

Unused (ch7)
Entries 1560864

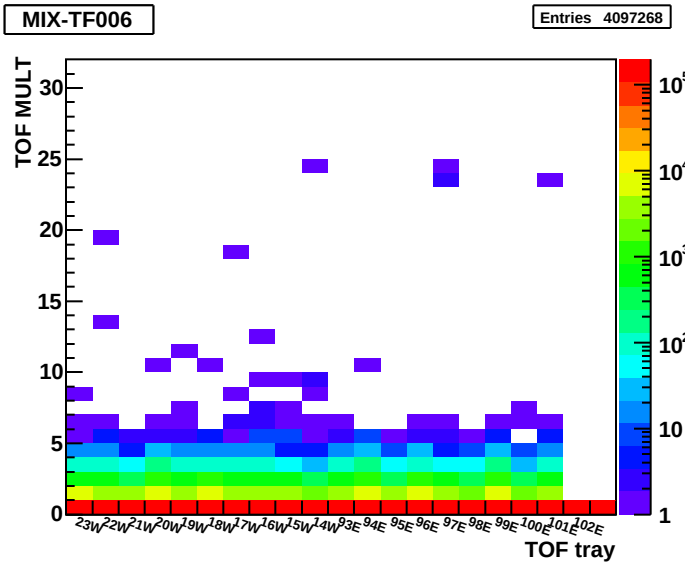

MIX-TF001
Entries 4097268
TOF MULT
TOF tray

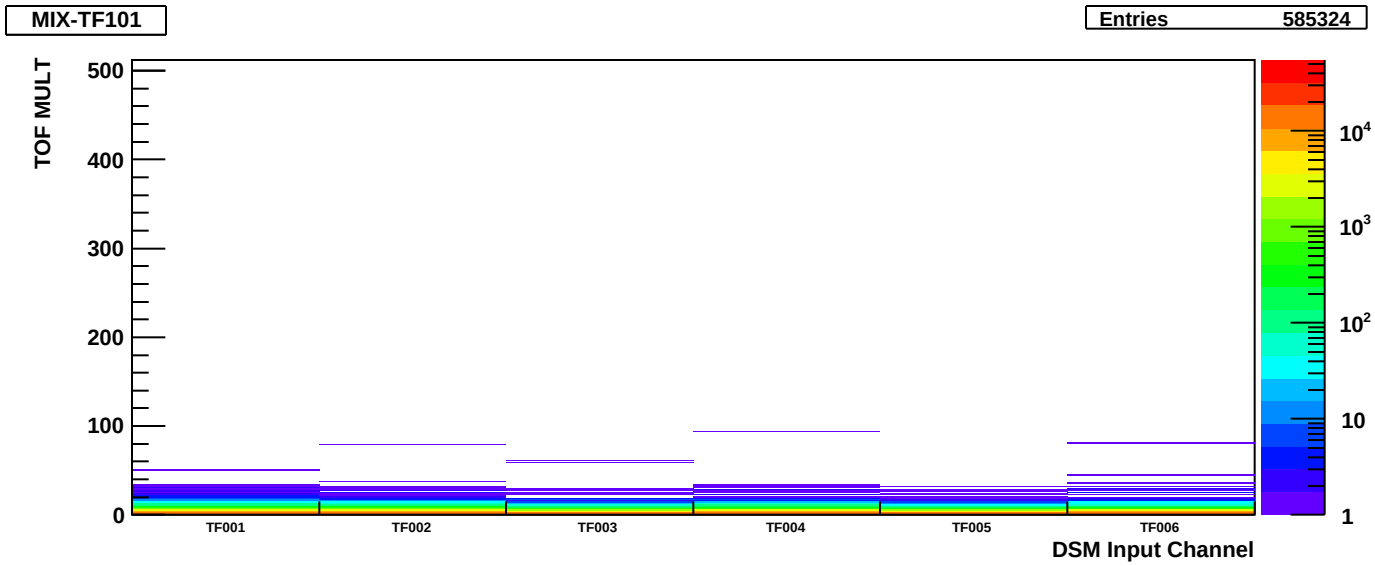

MIX-TF002
Entries 4097268
TOF MULT
TOF tray

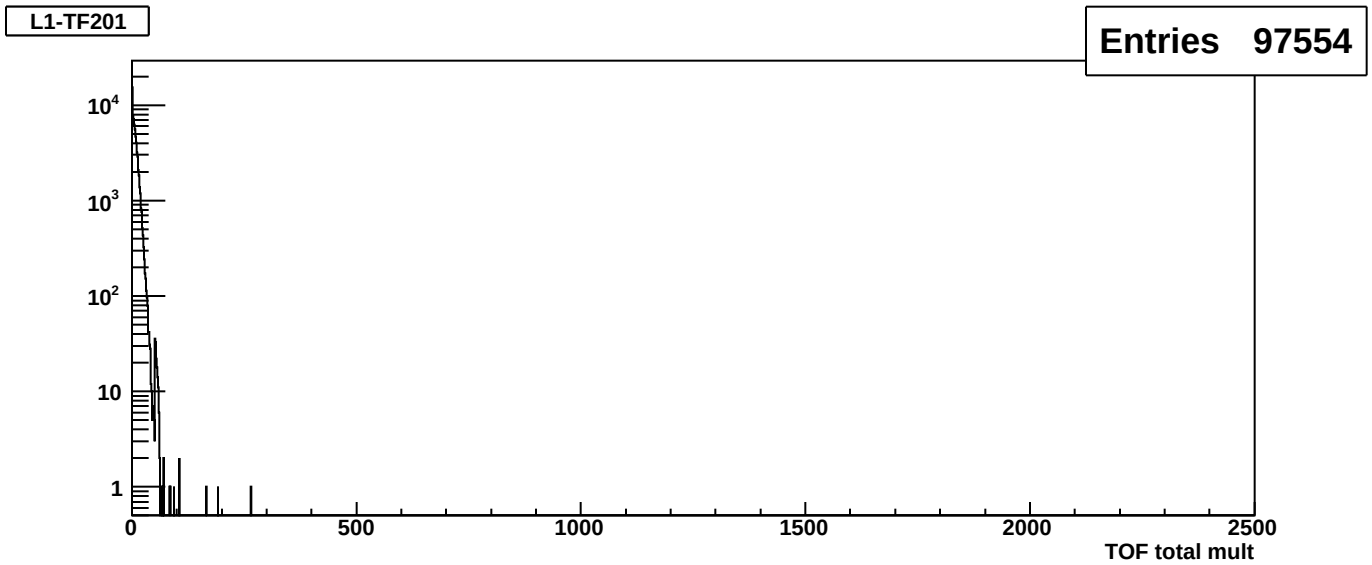

MIX-TF003
Entries 4097268
TOF MULT
TOF tray

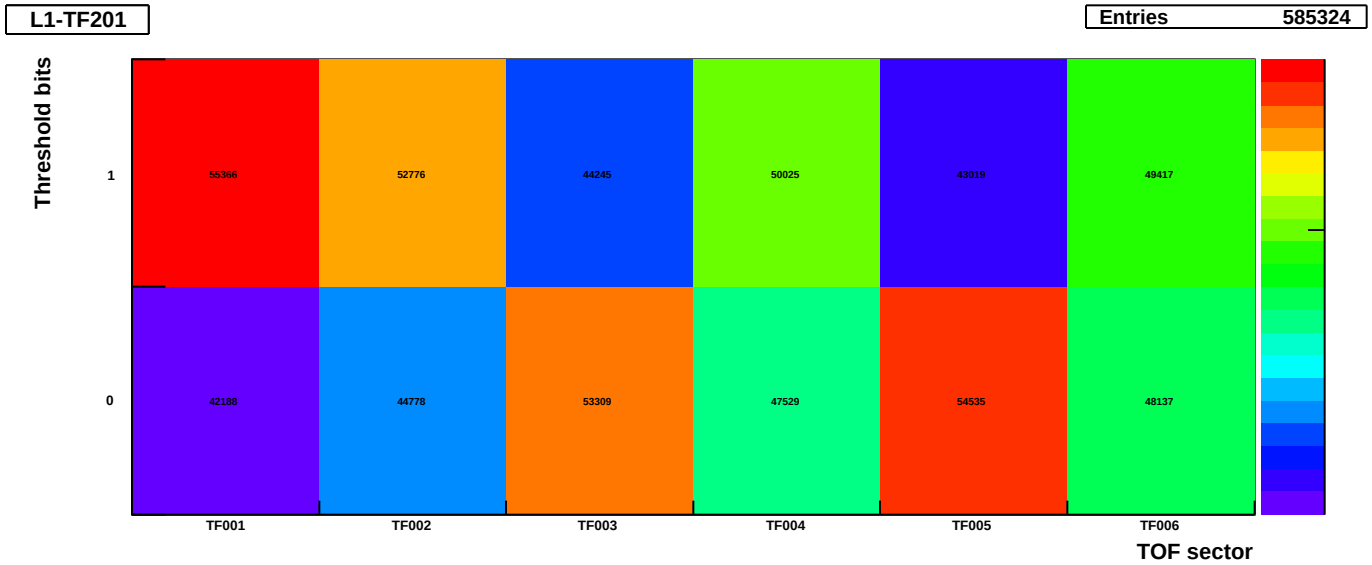

MIX-TF004
Entries 4097268
TOF MULT
TOF tray

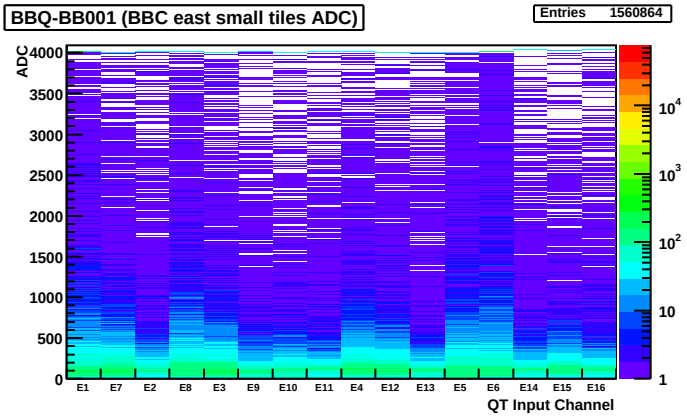

MIX-TF005
Entries 4097268
TOF MULT
TOF tray

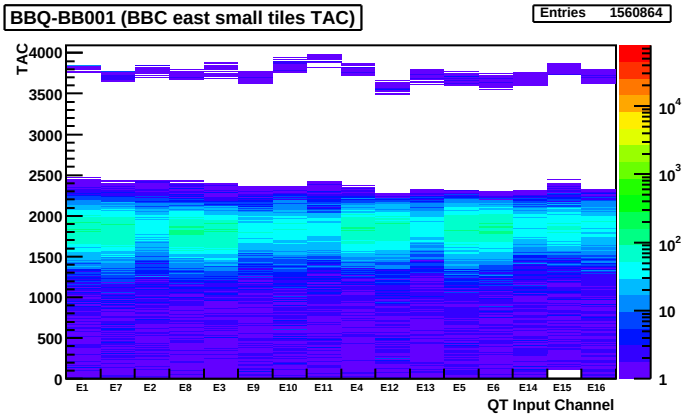

MIX-TF006
Entries 4097268
TOF MULT
TOF tray

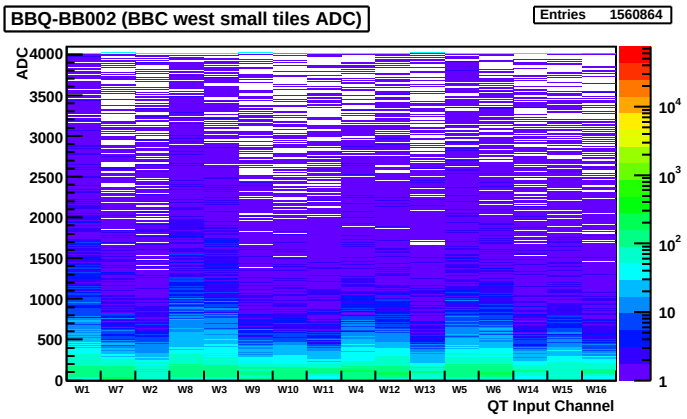

MIX-TF101
Entries 585324
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

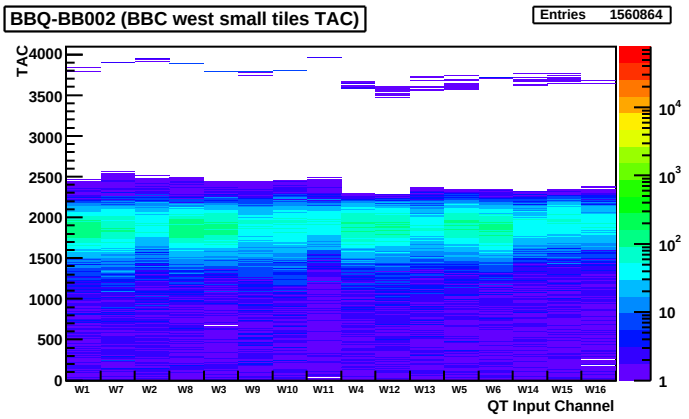

L1-TF201
Entries 97554
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

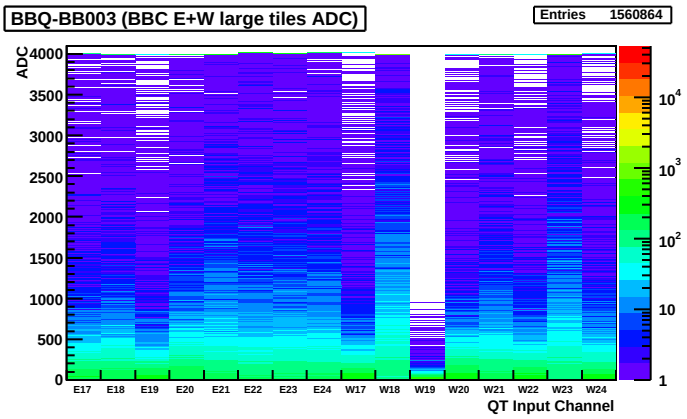

L1-TF201
Entries 585324
Threshold bits
1
0
55366
52776
44245
50025
43019
49417
42188
44778
53309
47529
54535
48137
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

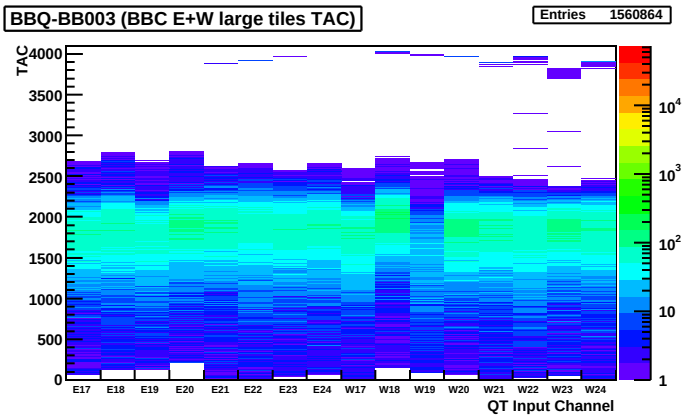

BBQ-BB001 (BBC east small tiles ADC)
Entries 1560864
ADC
QT Input Channel

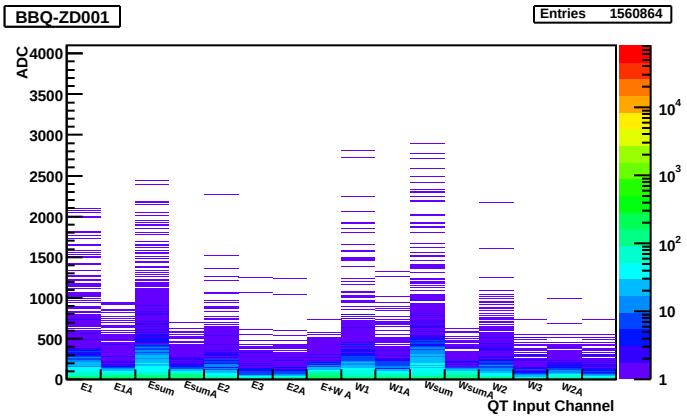

BBQ-BB001 (BBC east small tiles TAC)
Entries 1560864
TAC
QT Input Channel

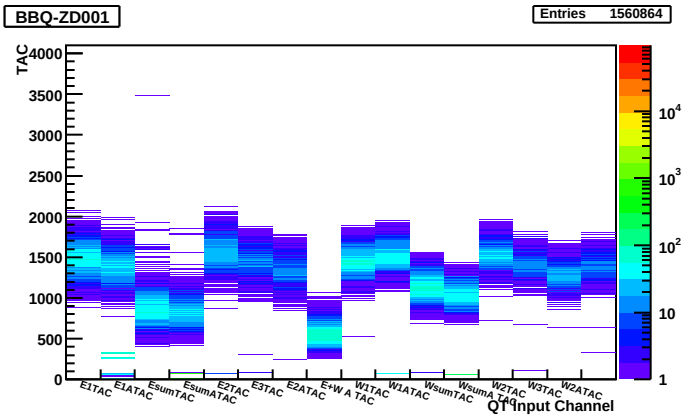

BBQ-BB002 (BBC west small tiles ADC)
Entries 1560864
ADC
QT Input Channel

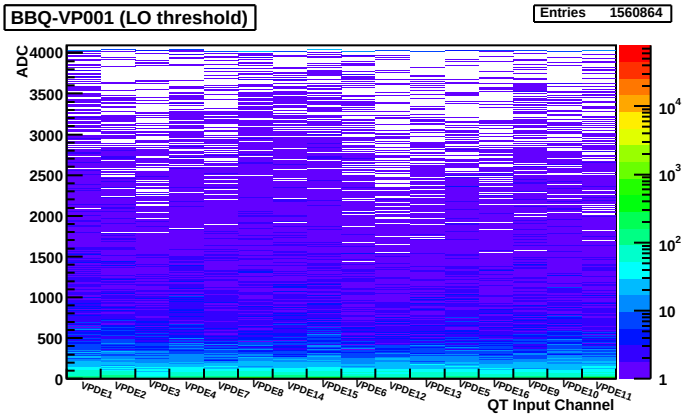

BBQ-BB002 (BBC west small tiles TAC)
Entries 1560864
TAC
QT Input Channel

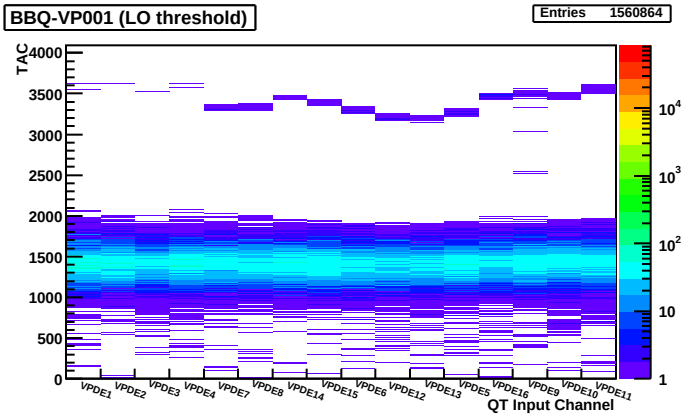

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1560864
ADC
QT Input Channel

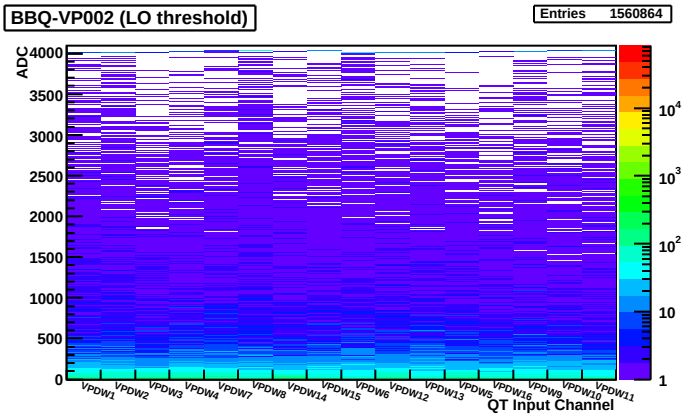

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1560864
TAC
QT Input Channel

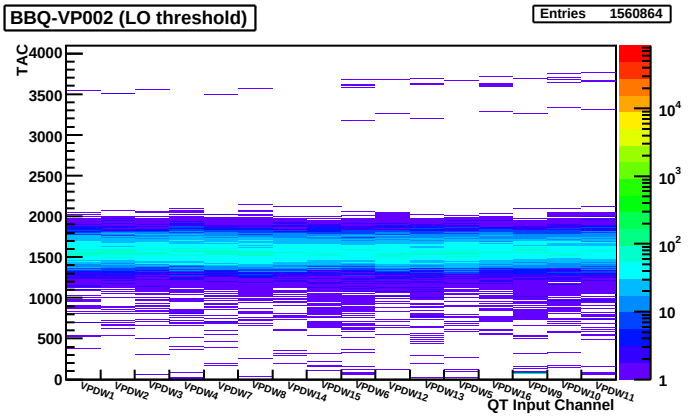

BBQ-ZD001
Entries 1560864
ADC
QT Input Channel

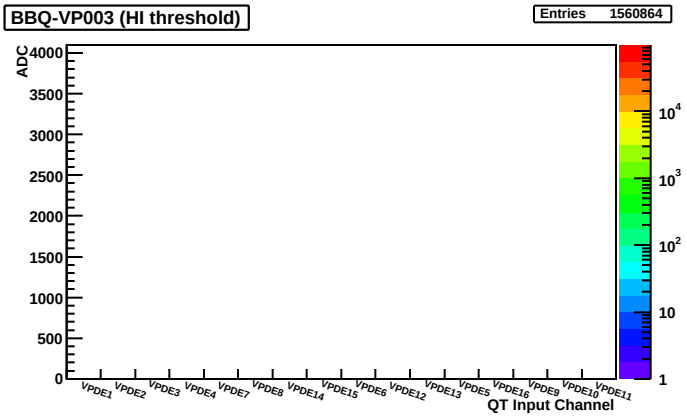

BBQ-ZD001
Entries 1560864
TAC
QT Input Channel

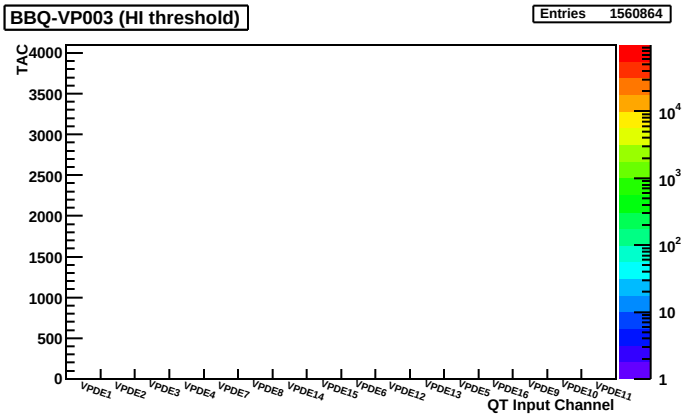

BBQ-VP001 (LO threshold)
Entries 1560864
ADC
QT Input Channel

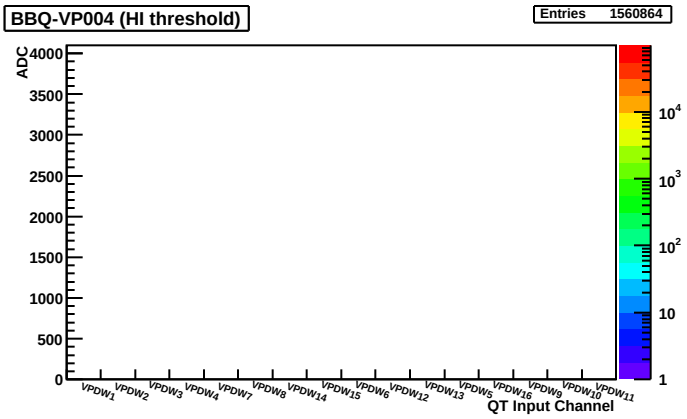

BBQ-VP001 (LO threshold)
Entries 1560864
TAC
QT Input Channel

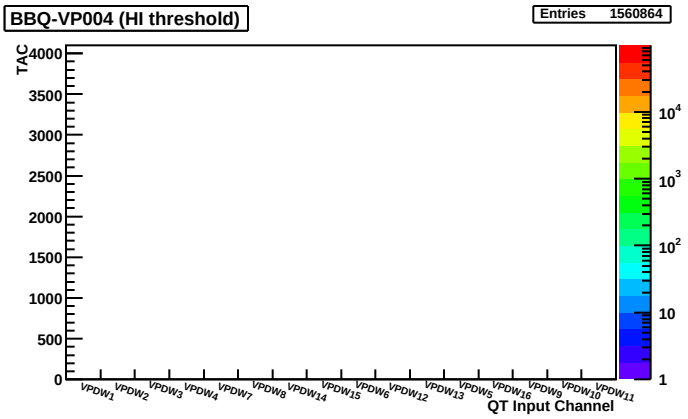

BBQ-VP002 (LO threshold)
Entries 1560864
ADC
QT Input Channel

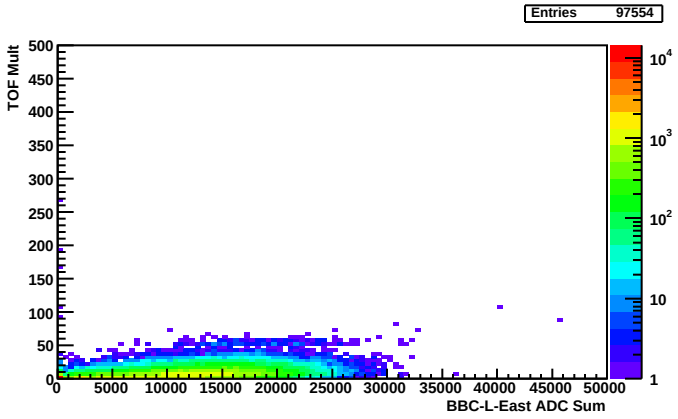

BBQ-VP002 (LO threshold)
Entries 1560864
TAC
QT Input Channel

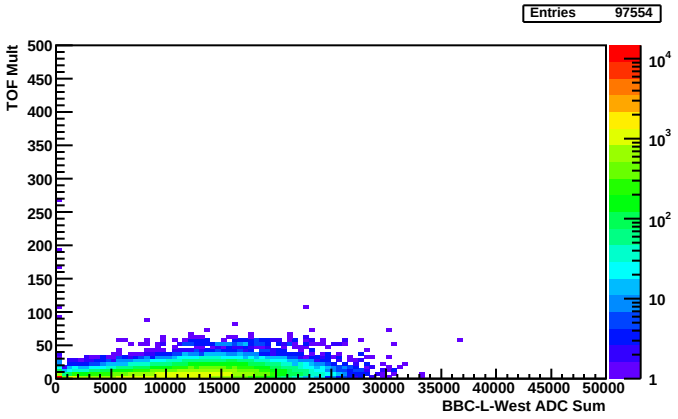

BBQ-VP003 (HI threshold)
Entries 1560864
ADC
QT Input Channel

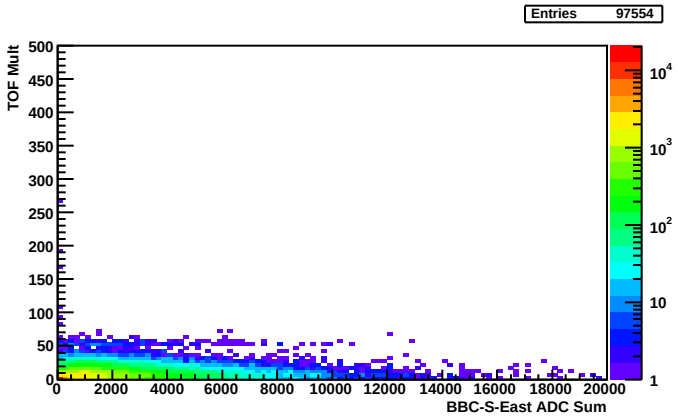

BBQ-VP003 (HI threshold)
Entries 1560864
TAC
QT Input Channel

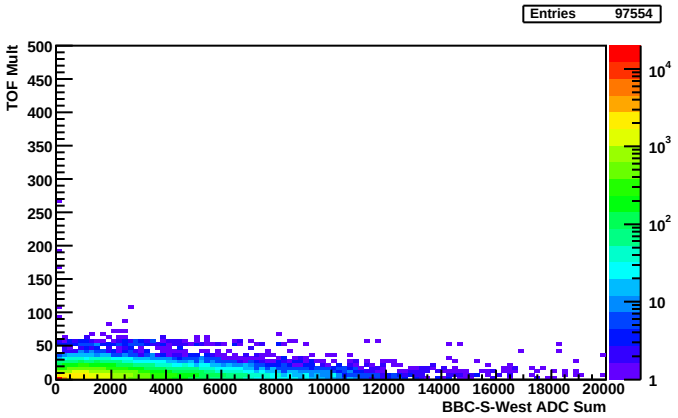

BBQ-VP004 (HI threshold)
Entries 1560864
ADC
QT Input Channel

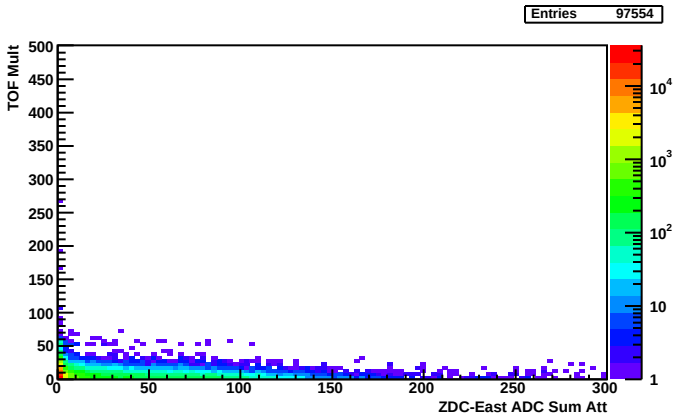

BBQ-VP004 (HI threshold)
Entries 1560864
TAC
QT Input Channel

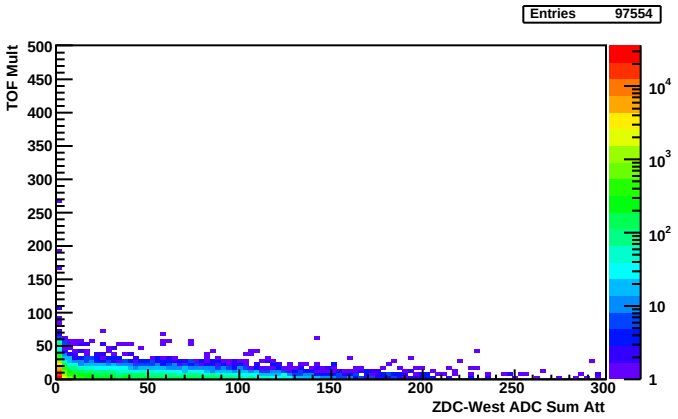

Entries 97554
TOF Mult
BBC-L-East ADC Sum

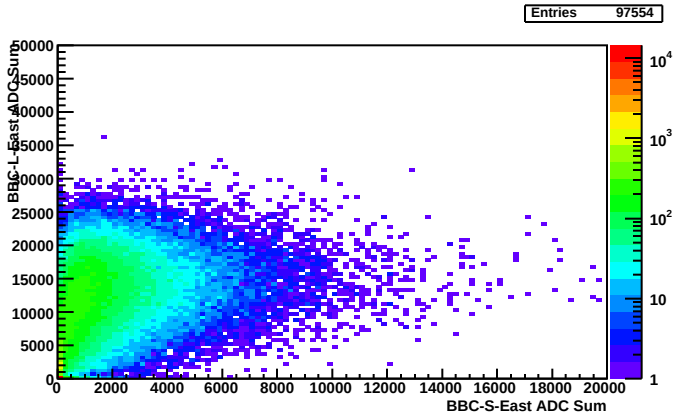

Entries 97554
TOF Mult
BBC-L-West ADC Sum

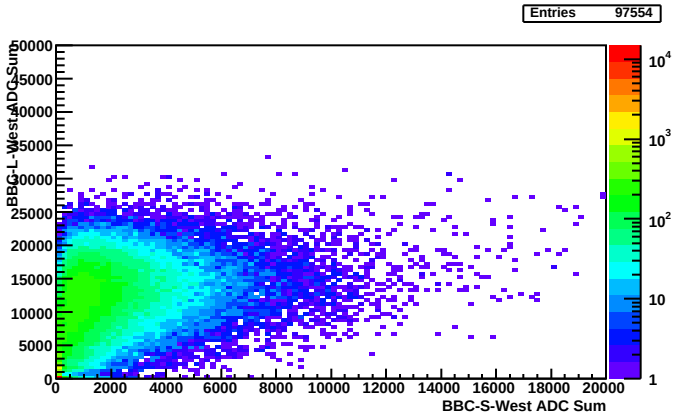

Entries 97554
TOF Mult
BBC-S-East ADC Sum

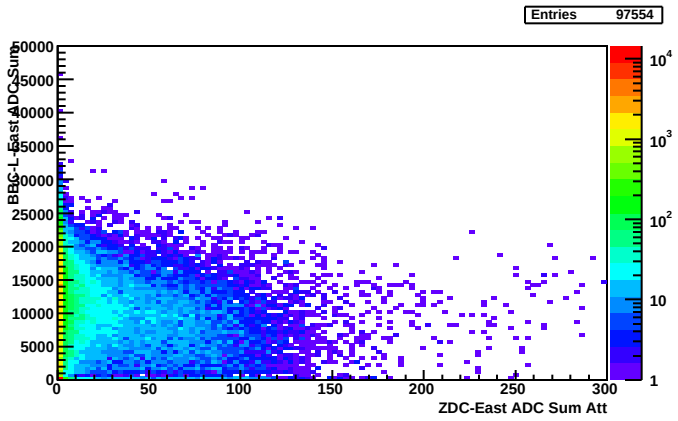

Entries 97554
TOF Mult
BBC-S-West ADC Sum

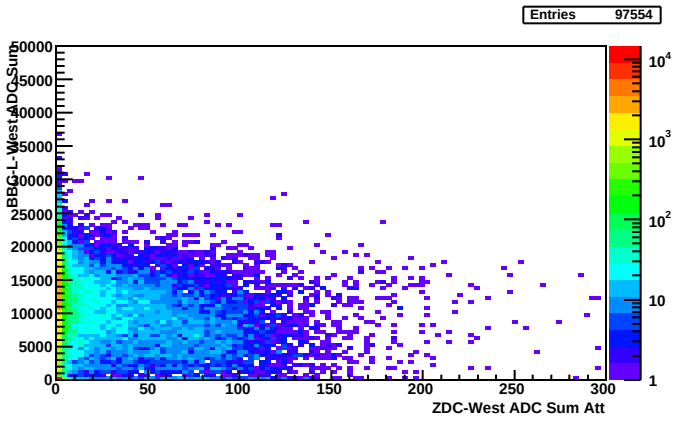

Entries 97554
TOF Mult
ZDC-East ADC Sum Att

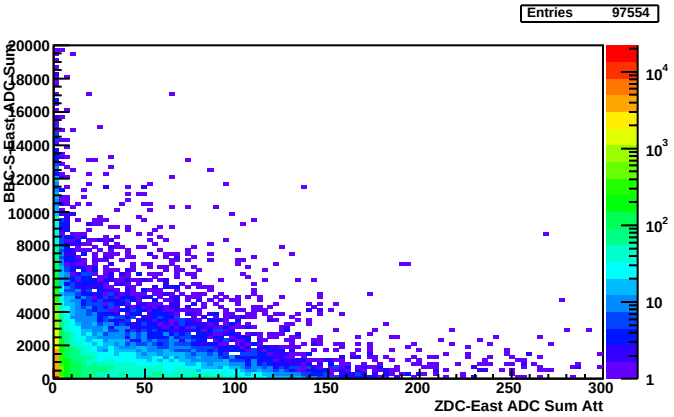

Entries 97554
TOF Mult
ZDC-West ADC Sum Att

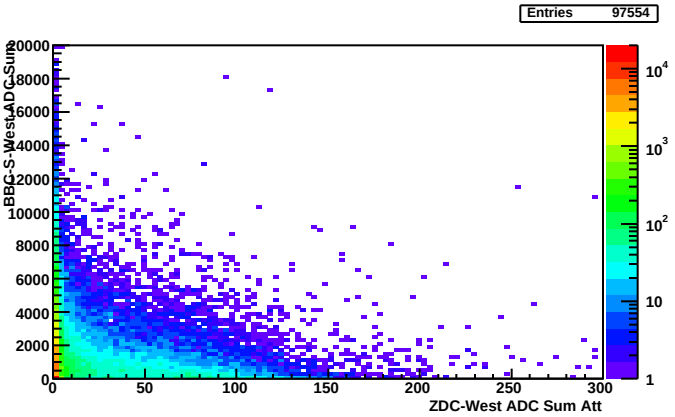

Entries 97554
BBC-L-East ADC Sum
BBC-S-East ADC Sum

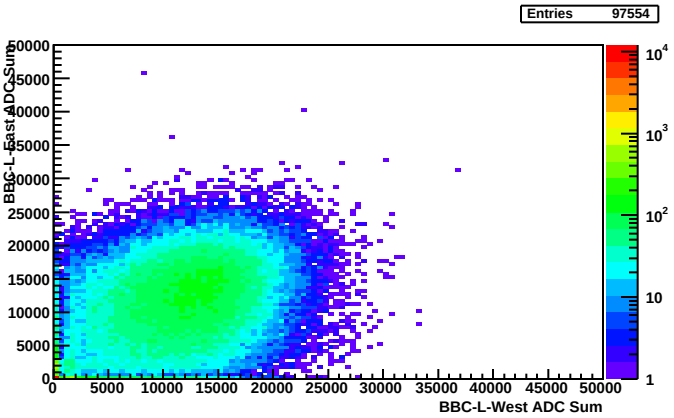

Entries 97554
BBC-L-West ADC Sum
BBC-S-West ADC Sum

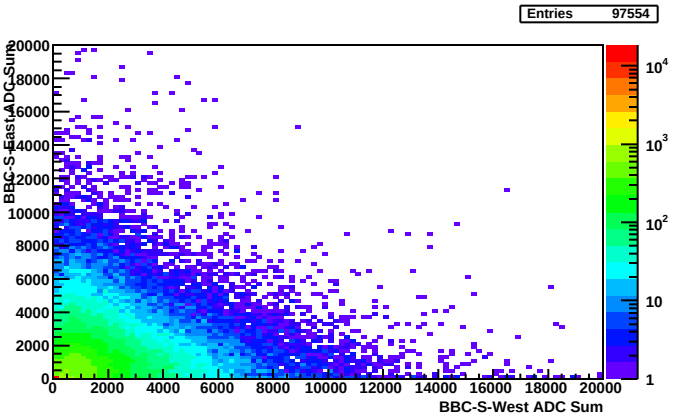

Entries 97554
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

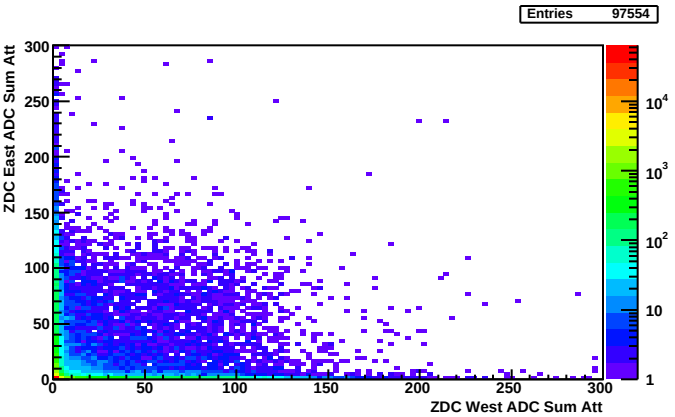

Entries 97554
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

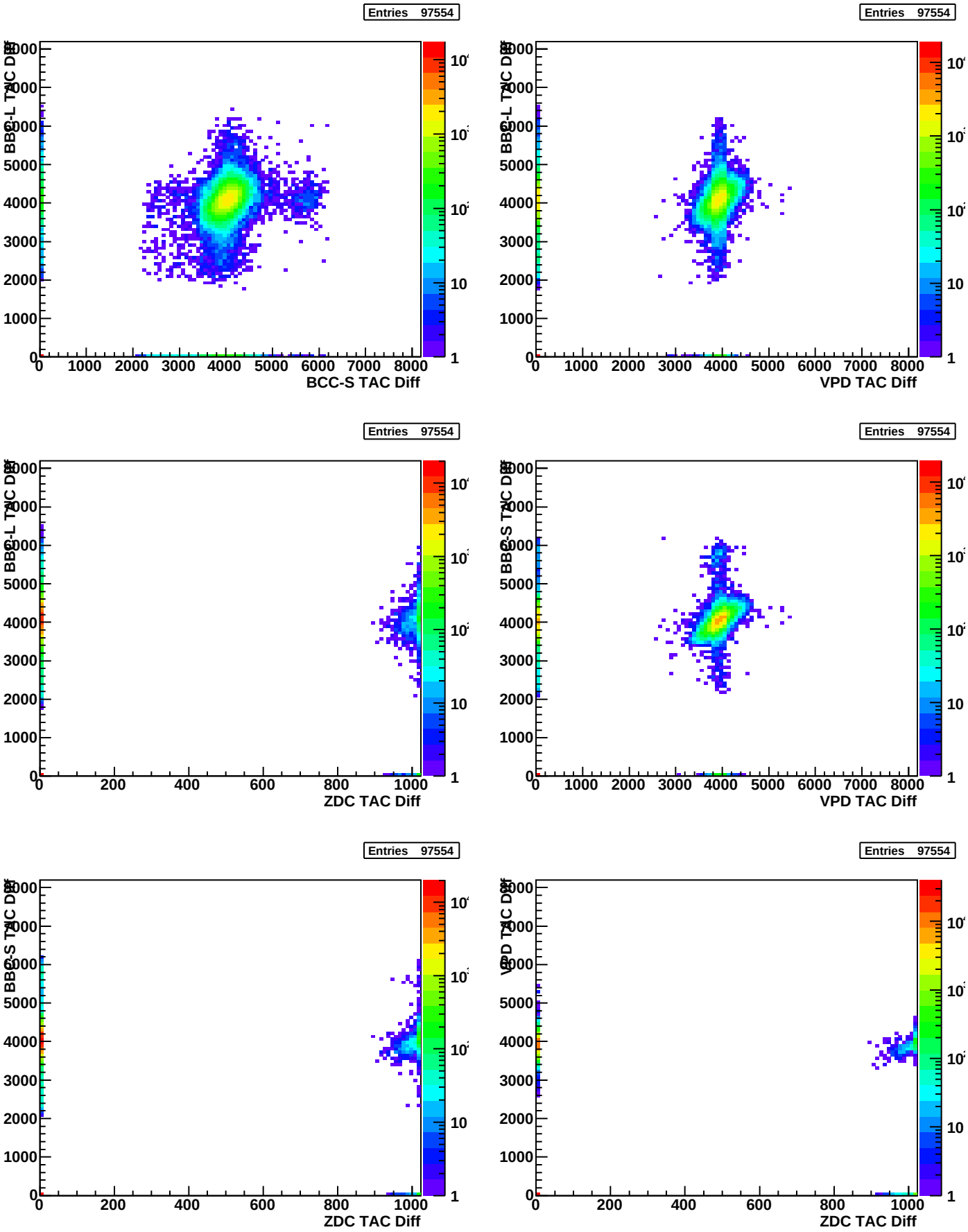

Entries 97554
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

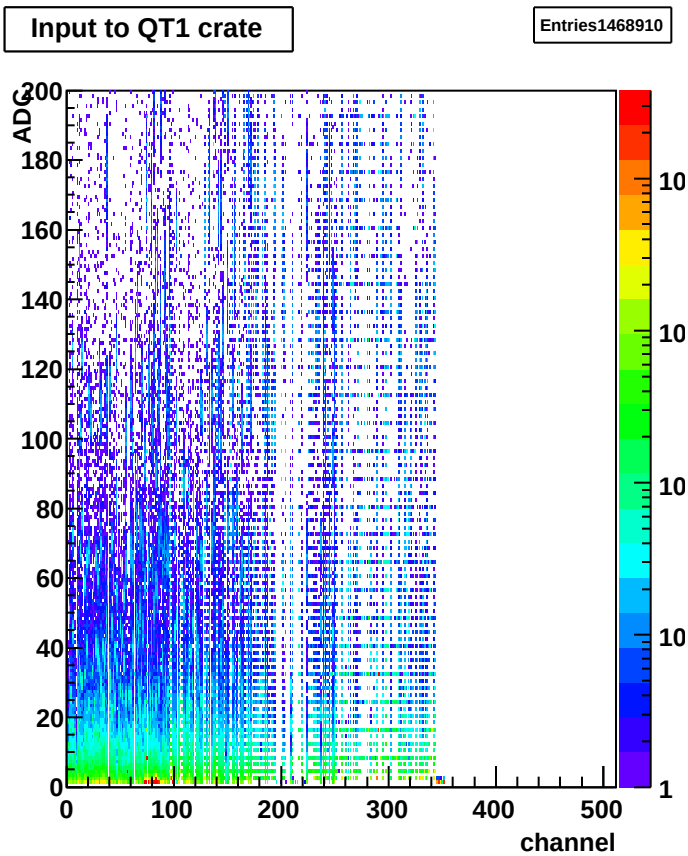

Entries 97554
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

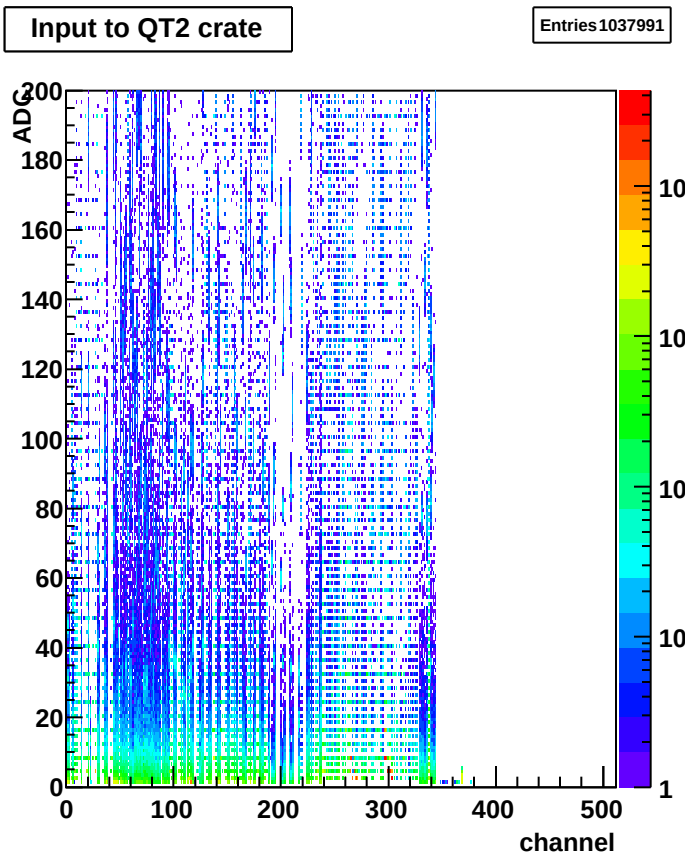

Entries 97554
BBC-L-East ADC Sum
BBC-L-West ADC Sum

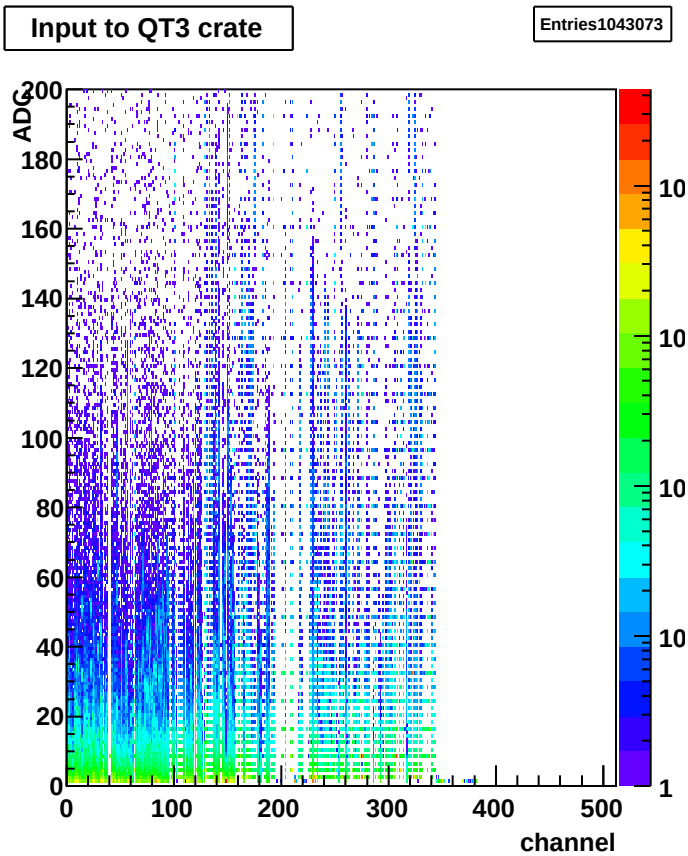

Entries 97554
BBC-S-East ADC Sum
BBC-S-West ADC Sum

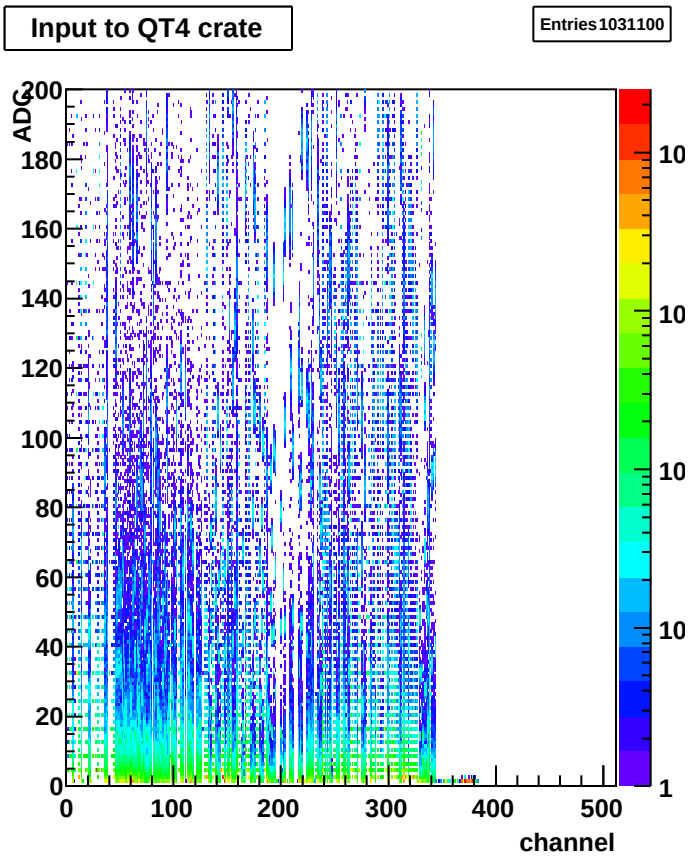

Entries 97554
ZDC East ADC Sum Att
ZDC West ADC Sum Att

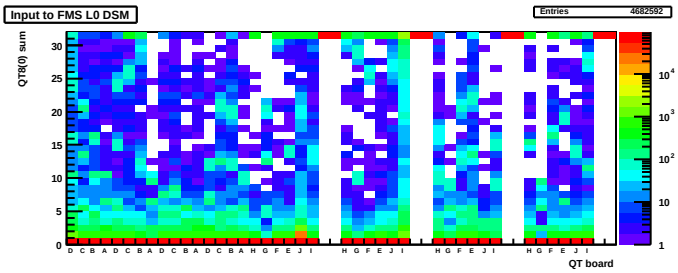

Entries 97554
BBC-L TAC Diff
BCC-S TAC Diff
Entries 97554
BBC-L TAC Diff
VPD TAC Diff
Entries 97554
BBC-L TAC Diff
ZDC TAC Diff
Entries 97554
BBC-S TAC Diff
VPD TAC Diff
Entries 97554
BBC-S TAC Diff
ZDC TAC Diff
Entries 97554
VPD TAC Diff
ZDC TAC Diff

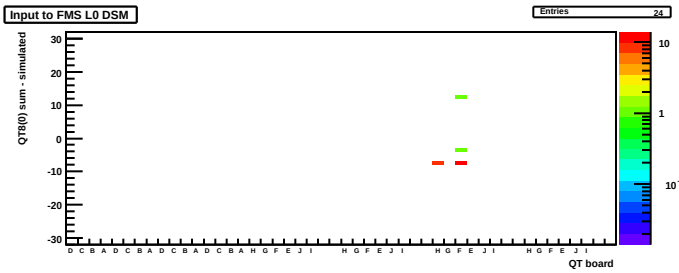

Input to QT1 crate
Entries1468910
ADC
channel

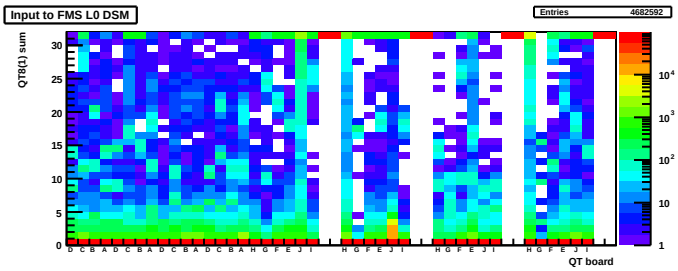

Input to QT2 crate
Entries1037991
ADC
channel

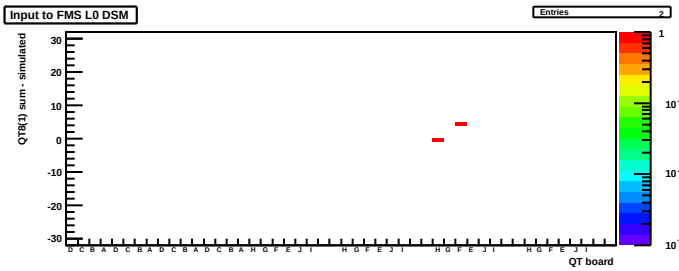

Input to QT3 crate
Entries1043073
ADC
channel

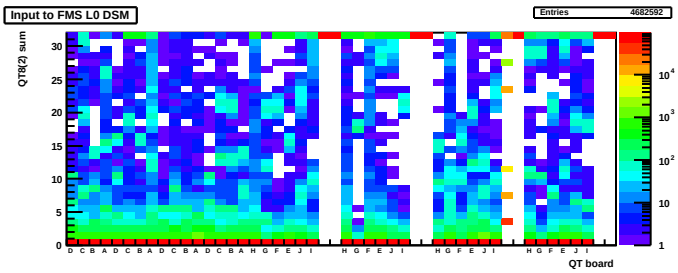

Input to QT4 crate
Entries1031100
ADC
channel

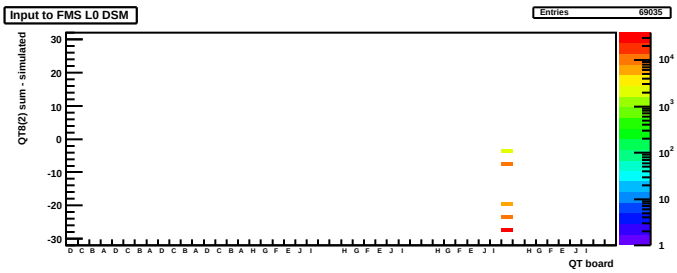

Input to FMS L0 DSM
Entries 4682592
QT8(0) sum
QT board

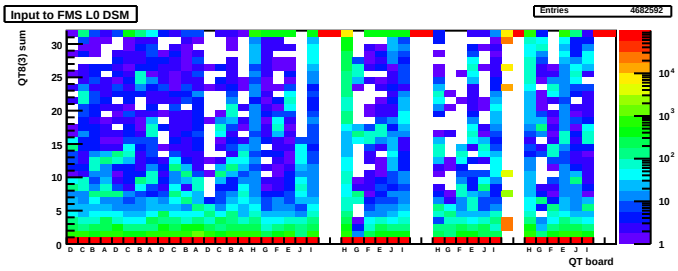

Input to FMS L0 DSM
Entries 24
QT8(0) sum - simulated
QT board

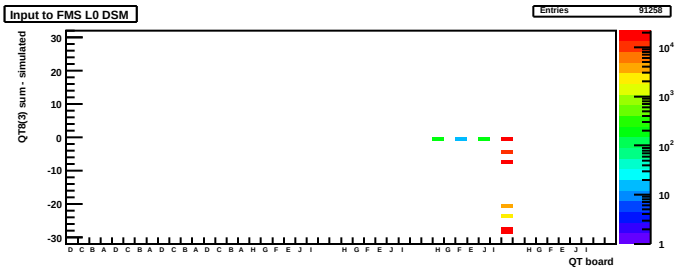

Input to FMS L0 DSM
Entries 4682592
QT8(1) sum
QT board

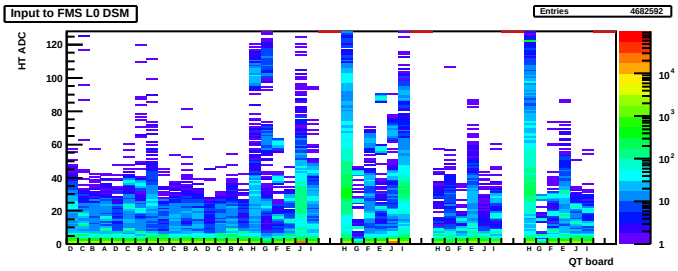

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

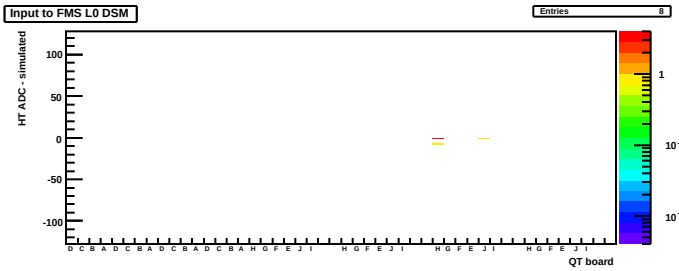

Input to FMS L0 DSM
Entries 4682592
QT8(2) sum
QT board

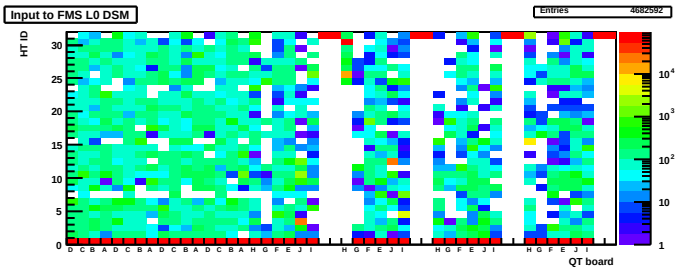

Input to FMS L0 DSM
Entries 69035
QT8(2) sum - simulated
QT board

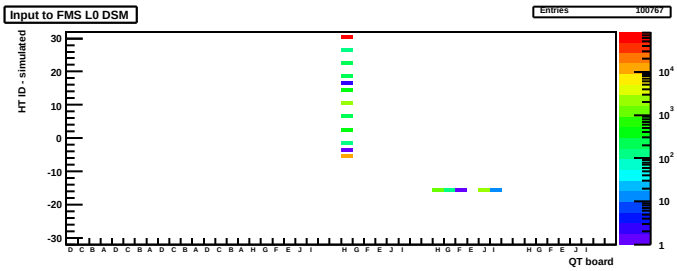

Input to FMS L0 DSM
Entries 4682592
QT8(3) sum
QT board

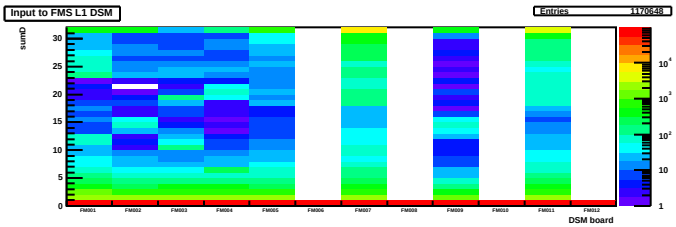

Input to FMS L0 DSM
Entries 91258
QT8(3) sum - simulated
QT board

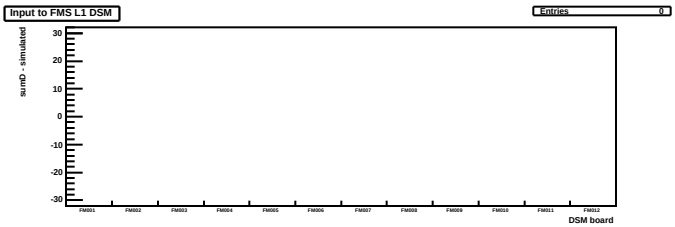

Input to FMS L0 DSM
Entries 4682592
HT ADC
QT board

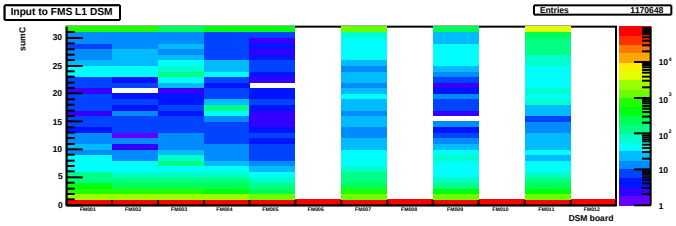

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

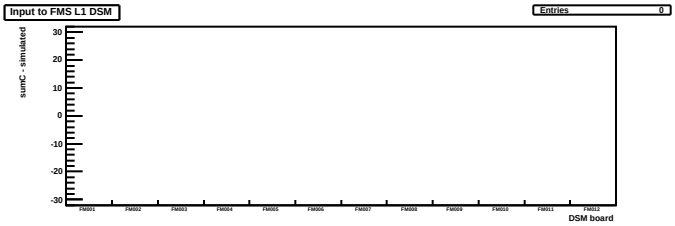

Input to FMS L0 DSM
Entries 4682592
HT ID
QT board

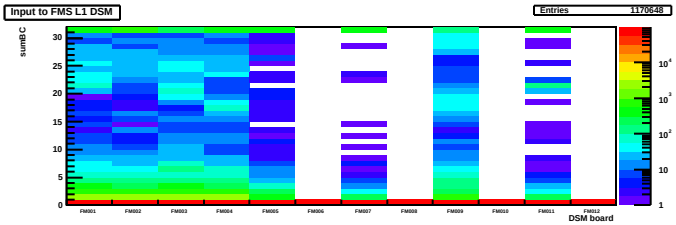

Input to FMS L0 DSM
Entries 100767
HT ID - simulated
QT board

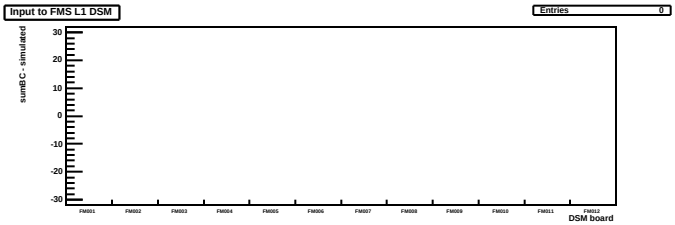

Input to FMS L1 DSM
Entries 1170648
sumD
DSM board

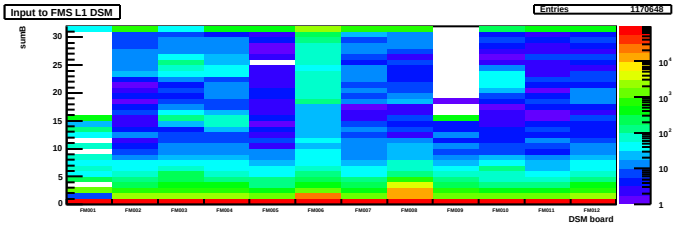

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

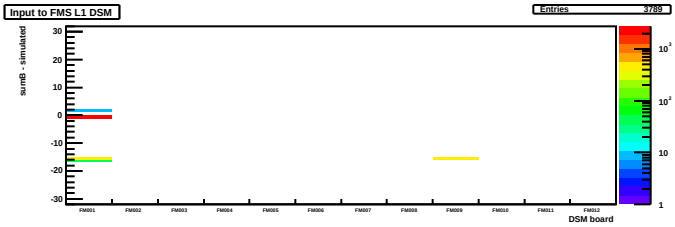

Input to FMS L1 DSM
Entries 1170648
sumC
DSM board

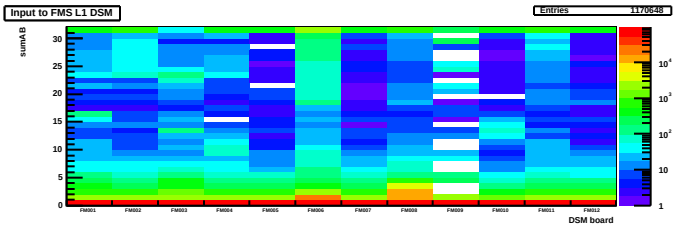

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

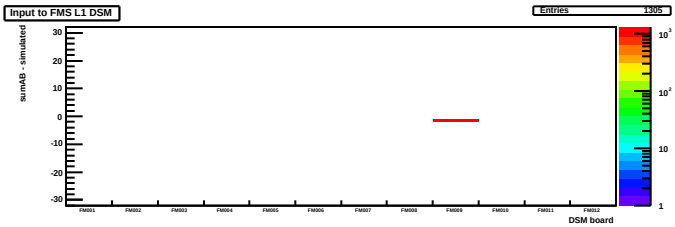

Input to FMS L1 DSM
Entries 1170648
sumBC
DSM board

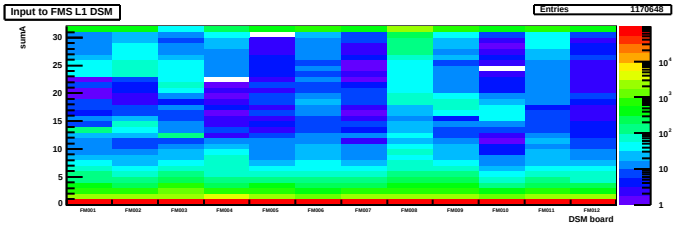

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

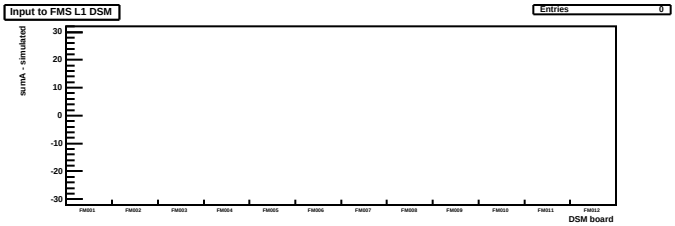

Input to FMS L1 DSM
Entries 1170648
sumB
DSM board

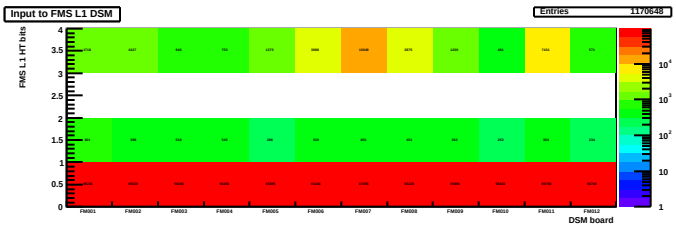

Input to FMS L1 DSM
Entries 3789
sumB - simulated
DSM board

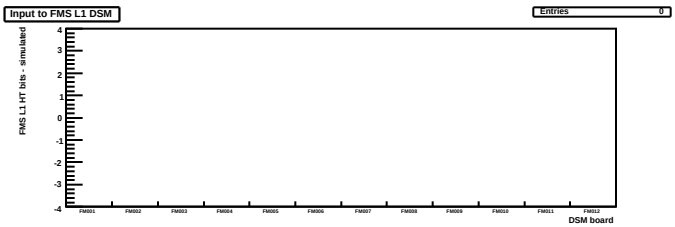

Input to FMS L1 DSM
Entries 1170648
sumAB
DSM board

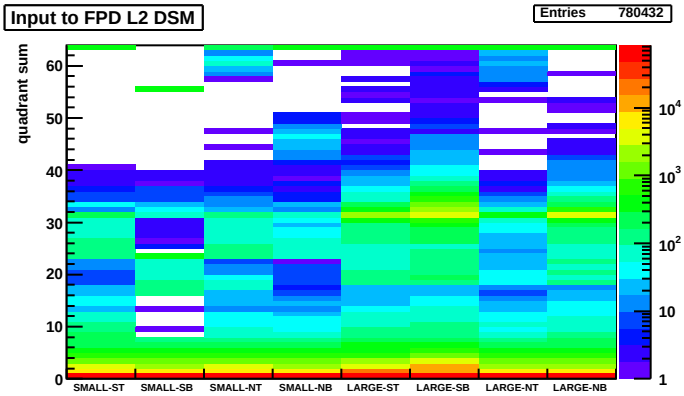

Input to FMS L1 DSM
Entries 1305
sumAB - simulated
DSM board

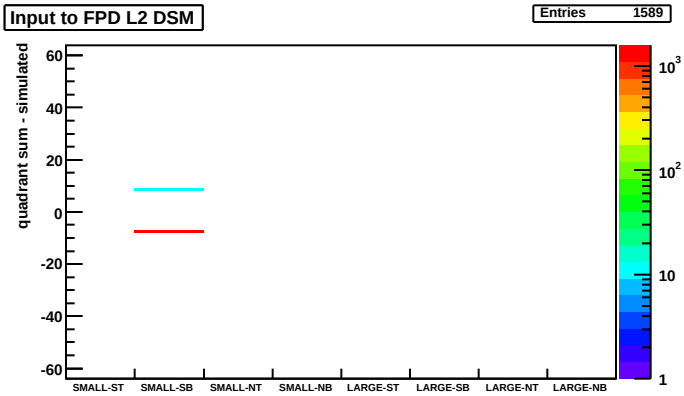

Input to FMS L1 DSM
Entries 1170648
sumA
DSM board

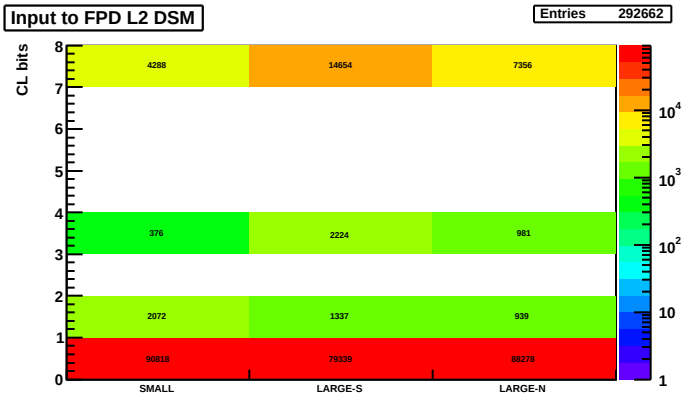

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

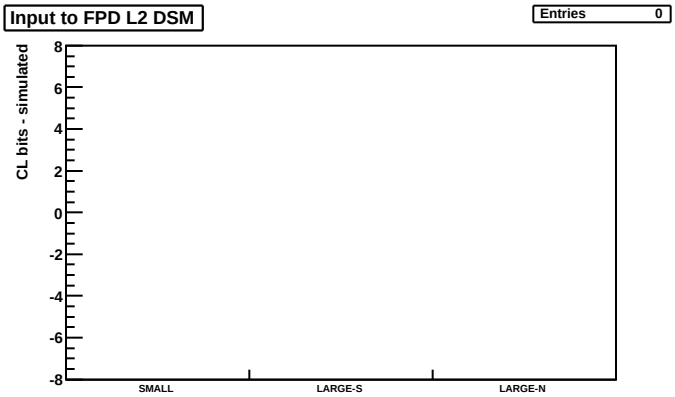

Input to FMS L1 DSM
Entries 1170648
FMS L1 HT bits
DSM board

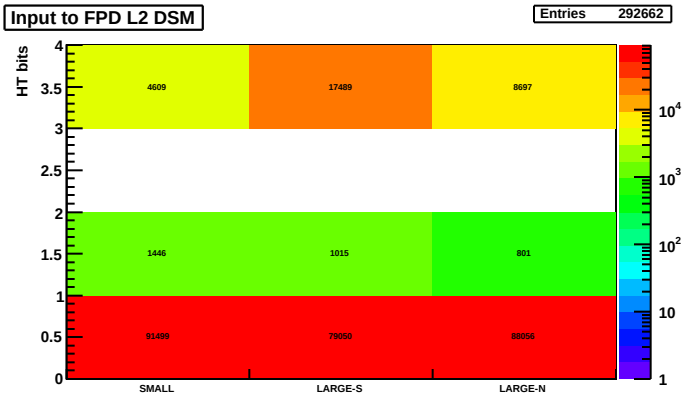

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

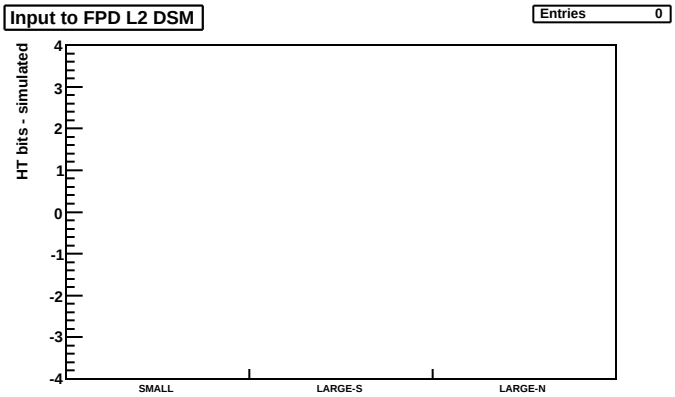

Input to FPD L2 DSM
Entries 780432
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

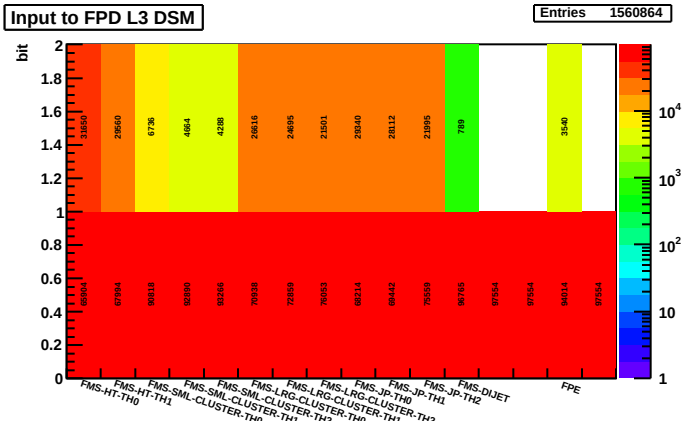

Input to FPD L2 DSM
Entries 1589
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

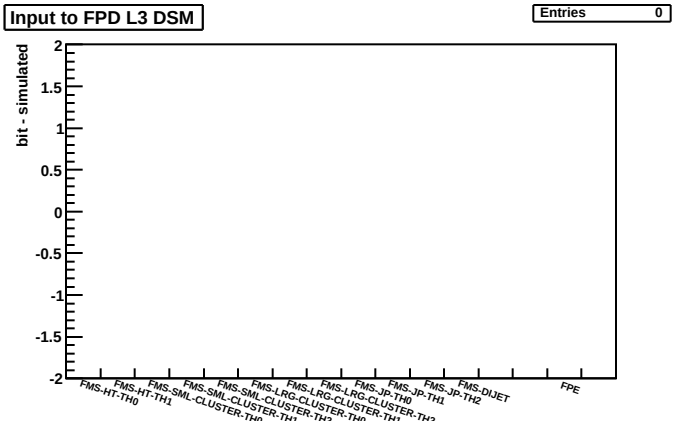

Input to FPD L2 DSM
Entries 292662
CL bits
4288
14654
7356
376
2224
981
2072
1337
939
90818
79339
88278
SMALL
LARGE-S
LARGE-N

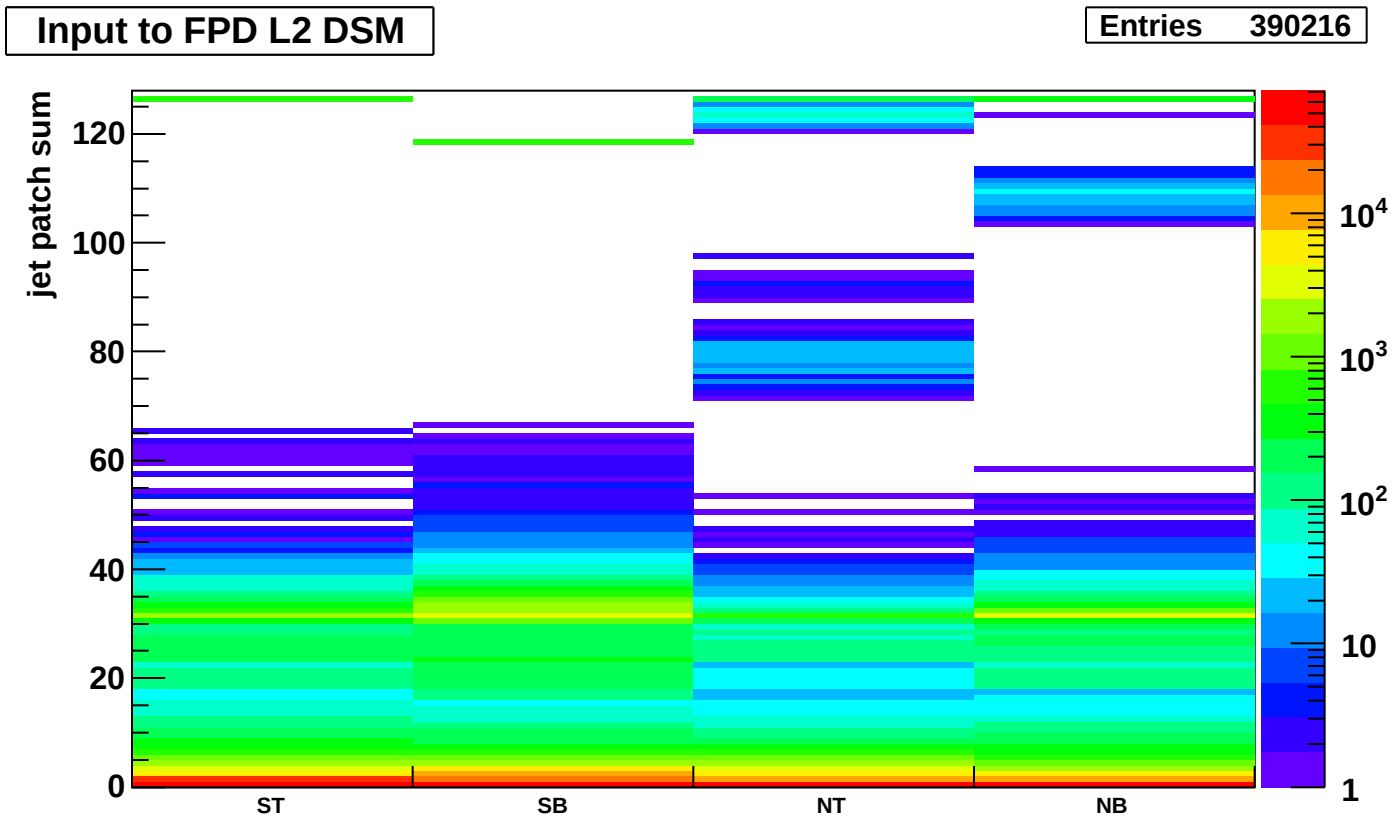

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

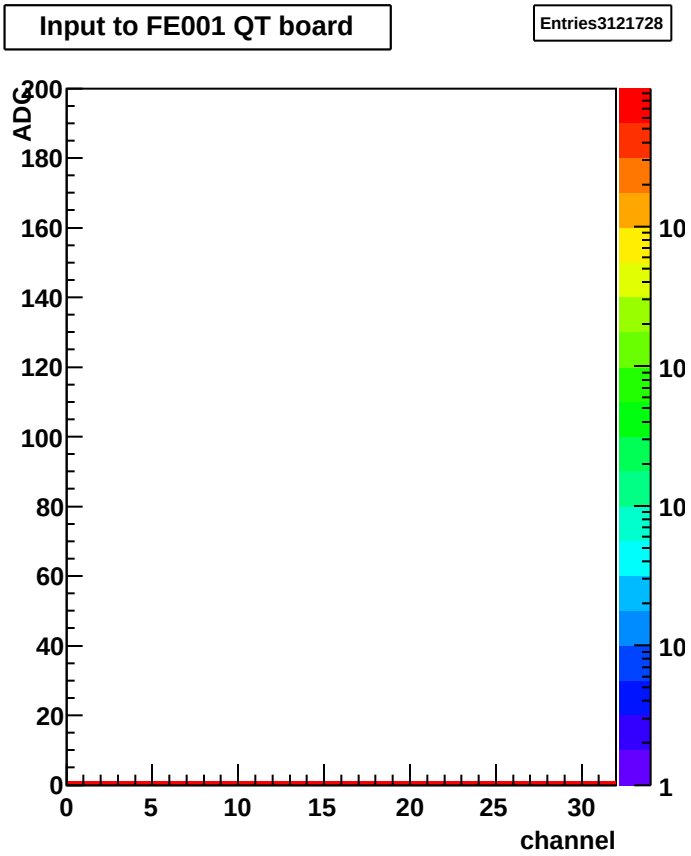

Input to FPD L2 DSM
Entries 292662
HT bits
4609
17489
8697
1446
1015
801
91499
79050
88056
SMALL
LARGE-S
LARGE-N

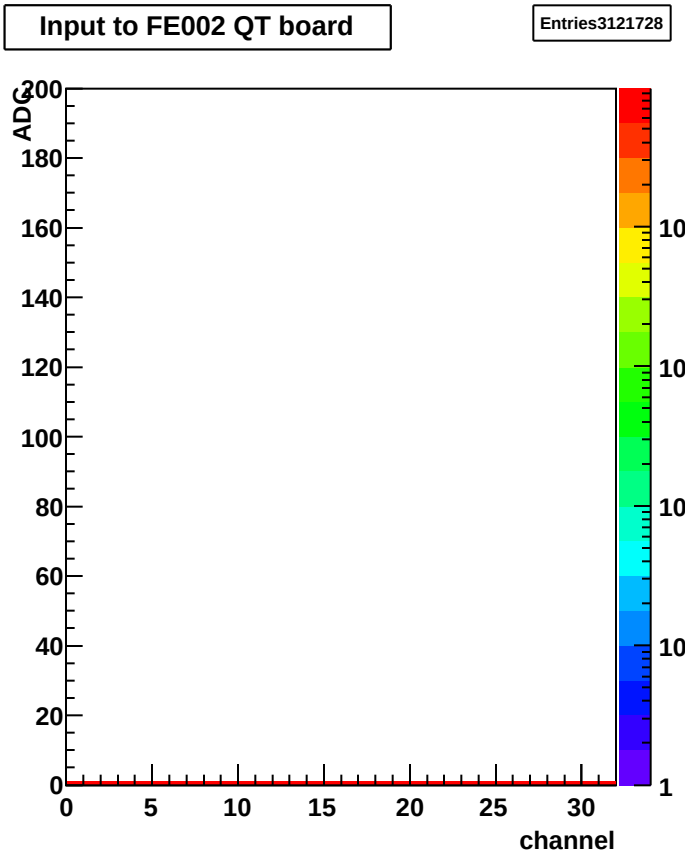

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

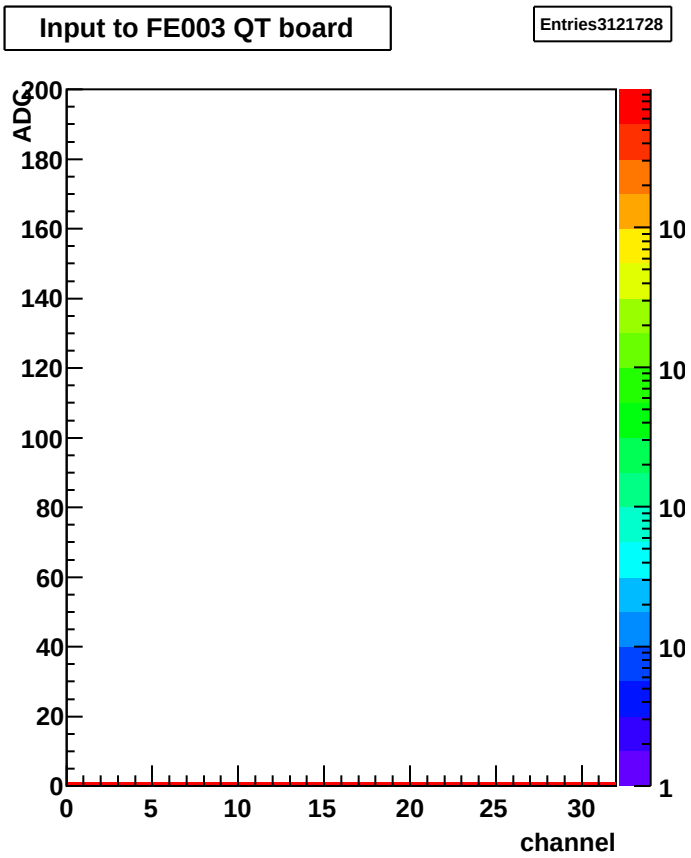

Input to FPD L3 DSM
Entries 1560864
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

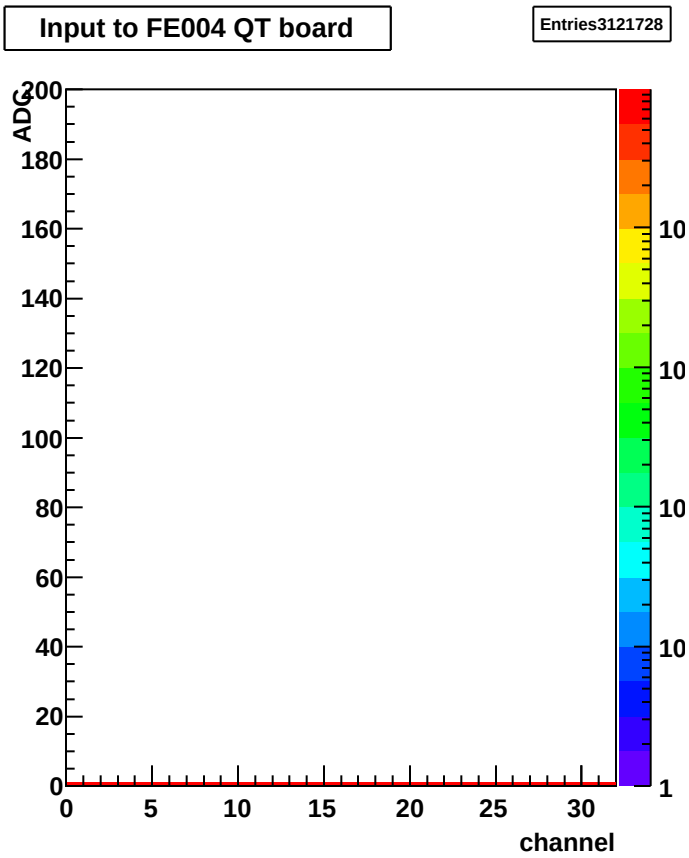

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

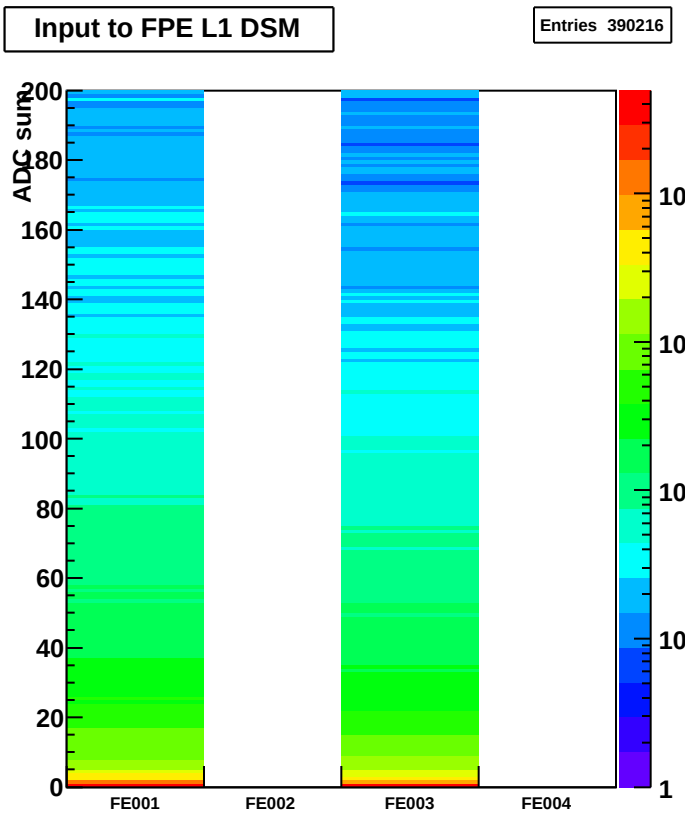

Input to FPD L2 DSM
Entries 390216
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

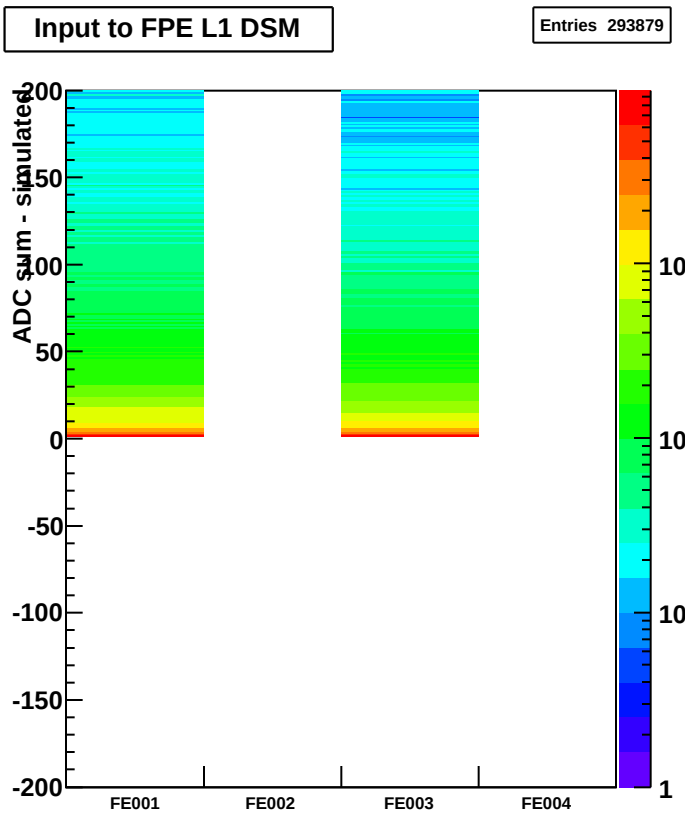

Input to FE001 QT board
Entries3121728
ADC
channel

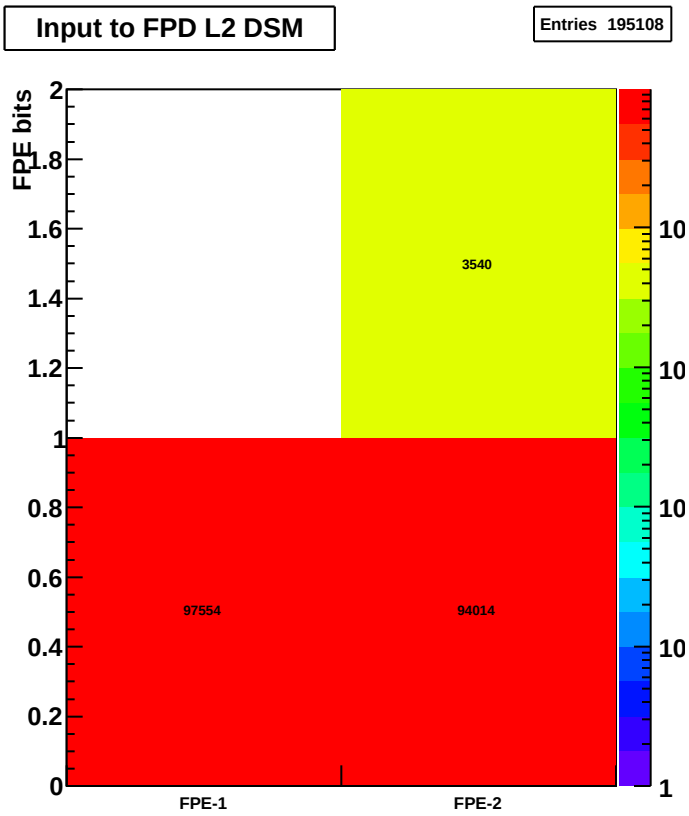

Input to FE002 QT board
Entries3121728
ADC
channel

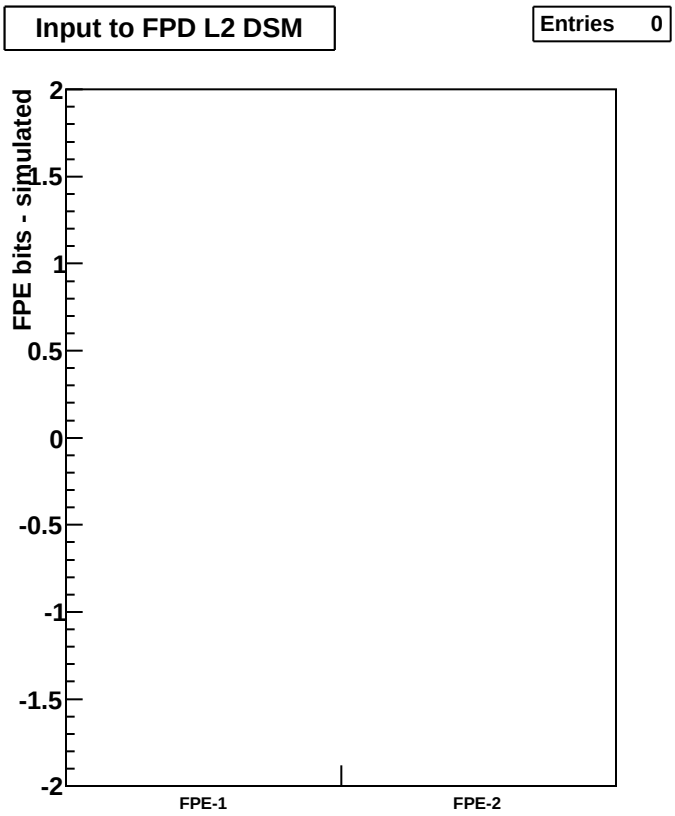

Input to FE003 QT board
Entries3121728
ADC
channel


Input to FE004 QT board
Entries3121728
ADC
channel


Input to FPE L1 DSM
Entries 390216
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 293879
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 195108
FPE bits
3540
97554
94014
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2