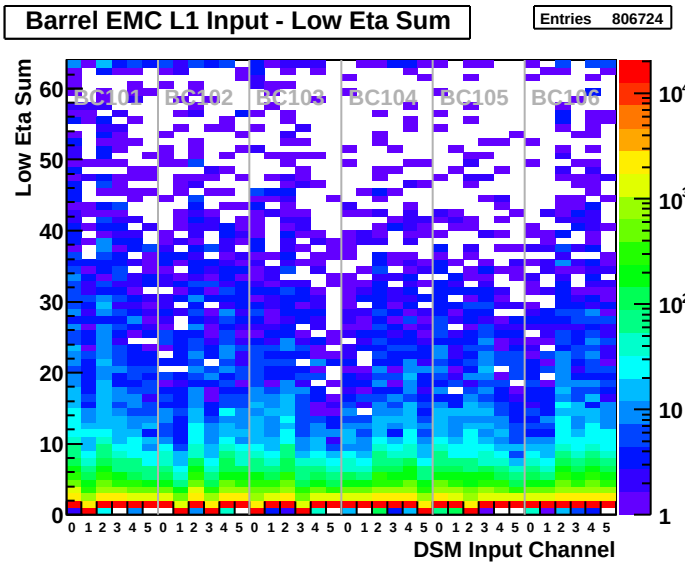
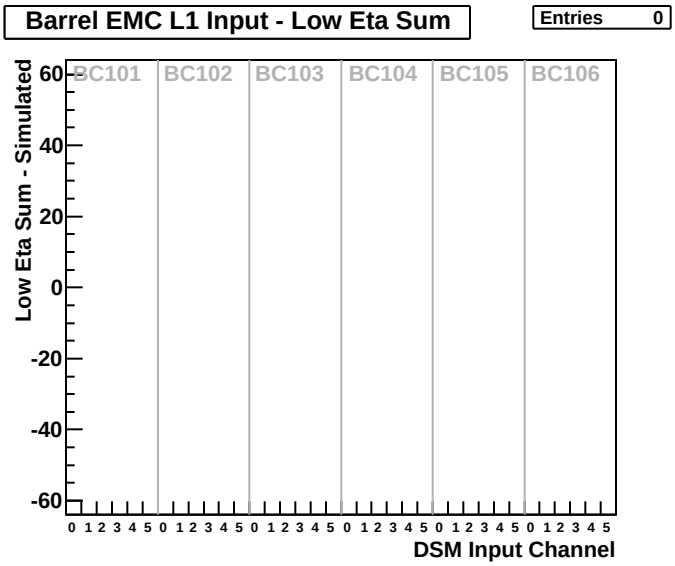
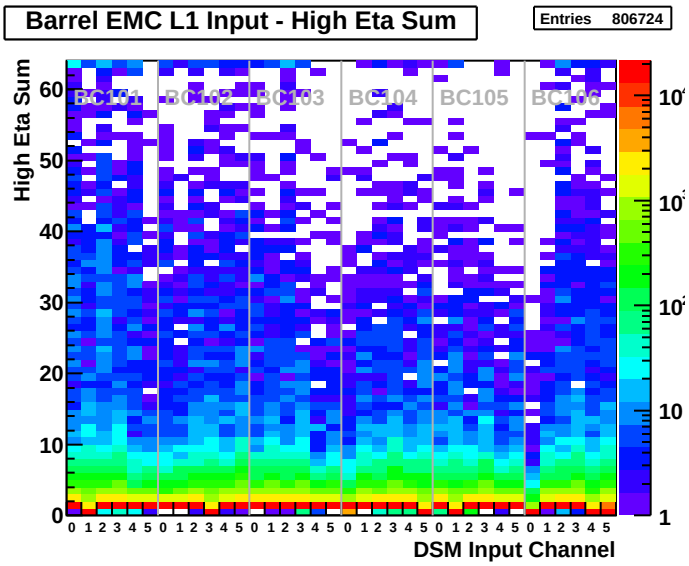
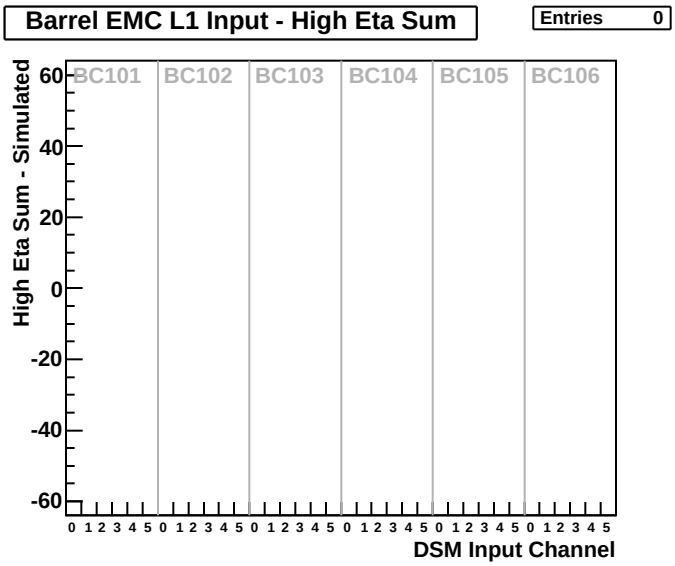
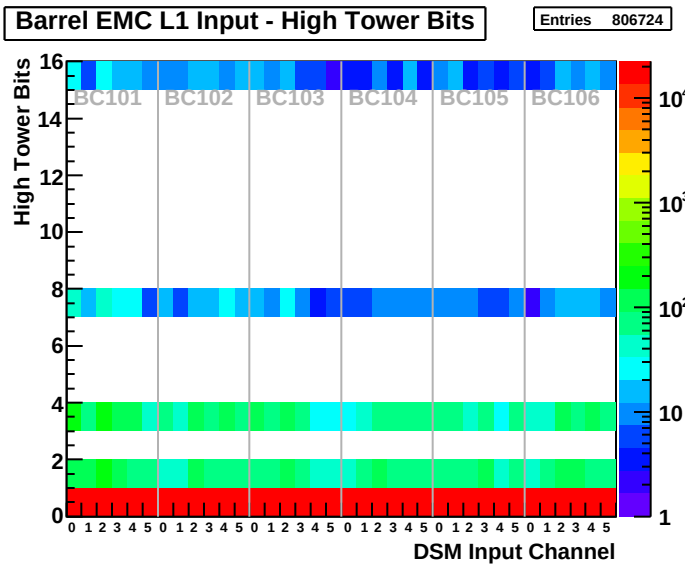
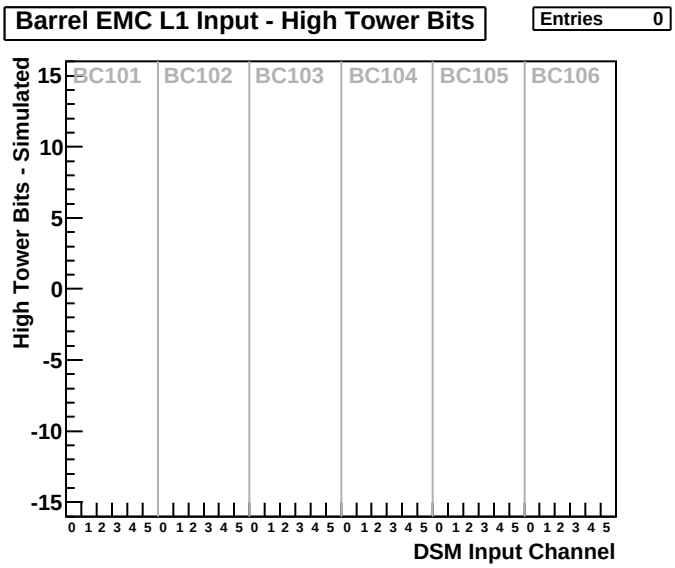

Barrel EMC L1 Input - Low Eta Sum
Entries 806724
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

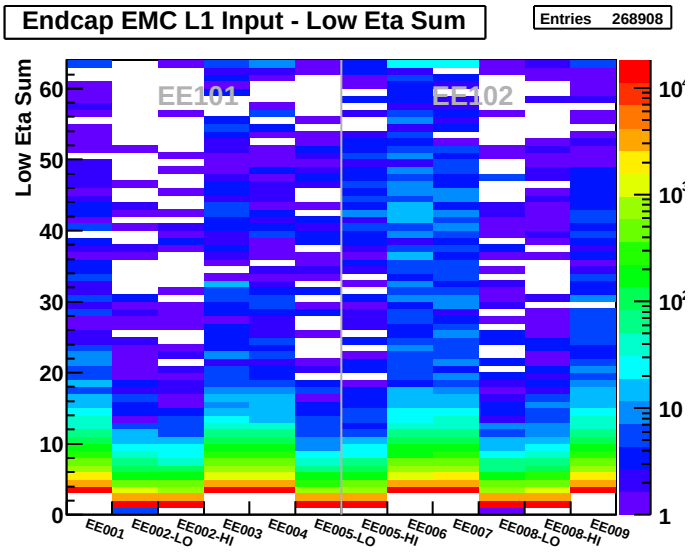

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

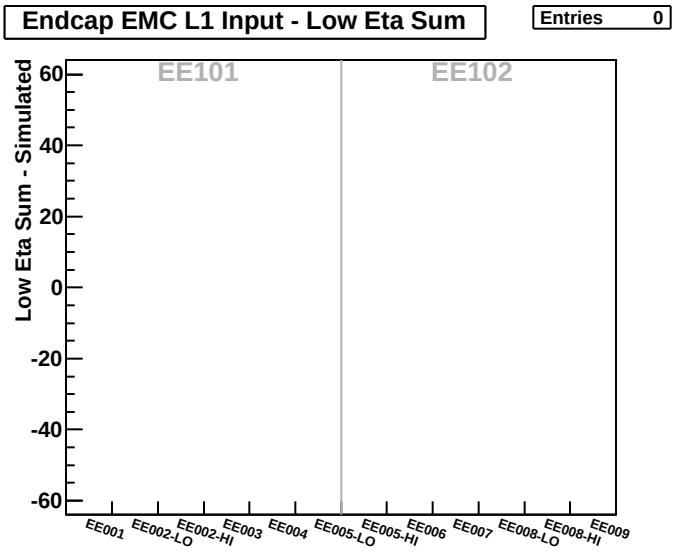

Barrel EMC L1 Input - High Eta Sum
Entries 806724
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

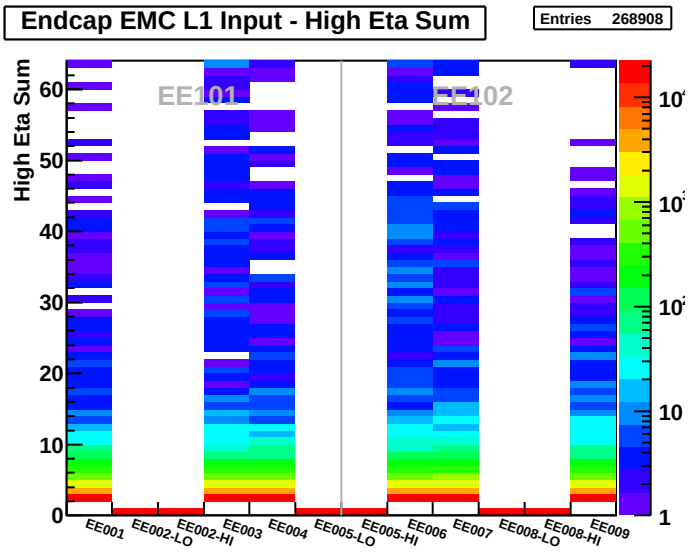

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

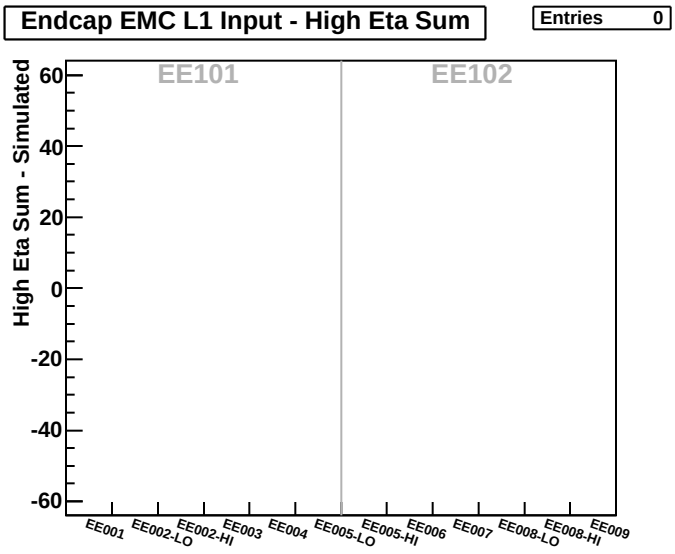

Barrel EMC L1 Input - High Tower Bits
Entries 806724
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

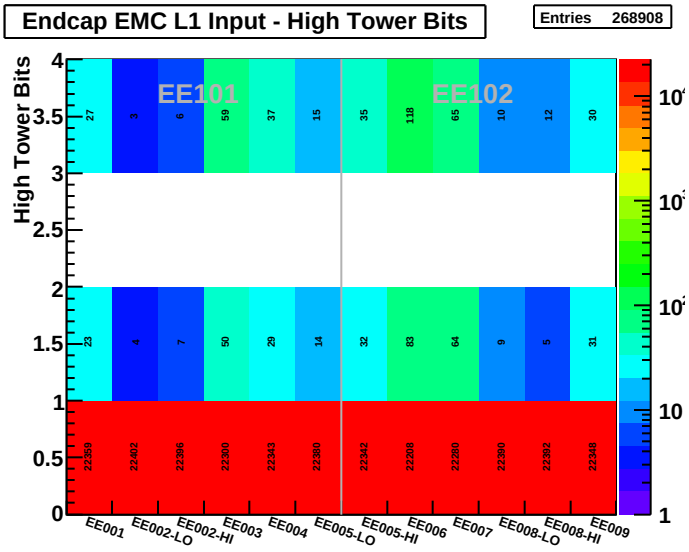

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

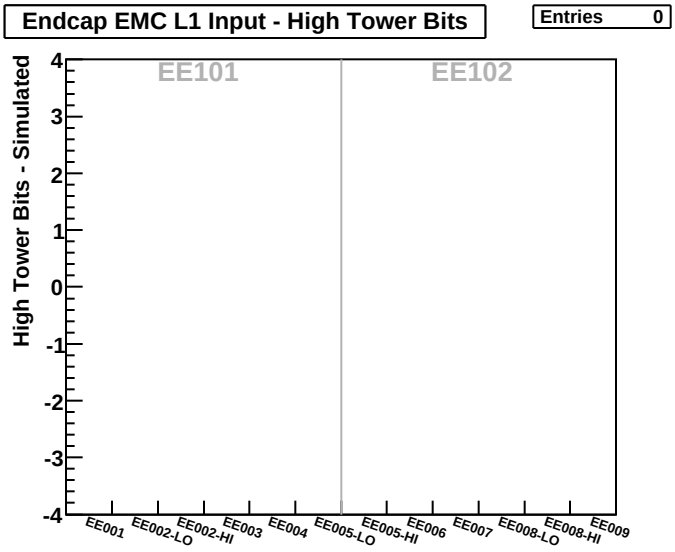

Endcap EMC L1 Input - Low Eta Sum
Entries 268908
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

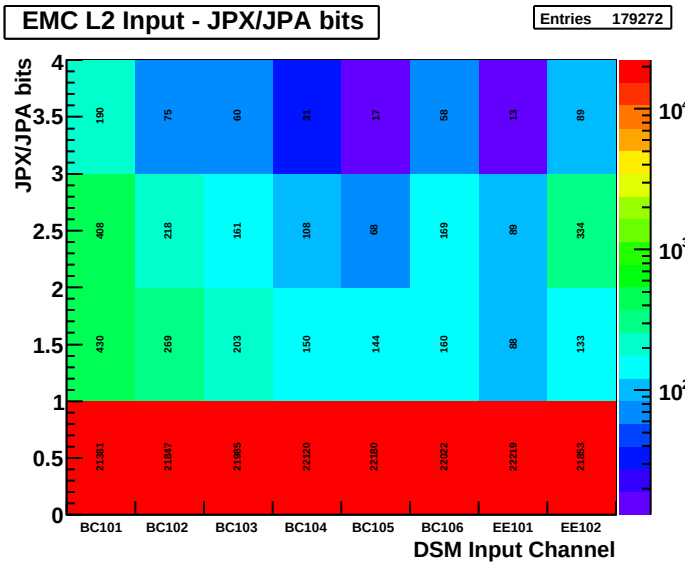

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

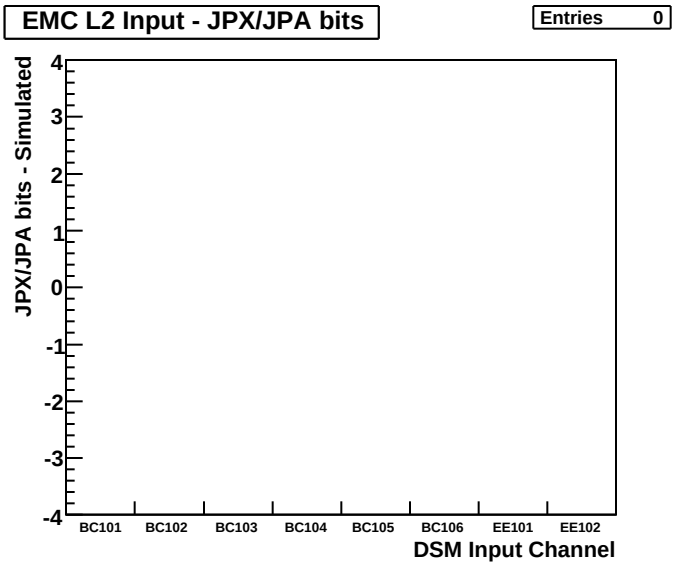

Endcap EMC L1 Input - High Eta Sum
Entries 268908
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

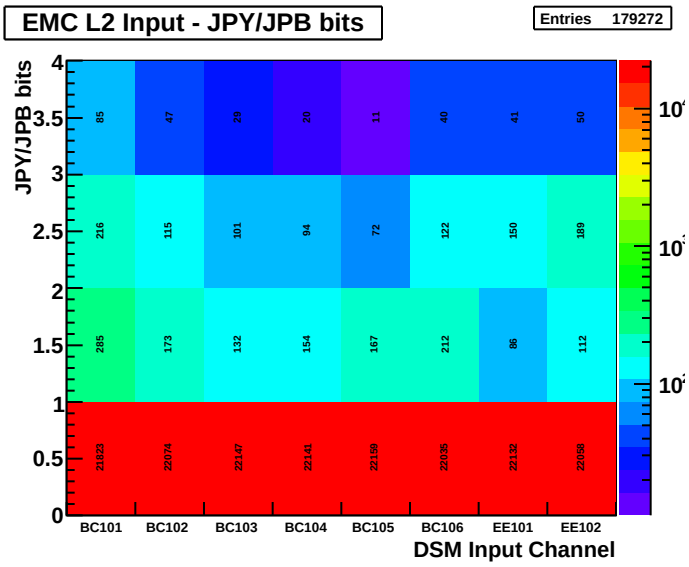

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

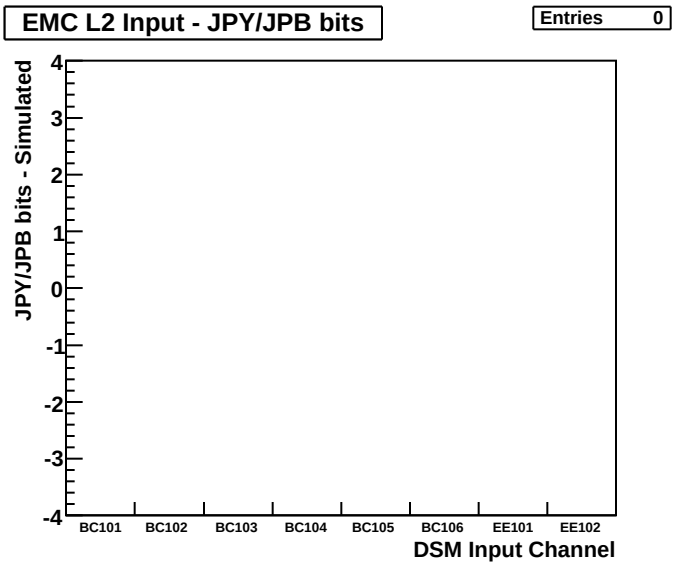

Endcap EMC L1 Input - High Tower Bits
Entries 268908
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

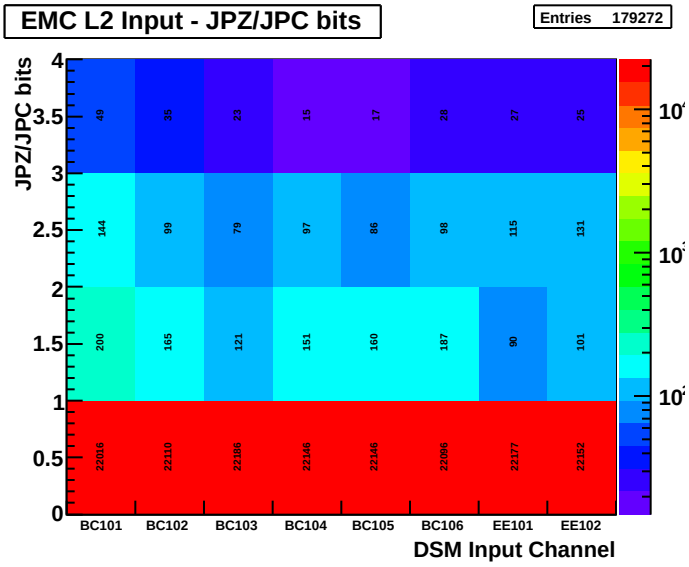

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

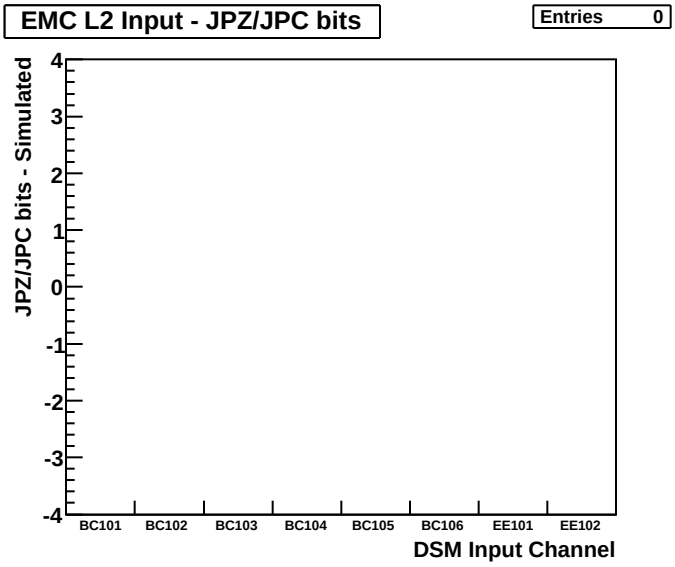

EMC L2 Input - JPX/JPA bits
Entries 179272
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
190 75 60 31 17 58 13 89
408 218 161 108 68 169 89 334
430 269 203 150 144 160 88 133
21381 21847 21985 22120 22180 22202 22219 21853

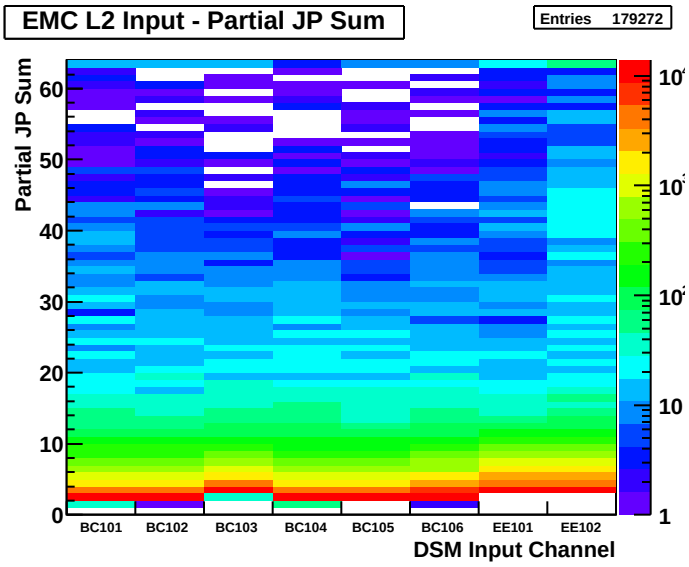

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

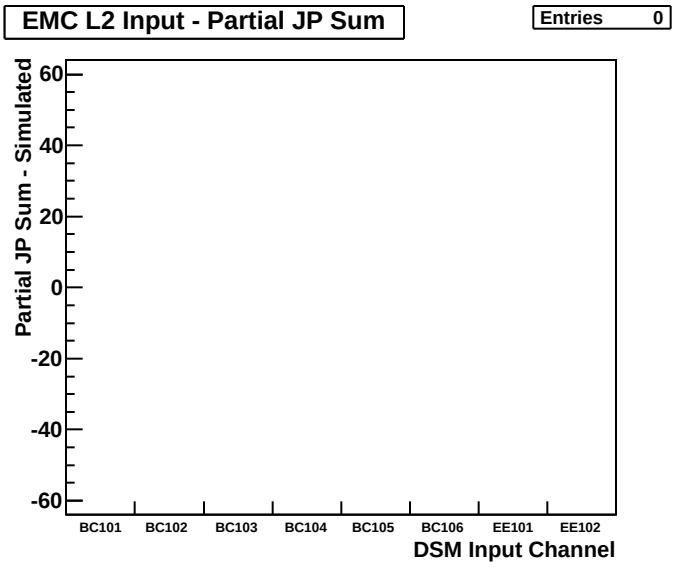

EMC L2 Input - JPY/JPB bits
Entries 179272
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
85 47 29 20 11 40 41 50
216 115 101 94 72 122 150 189
285 173 132 154 167 212 86 112
21823 22074 22147 22141 22159 22035 22132 22058

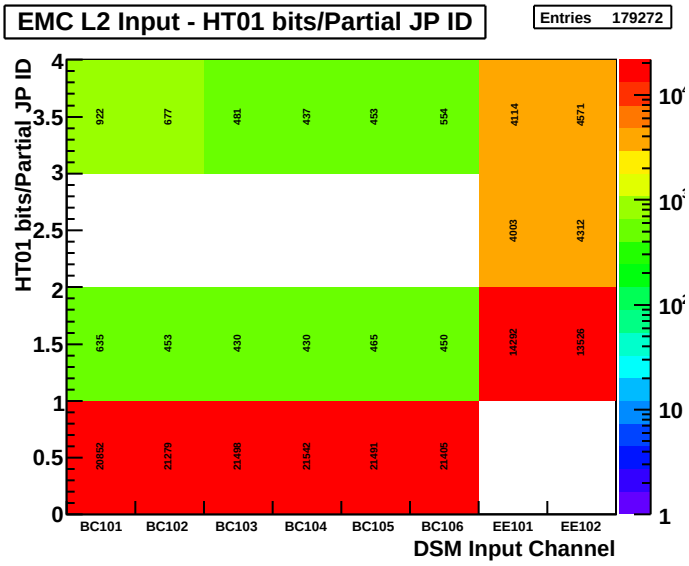

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

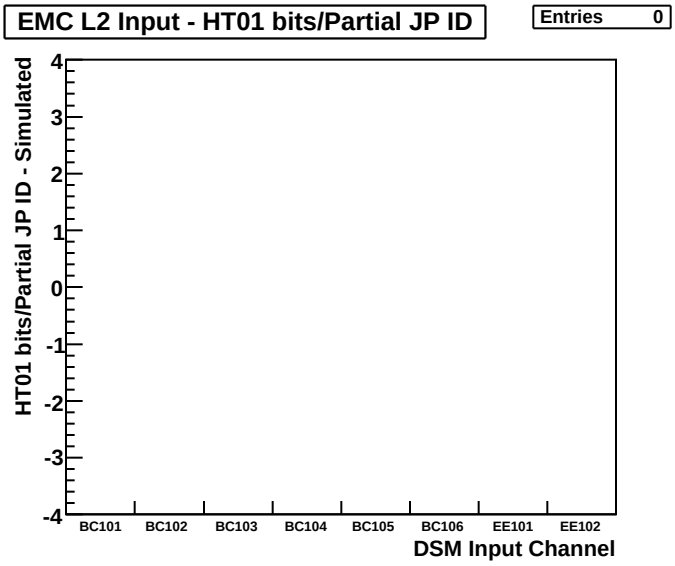

EMC L2 Input - JPZ/JPC bits
Entries 179272
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
49 35 23 15 17 28 27 25
144 99 79 97 86 98 115 131
200 165 121 151 160 187 90 101
22016 22110 22186 22146 22146 22096 22177 22152

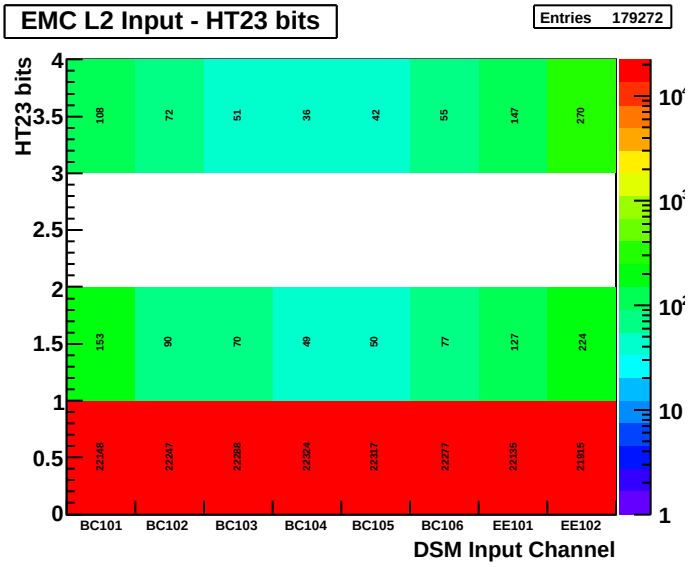

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

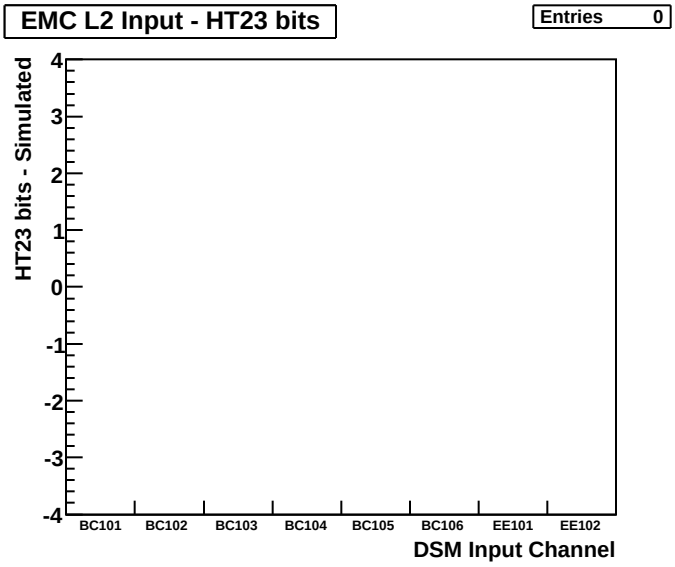

EMC L2 Input - Partial JP Sum
Entries 179272
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

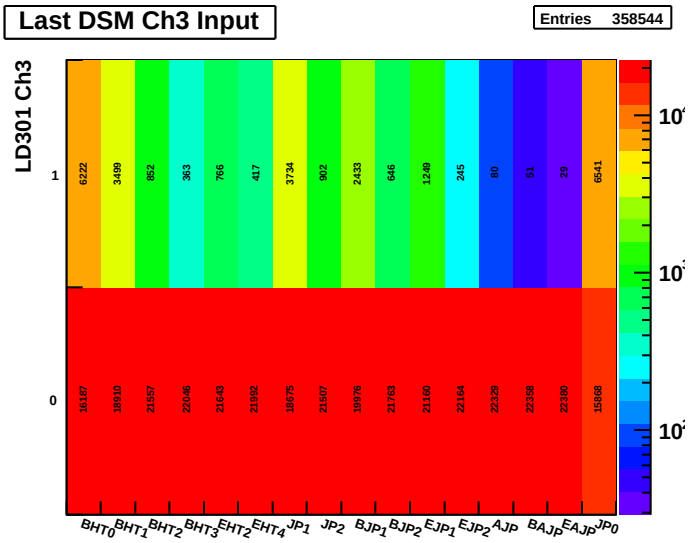

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

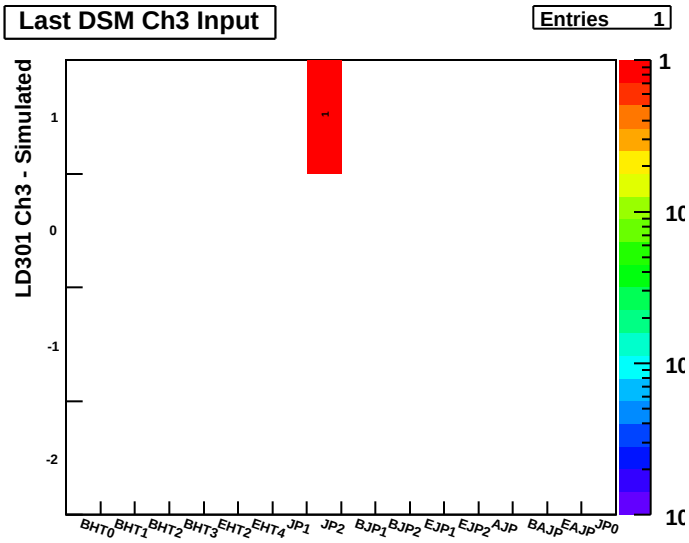

EMC L2 Input - HT01 bits/Partial JP ID
Entries 179272
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

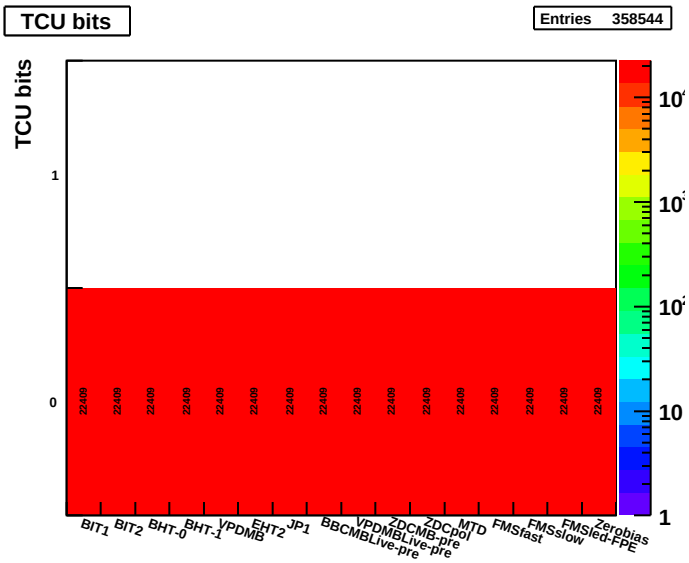

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

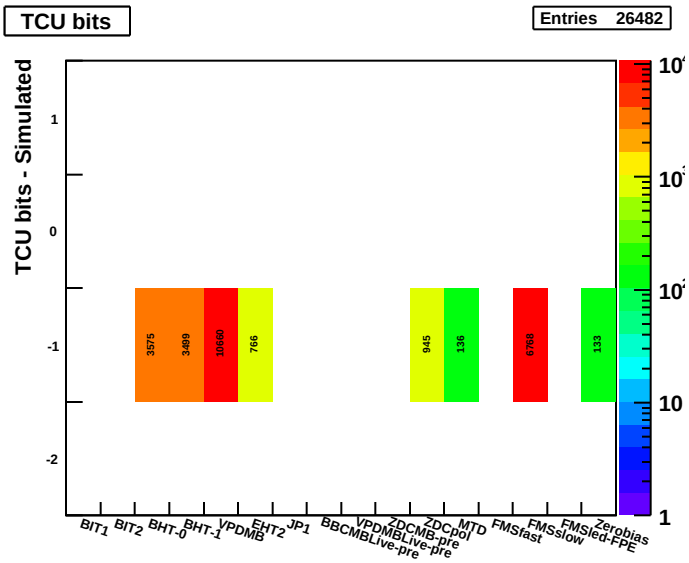

EMC L2 Input - HT23 bits
Entries 179272
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

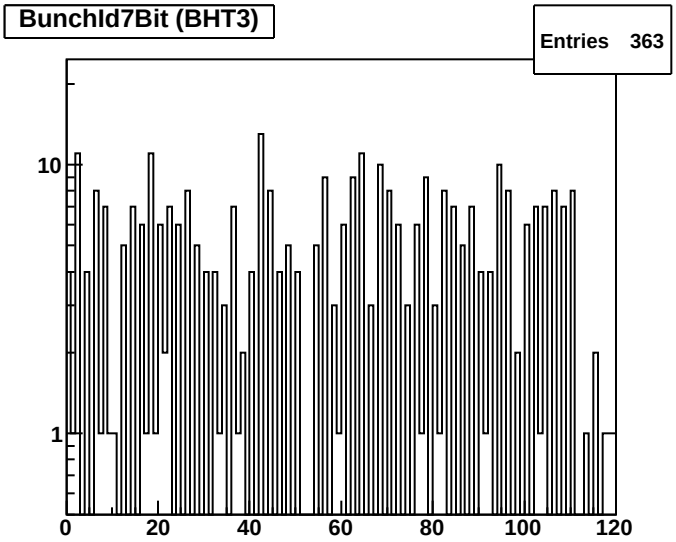

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

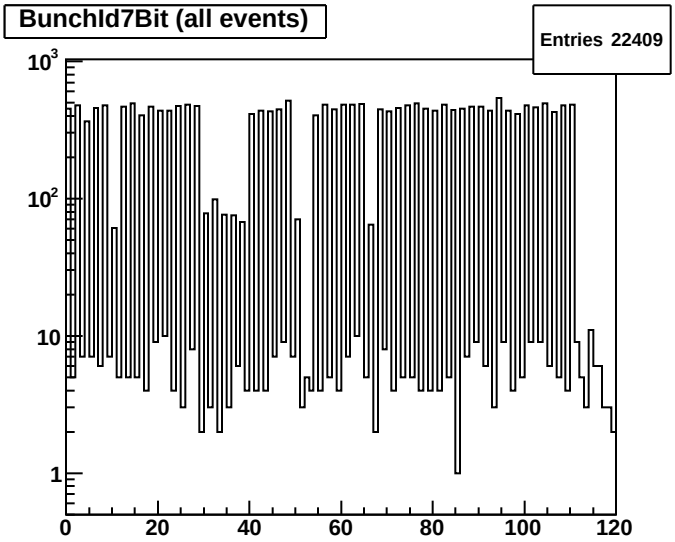

Last DSM Ch3 Input
Entries 358544
LD301 Ch3
6222 3499 852 363 766 417 3734 902 2433 646 1240 245 80 51 29 6541
16187 18910 21557 22046 21643 21992 18675 21507 19976 21763 21160 22164 22329 22358 22380 15868
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

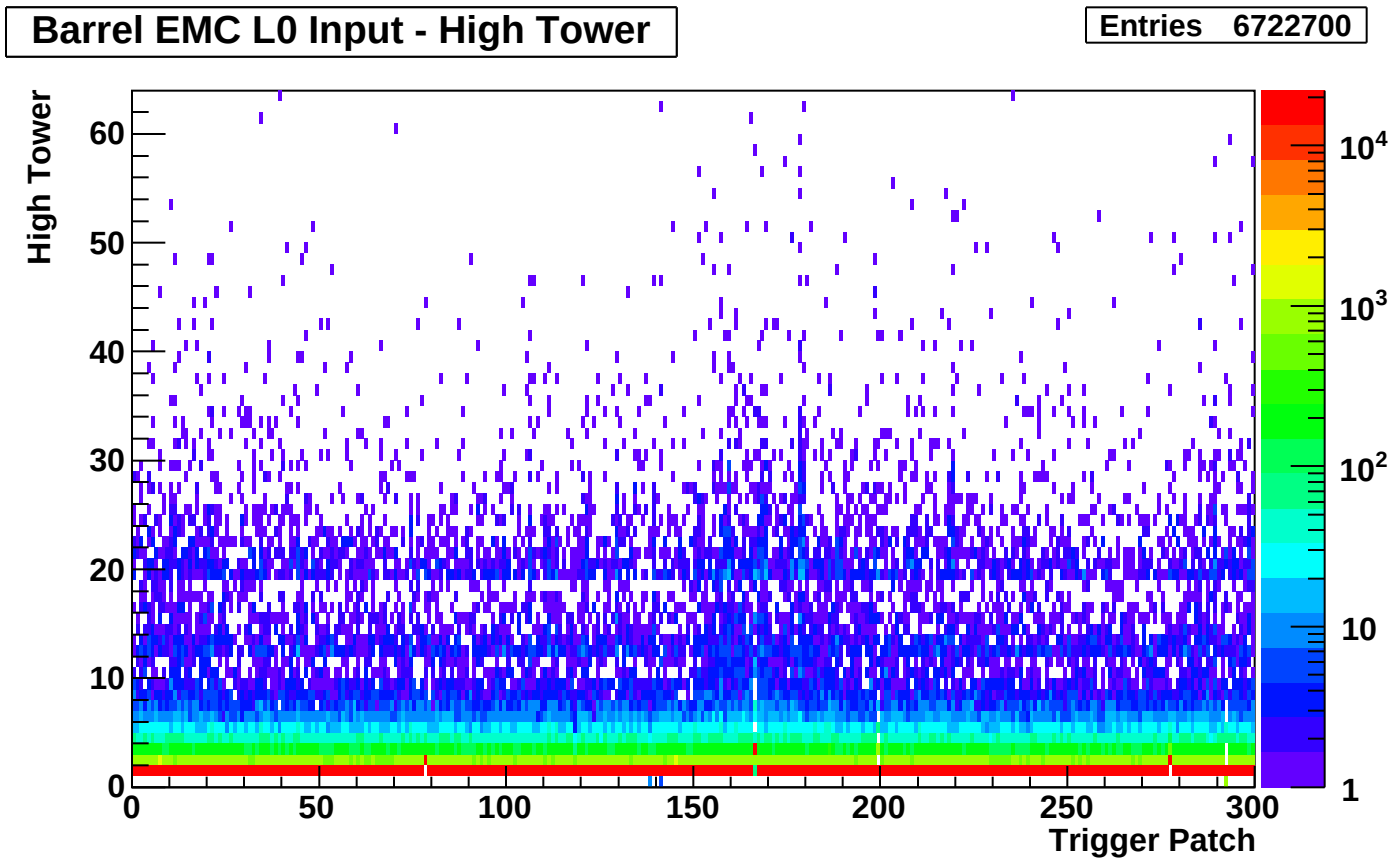

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

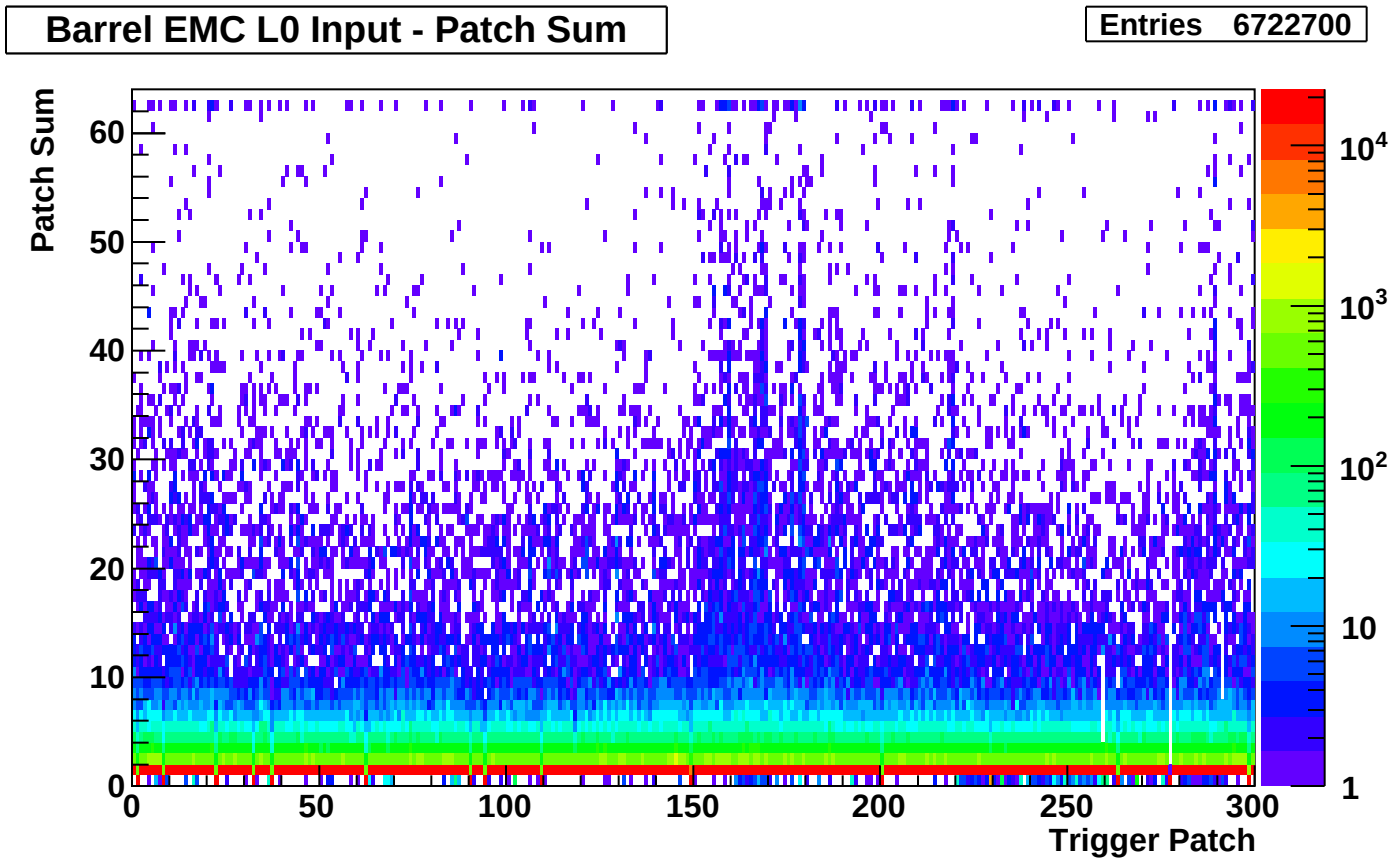

TCU bits
Entries 358544
TCU bits
22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409 22409
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

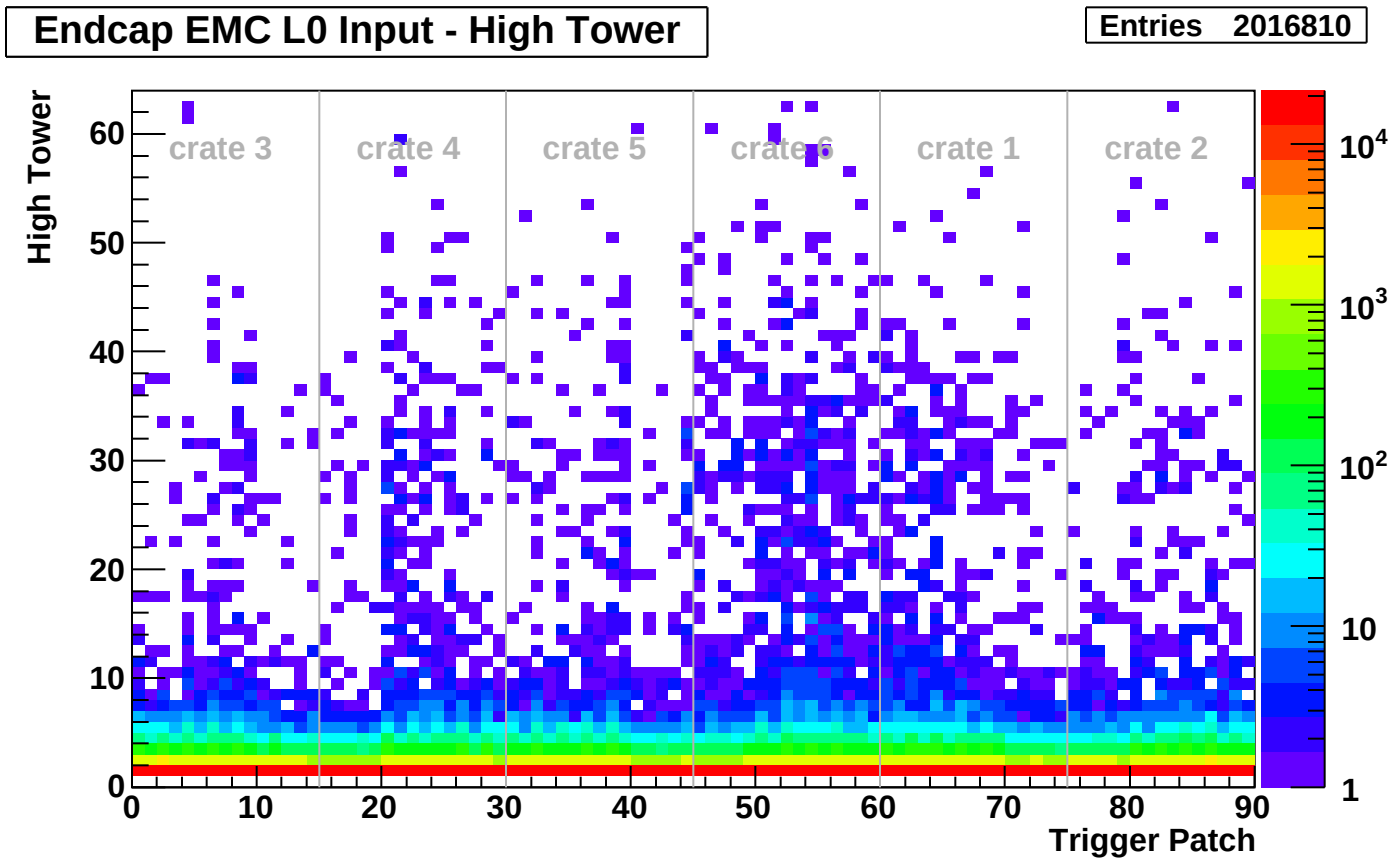

TCU bits
Entries 26482
TCU bits - Simulated
3575 3499 10660 766 945 136 6768 133
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

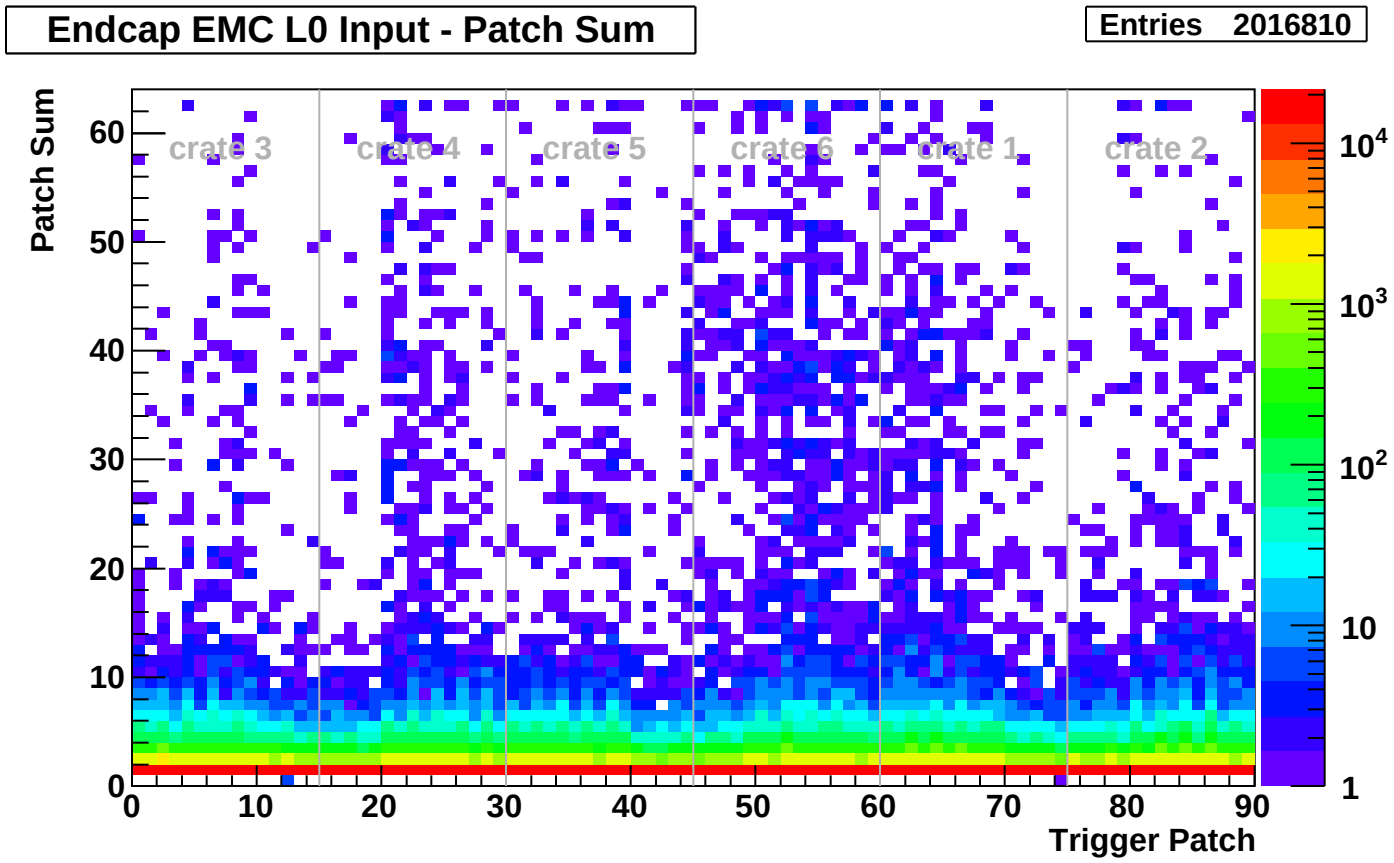

BunchId7Bit (BHT3)
Entries 363

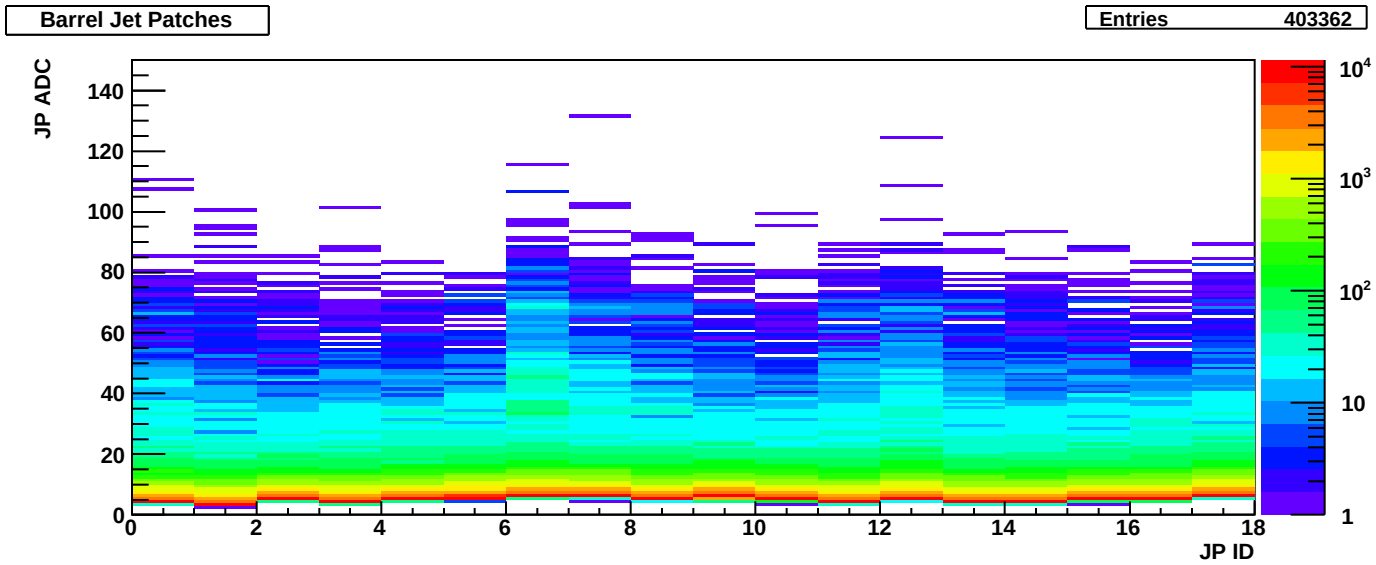

BunchId7Bit (all events)
Entries 22409

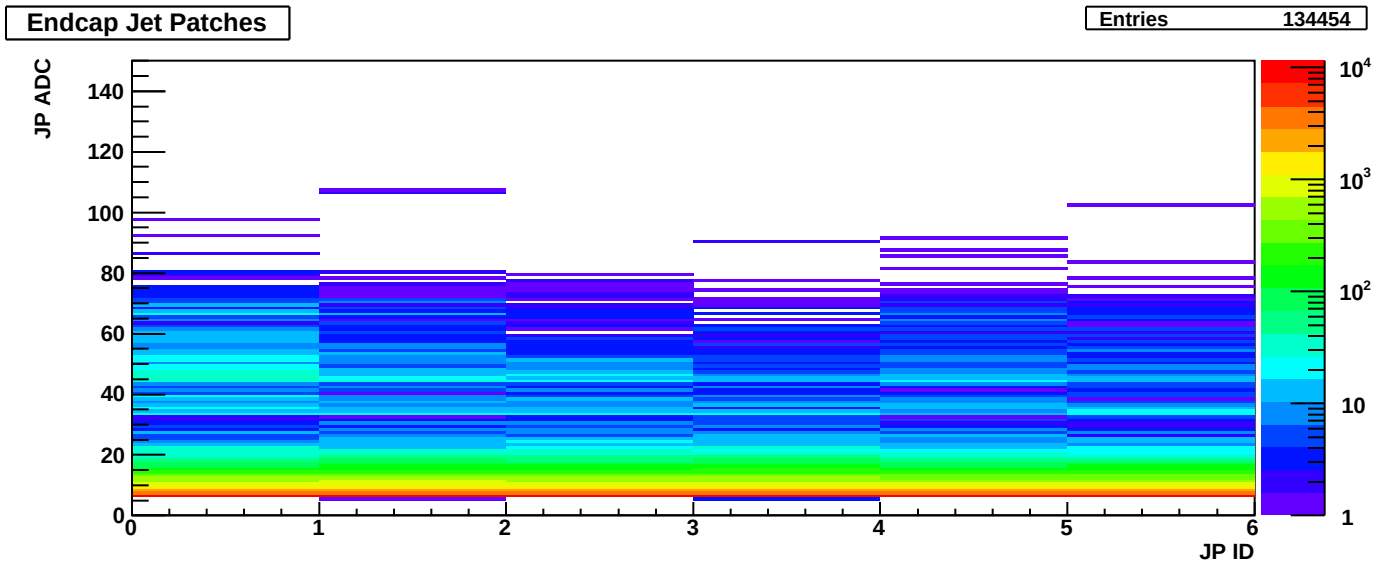

Barrel EMC L0 Input - High Tower
Entries 6722700
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

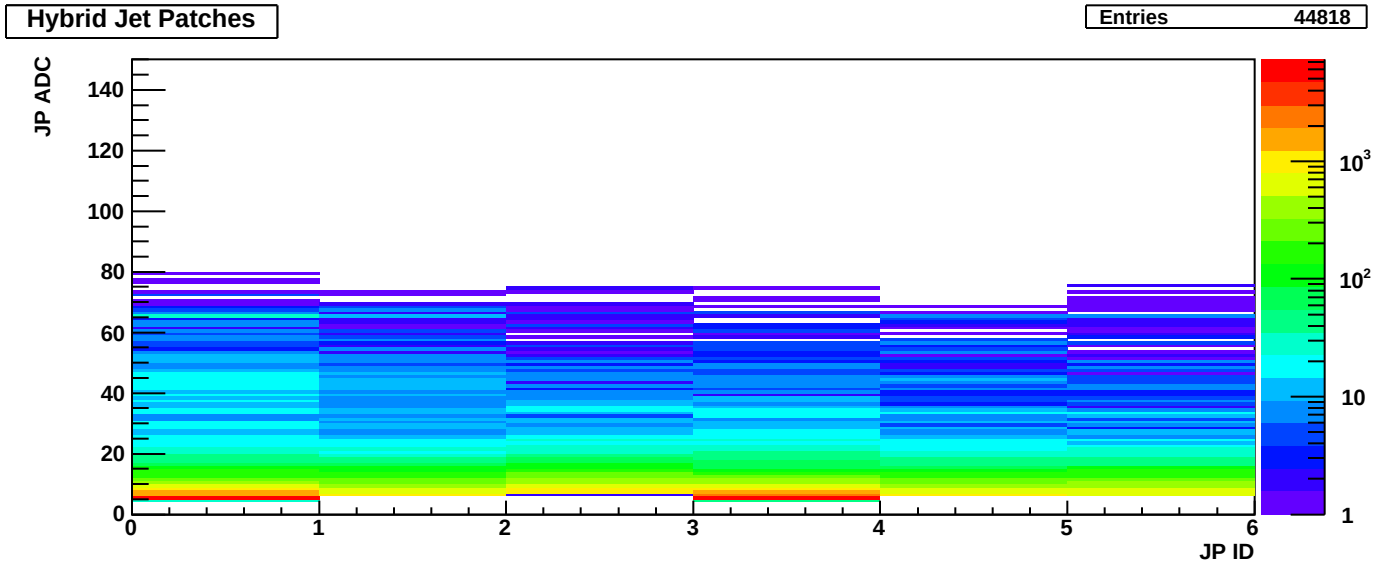

Barrel EMC L0 Input - Patch Sum
Entries 6722700
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

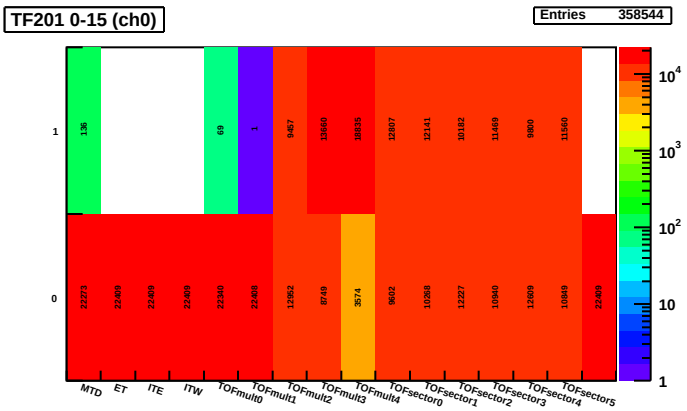

Endcap EMC L0 Input - High Tower
Entries 2016810
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

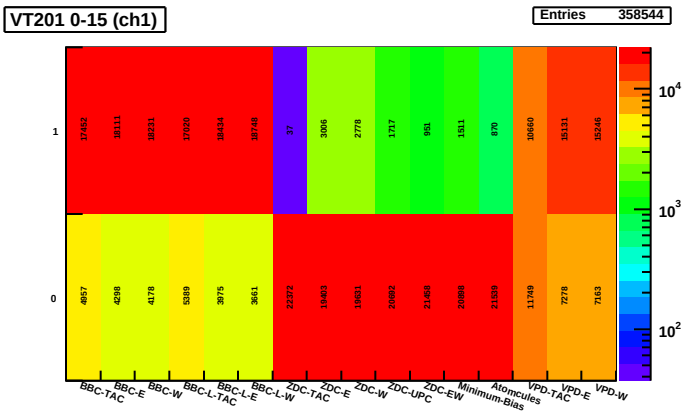

Endcap EMC L0 Input - Patch Sum
Entries 2016810
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

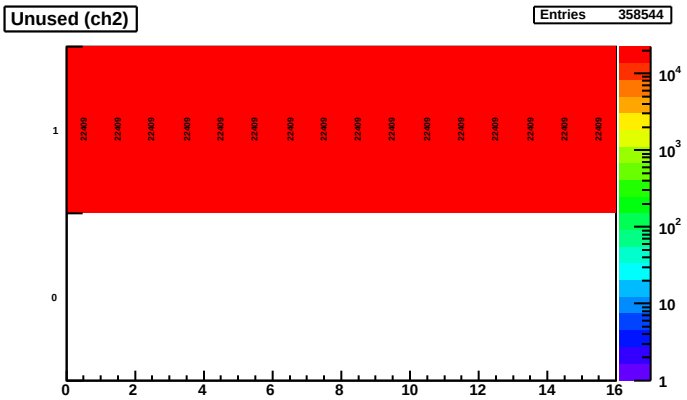

Barrel Jet Patches
Entries 403362
JP ADC
JP ID

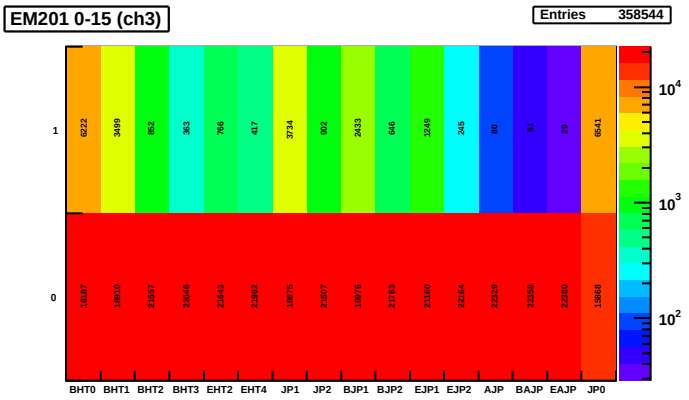

Endcap Jet Patches
Entries 134454
JP ADC
JP ID

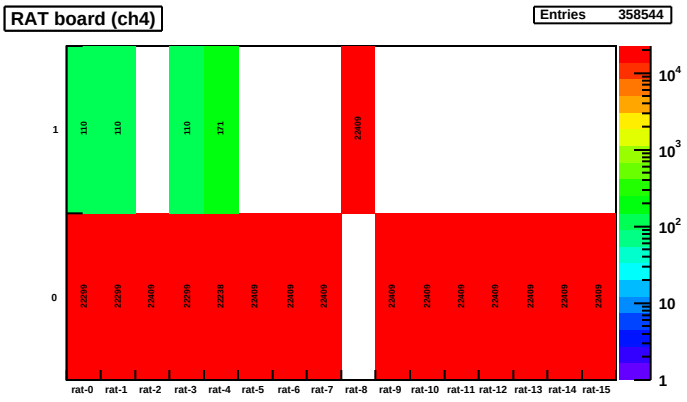

Hybrid Jet Patches
Entries 44818
JP ADC
JP ID

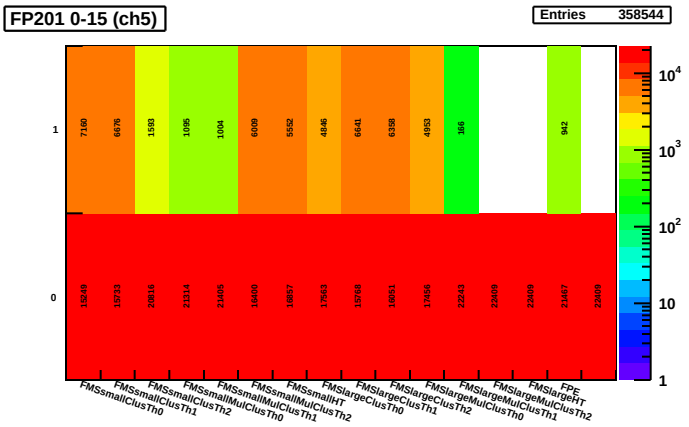

TF201 0-15 (ch0)
Entries 358544
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

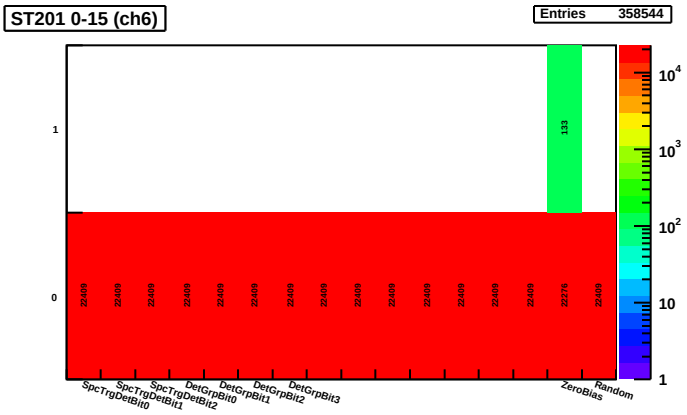

VT201 0-15 (ch1)
Entries 358544
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

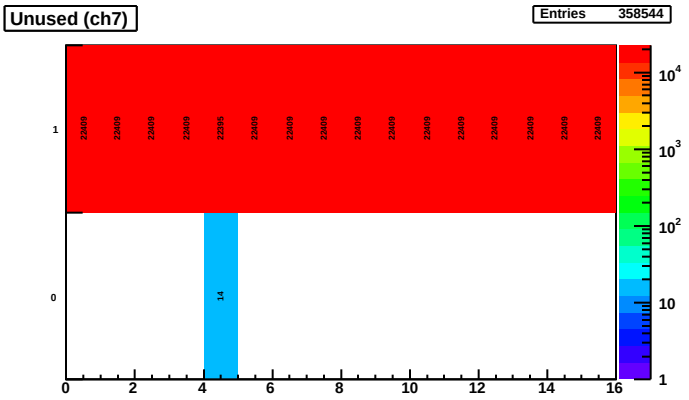

Unused (ch2)
Entries 358544

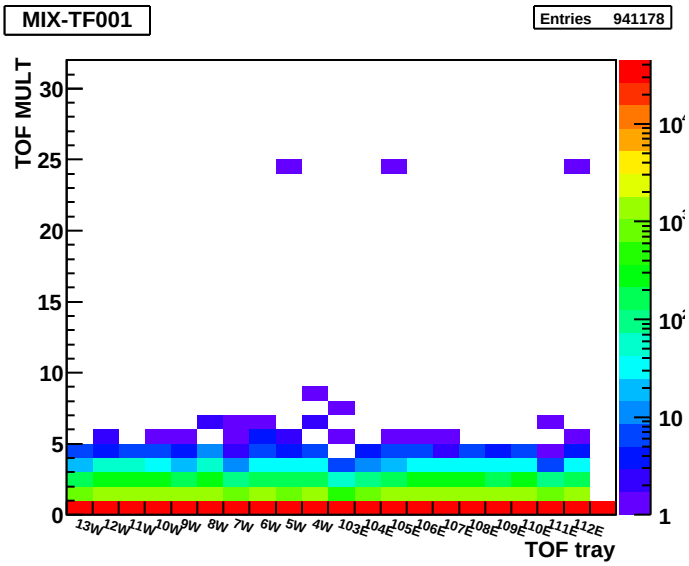

EM201 0-15 (ch3)
Entries 358544
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

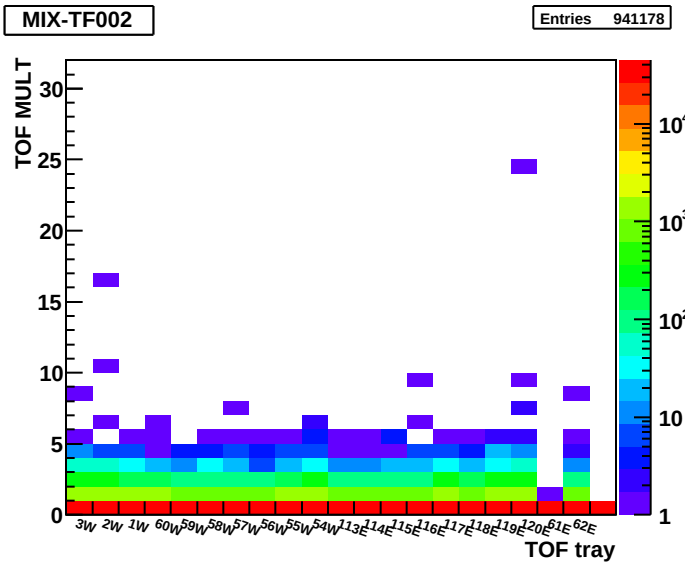

RAT board (ch4)
Entries 358544
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

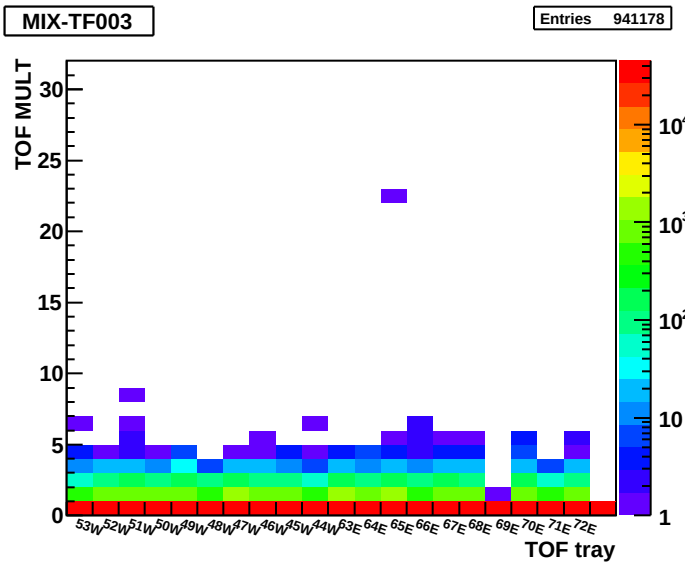

FP201 0-15 (ch5)
Entries 358544
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

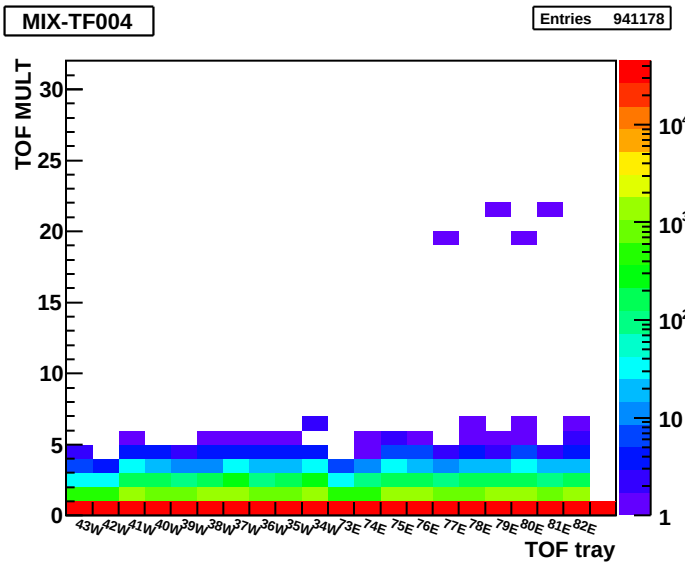

ST201 0-15 (ch6)
Entries 358544
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

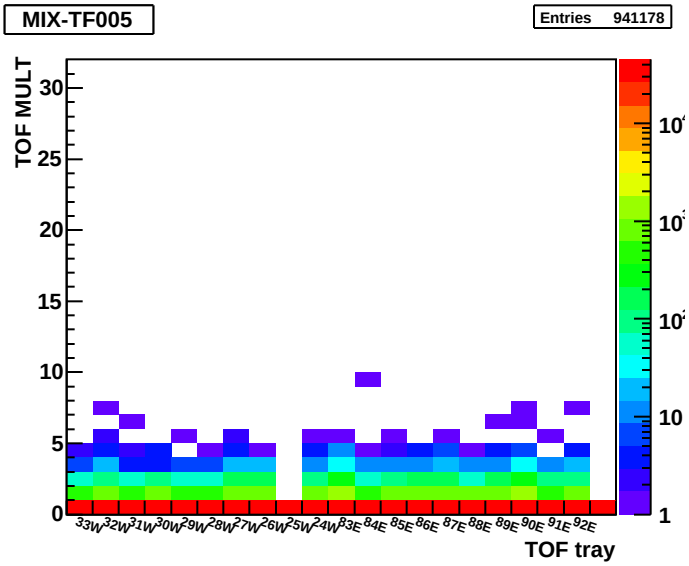

Unused (ch7)
Entries 358544

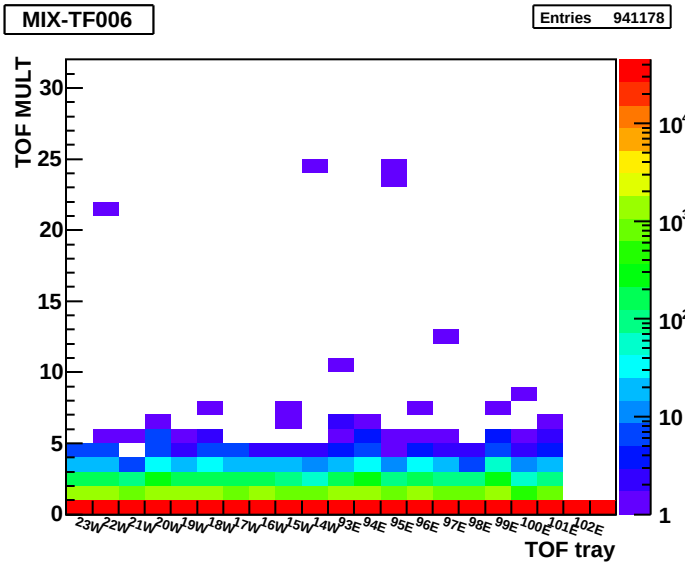

MIX-TF001
Entries 941178
TOF MULT
TOF tray

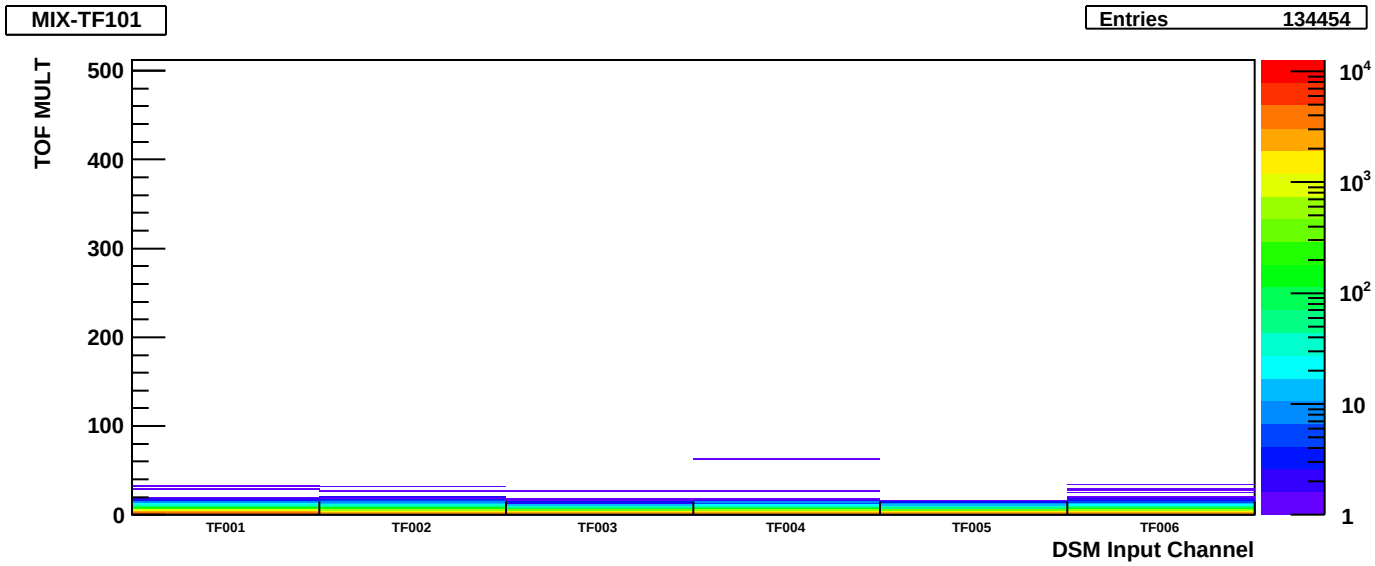

MIX-TF002
Entries 941178
TOF MULT
TOF tray

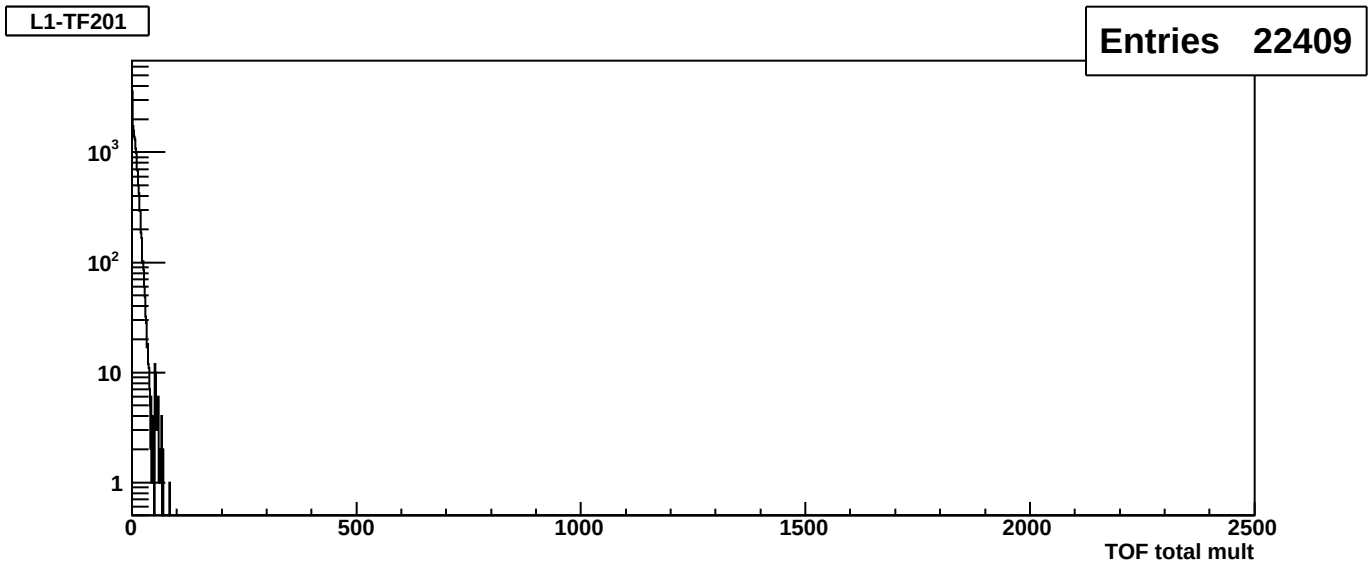

MIX-TF003
Entries 941178
TOF MULT
TOF tray

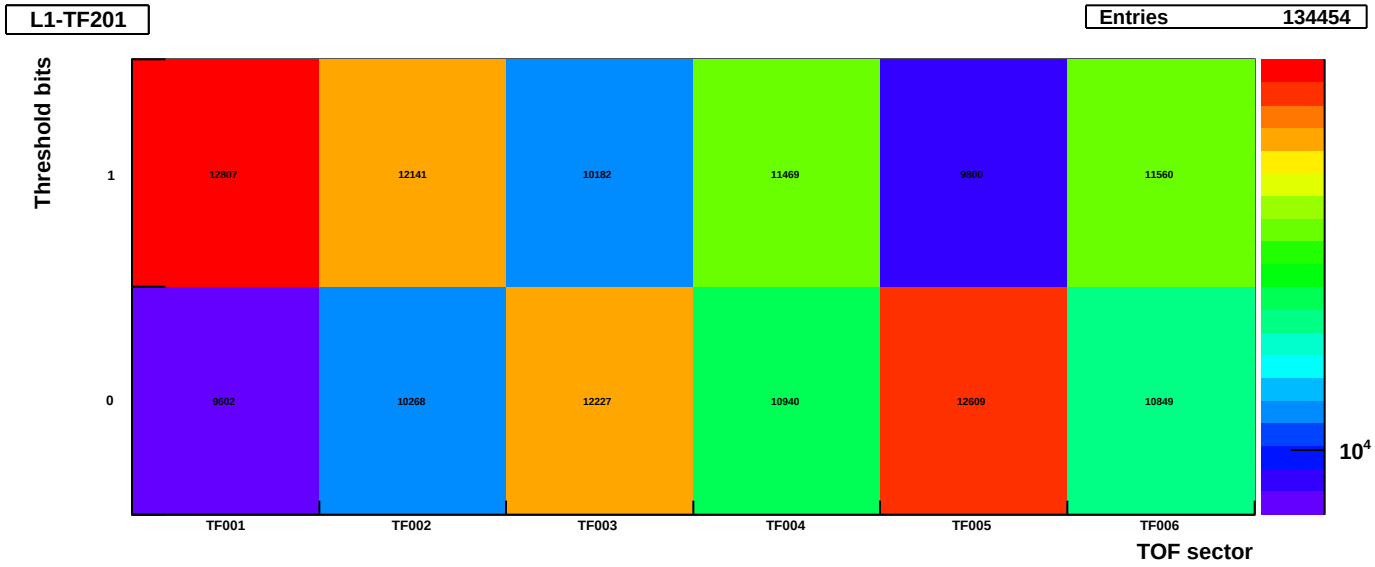

MIX-TF004
Entries 941178
TOF MULT
TOF tray

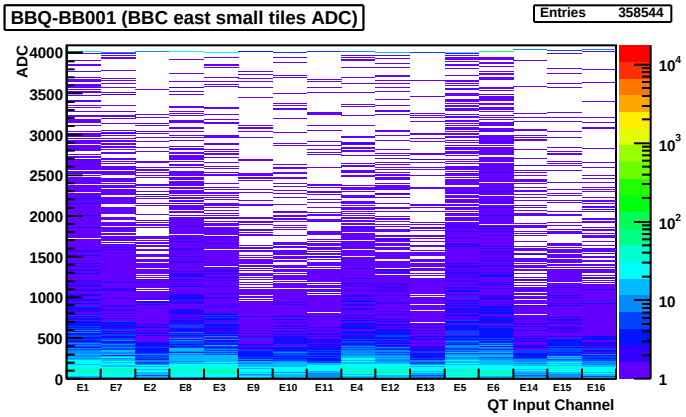

MIX-TF005
Entries 941178
TOF MULT
TOF tray

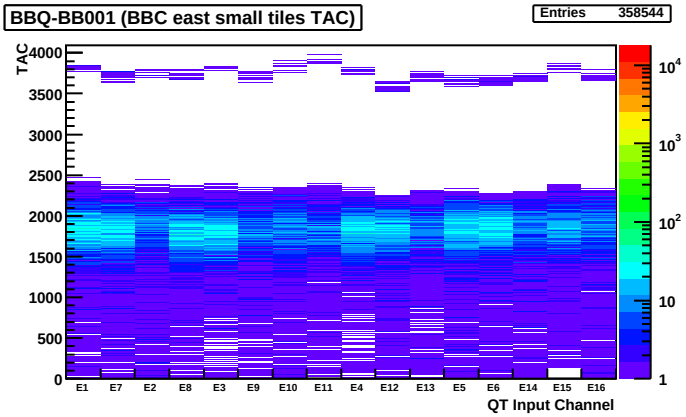

MIX-TF006
Entries 941178
TOF MULT
TOF tray

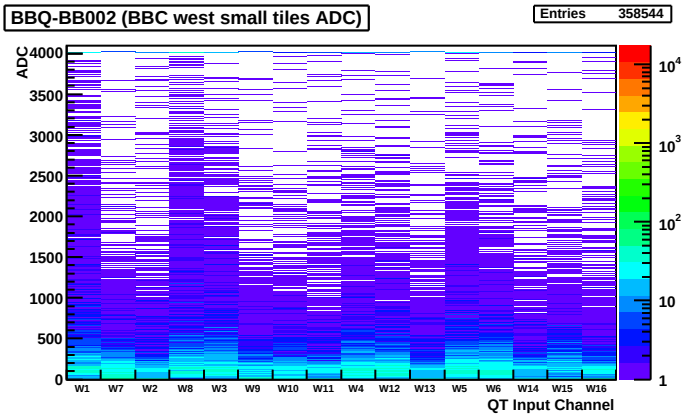

MIX-TF101
Entries 134454
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

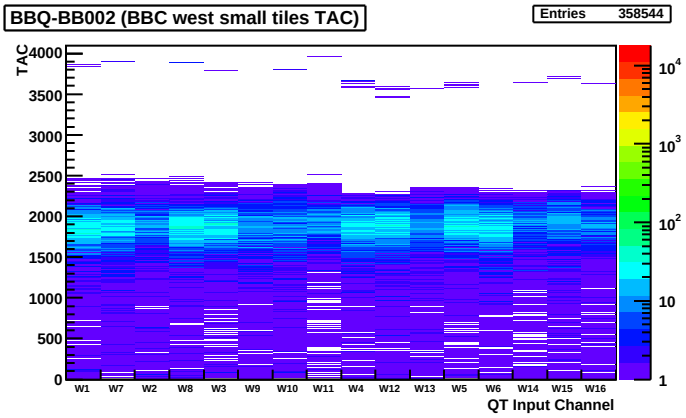

L1-TF201
Entries 22409
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

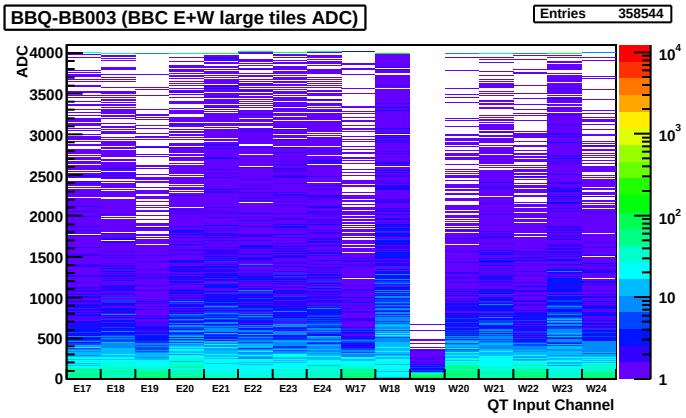

L1-TF201
Entries 134454
Threshold bits
1
0
12807
12141
10182
11469
9800
11560
9602
10268
12227
10940
12609
10849
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$

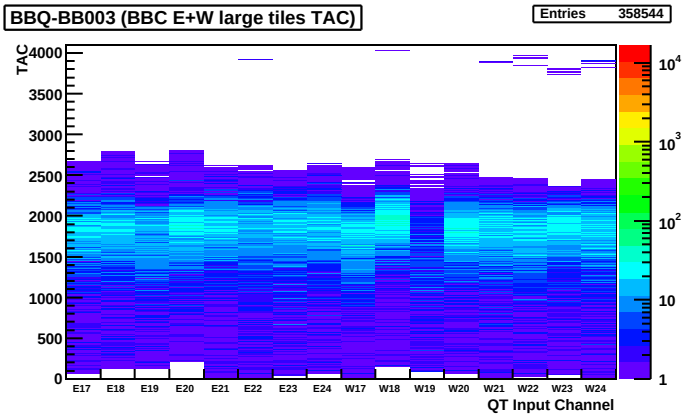

BBQ-BB001 (BBC east small tiles ADC)
Entries 358544
ADC
QT Input Channel

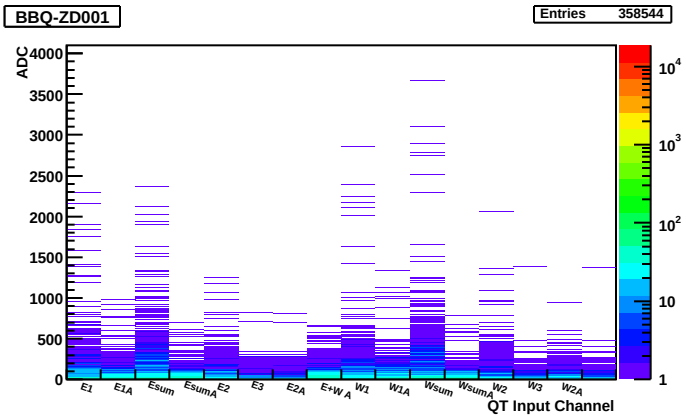

BBQ-BB001 (BBC east small tiles TAC)
Entries 358544
TAC
QT Input Channel

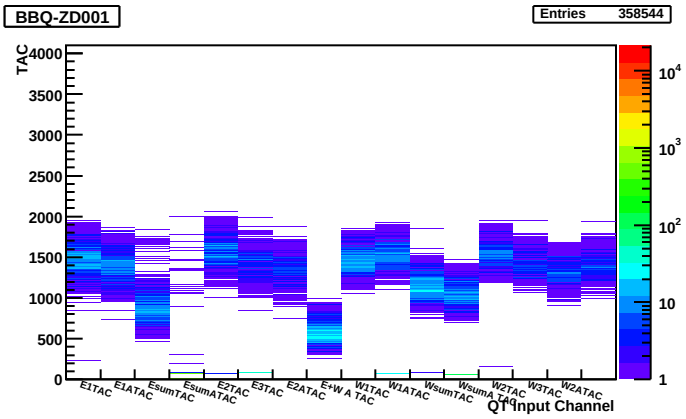

BBQ-BB002 (BBC west small tiles ADC)
Entries 358544
ADC
QT Input Channel

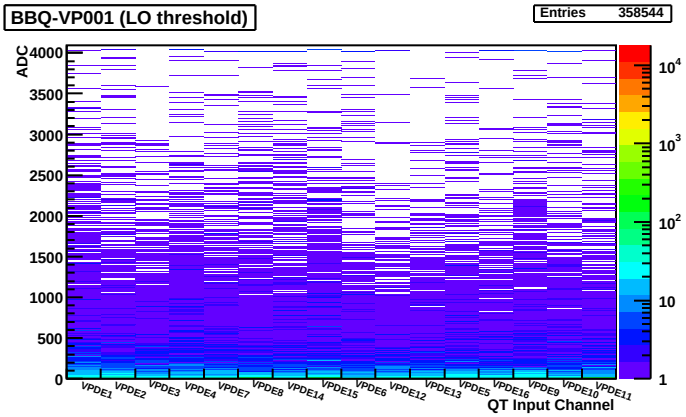

BBQ-BB002 (BBC west small tiles TAC)
Entries 358544
TAC
QT Input Channel

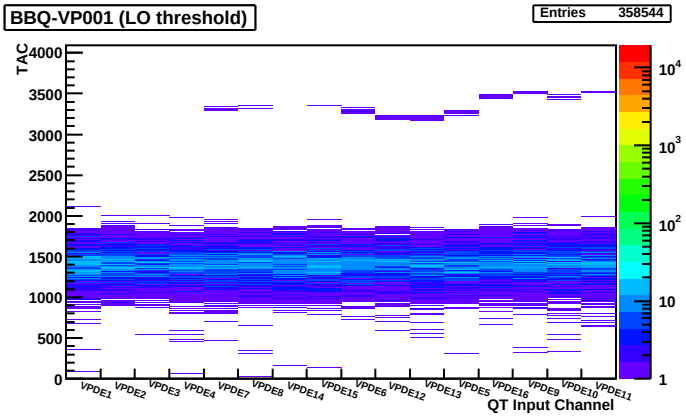

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 358544
ADC
QT Input Channel

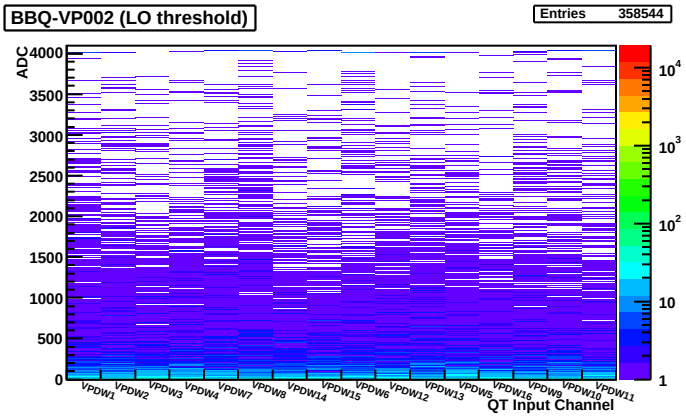

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 358544
TAC
QT Input Channel

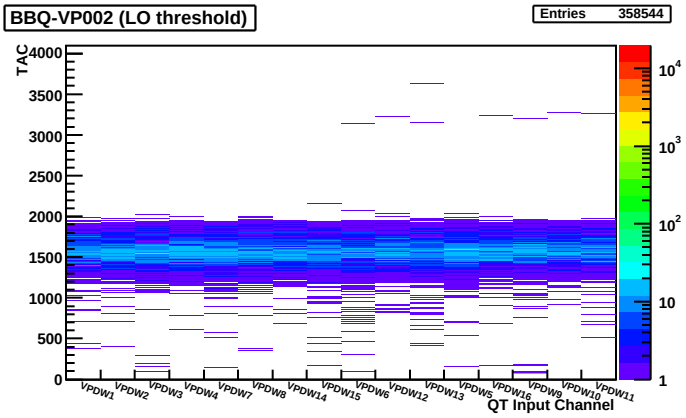

BBQ-ZD001
Entries 358544
ADC
QT Input Channel

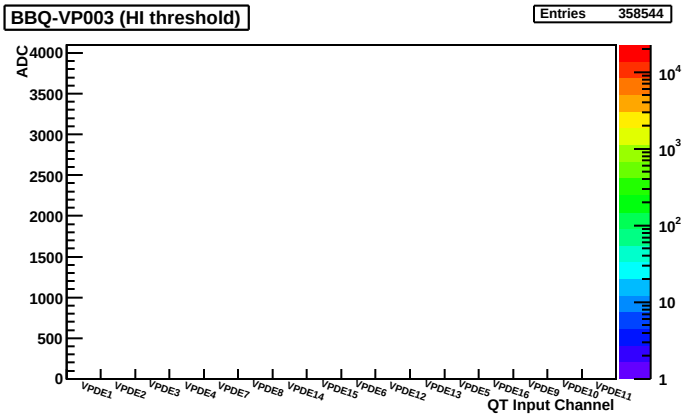

BBQ-ZD001
Entries 358544
TAC
QT Input Channel

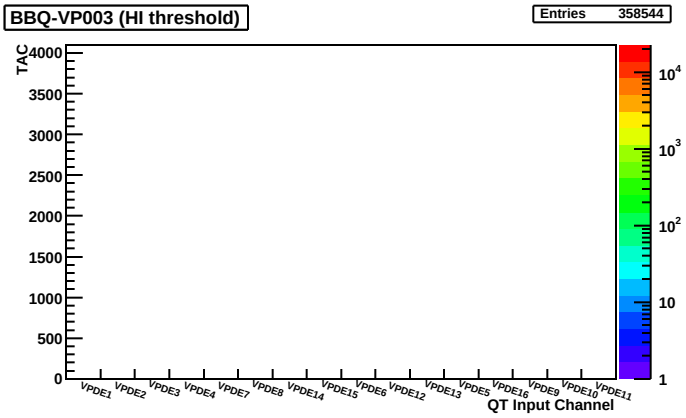

BBQ-VP001 (LO threshold)
Entries 358544
ADC
QT Input Channel

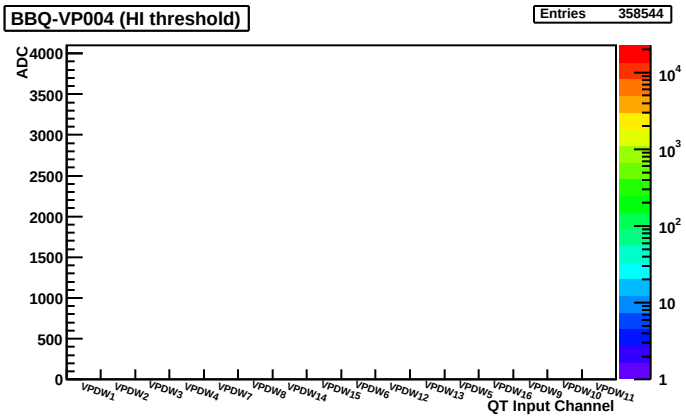

BBQ-VP001 (LO threshold)
Entries 358544
TAC
QT Input Channel

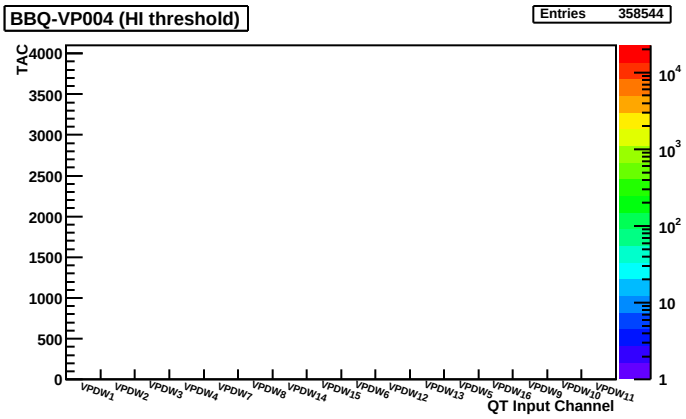

BBQ-VP002 (LO threshold)
Entries 358544
ADC
QT Input Channel

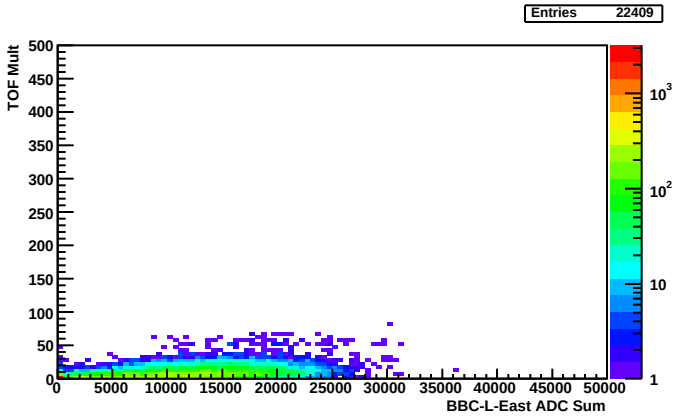

BBQ-VP002 (LO threshold)
Entries 358544
TAC
QT Input Channel

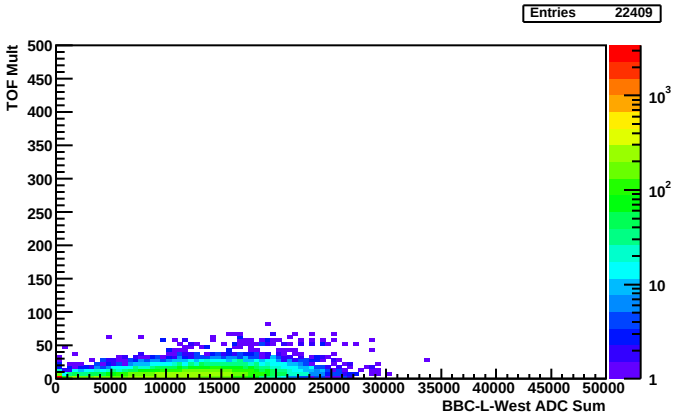

BBQ-VP003 (HI threshold)
Entries 358544
ADC
QT Input Channel

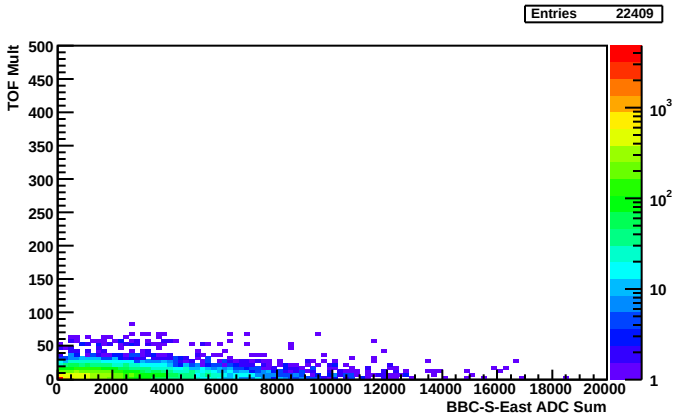

BBQ-VP003 (HI threshold)
Entries 358544
TAC
QT Input Channel

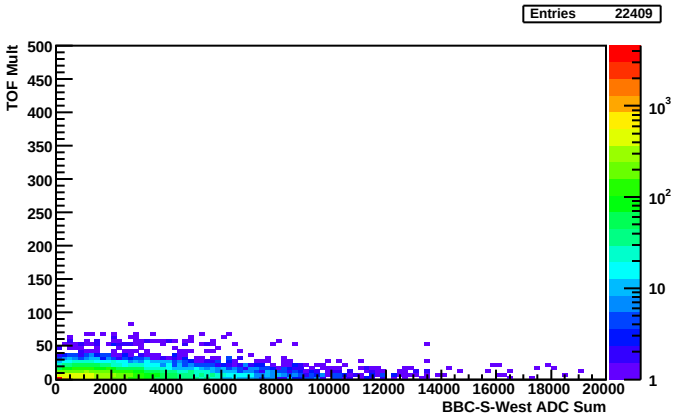

BBQ-VP004 (HI threshold)
Entries 358544
ADC
QT Input Channel

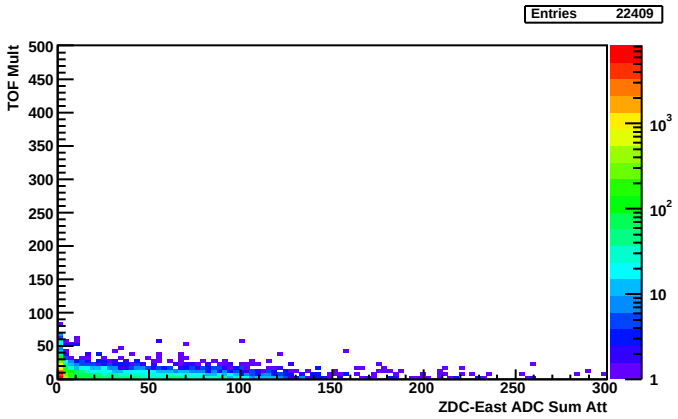

BBQ-VP004 (HI threshold)
Entries 358544
TAC
QT Input Channel

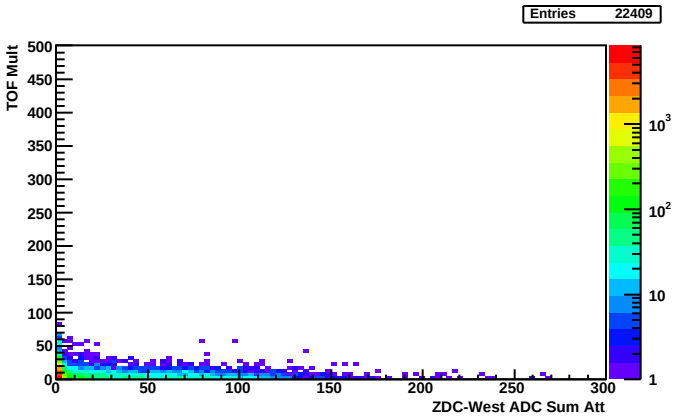

Entries 22409
TOF Mult
BBC-L-East ADC Sum

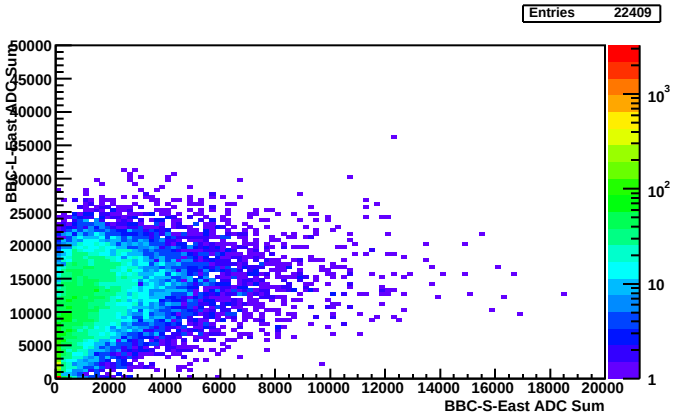

Entries 22409
TOF Mult
BBC-L-West ADC Sum

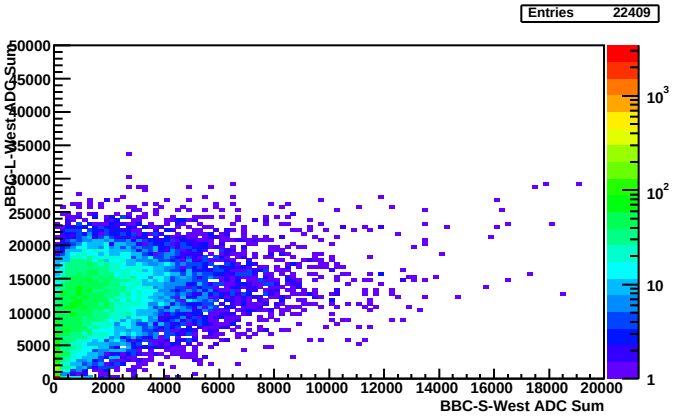

Entries 22409
TOF Mult
BBC-S-East ADC Sum

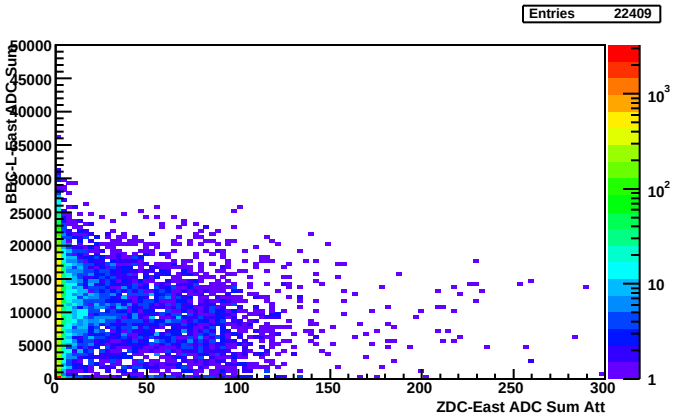

Entries 22409
TOF Mult
BBC-S-West ADC Sum

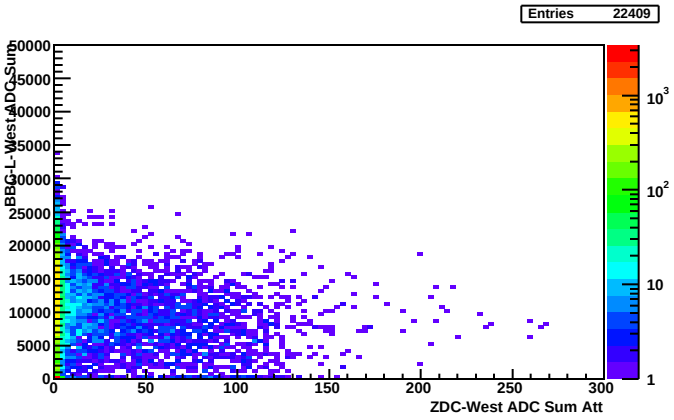

Entries 22409
TOF Mult
ZDC-East ADC Sum Att

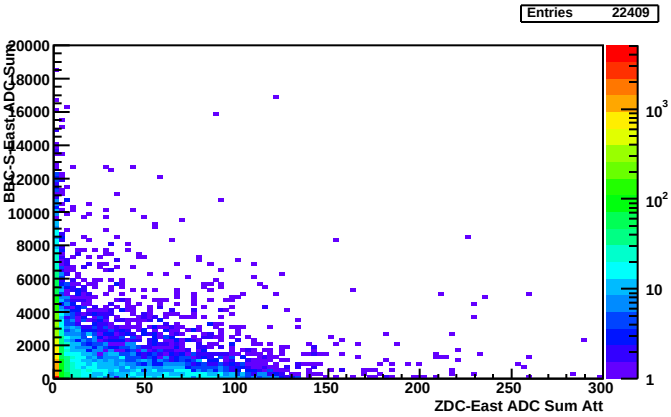

Entries 22409
TOF Mult
ZDC-West ADC Sum Att

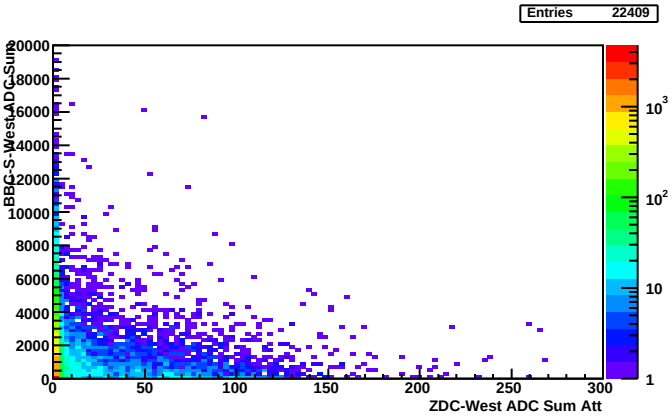

Entries 22409
BBC-L-East ADC Sum
BBC-S-East ADC Sum

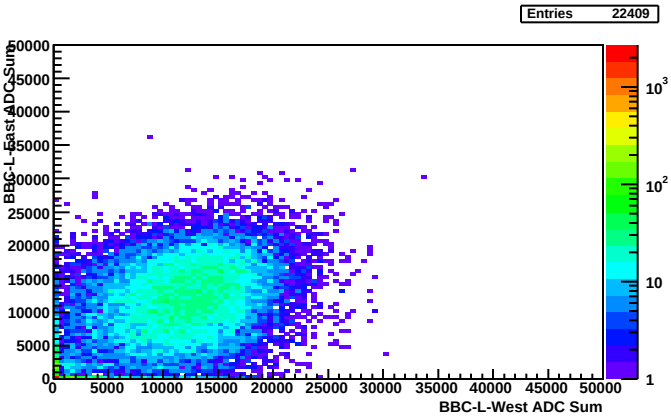

Entries 22409
BBC-L-West ADC Sum
BBC-S-West ADC Sum

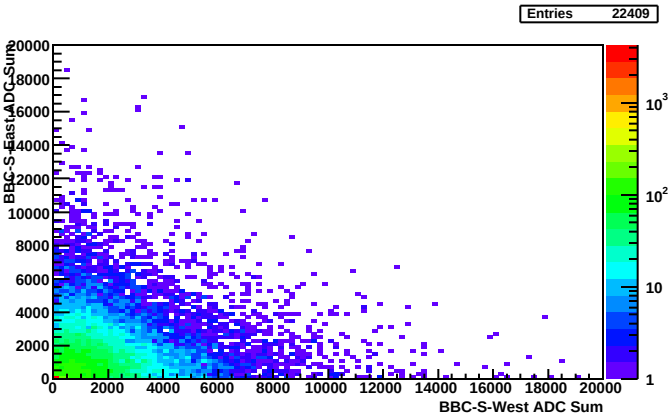

Entries 22409
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

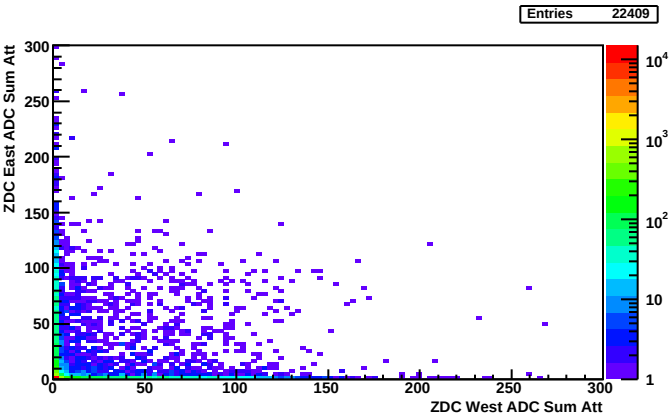

Entries 22409
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

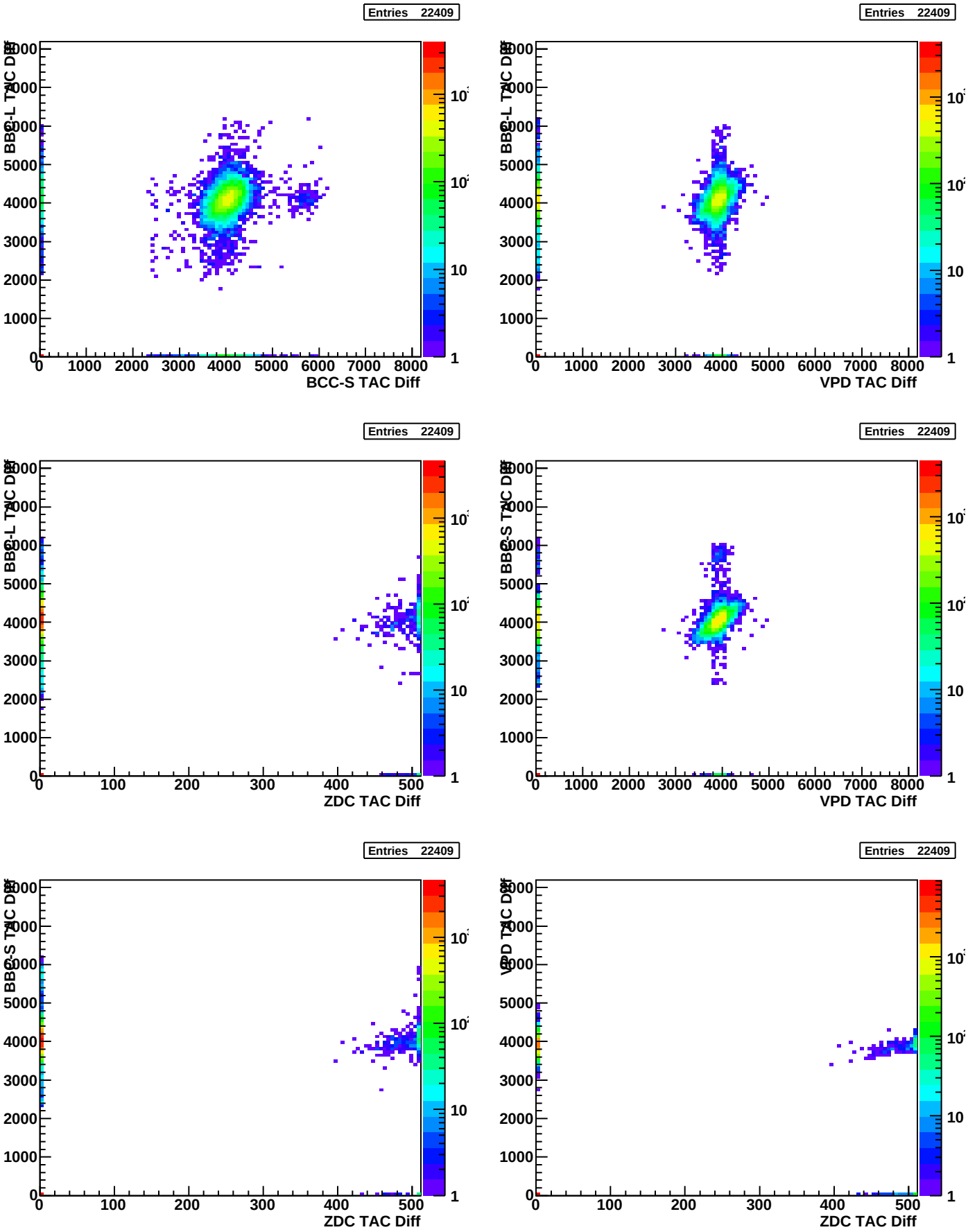

Entries 22409
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

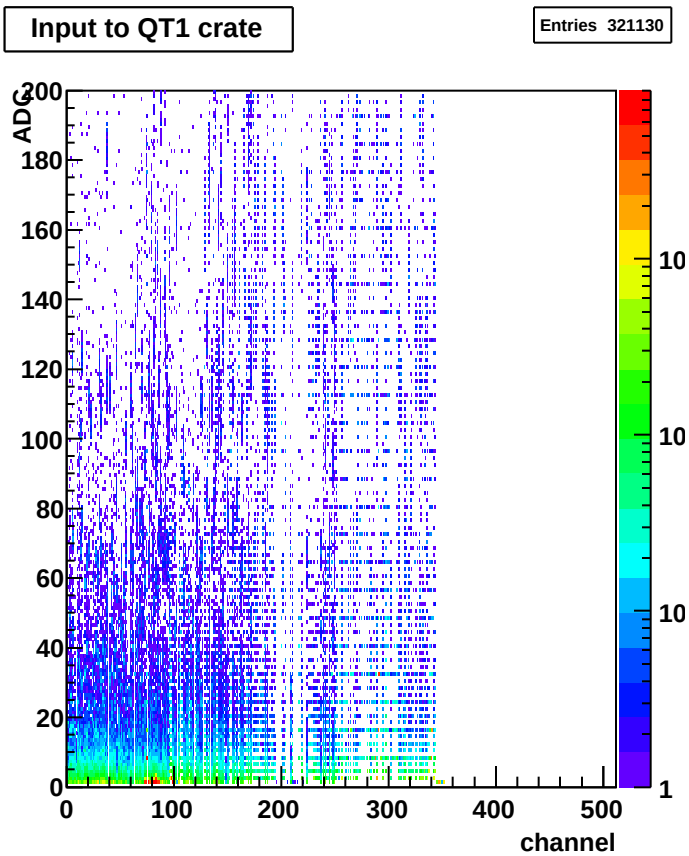

Entries 22409
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

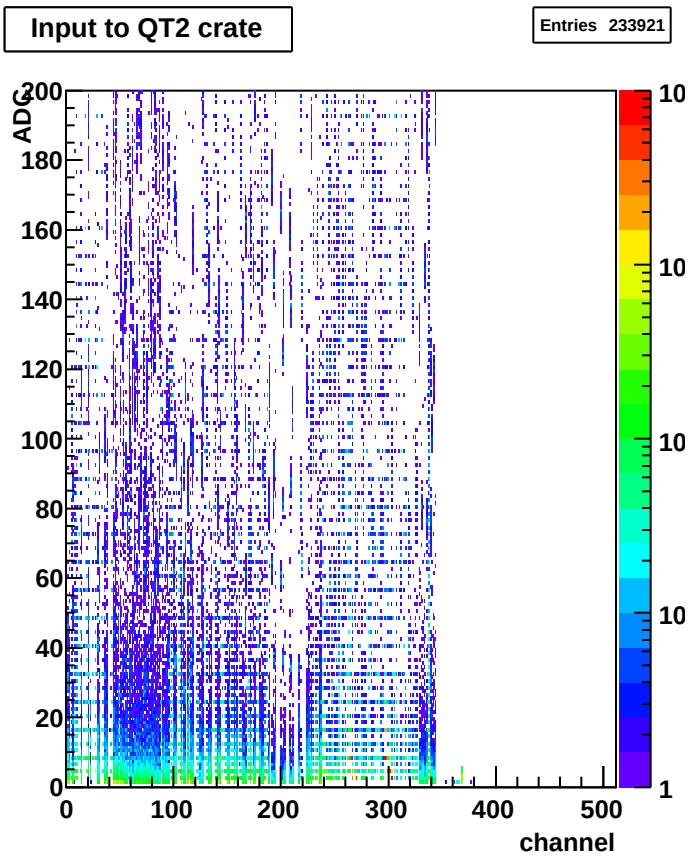

Entries 22409
BBC-L-East ADC Sum
BBC-L-West ADC Sum

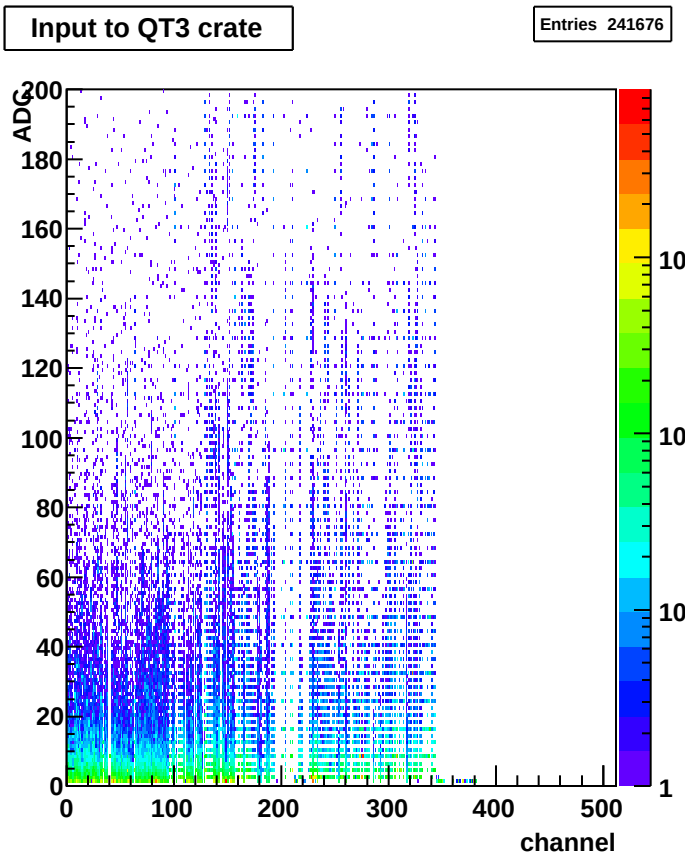

Entries 22409
BBC-S-East ADC Sum
BBC-S-West ADC Sum

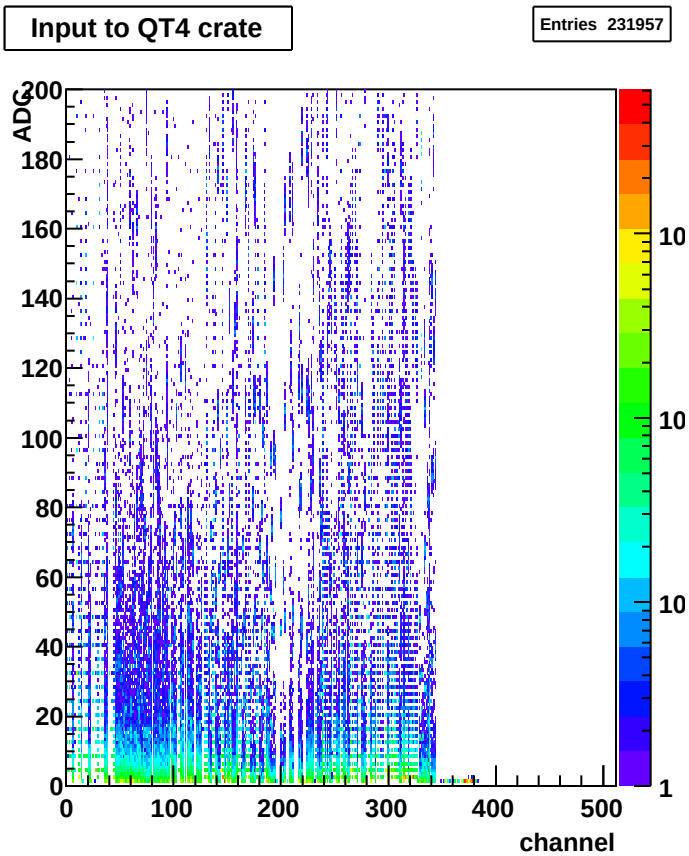

Entries 22409
ZDC East ADC Sum Att
ZDC West ADC Sum Att

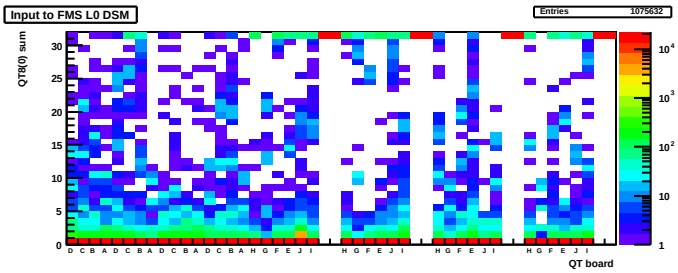

Entries 22409
BBC-L TAC Diff
BCC-S TAC Diff
Entries 22409
BBC-L TAC Diff
VPD TAC Diff
Entries 22409
BBC-L TAC Diff
ZDC TAC Diff
Entries 22409
BBC-S TAC Diff
VPD TAC Diff
Entries 22409
BBC-S TAC Diff
ZDC TAC Diff
Entries 22409
VPD TAC Diff
ZDC TAC Diff

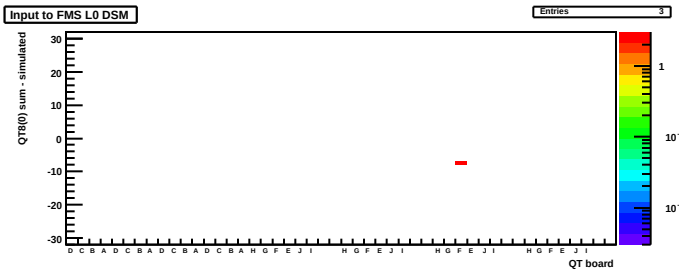

Input to QT1 crate
Entries 321130
ADC
channel

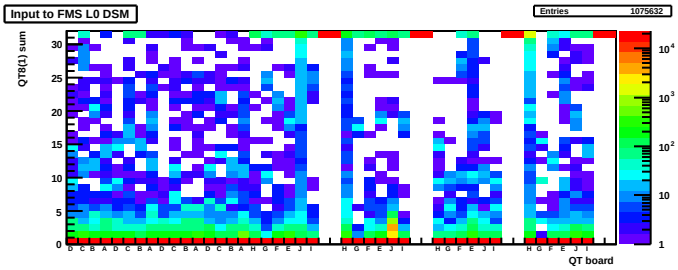

Input to QT2 crate
Entries 233921
ADC
channel

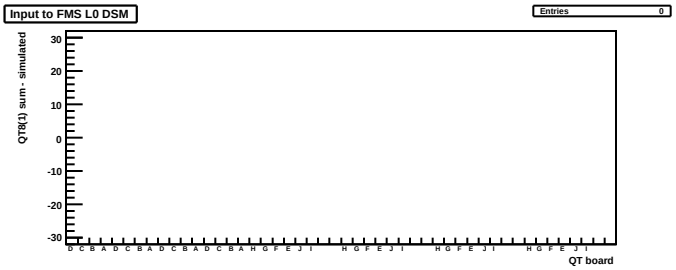

Input to QT3 crate
Entries 241676
ADC
channel

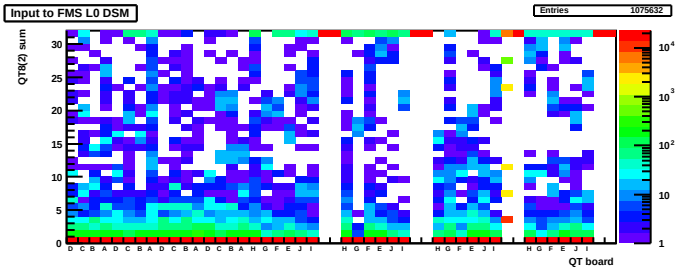

Input to QT4 crate
Entries 231957
ADC
channel

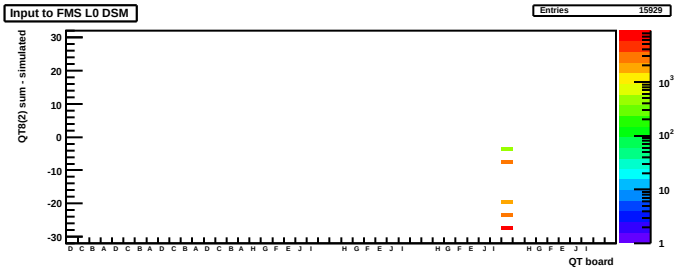

Input to FMS L0 DSM
Entries 1075632
QT8(0) sum
QT board

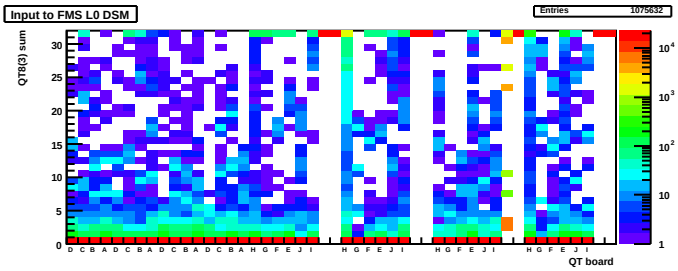

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

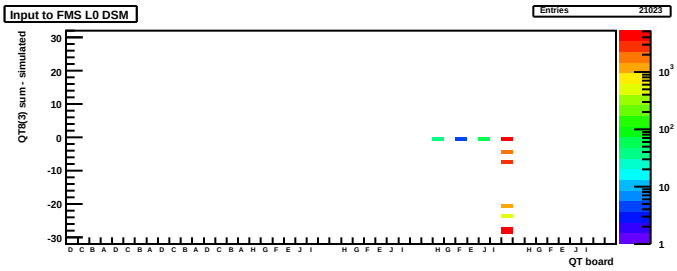

Input to FMS L0 DSM
Entries 1075632
QT8(1) sum
QT board

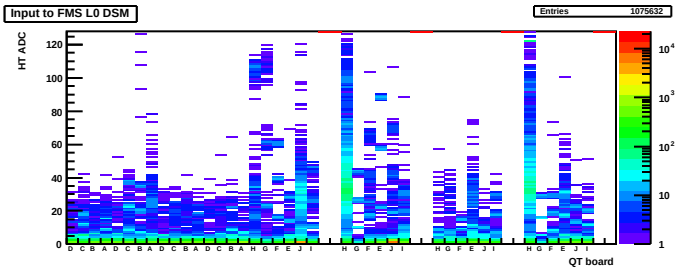

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

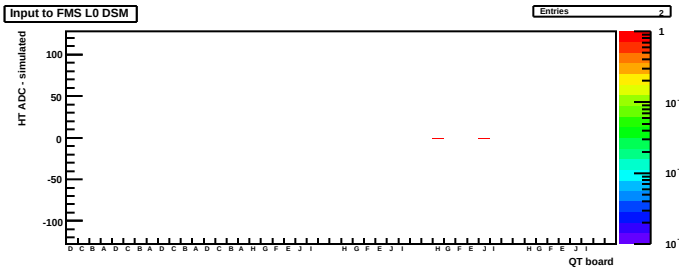

Input to FMS L0 DSM
Entries 1075632
QT8(2) sum
QT board

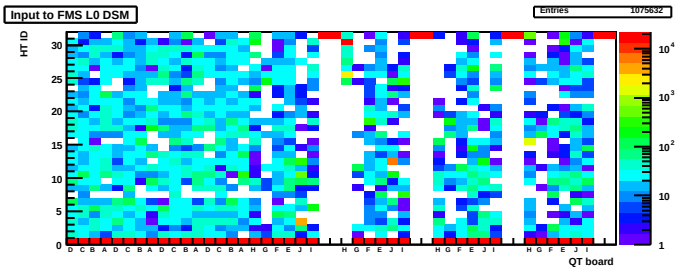

Input to FMS L0 DSM
Entries 15929
QT8(2) sum - simulated
QT board

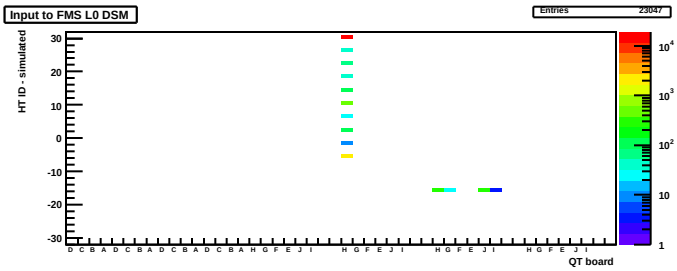

Input to FMS L0 DSM
Entries 1075632
QT8(3) sum
QT board

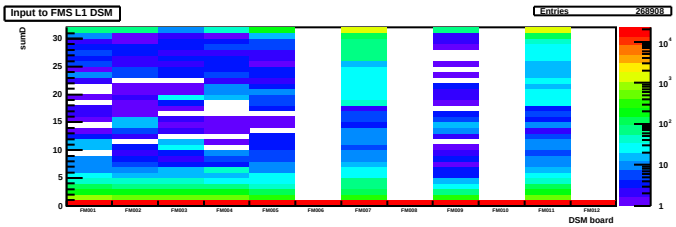

Input to FMS L0 DSM
Entries 21023
QT8(3) sum - simulated
QT board

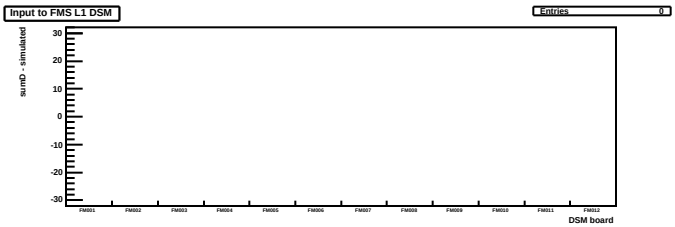

Input to FMS L0 DSM
Entries 1075632
HT ADC
QT board

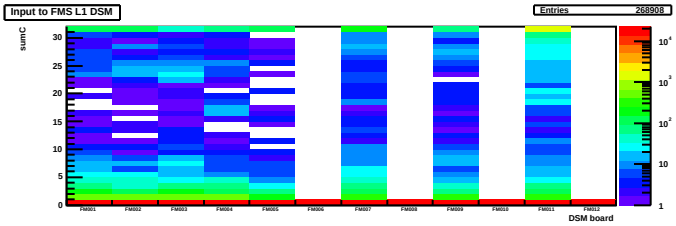

Input to FMS L0 DSM
Entries 2
HT ADC - simulated
QT board

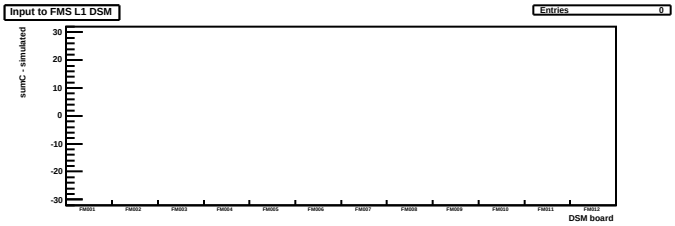

Input to FMS L0 DSM
Entries 1075632
HT ID
QT board

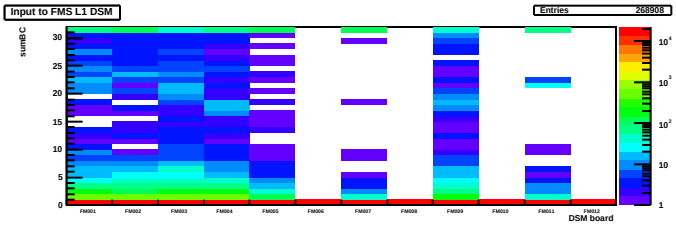

Input to FMS L0 DSM
Entries 23047
HT ID - simulated
QT board

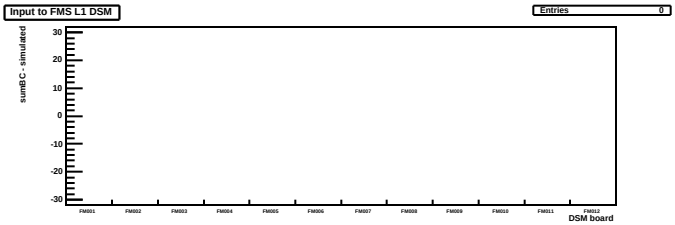

Input to FMS L1 DSM
Entries 268908
sumD
DSM board

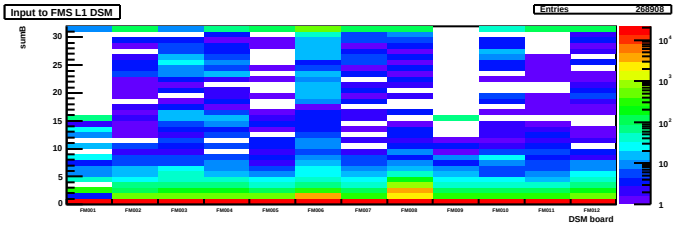

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

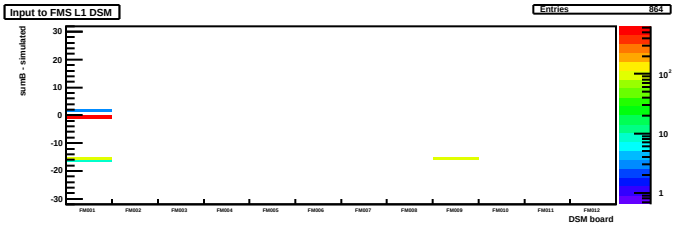

Input to FMS L1 DSM
Entries 268908
sumC
DSM board

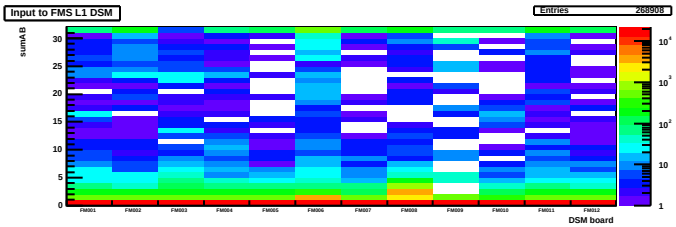

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

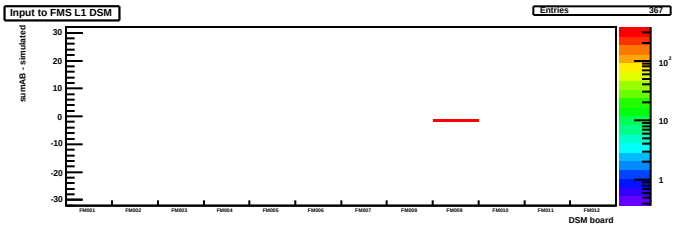

Input to FMS L1 DSM
Entries 268908
sumBC
DSM board

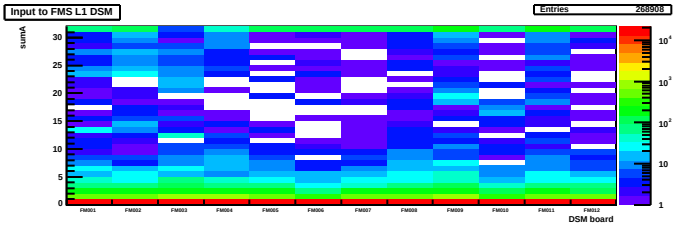

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

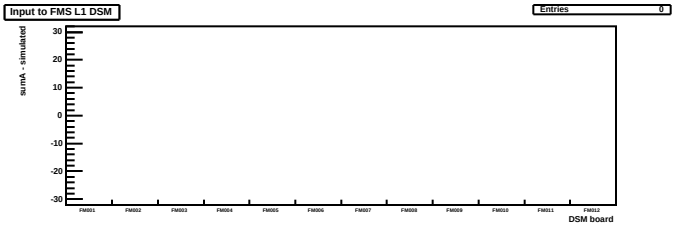

Input to FMS L1 DSM
Entries 268908
sumB
DSM board

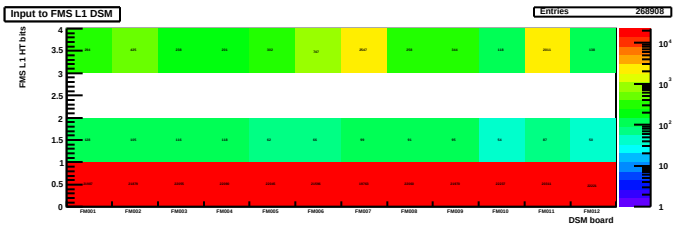

Input to FMS L1 DSM
Entries 864
sumB - simulated
DSM board

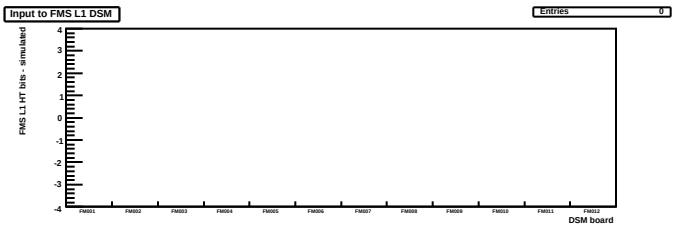

Input to FMS L1 DSM
Entries 268908
sumAB
DSM board

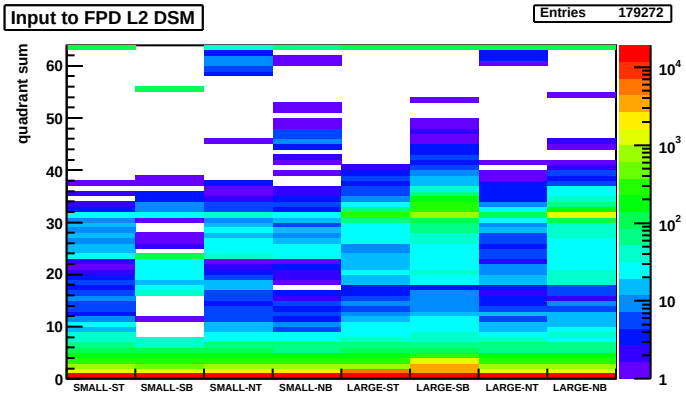

Input to FMS L1 DSM
Entries 367
sumAB - simulated
DSM board

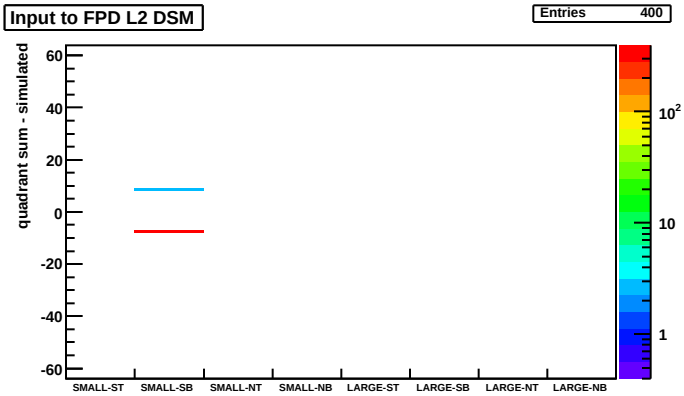

Input to FMS L1 DSM
Entries 268908
sumA
DSM board

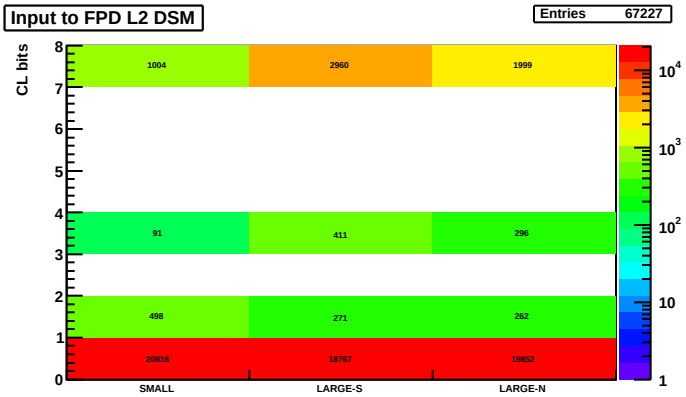

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

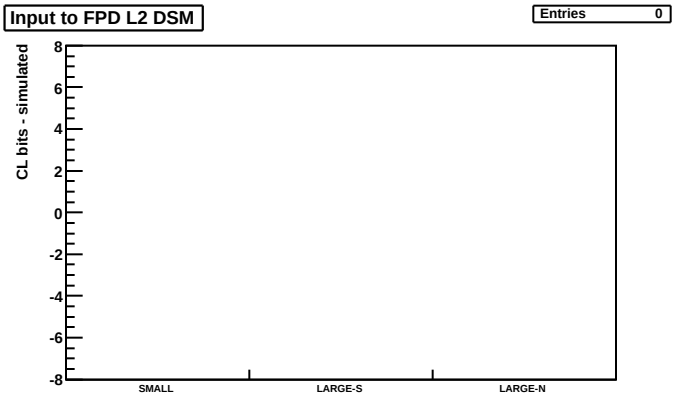

Input to FMS L1 DSM
Entries 268908
FMS L1 HT bits
DSM board

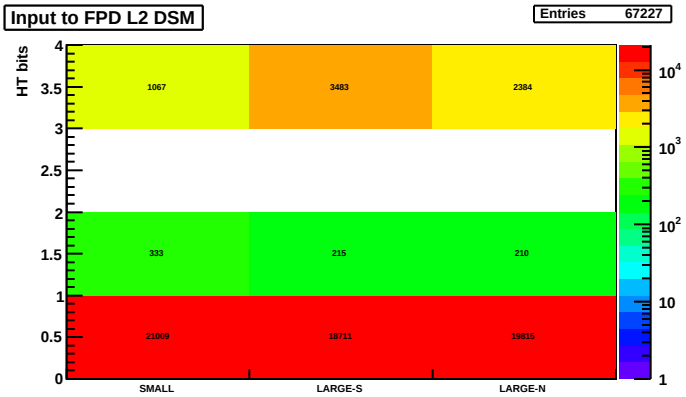

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

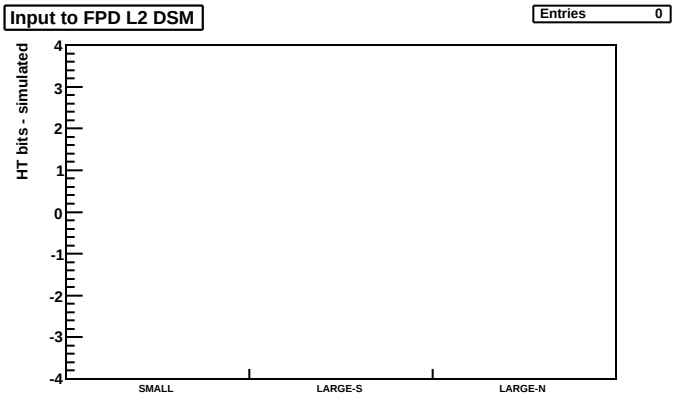

Input to FPD L2 DSM
Entries 179272
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

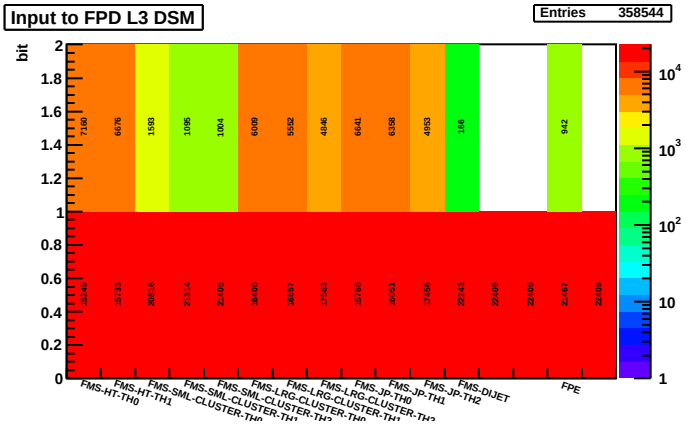

Input to FPD L2 DSM
Entries 400
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

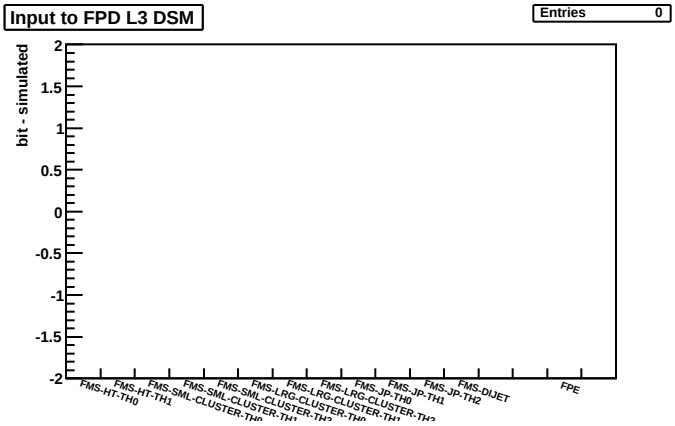

Input to FPD L2 DSM
Entries 67227
CL bits
1004
2960
1999
91
411
296
498
271
262
20816
18767
19852
SMALL
LARGE-S
LARGE-N

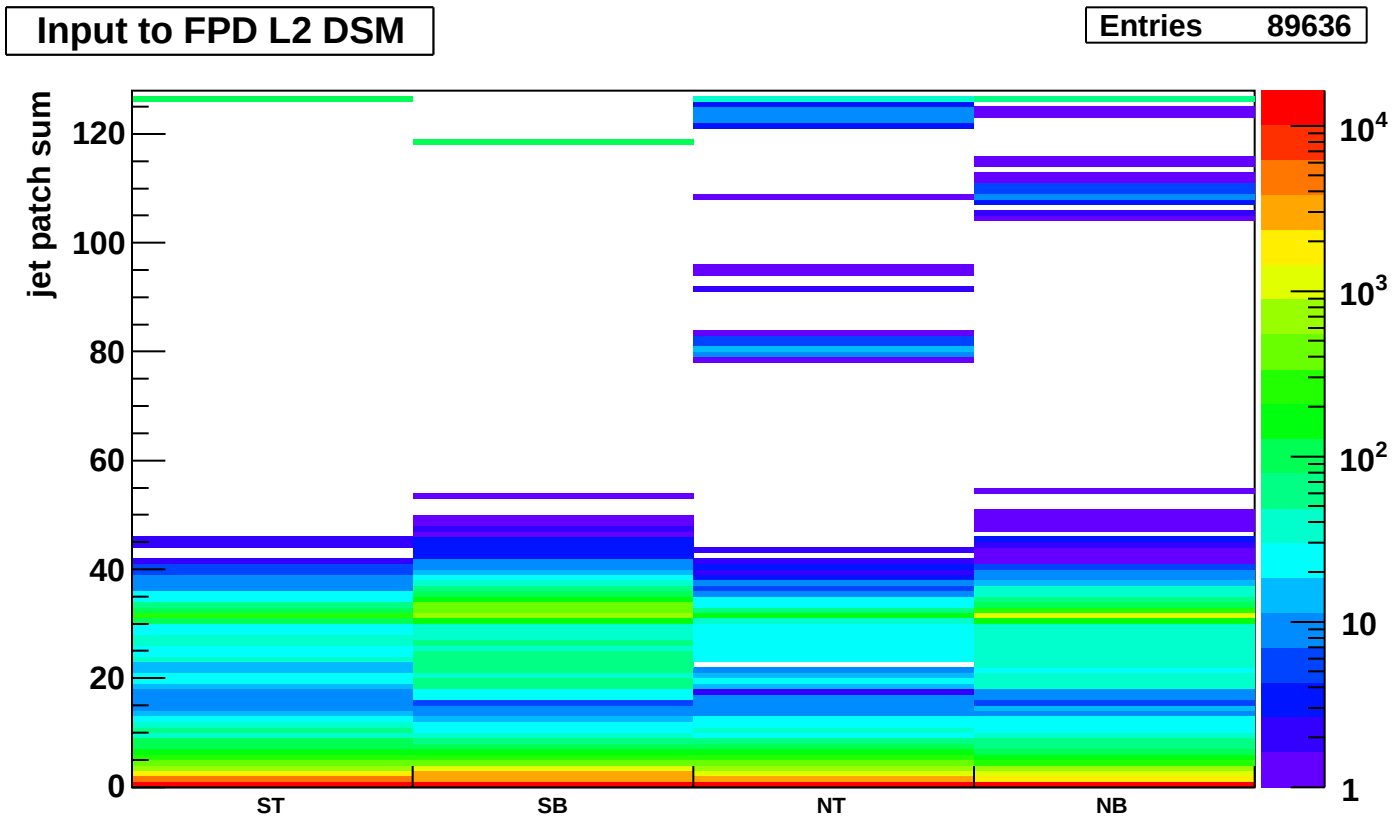

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

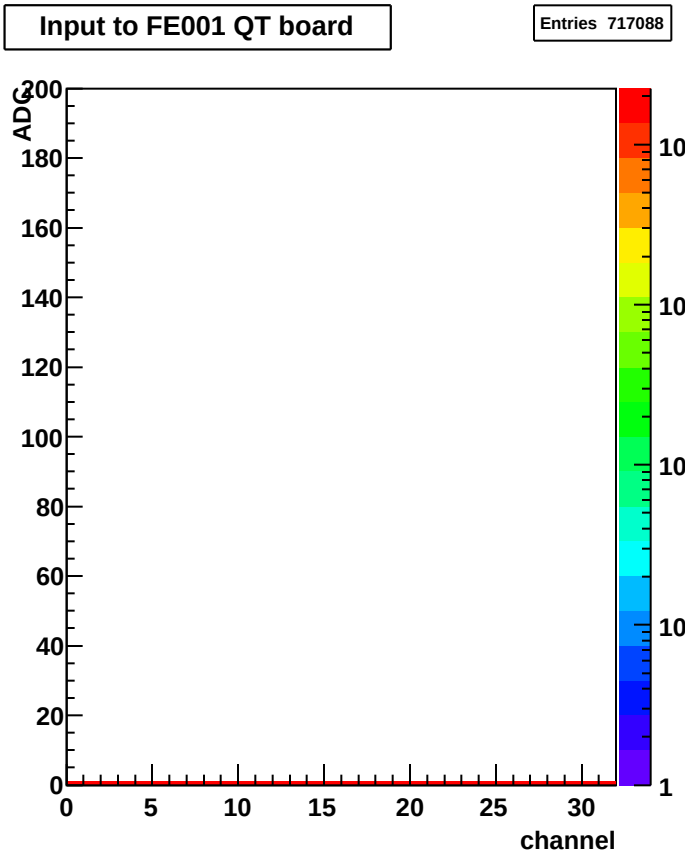

Input to FPD L2 DSM
Entries 67227
HT bits
1067
3483
2384
333
215
210
21009
18711
19815
SMALL
LARGE-S
LARGE-N

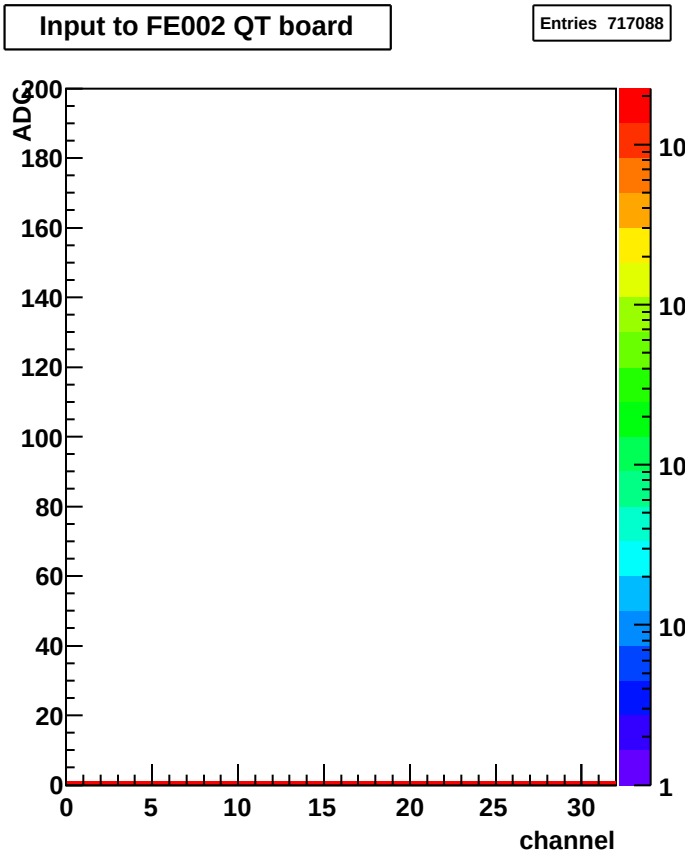

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

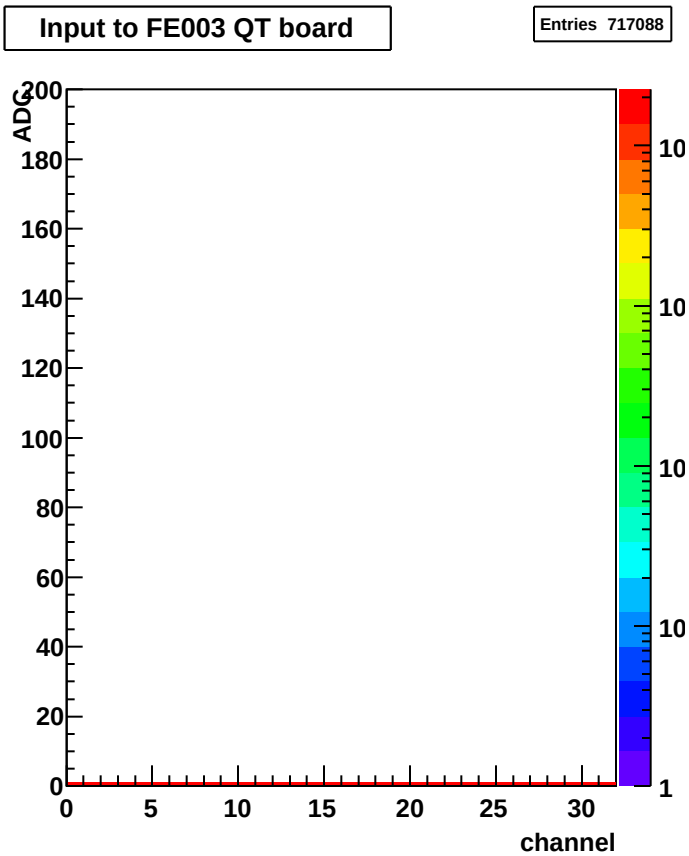

Input to FPD L3 DSM
Entries 358544
bit
7100
6676
1593
1095
1004
6009
5552
4846
6641
6398
4953
156
942
12349
15733
20816
23314
23405
16400
16857
17563
15768
16051
17456
22243
22409
22409
21467
22409
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

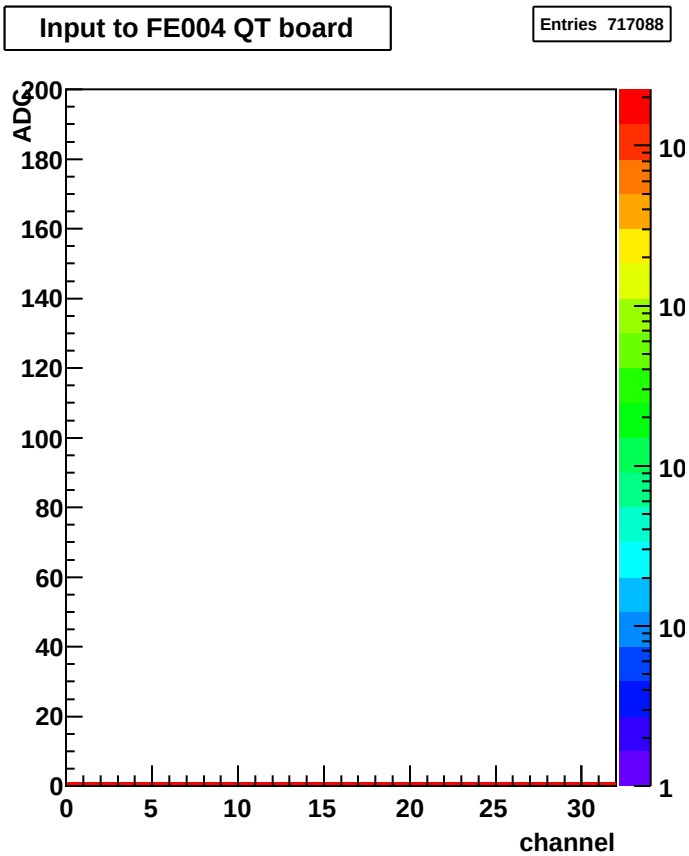

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

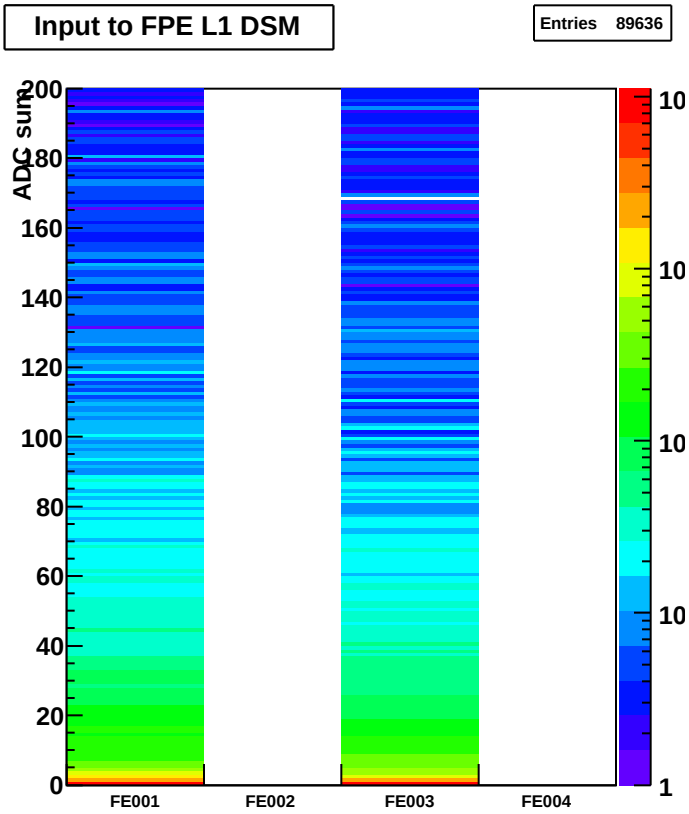

Input to FPD L2 DSM
Entries 89636
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$

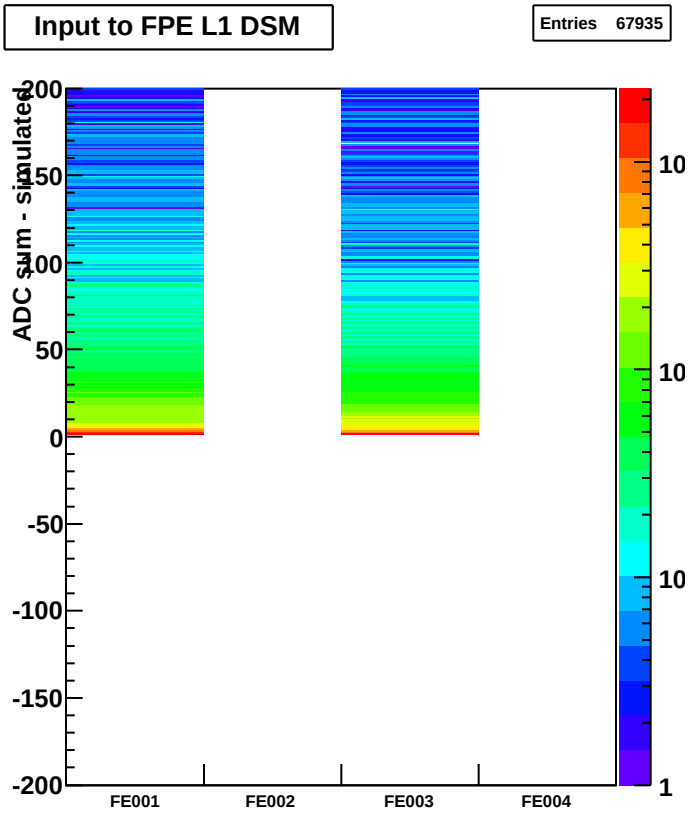

Input to FE001 QT board
Entries 717088
ADC
channel

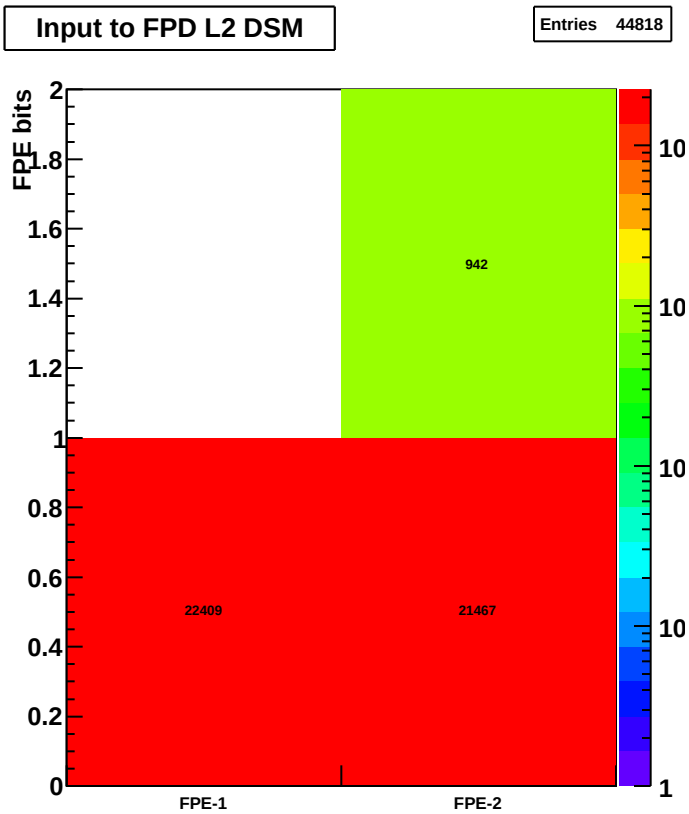

Input to FE002 QT board
Entries 717088
ADC
channel

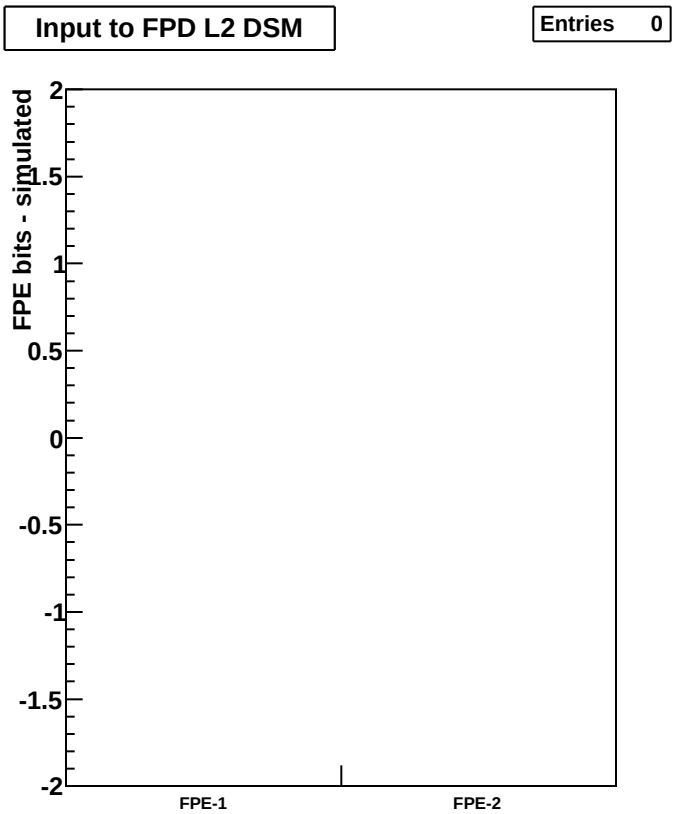

Input to FE003 QT board
Entries 717088
ADC
channel


Input to FE004 QT board
Entries 717088
ADC
channel


Input to FPE L1 DSM
Entries 89636
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 67935
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 44818
FPE bits
942
22409
21467
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2