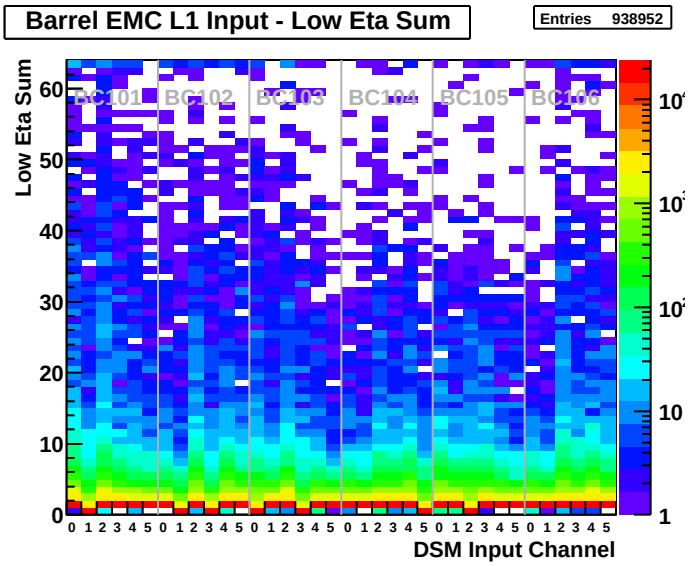
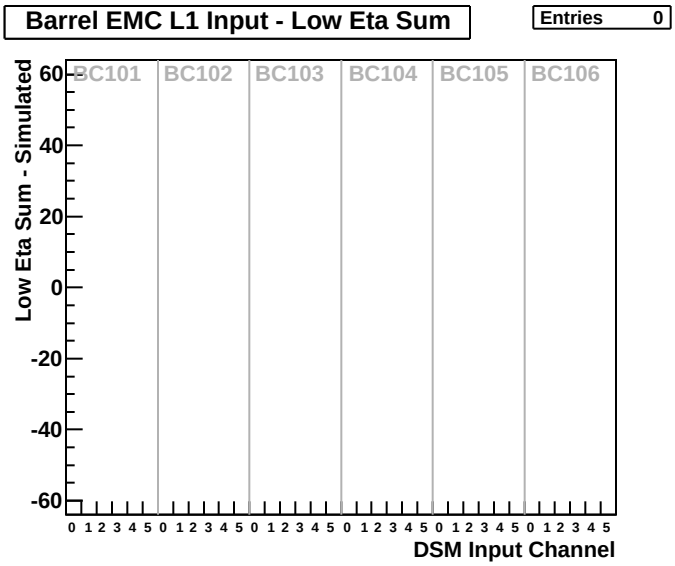
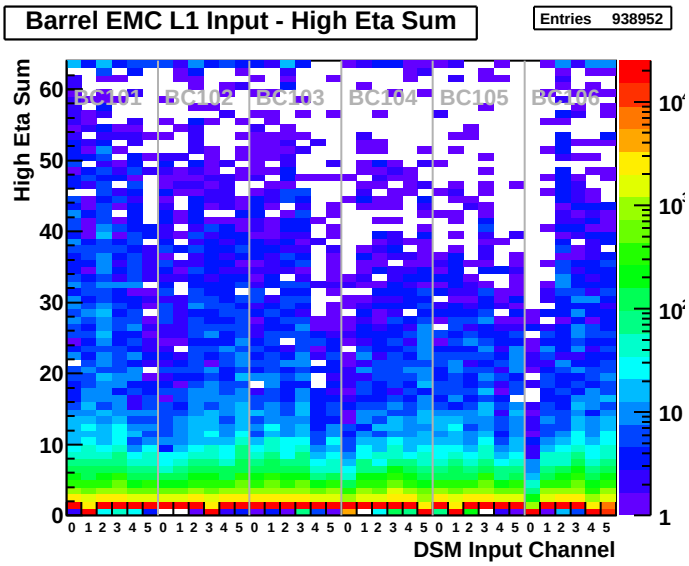
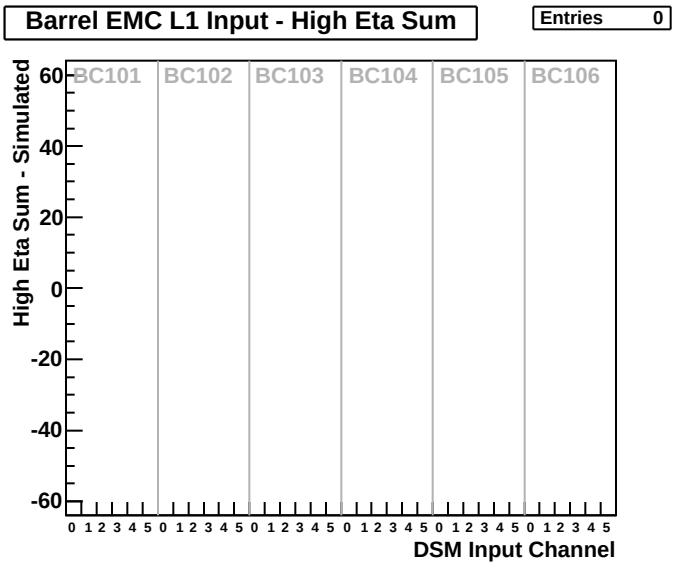
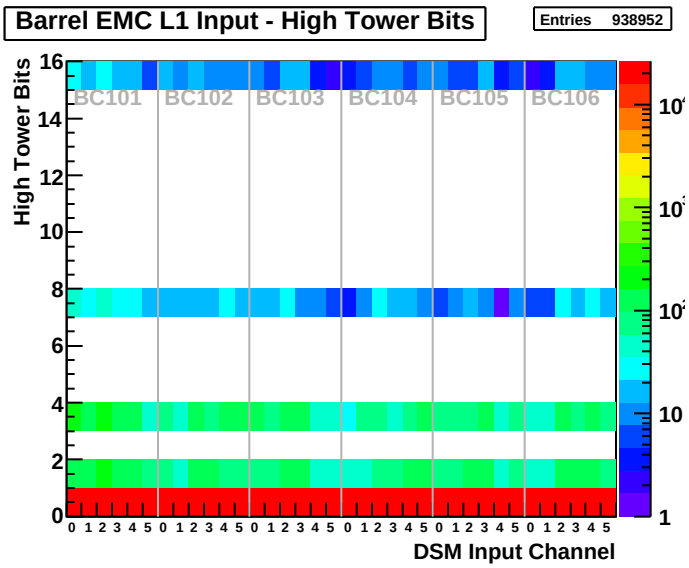
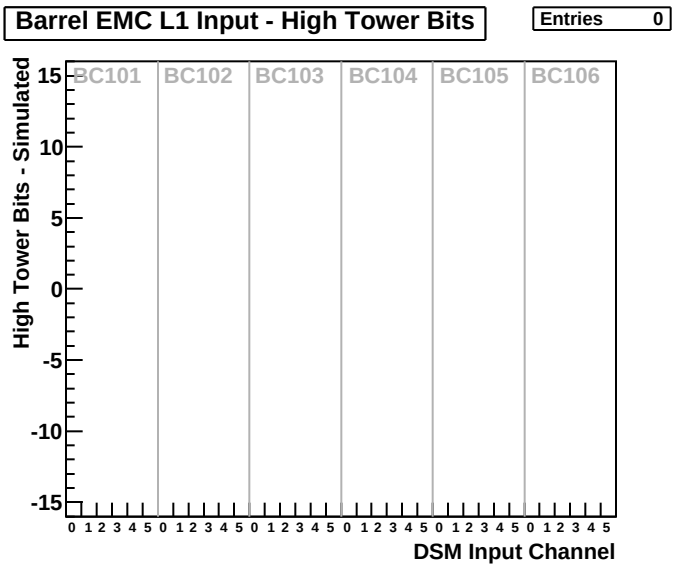

Barrel EMC L1 Input - Low Eta Sum
Entries 938952
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

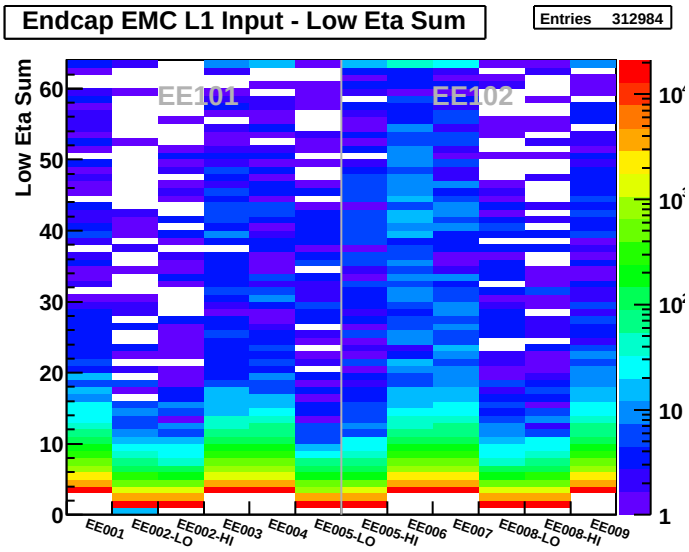

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

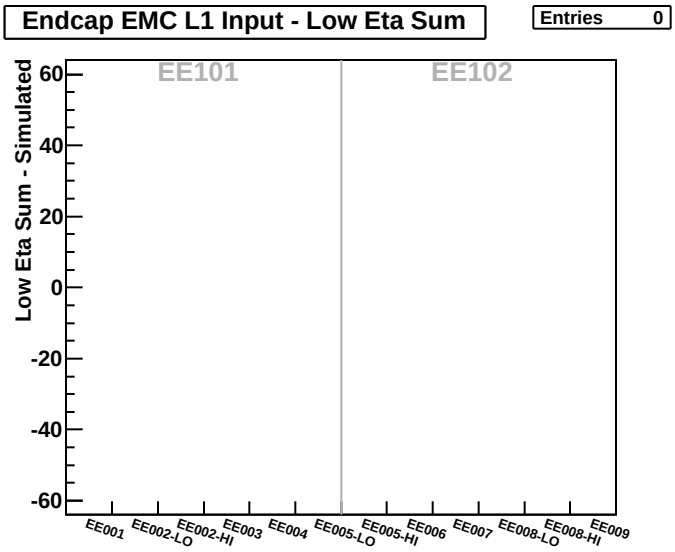

Barrel EMC L1 Input - High Eta Sum
Entries 938952
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

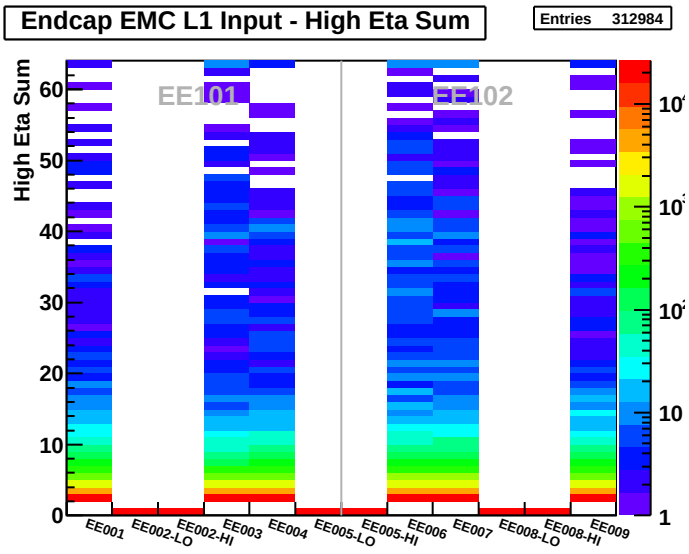

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

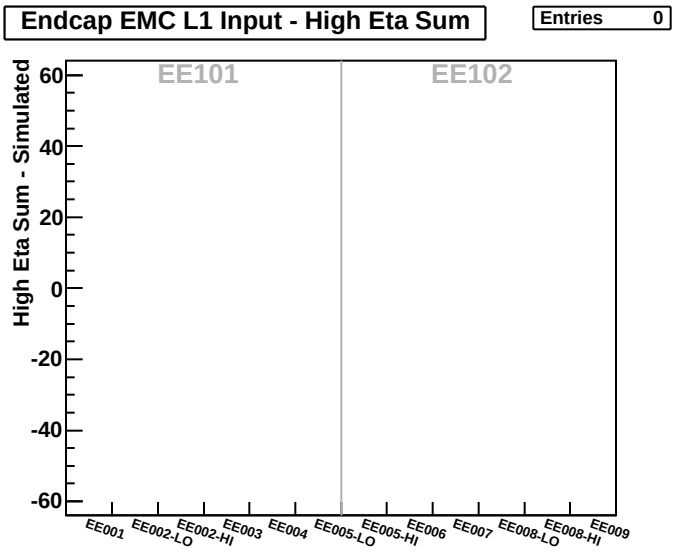

Barrel EMC L1 Input - High Tower Bits
Entries 938952
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

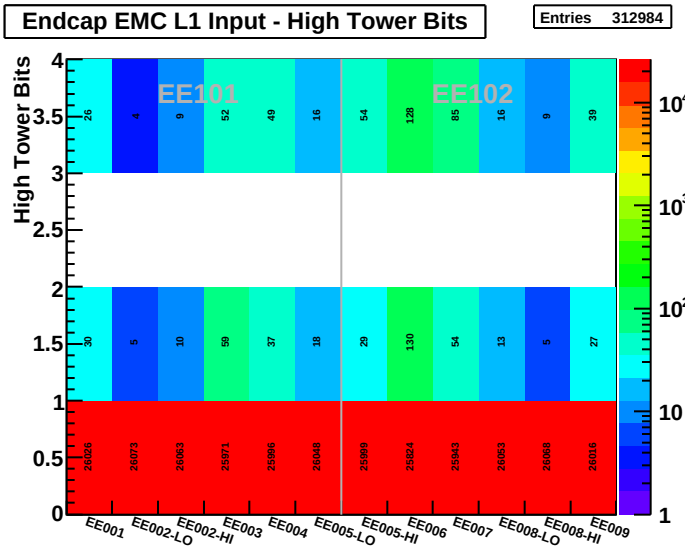

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

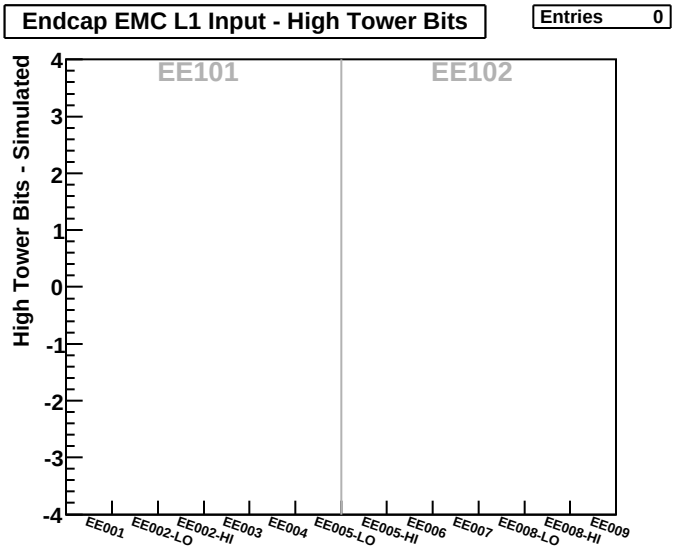

Endcap EMC L1 Input - Low Eta Sum
Entries 312984
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

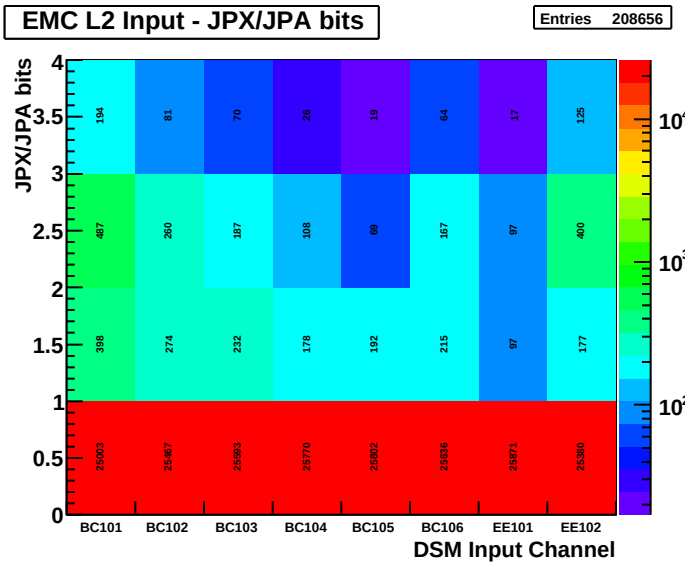

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

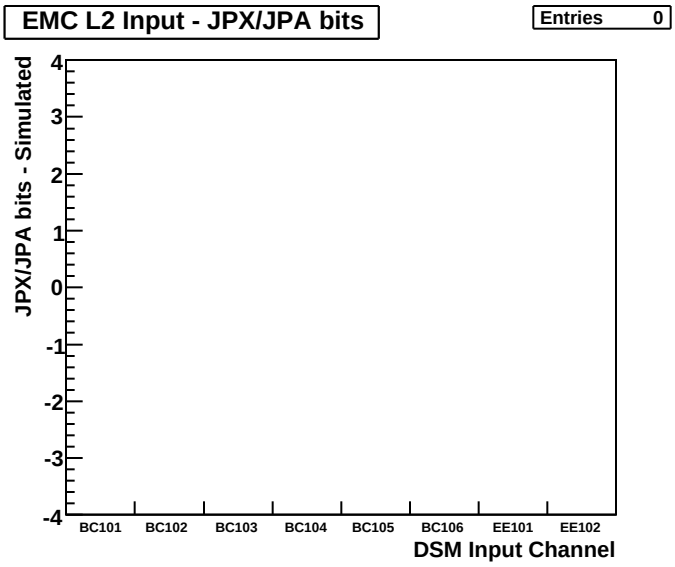

Endcap EMC L1 Input - High Eta Sum
Entries 312984
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

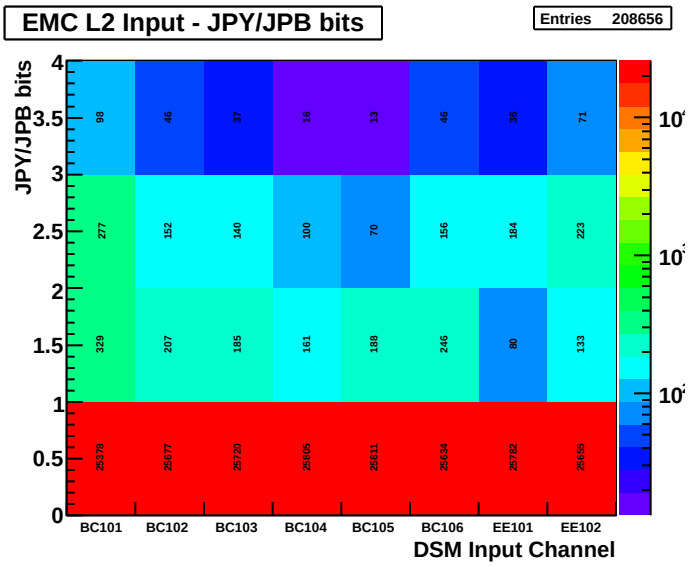

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

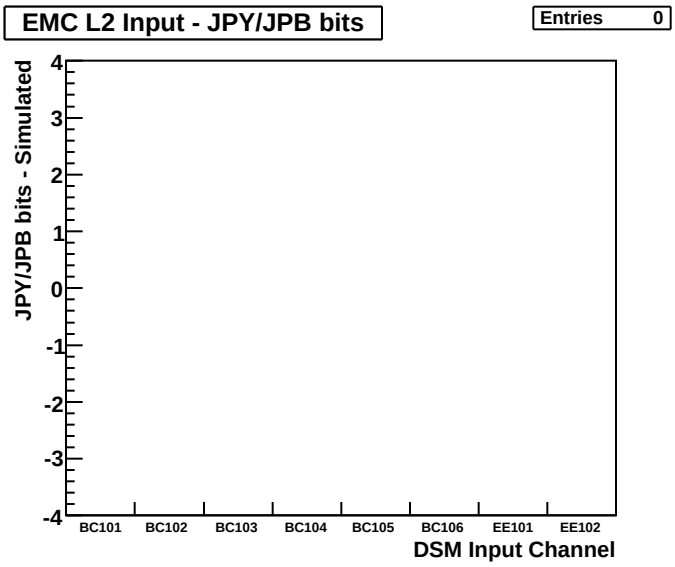

Endcap EMC L1 Input - High Tower Bits
Entries 312984
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

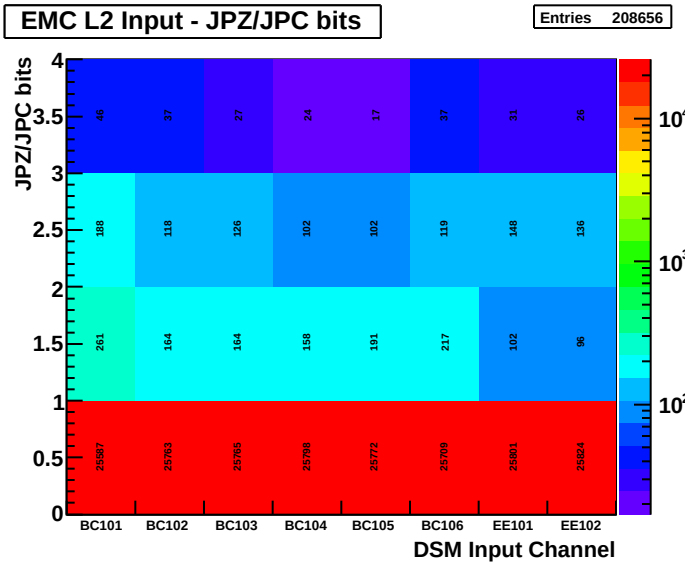

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

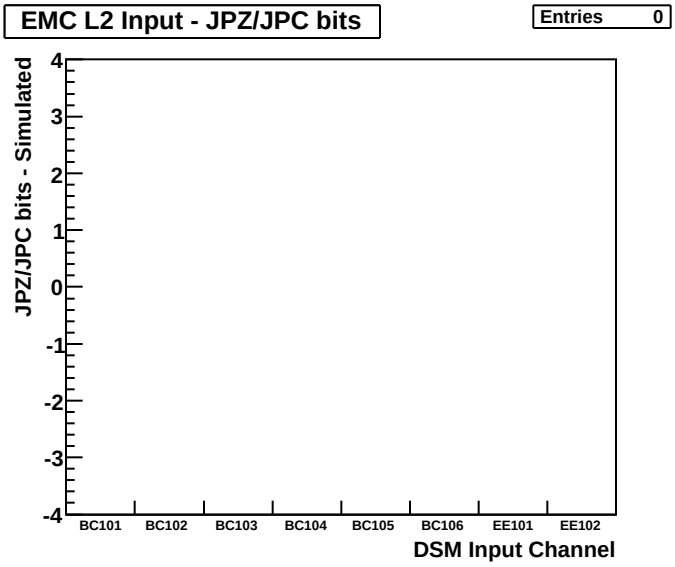

EMC L2 Input - JPX/JPA bits
Entries 208656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
194 81 70 26 19 64 17 125
487 260 187 108 69 167 97 400
398 274 232 178 192 215 97 177
25603 25467 25593 25770 25902 25636 25871 25380

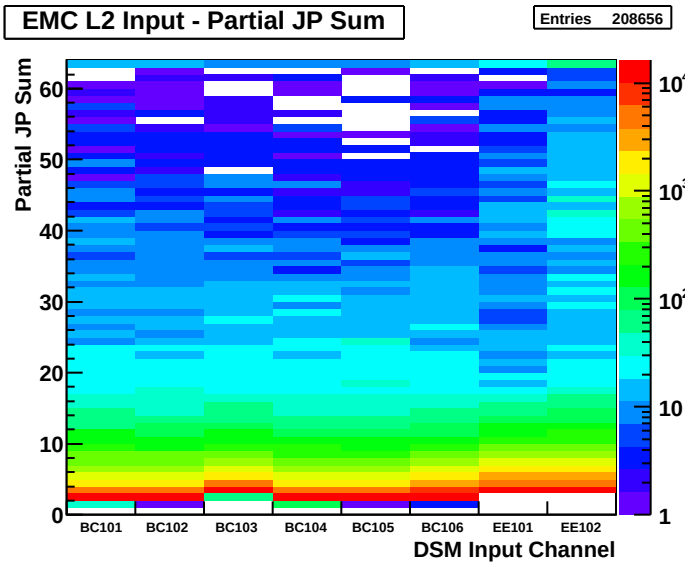

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

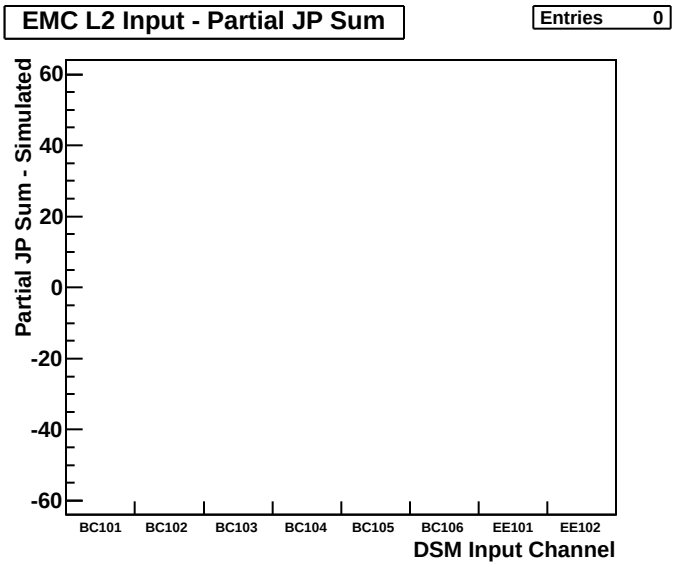

EMC L2 Input - JPY/JPB bits
Entries 208656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
98 46 37 16 13 46 36 71
277 152 140 100 70 156 184 223
329 207 185 161 188 246 80 133
25378 25677 25720 25805 25811 25634 25782 25655

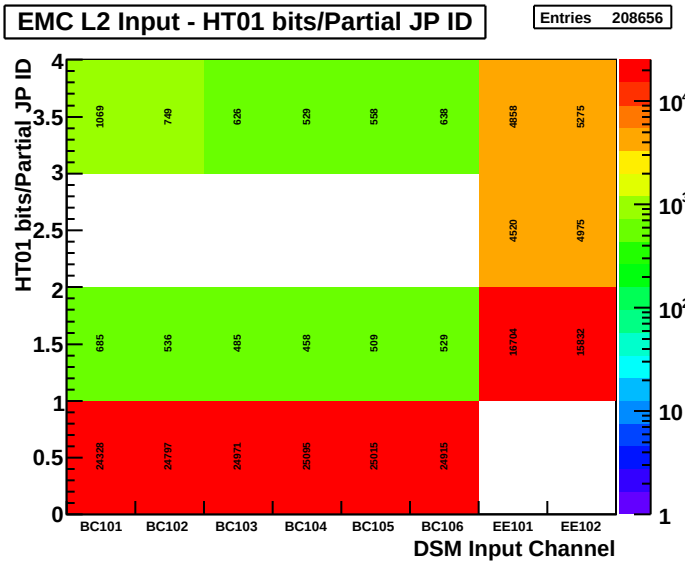

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

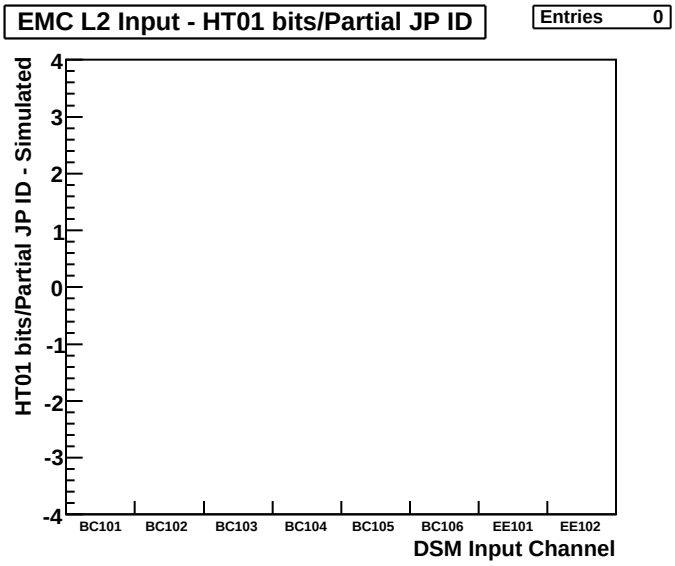

EMC L2 Input - JPZ/JPC bits
Entries 208656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46 37 27 24 17 37 31 26
188 118 126 102 102 119 148 136
261 164 164 158 191 217 102 96
25587 25763 25765 25798 25772 25709 25801 25824

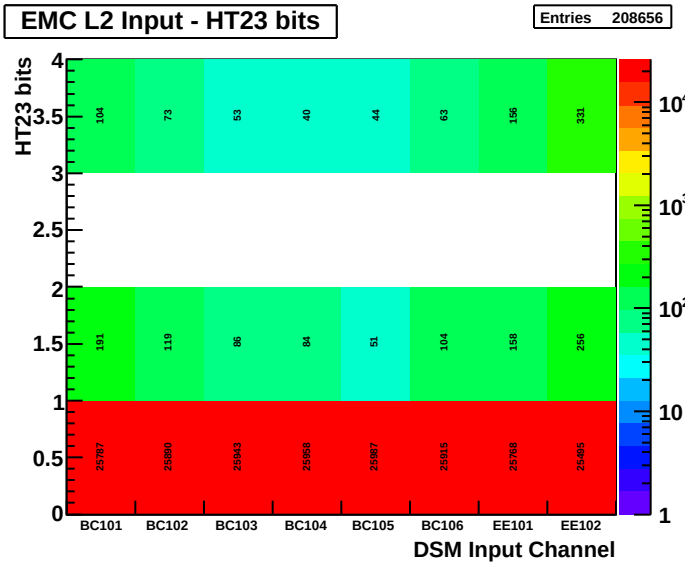

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

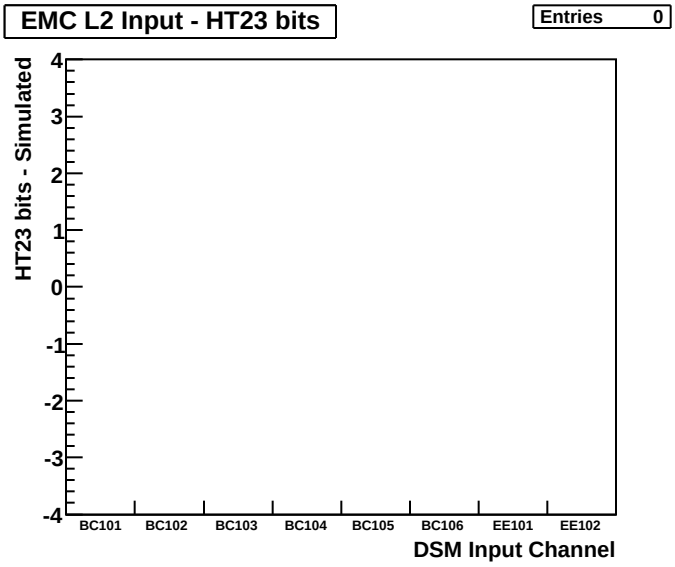

EMC L2 Input - Partial JP Sum
Entries 208656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

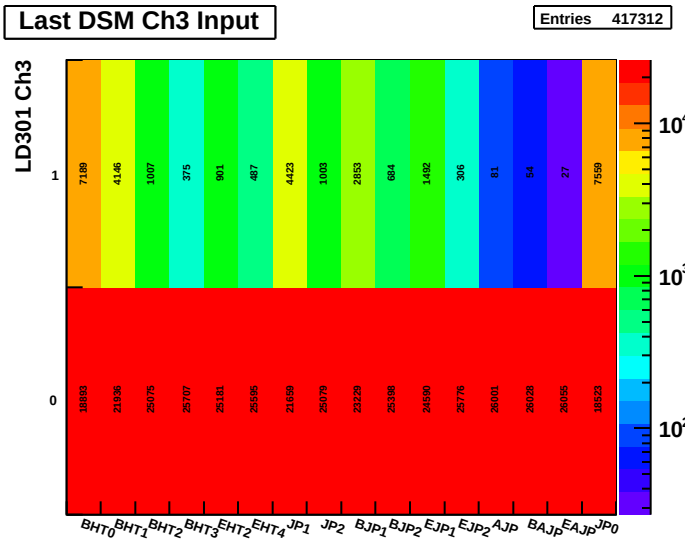

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

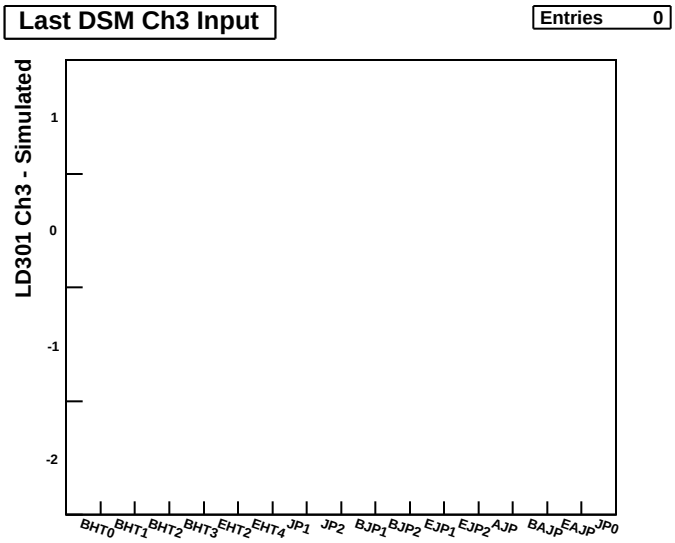

EMC L2 Input - HT01 bits/Partial JP ID
Entries 208656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

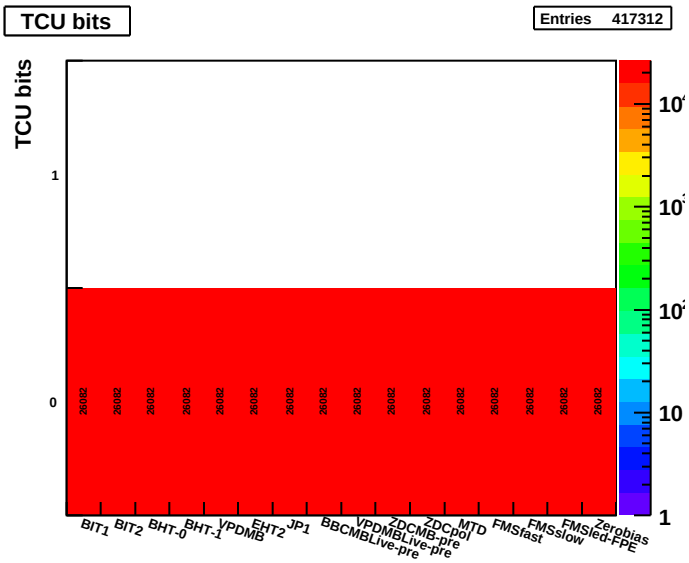

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

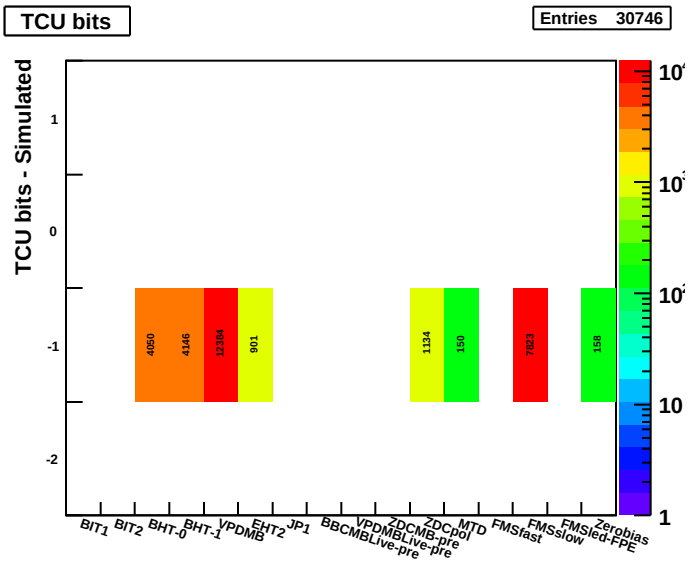

EMC L2 Input - HT23 bits
Entries 208656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

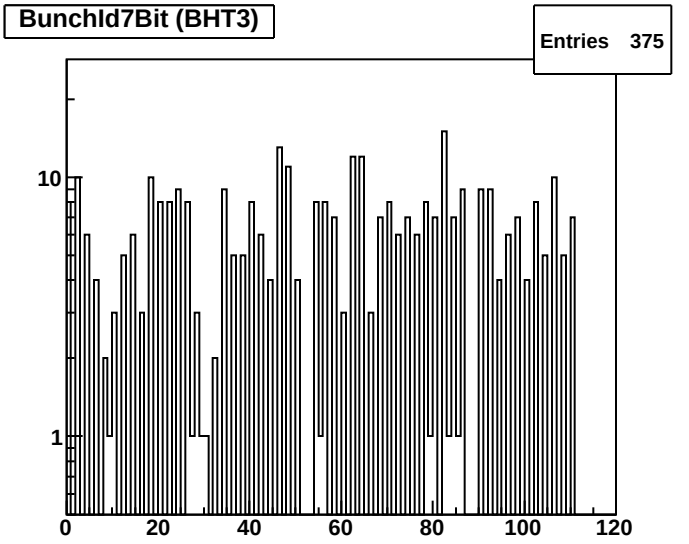

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

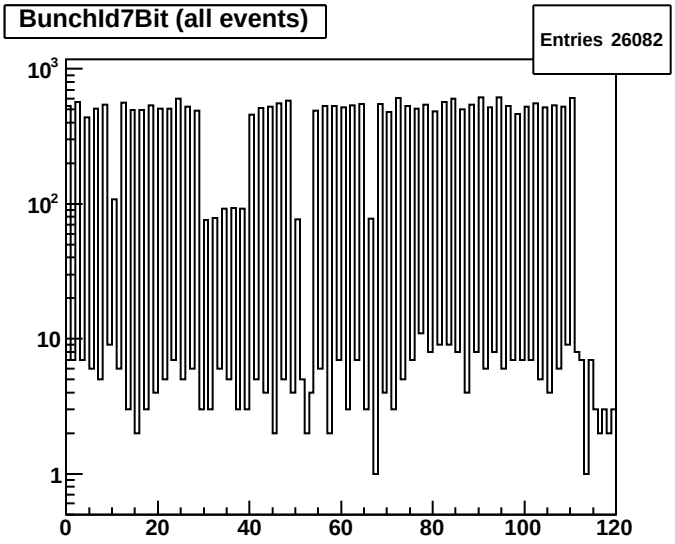

Last DSM Ch3 Input
Entries 417312
LD301 Ch3
7189 4146 1007 375 901 487 4423 1003 2853 684 1492 306 81 54 27 7559
18893 21936 25075 25707 25181 25595 21659 25079 23229 25398 24590 25776 26001 26028 26055 18523
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

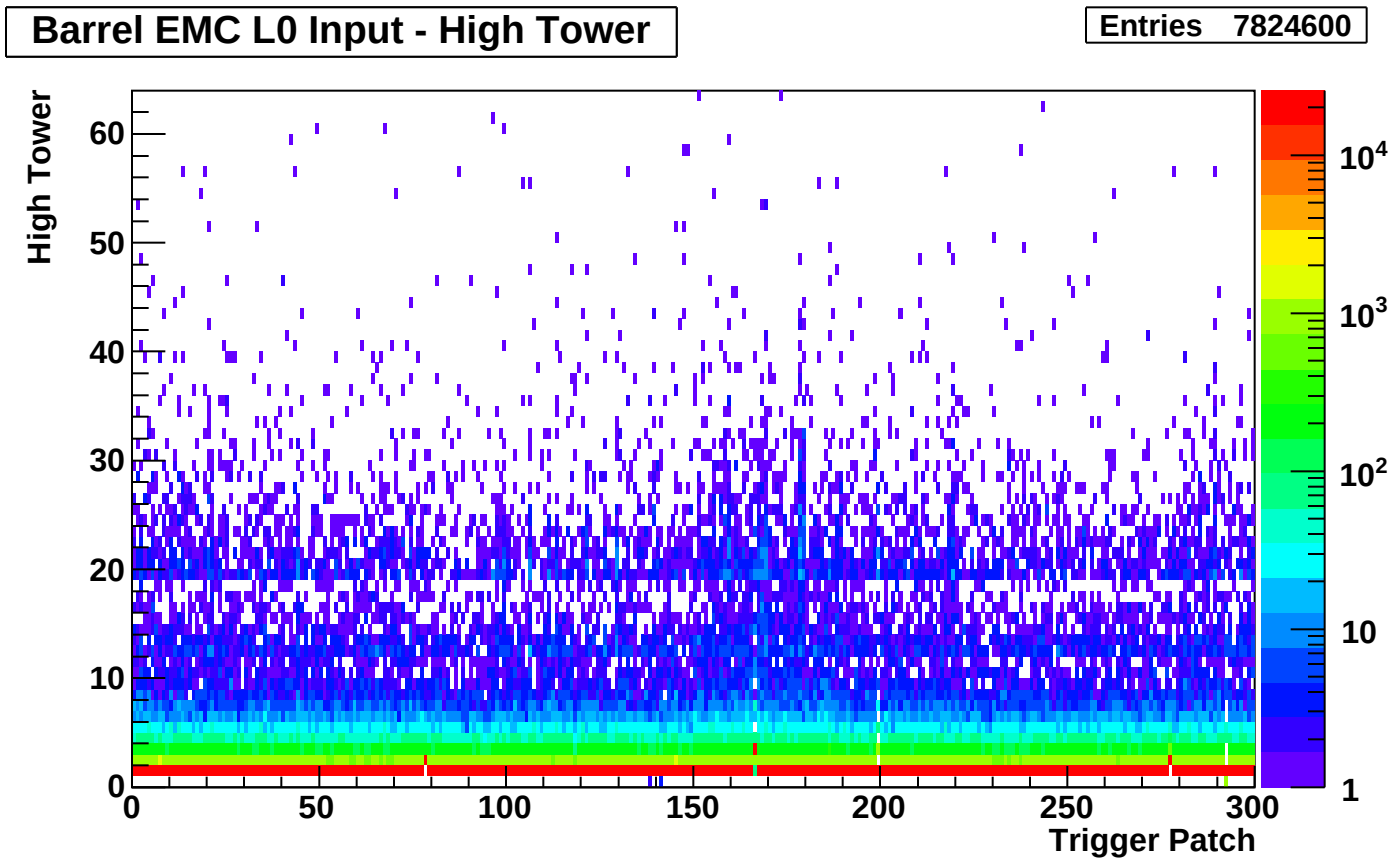

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

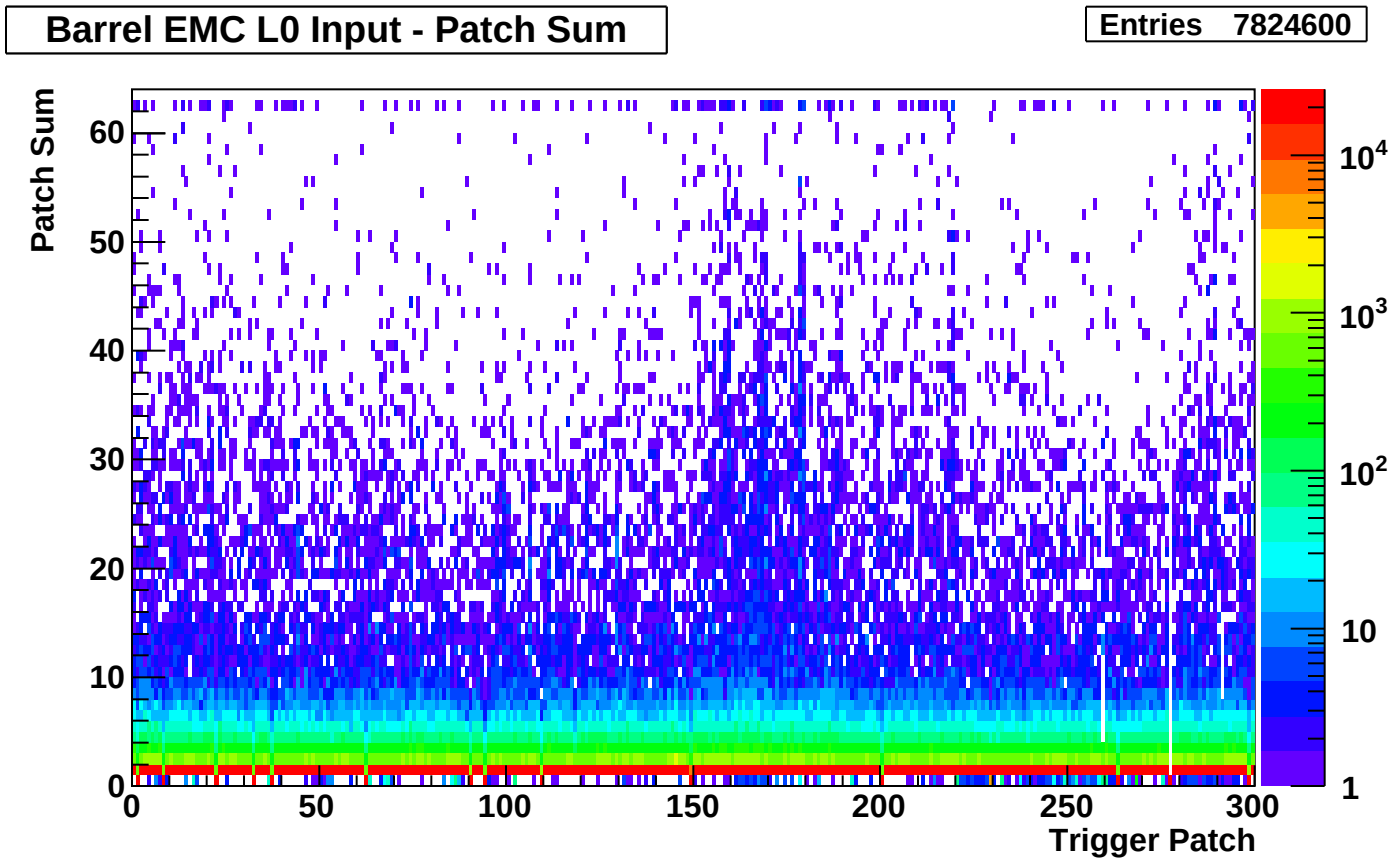

TCU bits
Entries 417312
TCU bits
26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082 26082
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

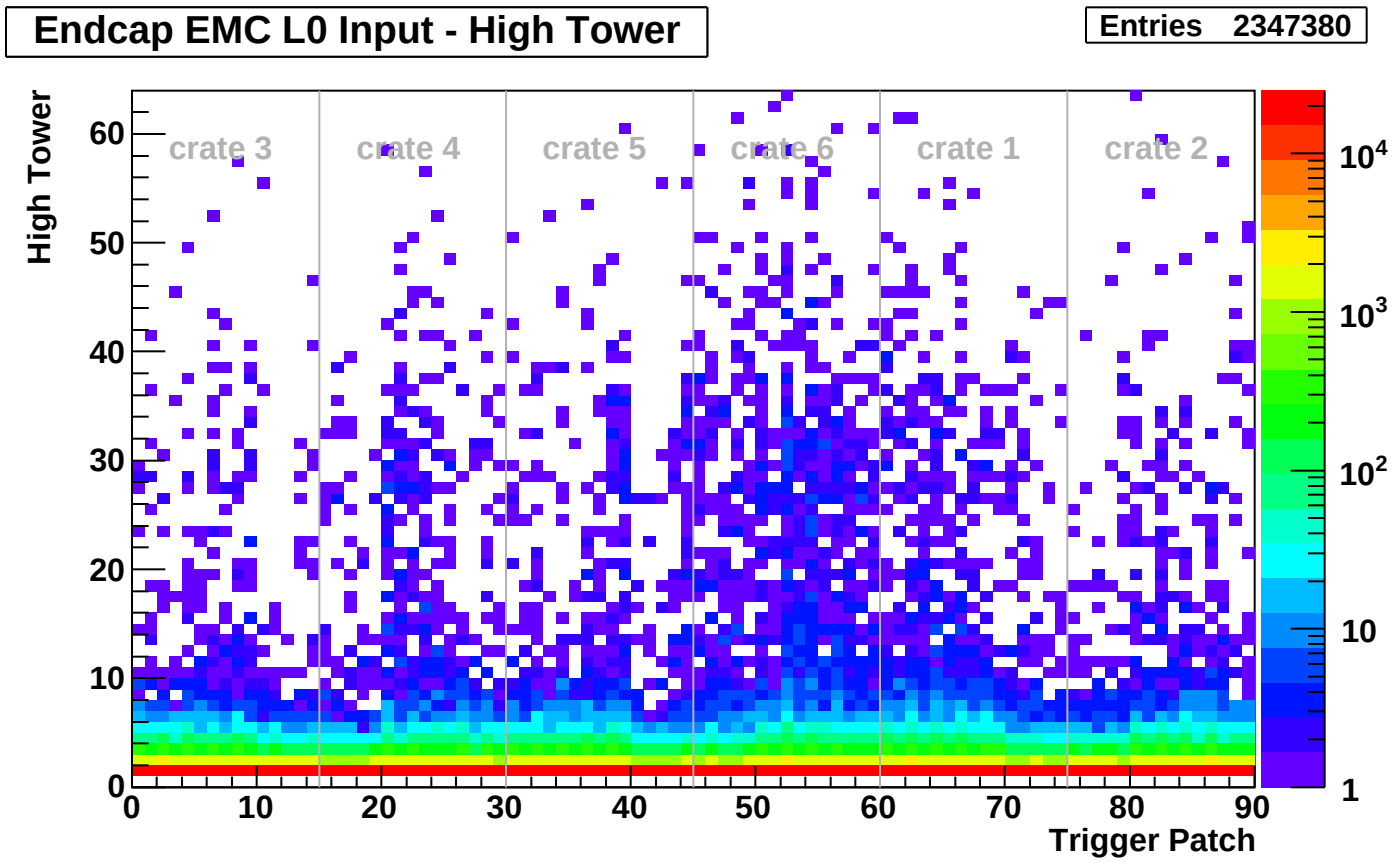

TCU bits
Entries 30746
TCU bits - Simulated
4050 4146 12384 901 1134 150 7823 158
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

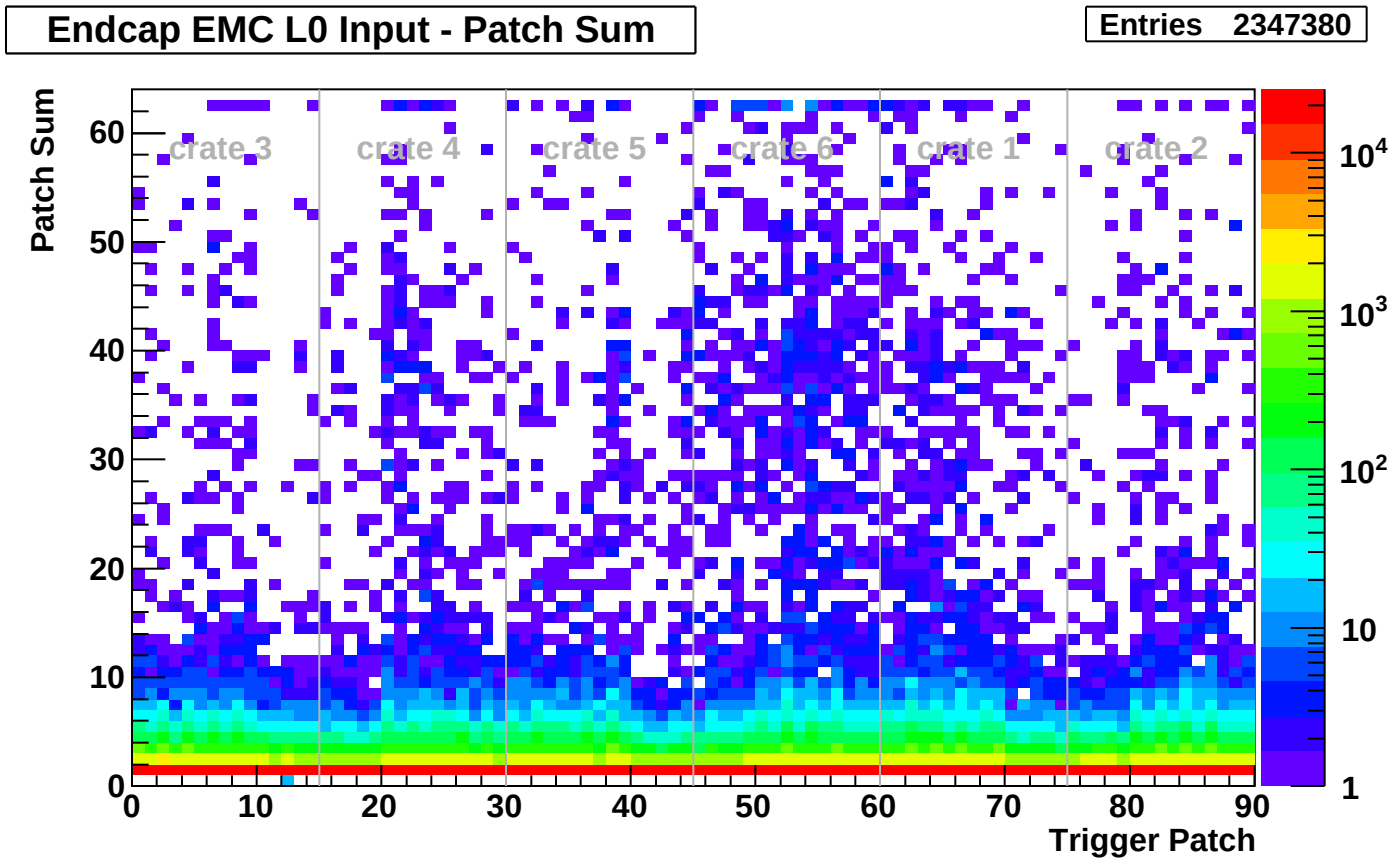

BunchId7Bit (BHT3)
Entries 375

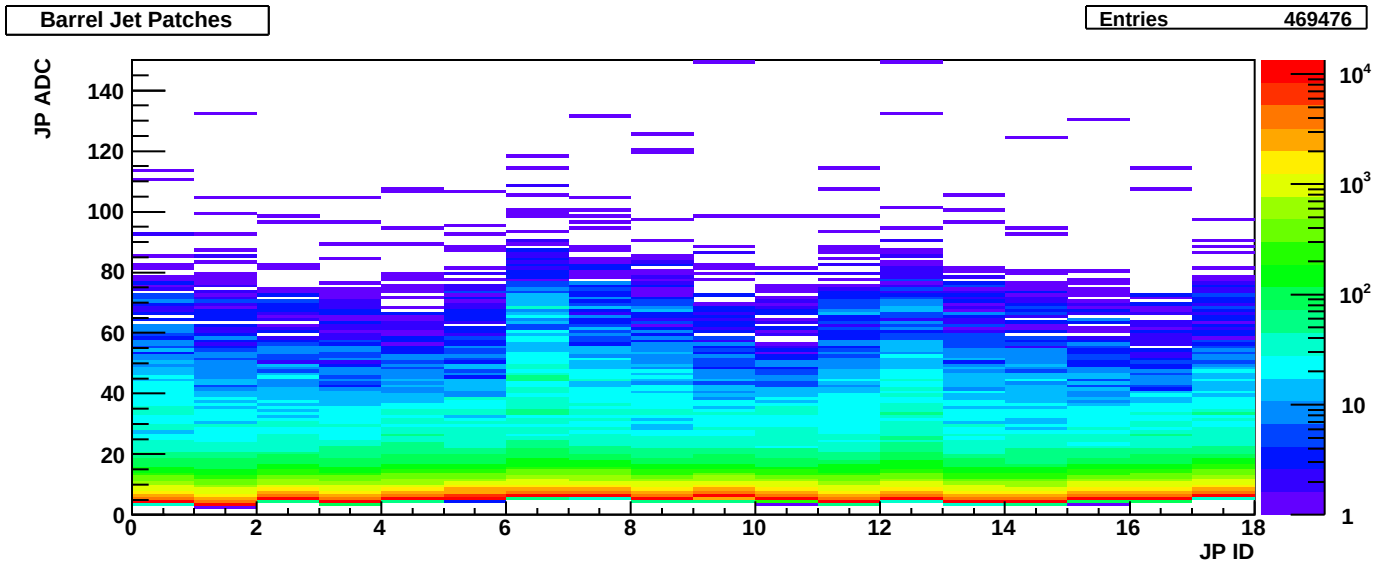

BunchId7Bit (all events)
Entries 26082

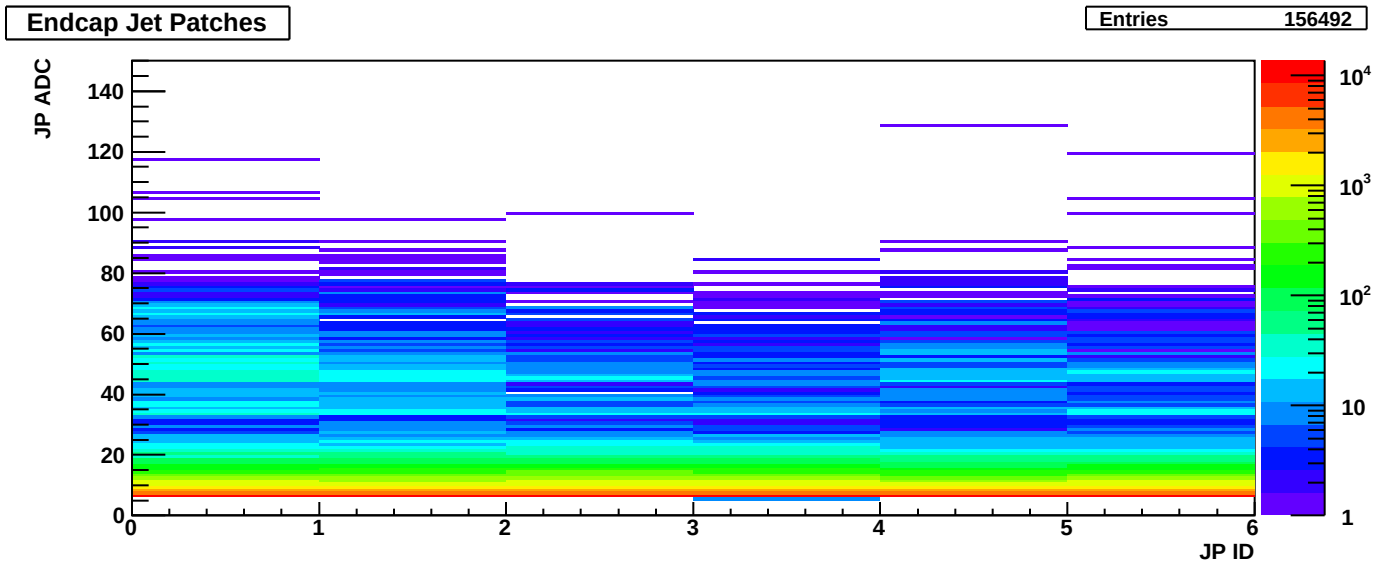

Barrel EMC L0 Input - High Tower
Entries 7824600
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

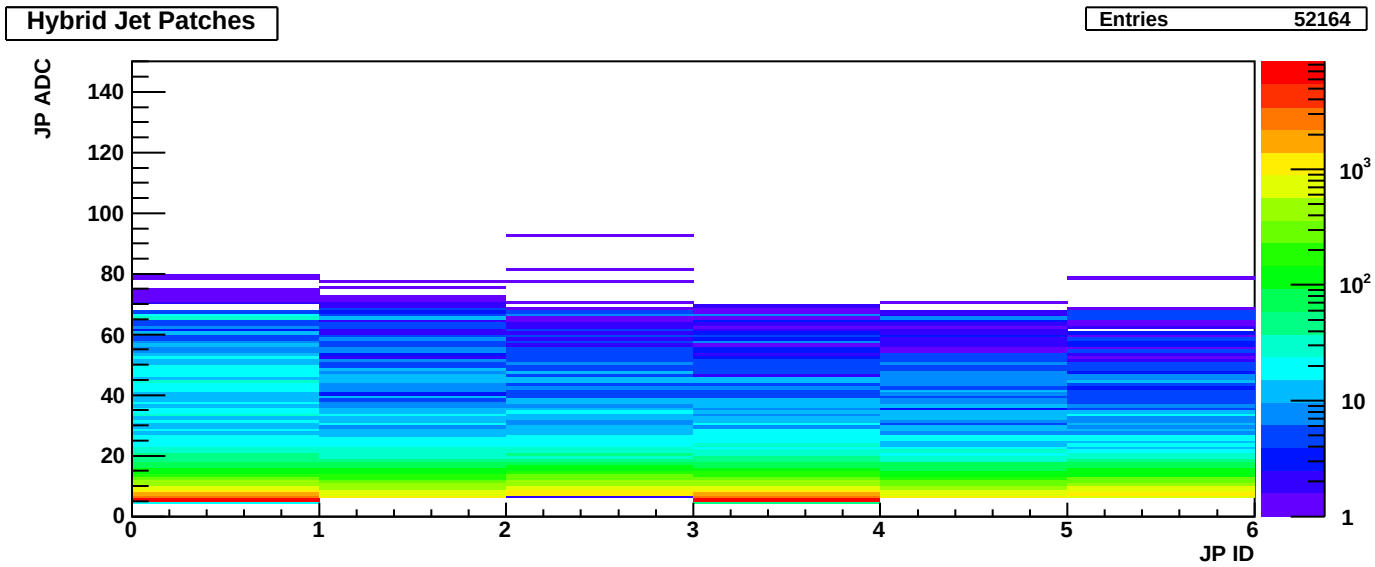

Barrel EMC L0 Input - Patch Sum
Entries 7824600
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

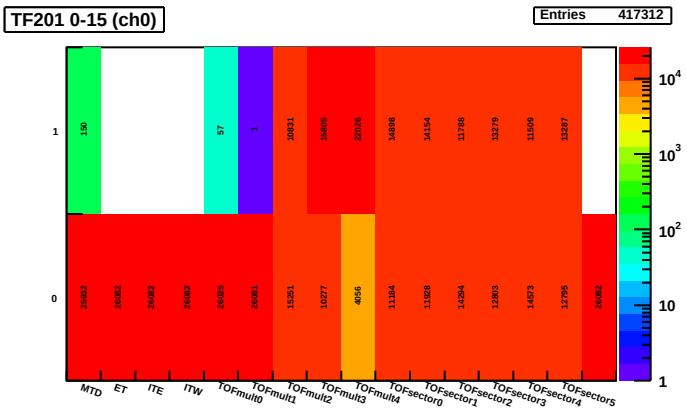

Endcap EMC L0 Input - High Tower
Entries 2347380
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

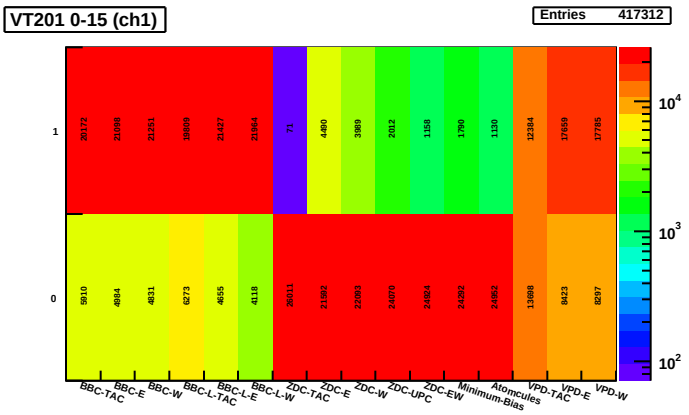

Endcap EMC L0 Input - Patch Sum
Entries 2347380
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

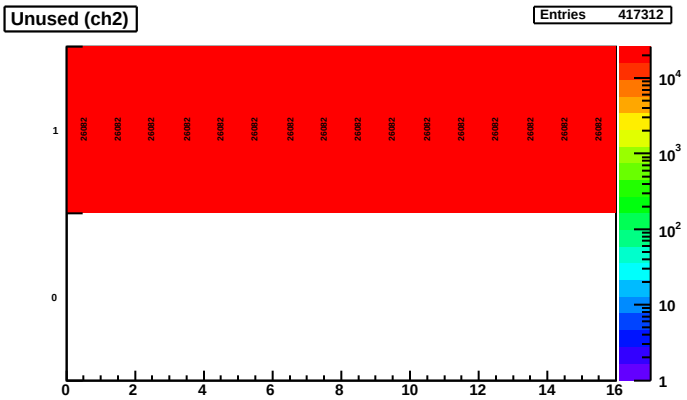

Barrel Jet Patches
Entries 469476
JP ADC
JP ID

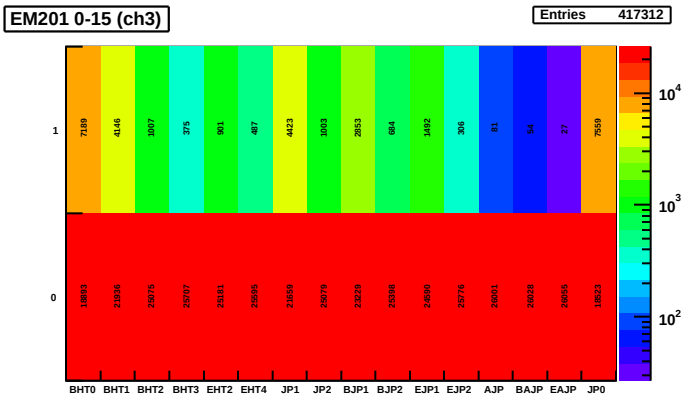

Endcap Jet Patches
Entries 156492
JP ADC
JP ID

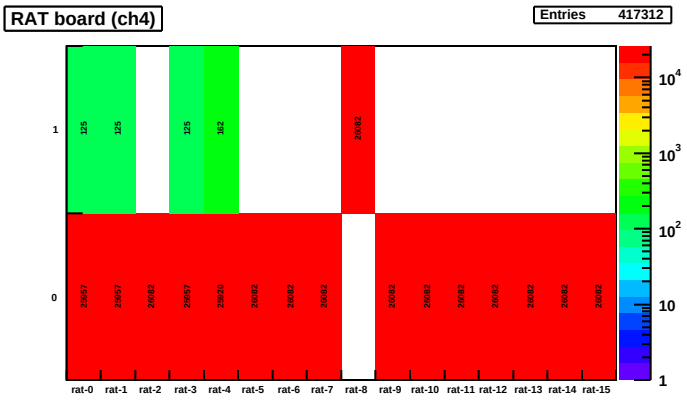

Hybrid Jet Patches
Entries 52164
JP ADC
JP ID

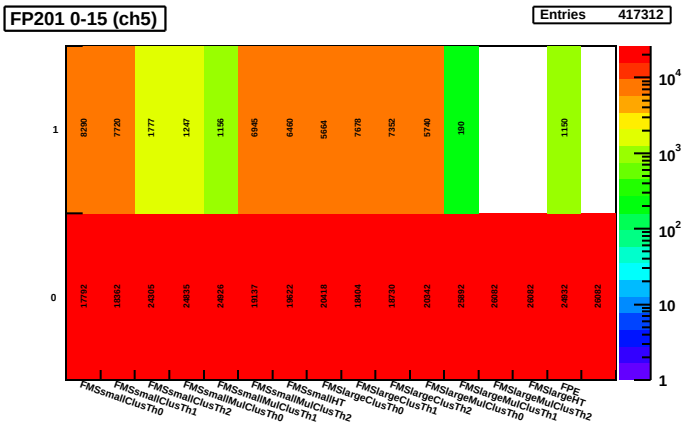

TF201 0-15 (ch0)
Entries 417312
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

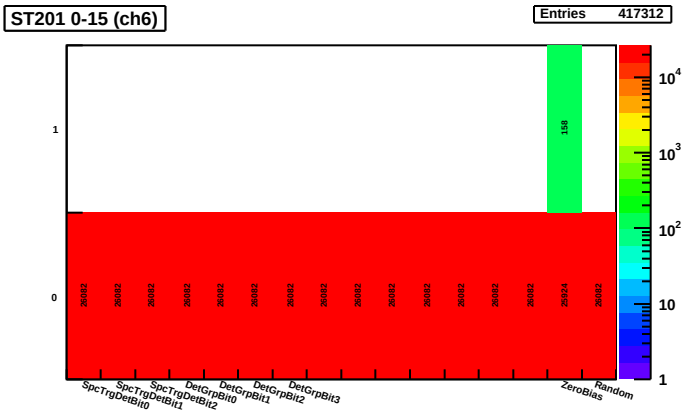

VT201 0-15 (ch1)
Entries 417312
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

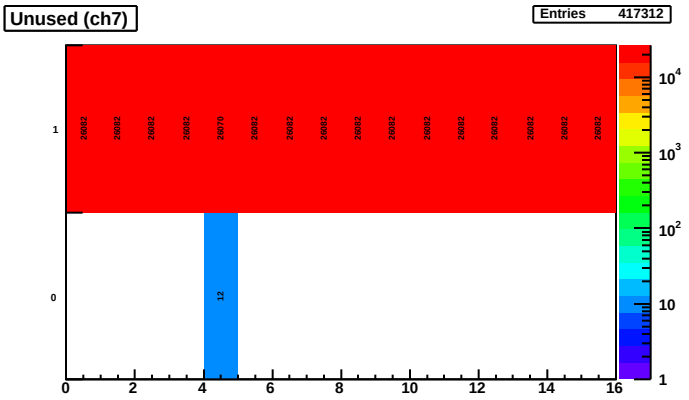

Unused (ch2)
Entries 417312

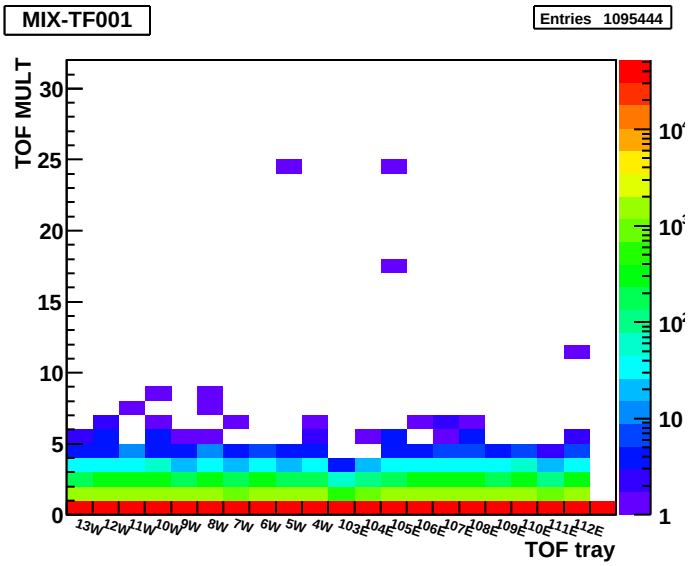

EM201 0-15 (ch3)
Entries 417312
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

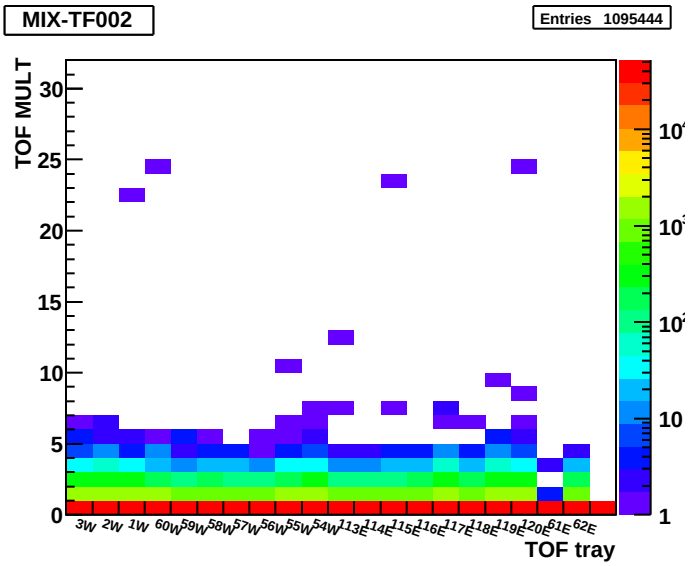

RAT board (ch4)
Entries 417312
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

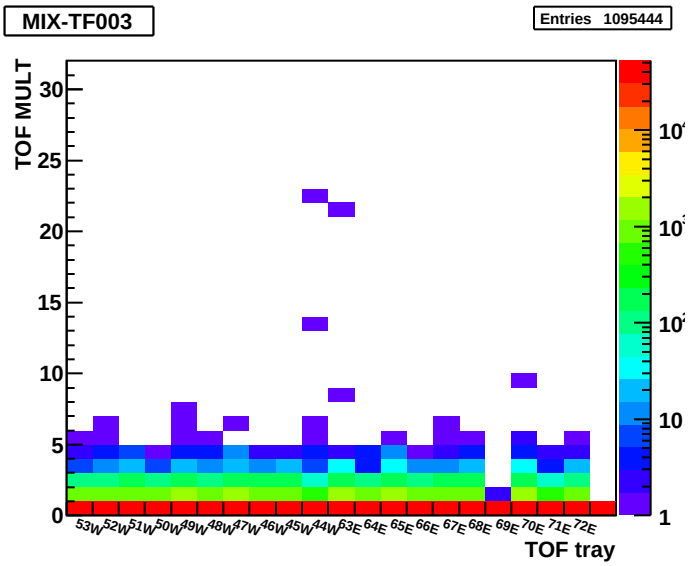

FP201 0-15 (ch5)
Entries 417312
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

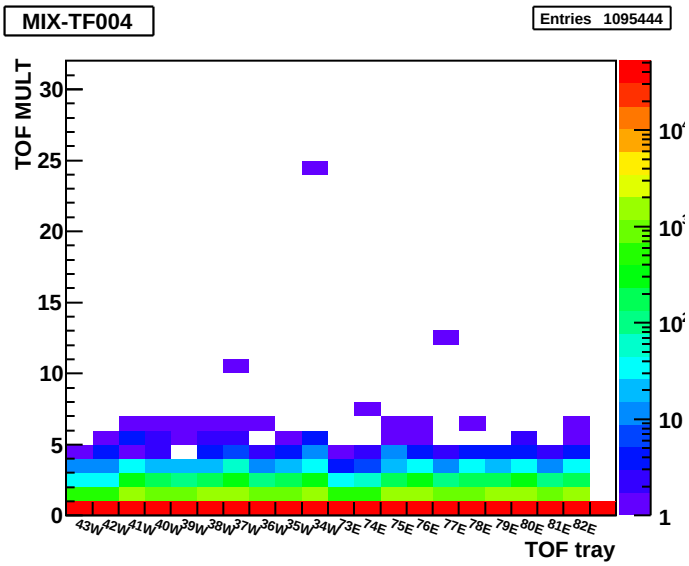

ST201 0-15 (ch6)
Entries 417312
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

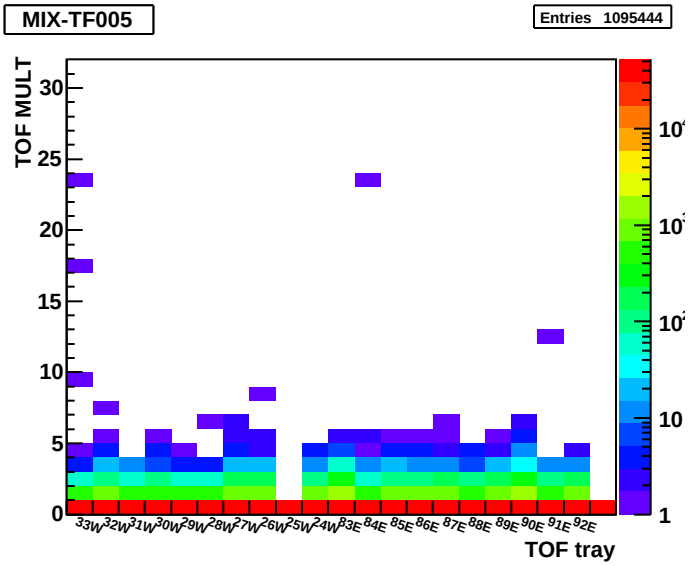

Unused (ch7)
Entries 417312

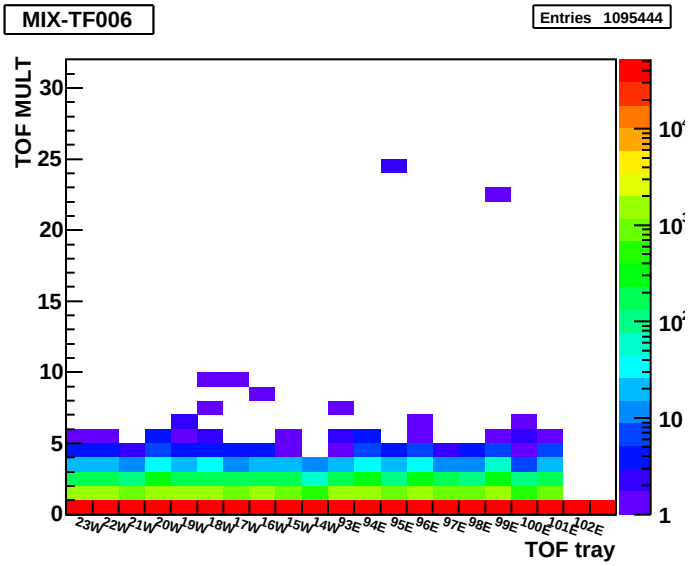

MIX-TF001
Entries 1095444
TOF MULT
TOF tray

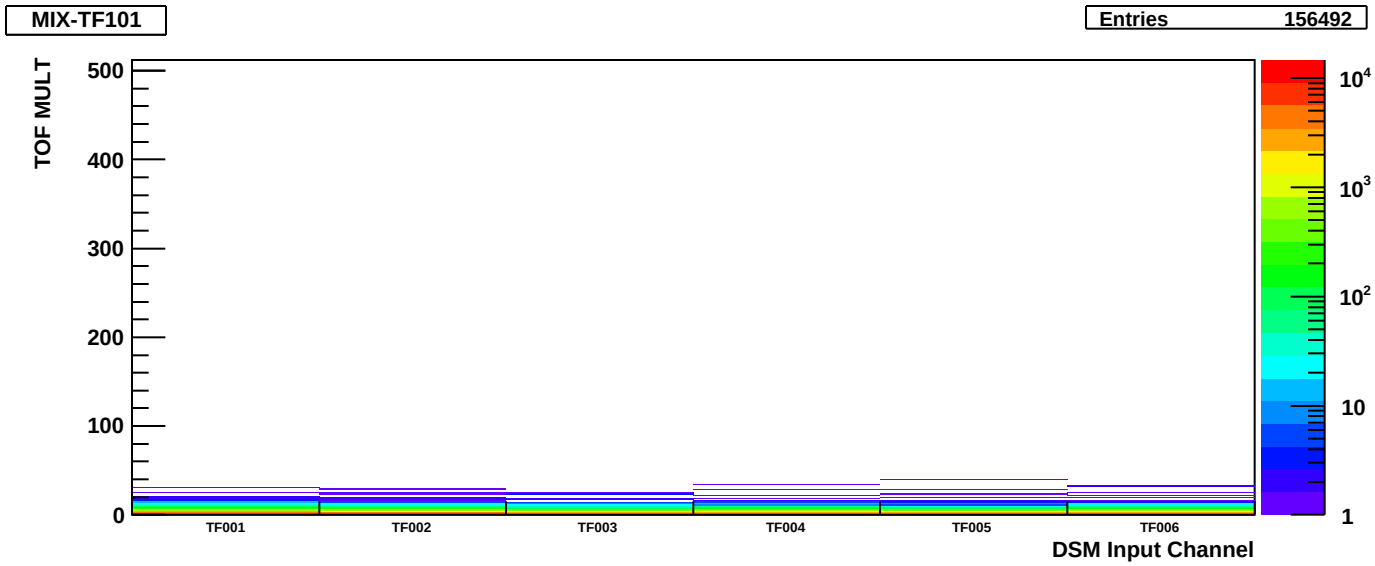

MIX-TF002
Entries 1095444
TOF MULT
TOF tray

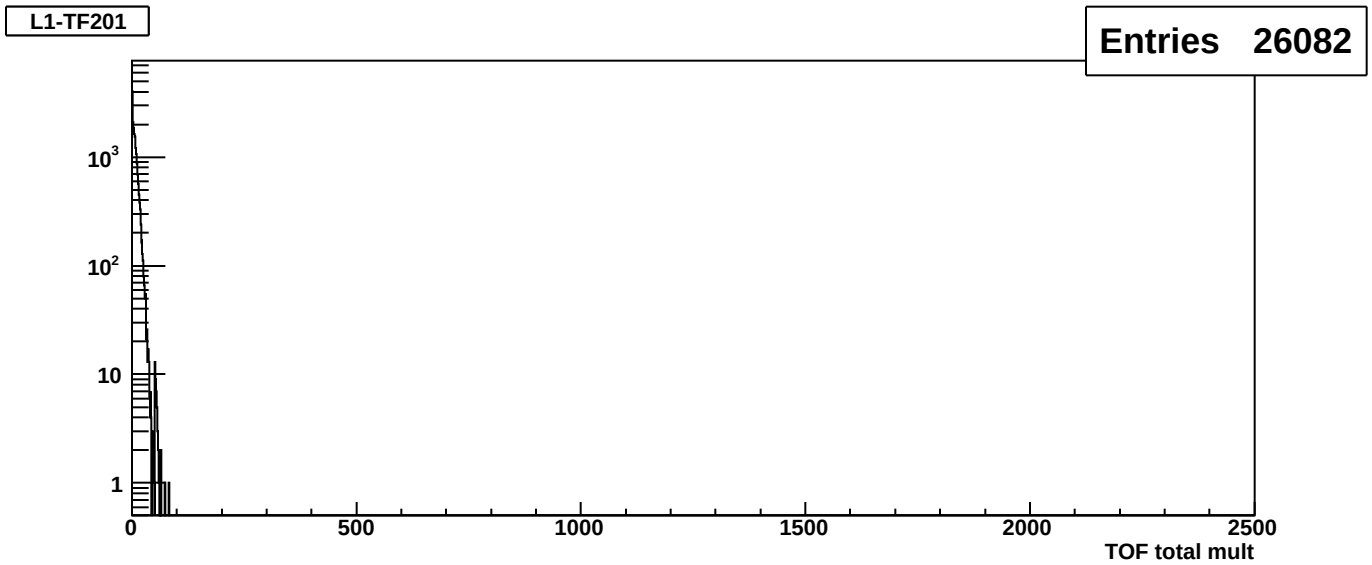

MIX-TF003
Entries 1095444
TOF MULT
TOF tray

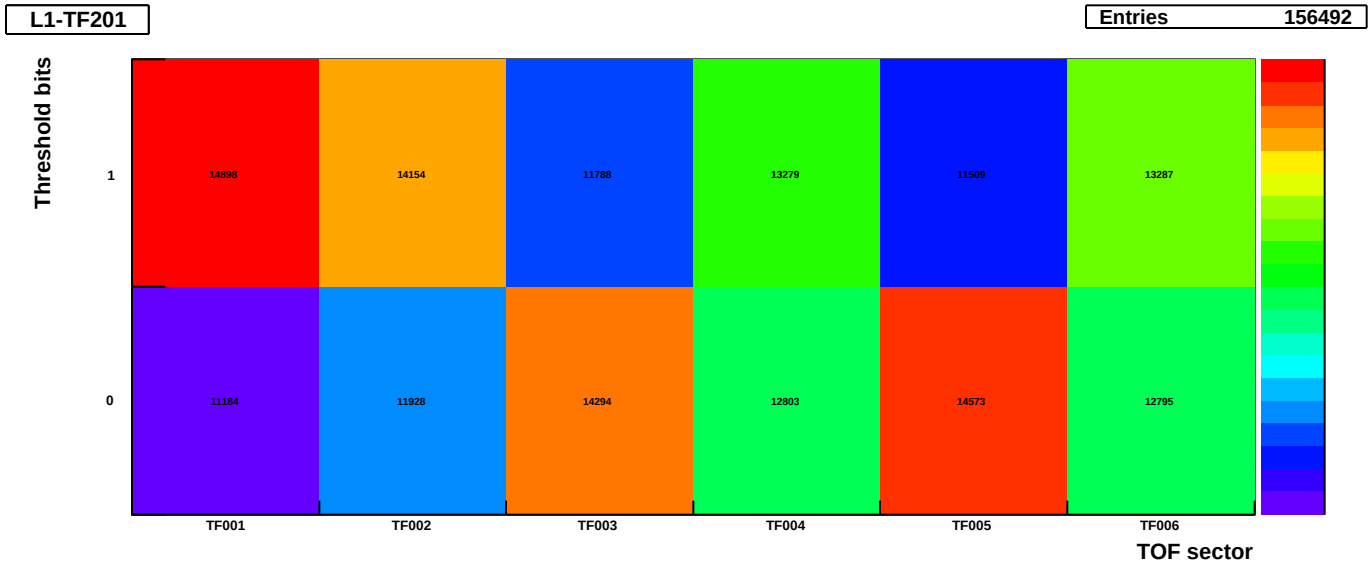

MIX-TF004
Entries 1095444
TOF MULT
TOF tray

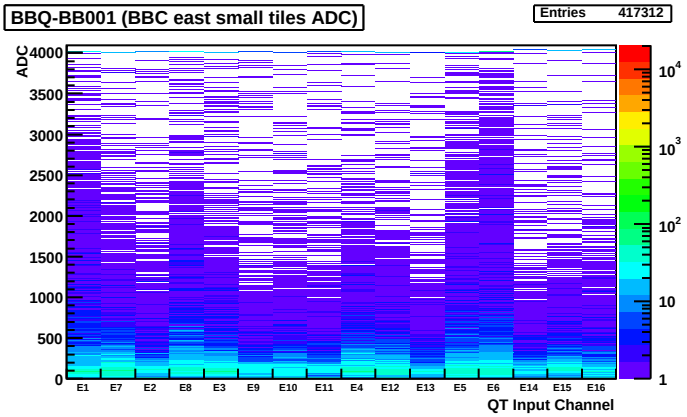

MIX-TF005
Entries 1095444
TOF MULT
TOF tray

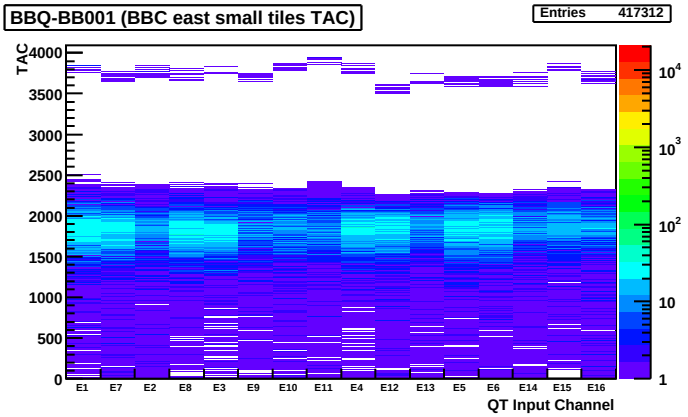

MIX-TF006
Entries 1095444
TOF MULT
TOF tray

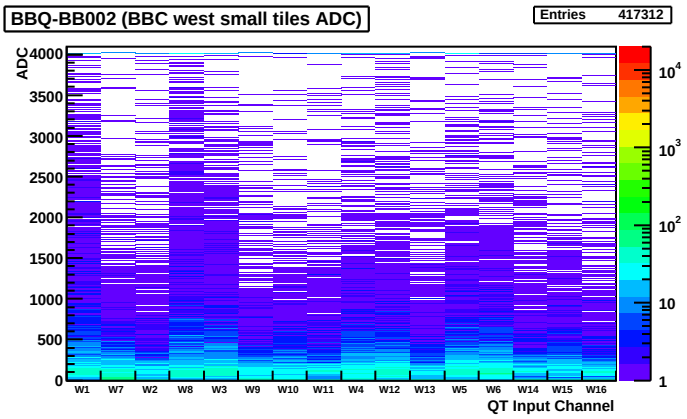

MIX-TF101
Entries 156492
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

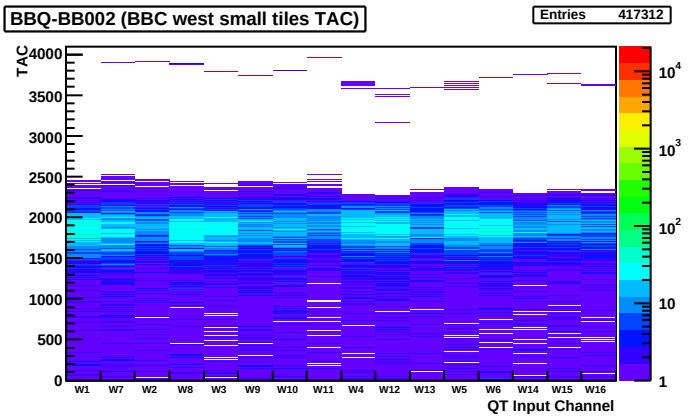

L1-TF201
Entries 26082
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

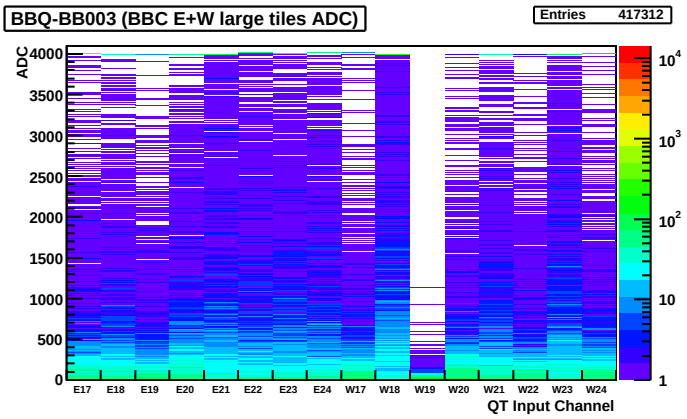

L1-TF201
Entries 156492
Threshold bits
1
0
14898
14154
11788
13279
11509
13287
11184
11928
14294
12803
14573
12795
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

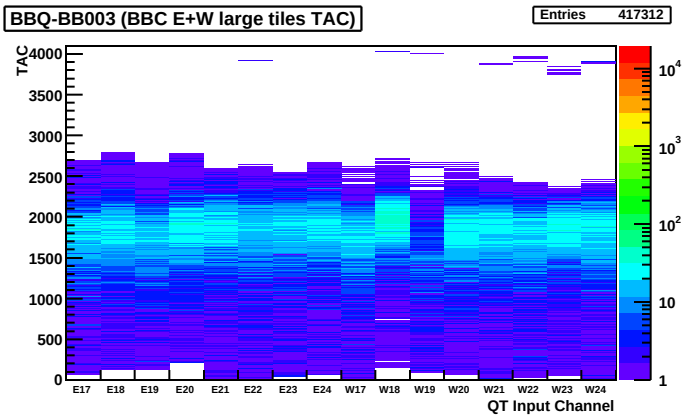

BBQ-BB001 (BBC east small tiles ADC)
Entries 417312
ADC
QT Input Channel

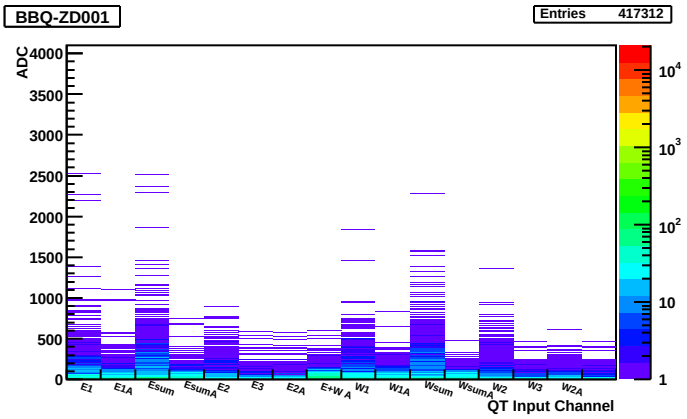

BBQ-BB001 (BBC east small tiles TAC)
Entries 417312
TAC
QT Input Channel

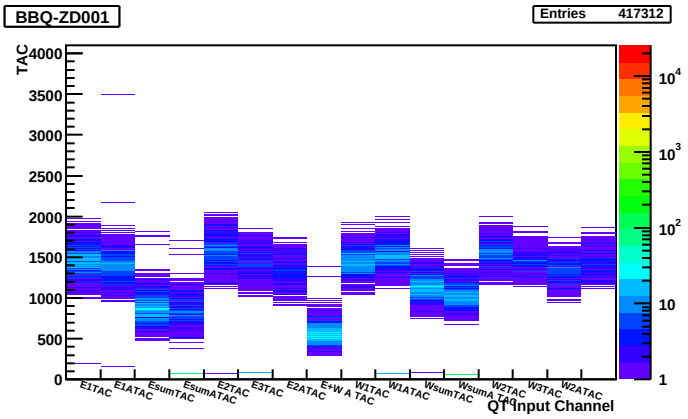

BBQ-BB002 (BBC west small tiles ADC)
Entries 417312
ADC
QT Input Channel

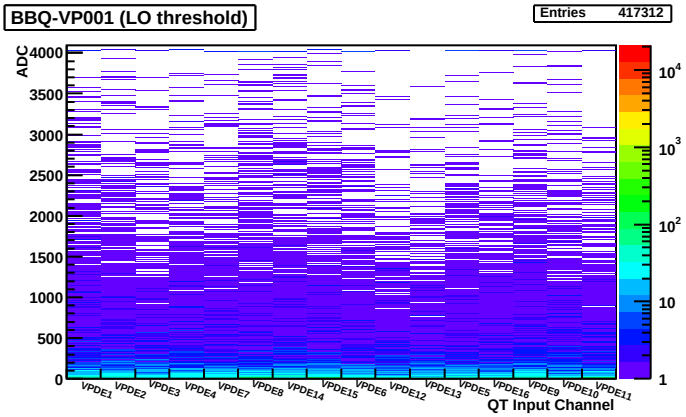

BBQ-BB002 (BBC west small tiles TAC)
Entries 417312
TAC
QT Input Channel

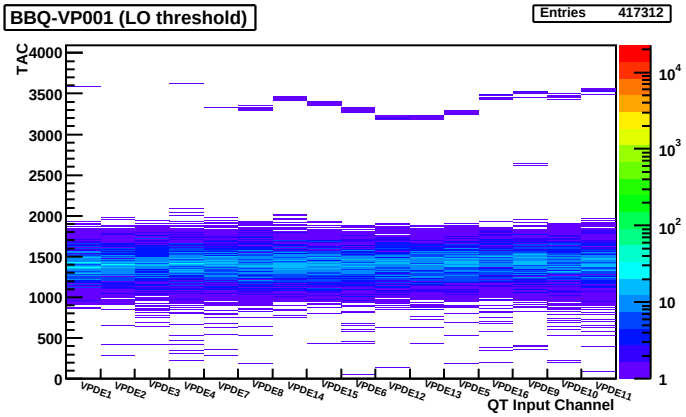

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 417312
ADC
QT Input Channel

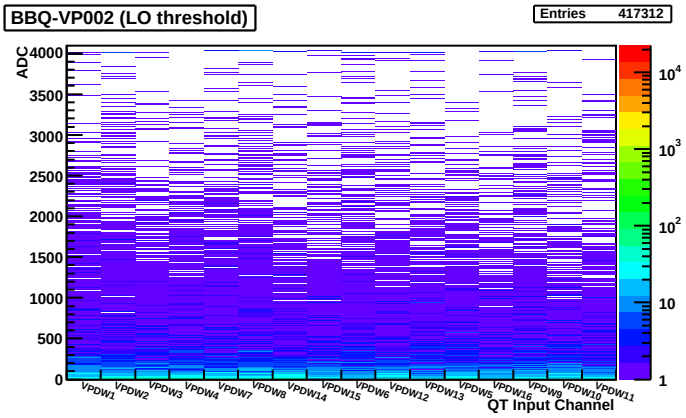

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 417312
TAC
QT Input Channel

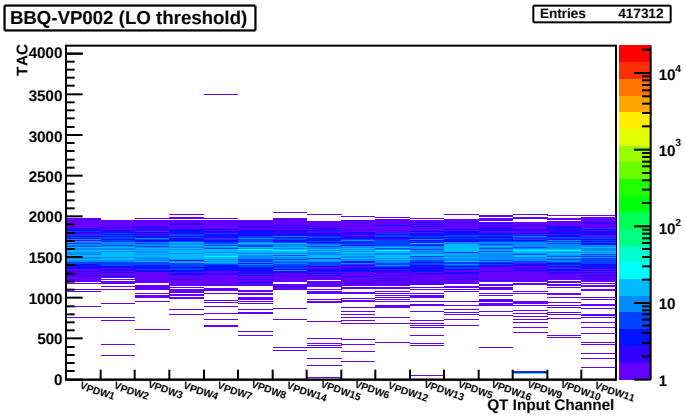

BBQ-ZD001
Entries 417312
ADC
QT Input Channel

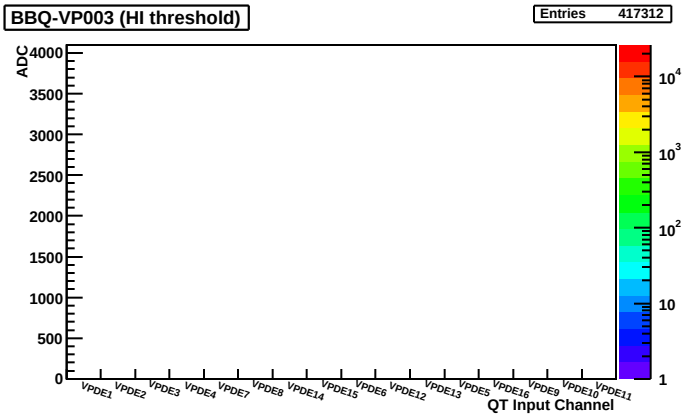

BBQ-ZD001
Entries 417312
TAC
QT Input Channel

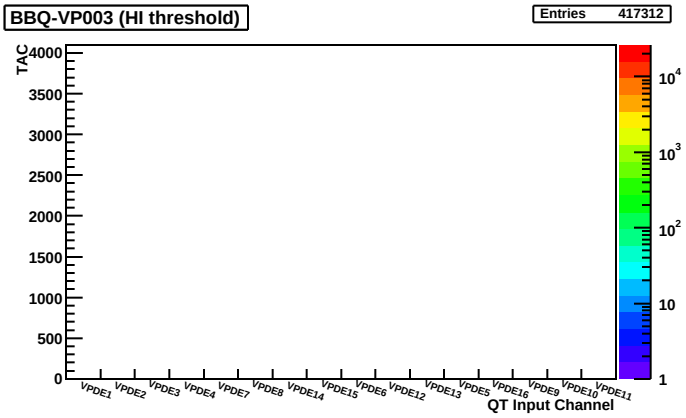

BBQ-VP001 (LO threshold)
Entries 417312
ADC
QT Input Channel

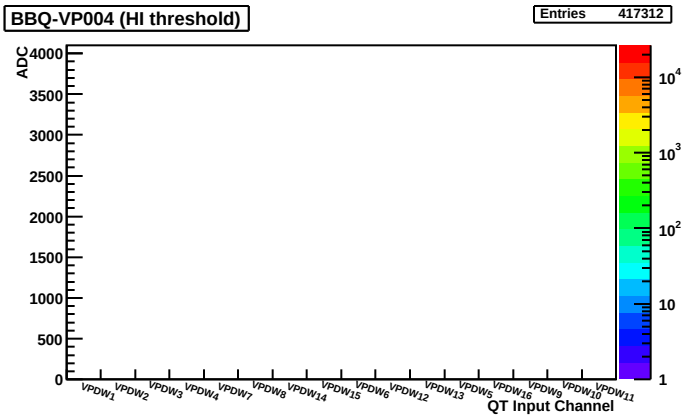

BBQ-VP001 (LO threshold)
Entries 417312
TAC
QT Input Channel

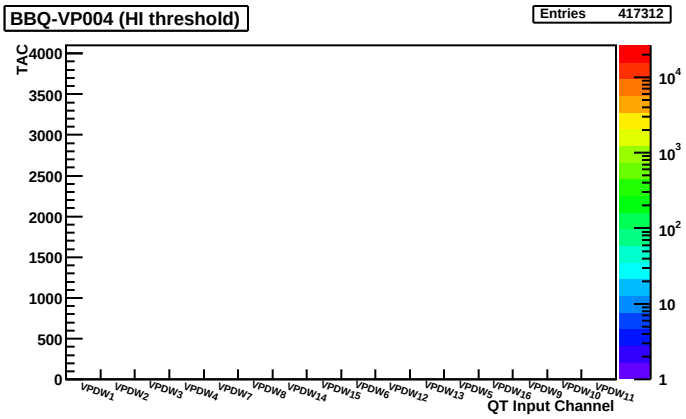

BBQ-VP002 (LO threshold)
Entries 417312
ADC
QT Input Channel

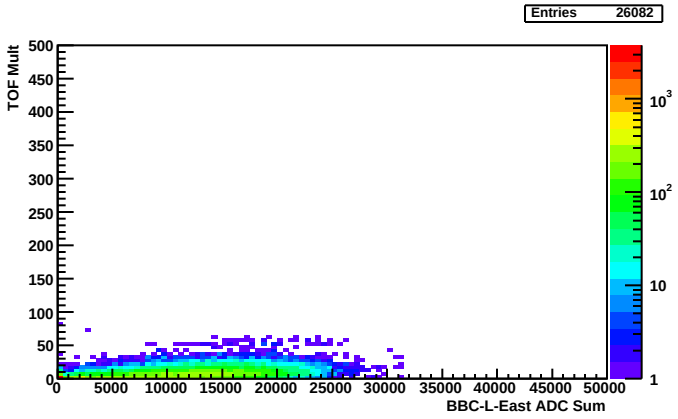

BBQ-VP002 (LO threshold)
Entries 417312
TAC
QT Input Channel

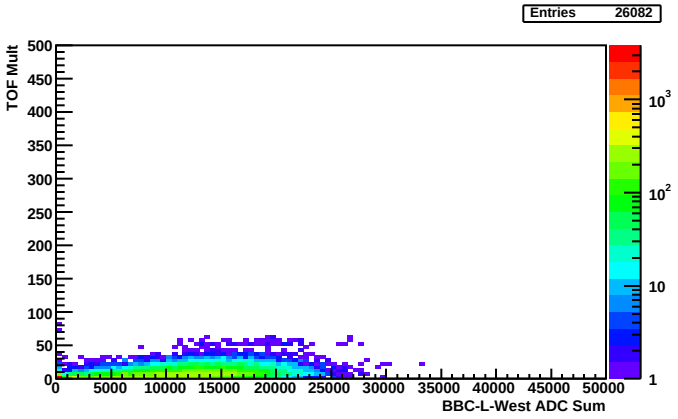

BBQ-VP003 (HI threshold)
Entries 417312
ADC
QT Input Channel

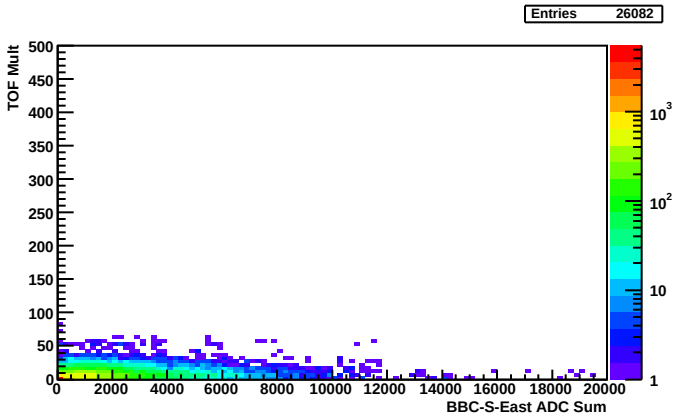

BBQ-VP003 (HI threshold)
Entries 417312
TAC
QT Input Channel

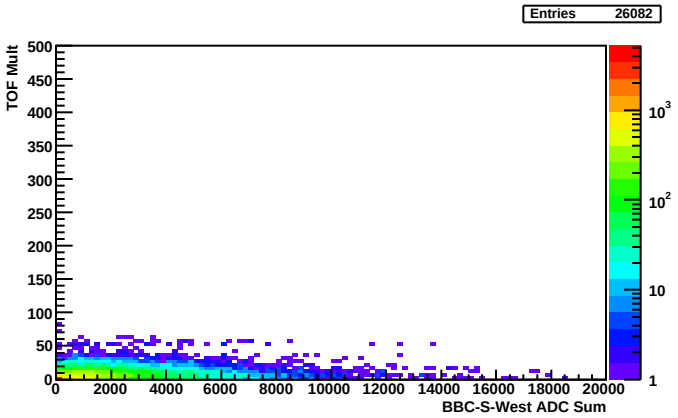

BBQ-VP004 (HI threshold)
Entries 417312
ADC
QT Input Channel

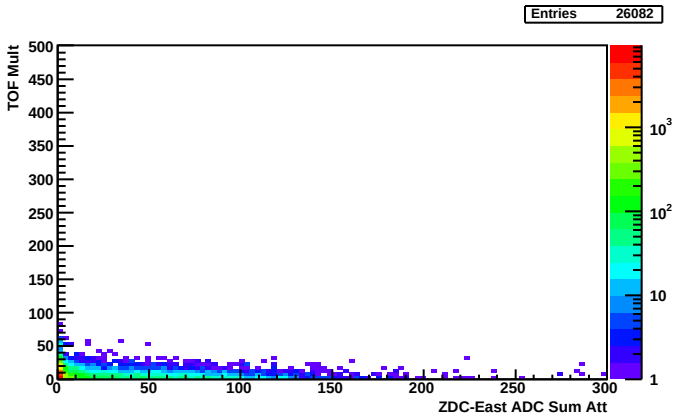

BBQ-VP004 (HI threshold)
Entries 417312
TAC
QT Input Channel

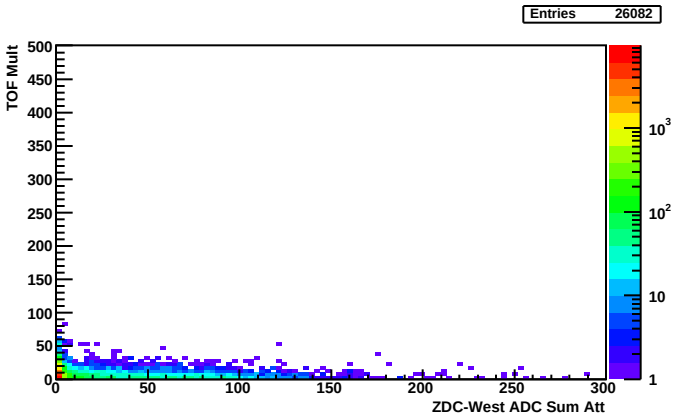

Entries 26082
TOF Mult
BBC-L-East ADC Sum

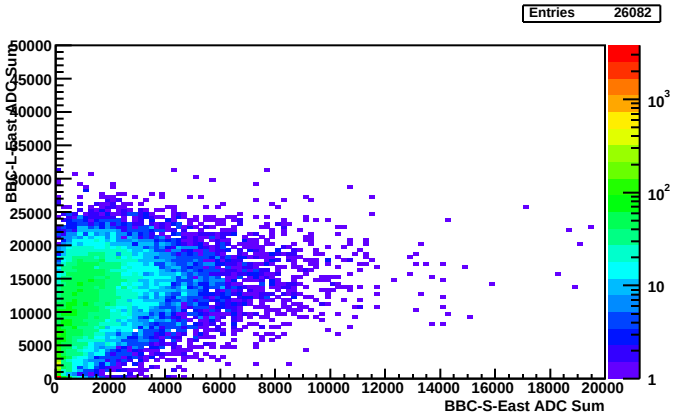

Entries 26082
TOF Mult
BBC-L-West ADC Sum

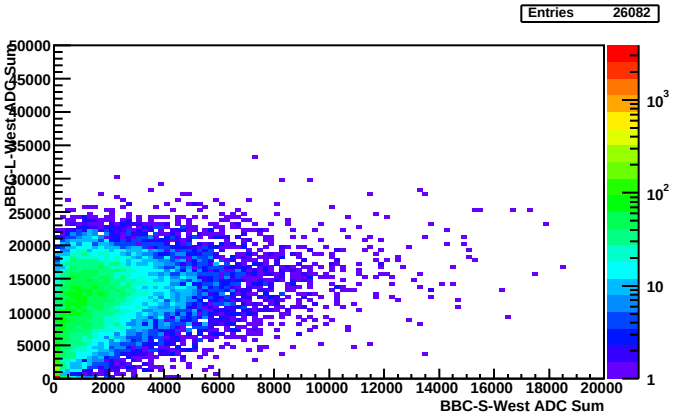

Entries 26082
TOF Mult
BBC-S-East ADC Sum

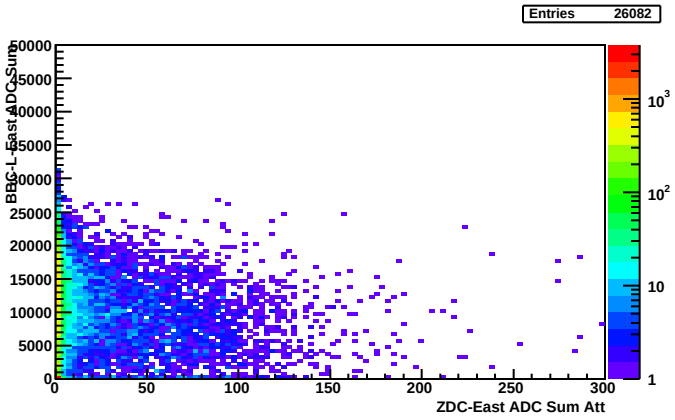

Entries 26082
TOF Mult
BBC-S-West ADC Sum

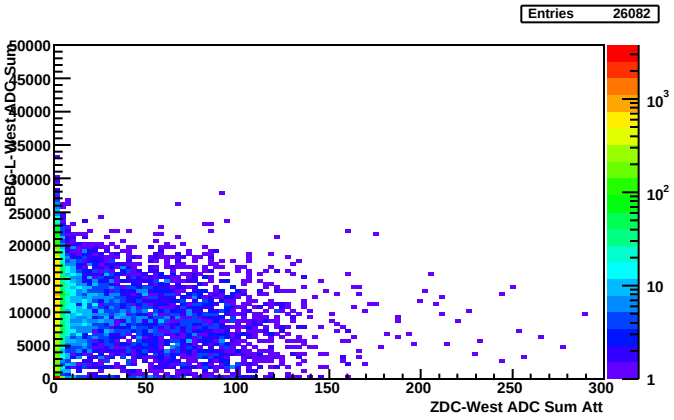

Entries 26082
TOF Mult
ZDC-East ADC Sum Att

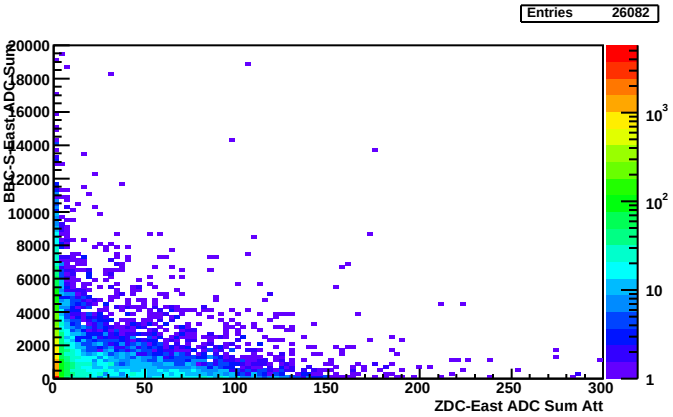

Entries 26082
TOF Mult
ZDC-West ADC Sum Att

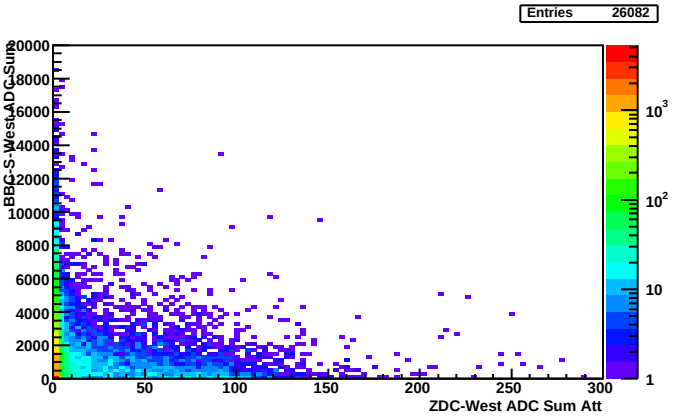

Entries 26082
BBC-L-East ADC Sum
BBC-S-East ADC Sum

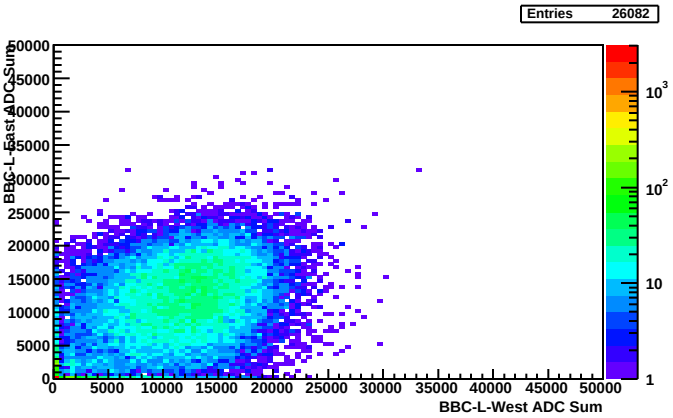

Entries 26082
BBC-L-West ADC Sum
BBC-S-West ADC Sum

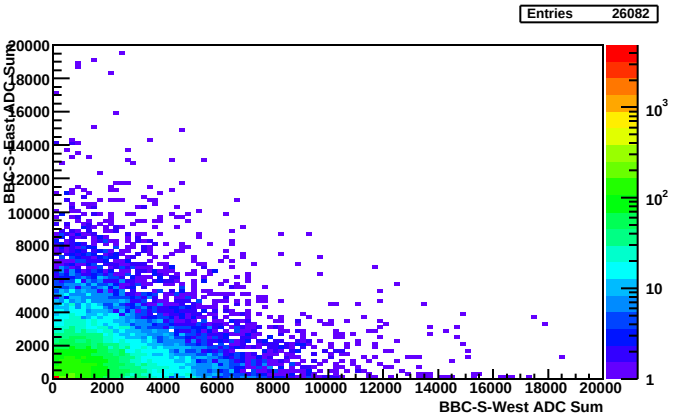

Entries 26082
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

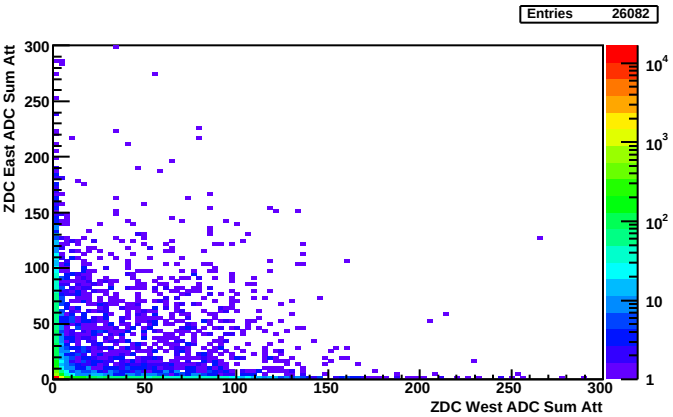

Entries 26082
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

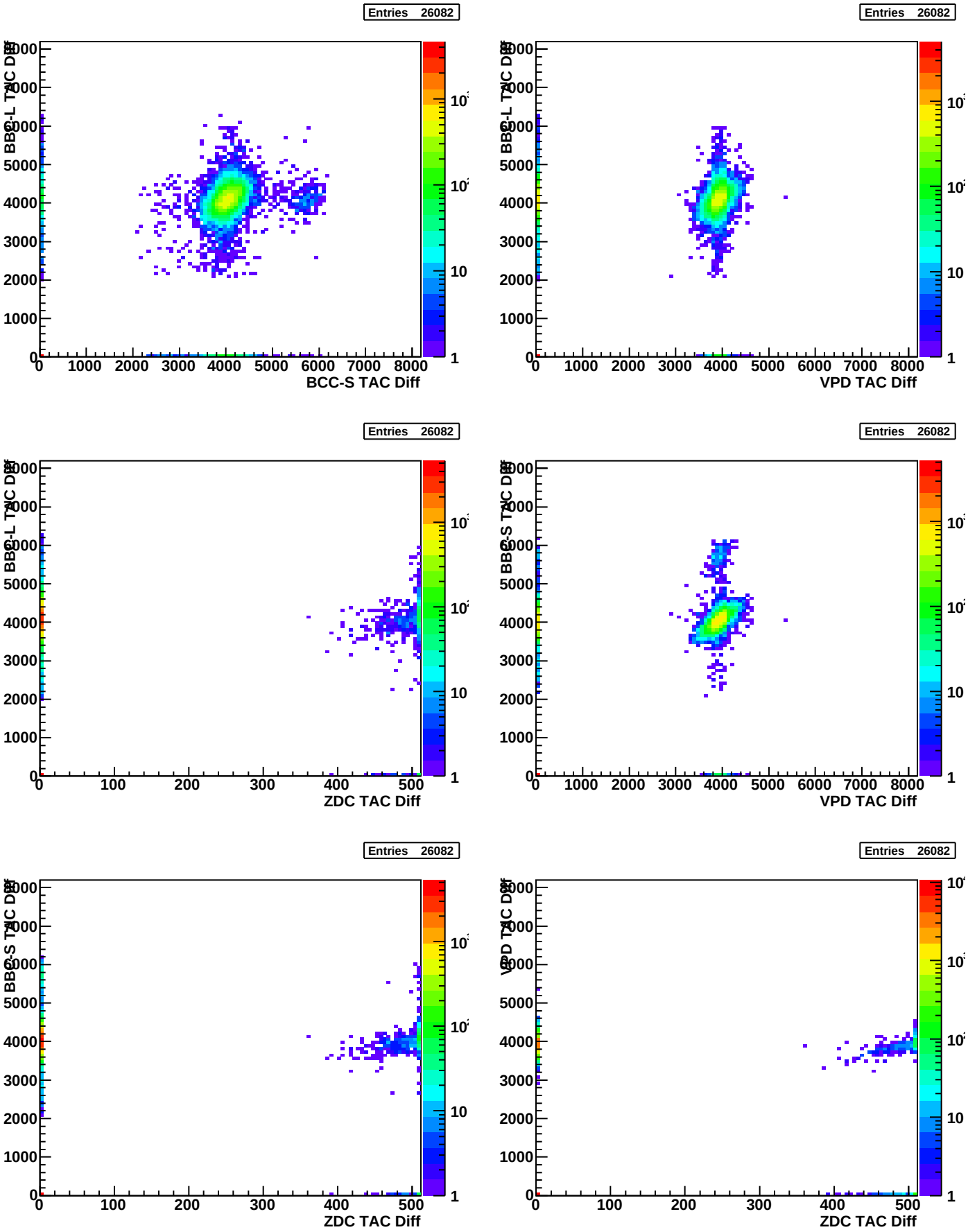

Entries 26082
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

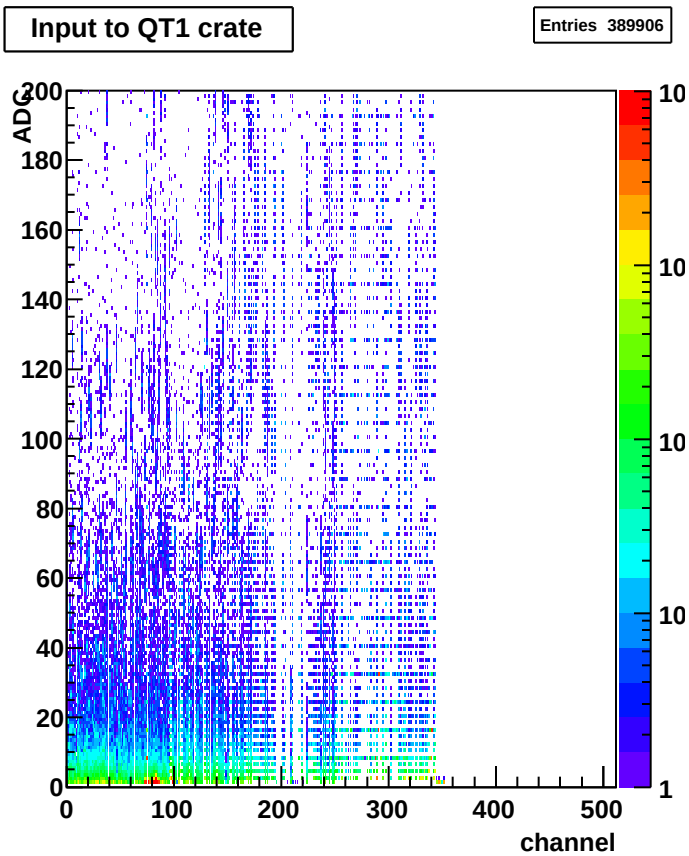

Entries 26082
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

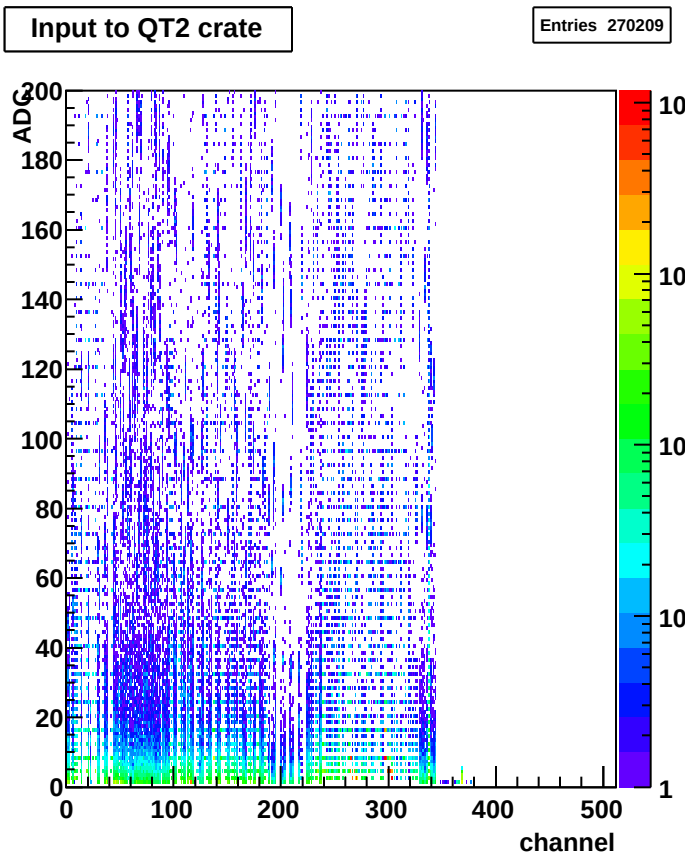

Entries 26082
BBC-L-East ADC Sum
BBC-L-West ADC Sum

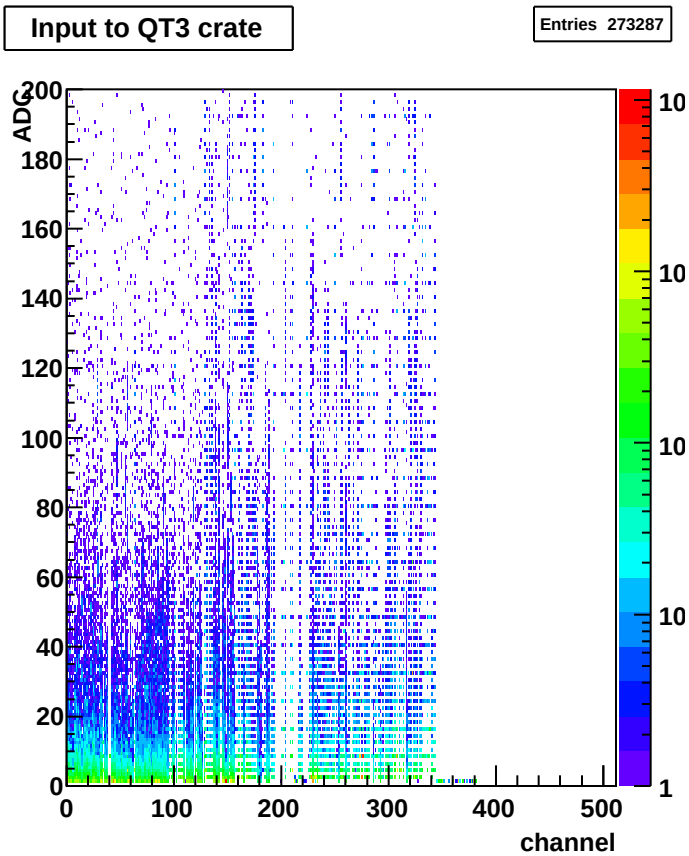

Entries 26082
BBC-S-East ADC Sum
BBC-S-West ADC Sum

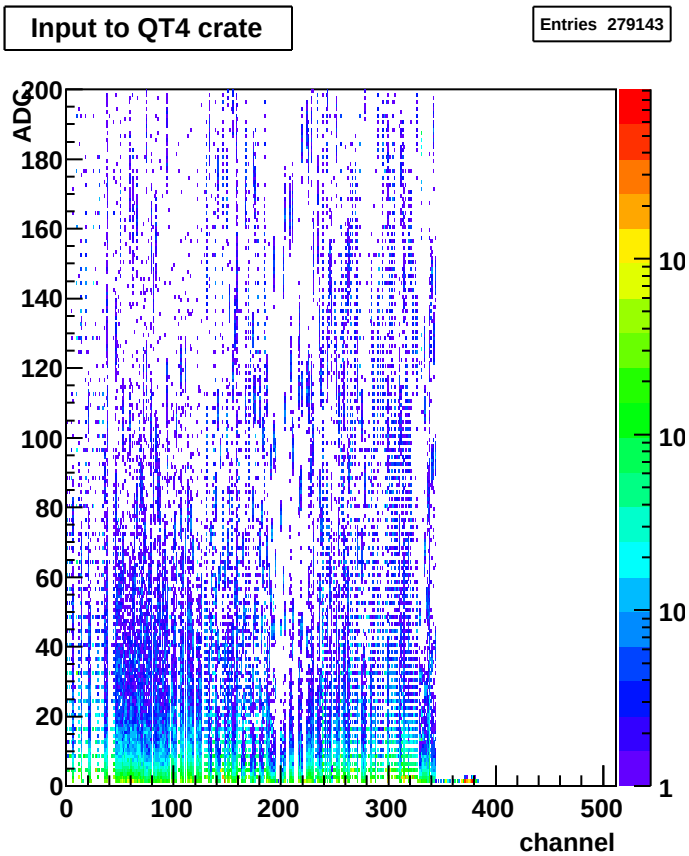

Entries 26082
ZDC East ADC Sum Att
ZDC West ADC Sum Att

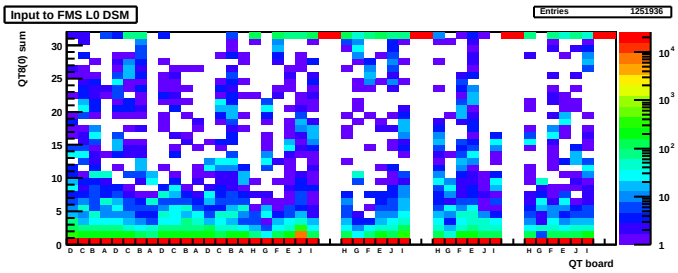

Entries 26082
BBC-L TAC Diff
BCC-S TAC Diff
Entries 26082
BBC-L TAC Diff
VPD TAC Diff
Entries 26082
BBC-L TAC Diff
ZDC TAC Diff
Entries 26082
BBC-S TAC Diff
VPD TAC Diff
Entries 26082
BBC-S TAC Diff
ZDC TAC Diff
Entries 26082
VPD TAC Diff
ZDC TAC Diff

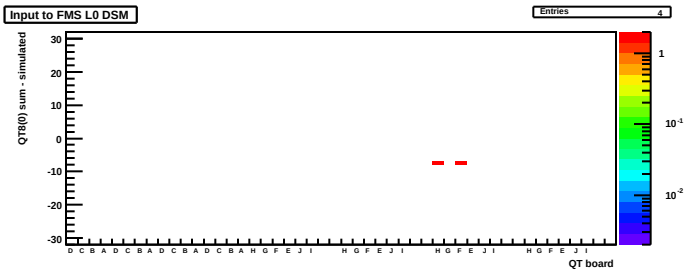

Input to QT1 crate
Entries 389906
ADC
channel

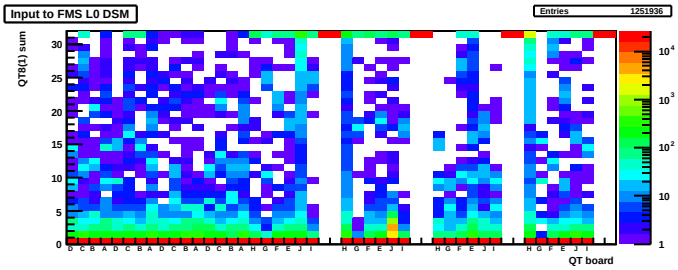

Input to QT2 crate
Entries 270209
ADC
channel

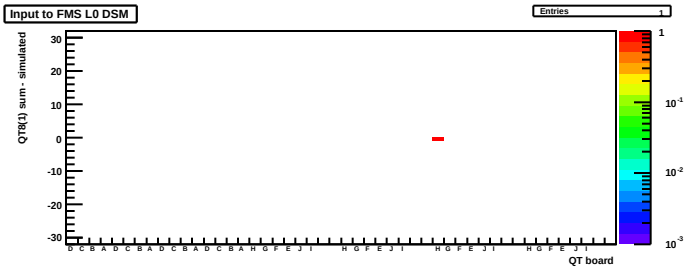

Input to QT3 crate
Entries 273287
ADC
channel

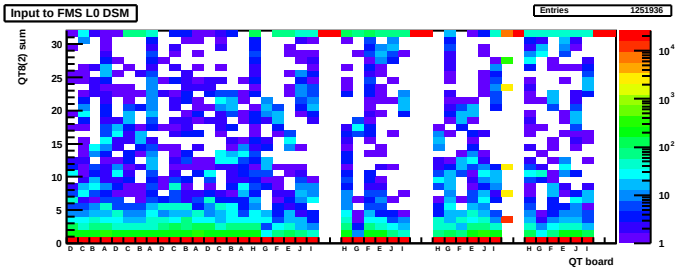

Input to QT4 crate
Entries 279143
ADC
channel

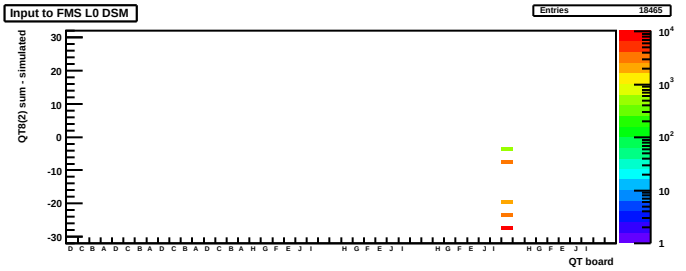

Input to FMS L0 DSM
Entries 1251936
QT8(0) sum
QT board

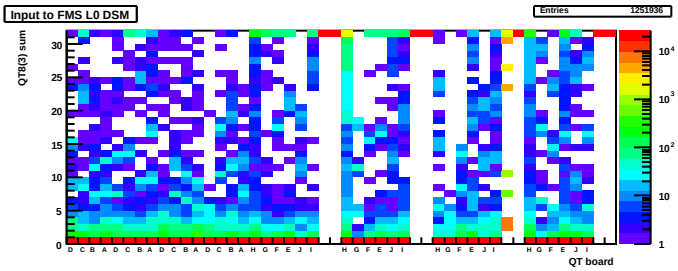

Input to FMS L0 DSM
Entries 4
QT8(0) sum - simulated
QT board

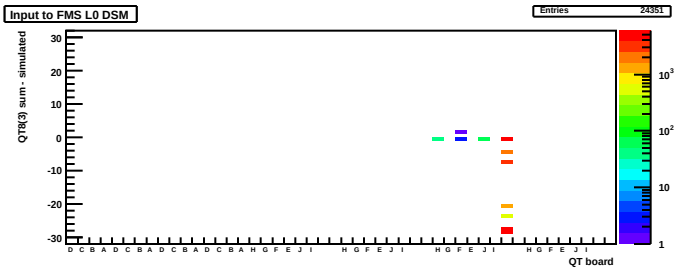

Input to FMS L0 DSM
Entries 1251936
QT8(1) sum
QT board

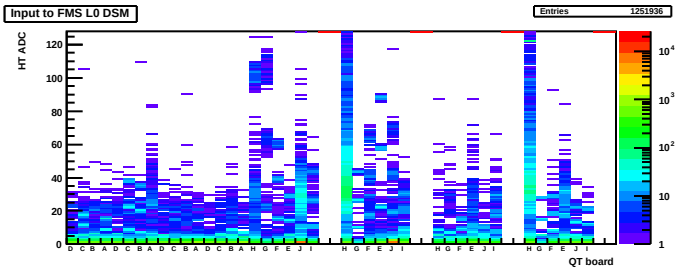

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

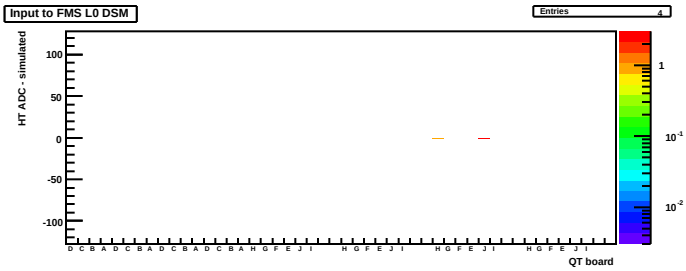

Input to FMS L0 DSM
Entries 1251936
QT8(2) sum
QT board

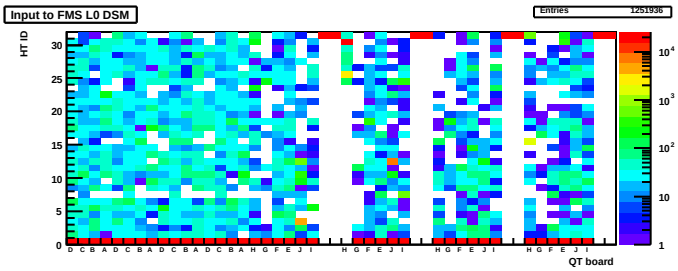

Input to FMS L0 DSM
Entries 18465
QT8(2) sum - simulated
QT board

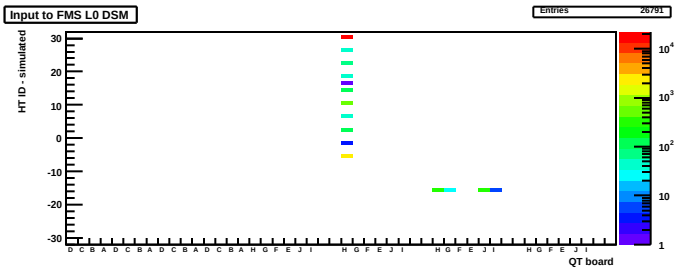

Input to FMS L0 DSM
Entries 1251936
QT8(3) sum
QT board

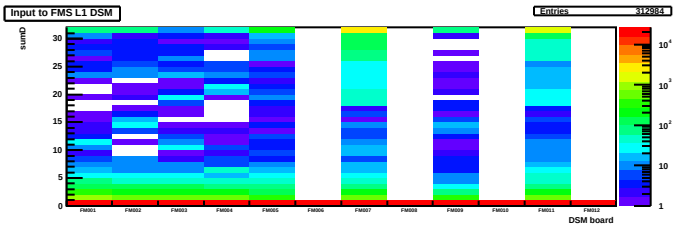

Input to FMS L0 DSM
Entries 24351
QT8(3) sum - simulated
QT board

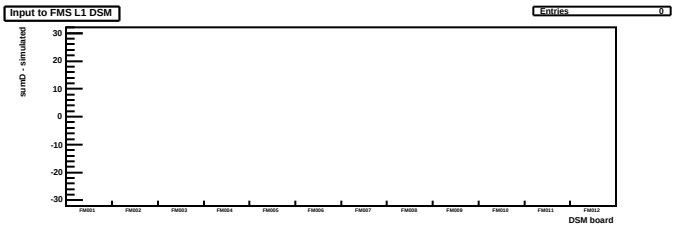

Input to FMS L0 DSM
Entries 1251936
HT ADC
QT board

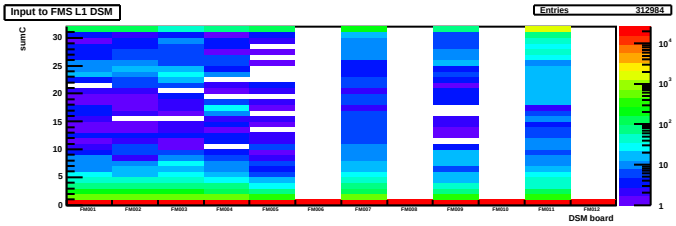

Input to FMS L0 DSM
Entries 4
HT ADC - simulated
QT board

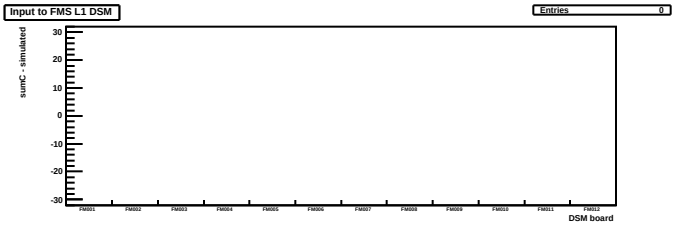

Input to FMS L0 DSM
Entries 1251936
HT ID
QT board

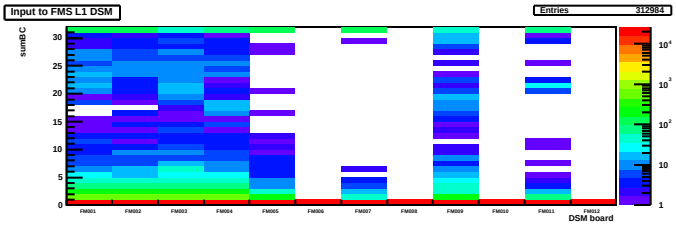

Input to FMS L0 DSM
Entries 26791
HT ID - simulated
QT board

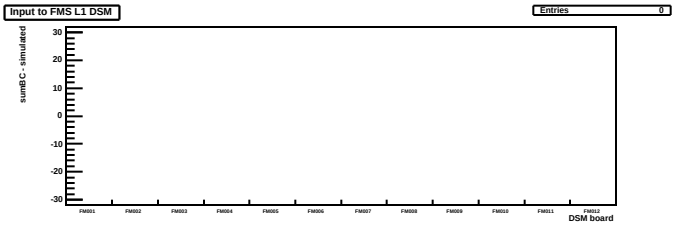

Input to FMS L1 DSM
Entries 312984
sumD
DSM board

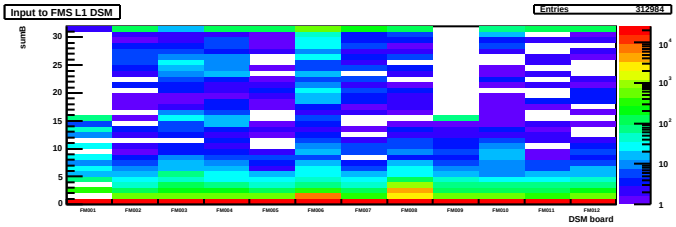

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

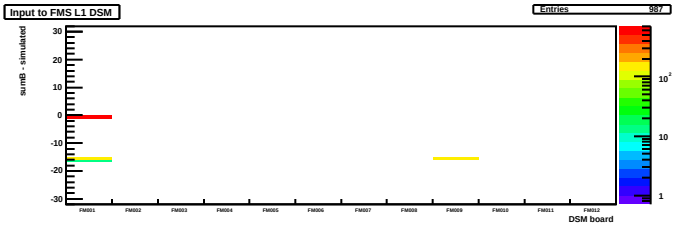

Input to FMS L1 DSM
Entries 312984
sumC
DSM board

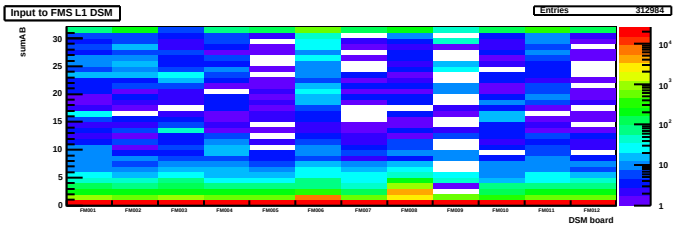

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

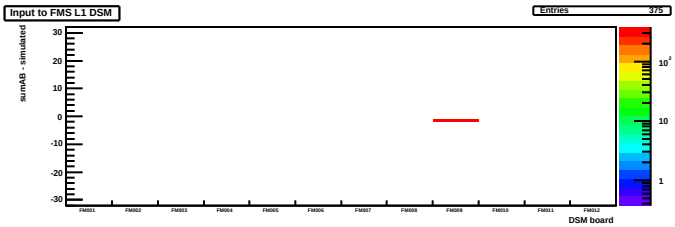

Input to FMS L1 DSM
Entries 312984
sumBC
DSM board

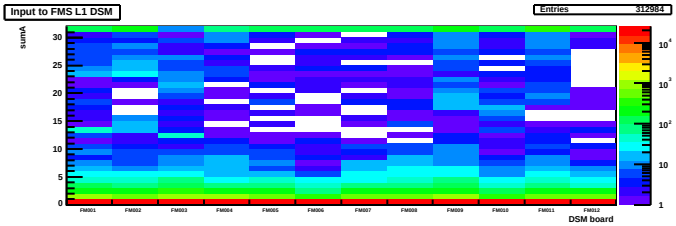

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

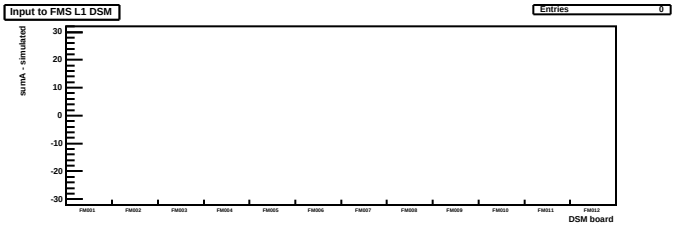

Input to FMS L1 DSM
Entries 312984
sumB
DSM board

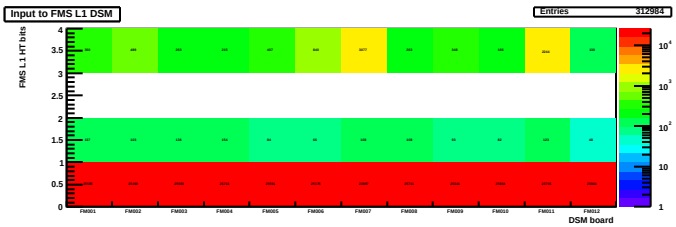

Input to FMS L1 DSM
Entries 987
sumB - simulated
DSM board

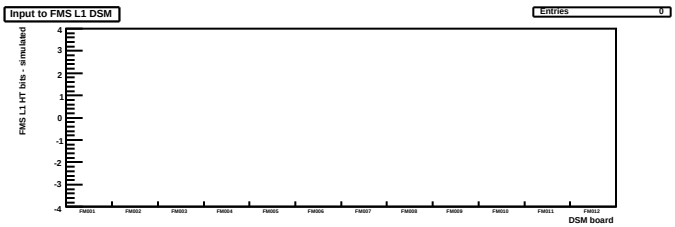

Input to FMS L1 DSM
Entries 312984
sumAB
DSM board

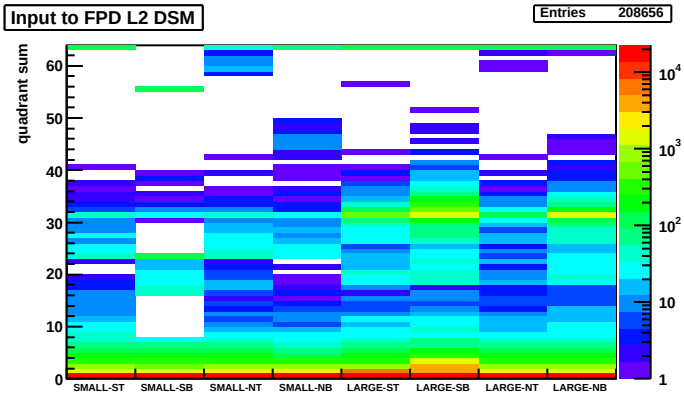

Input to FMS L1 DSM
Entries 375
sumAB - simulated
DSM board

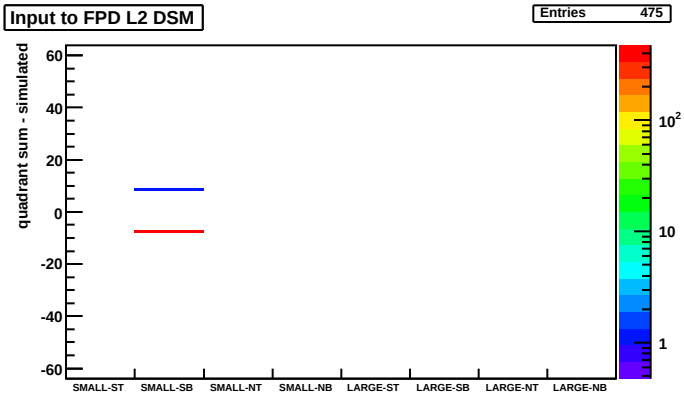

Input to FMS L1 DSM
Entries 312984
sumA
DSM board

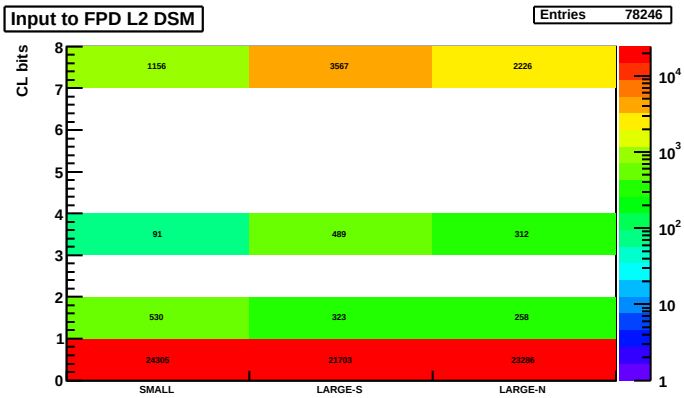

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

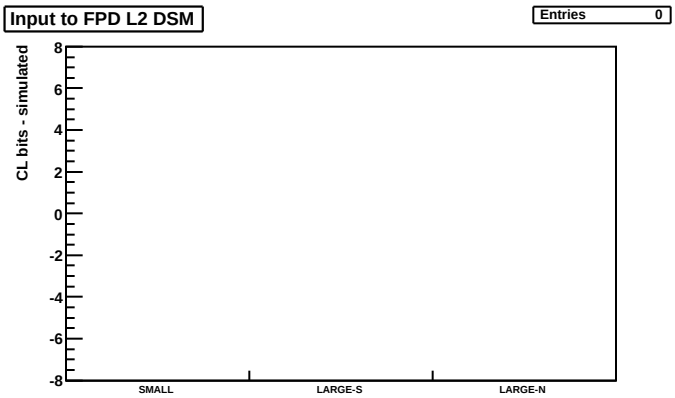

Input to FMS L1 DSM
Entries 312984
FMS L1 HT bits
DSM board

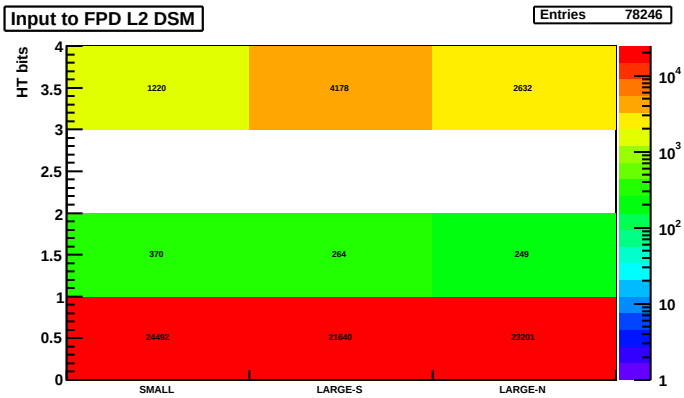

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

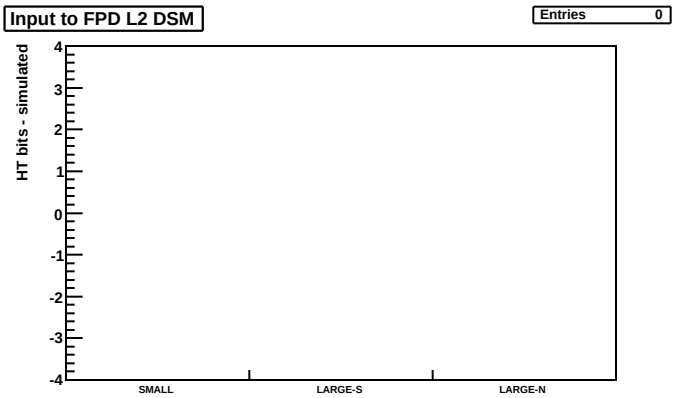

Input to FPD L2 DSM
Entries 208656
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

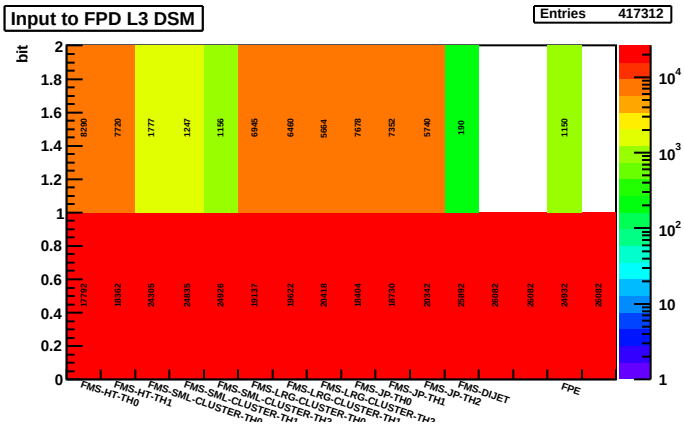

Input to FPD L2 DSM
Entries 475
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

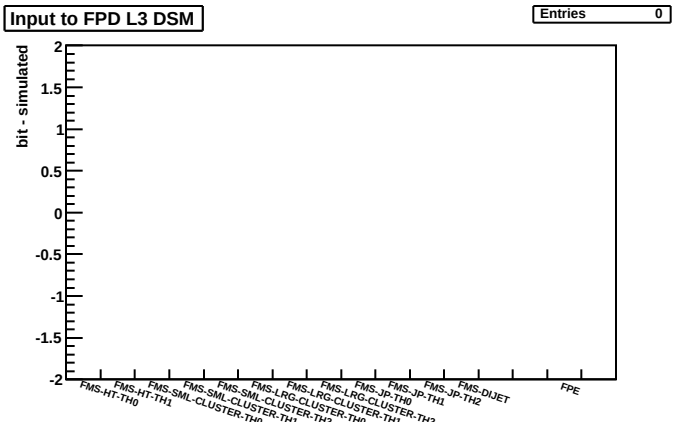

Input to FPD L2 DSM
Entries 78246
CL bits
1156
3567
2226
91
489
312
530
323
258
24305
21703
23286
SMALL
LARGE-S
LARGE-N

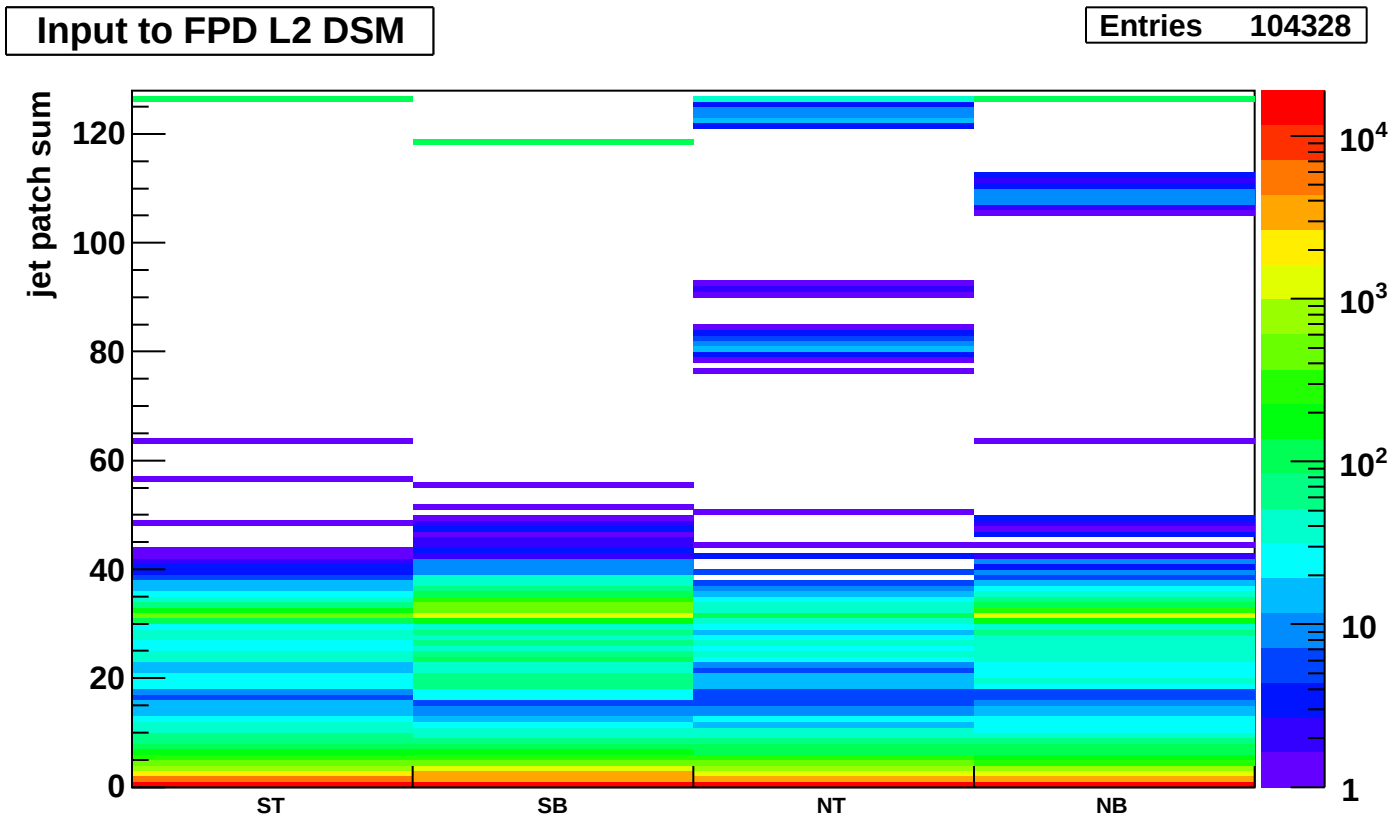

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

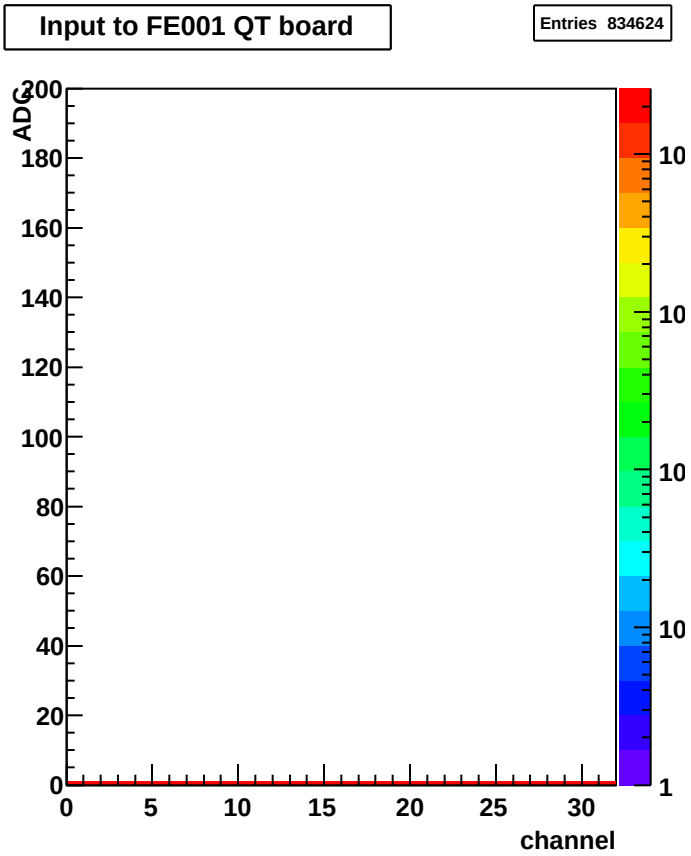

Input to FPD L2 DSM
Entries 78246
HT bits
1220
4178
2632
370
264
249
24492
21640
23201
SMALL
LARGE-S
LARGE-N

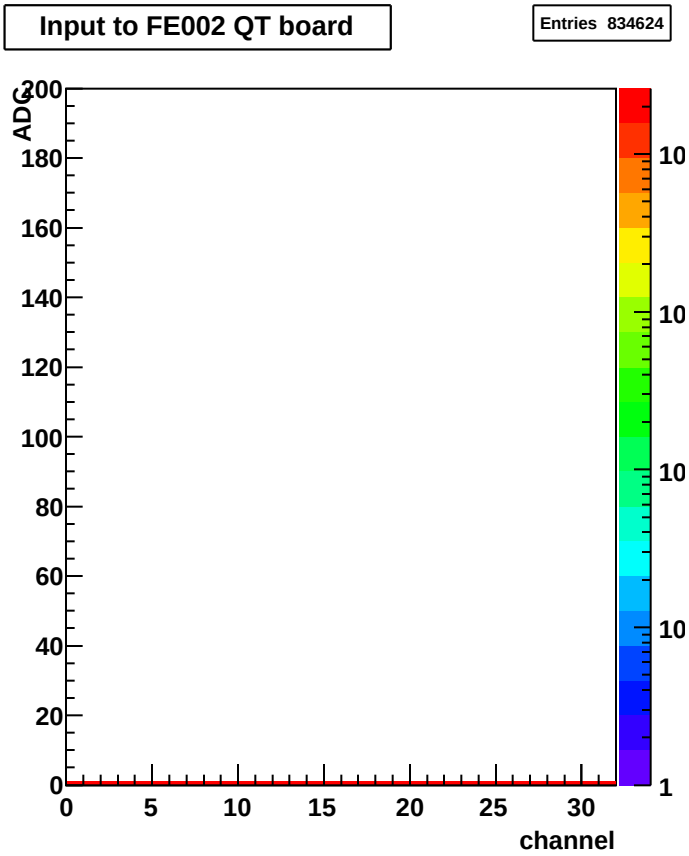

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

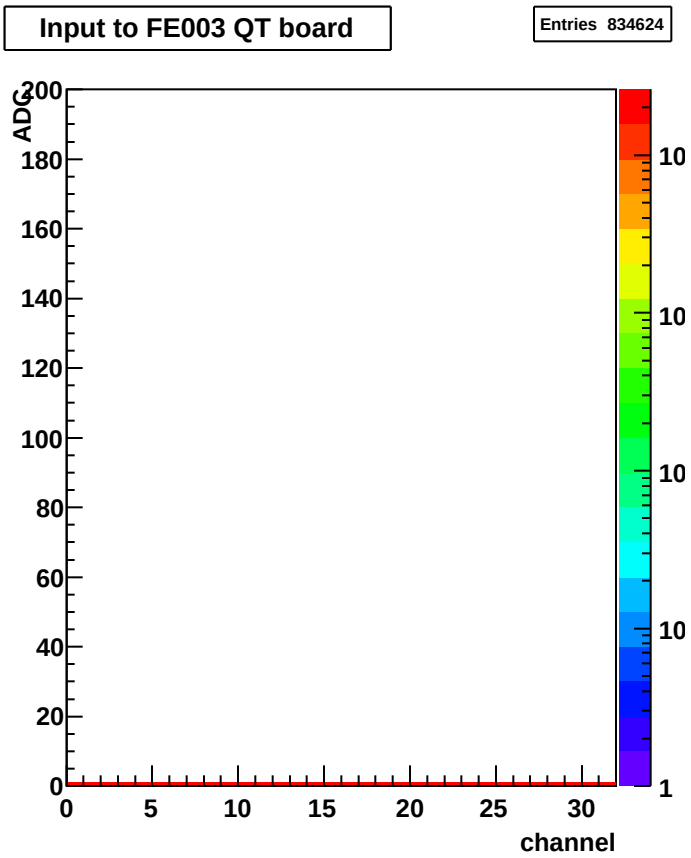

Input to FPD L3 DSM
Entries 417312
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

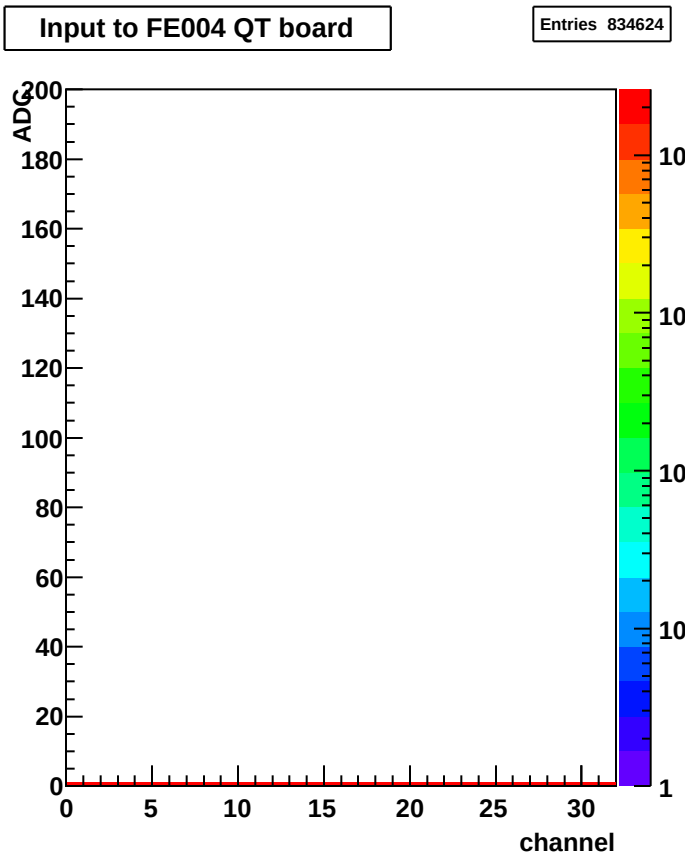

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

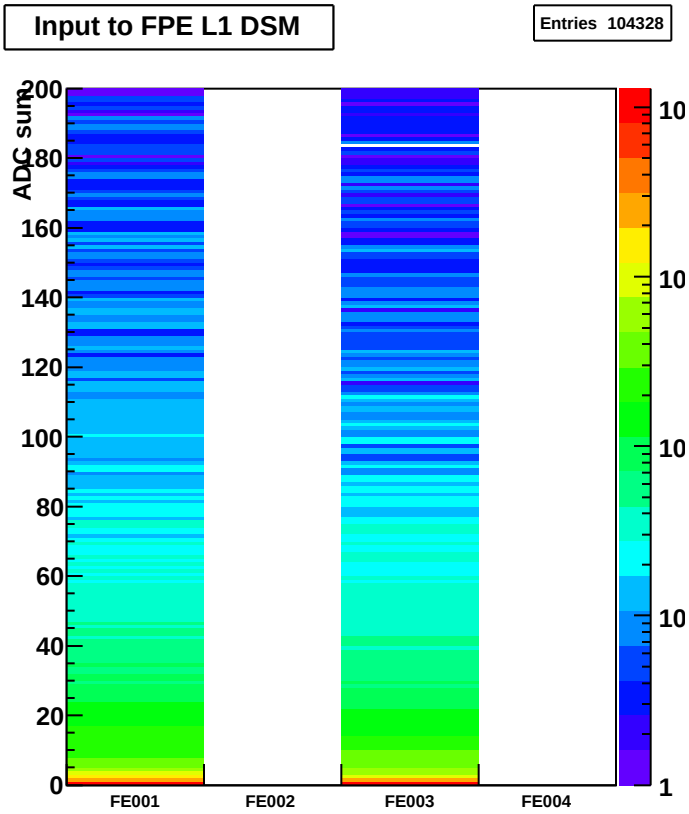

Input to FPD L2 DSM
Entries 104328
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

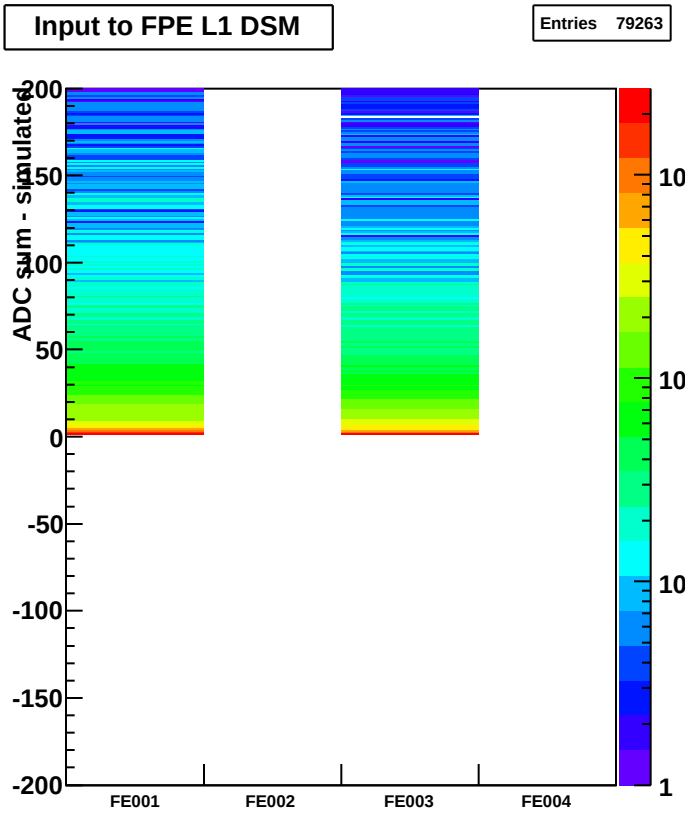

Input to FE001 QT board
Entries 834624
ADC
channel

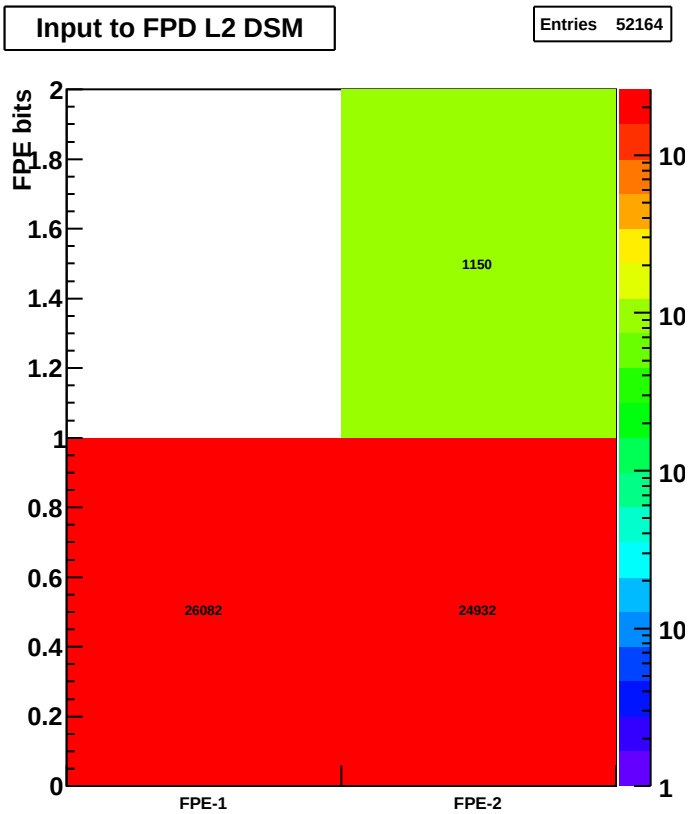

Input to FE002 QT board
Entries 834624
ADC
channel

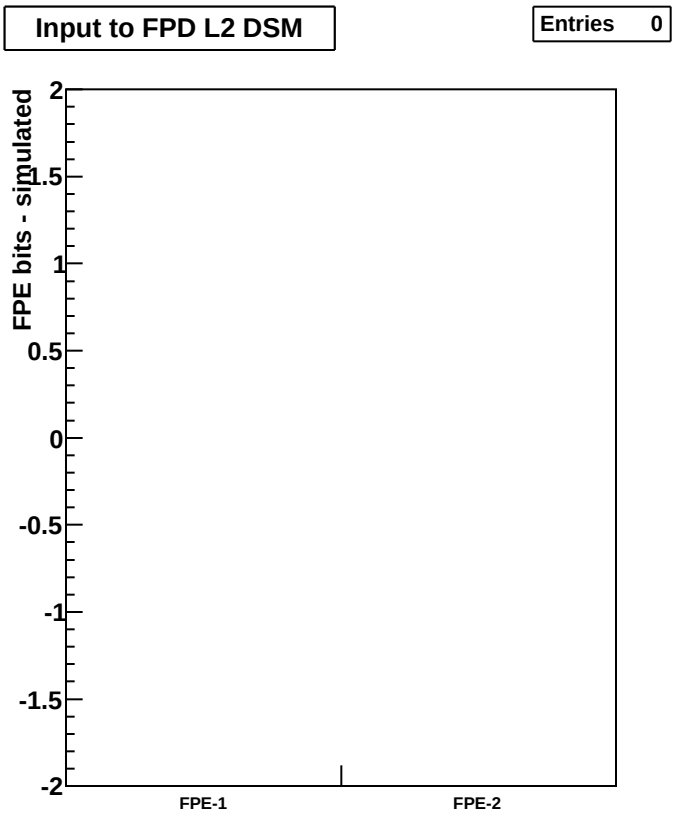

Input to FE003 QT board
Entries 834624
ADC
channel


Input to FE004 QT board
Entries 834624
ADC
channel


Input to FPE L1 DSM
Entries 104328
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 79263
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 52164
FPE bits
1150
26082
24932
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2