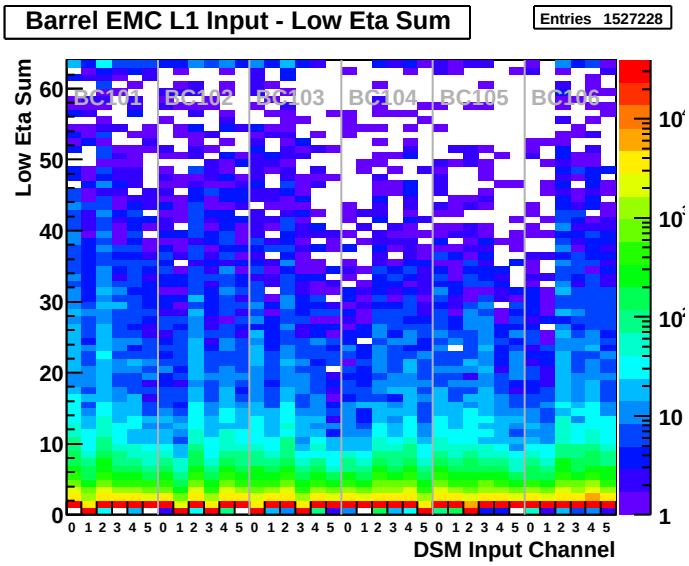
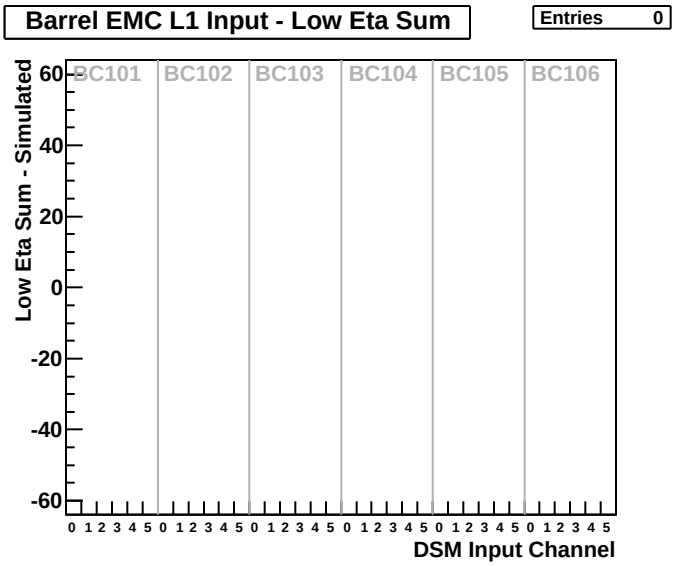
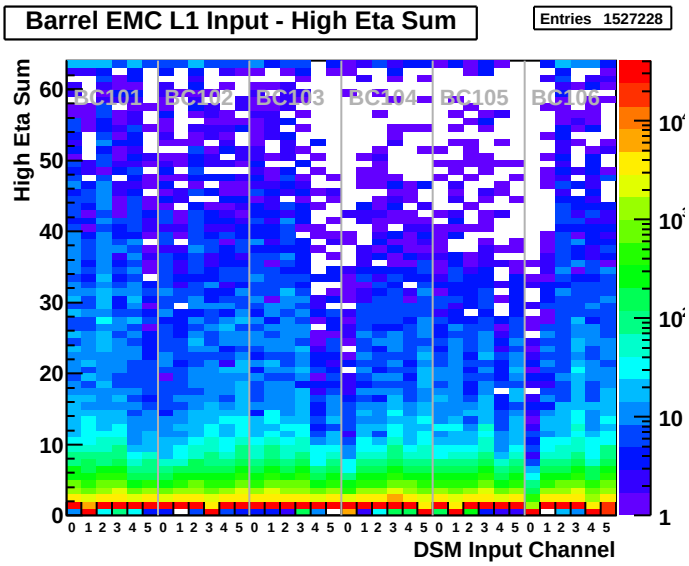
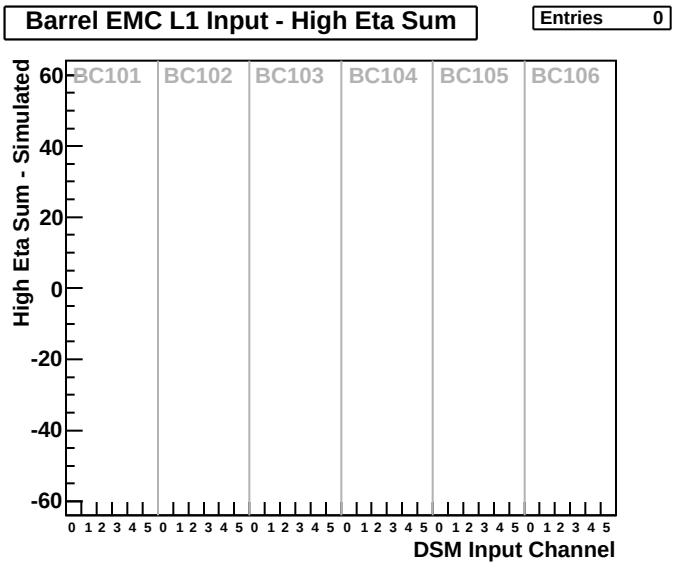
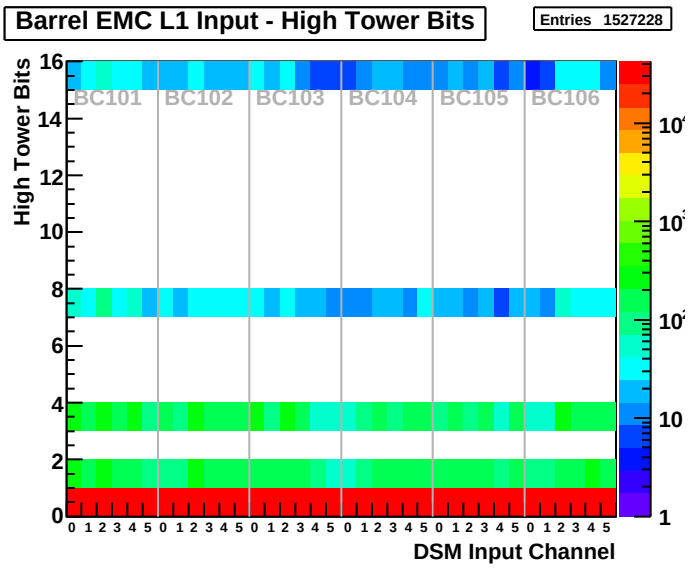
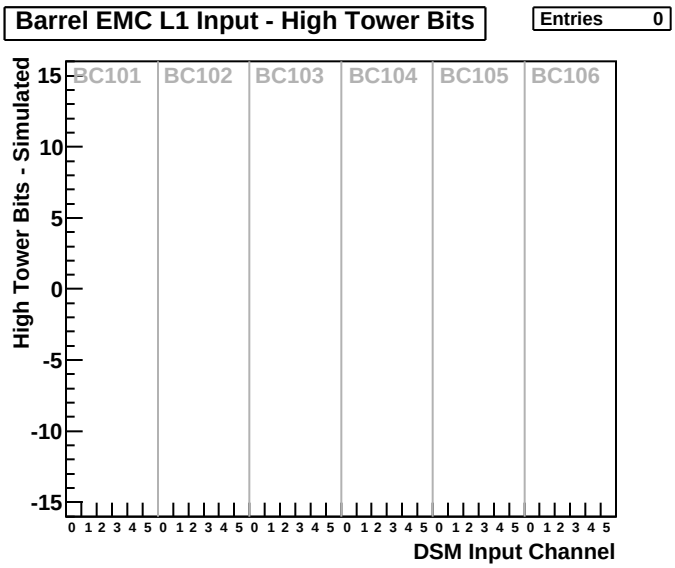

Barrel EMC L1 Input - Low Eta Sum
Entries 1527228
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

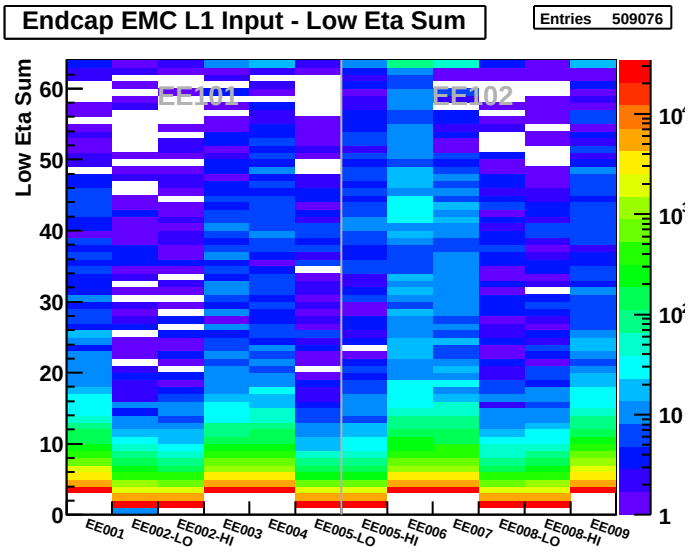

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

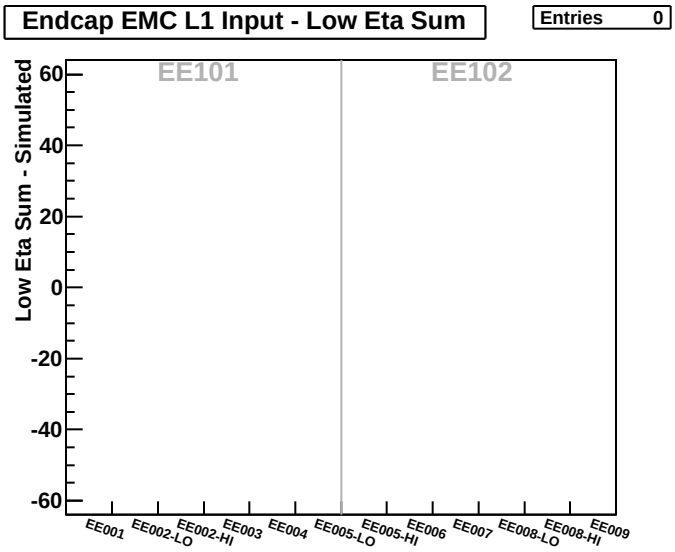

Barrel EMC L1 Input - High Eta Sum
Entries 1527228
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

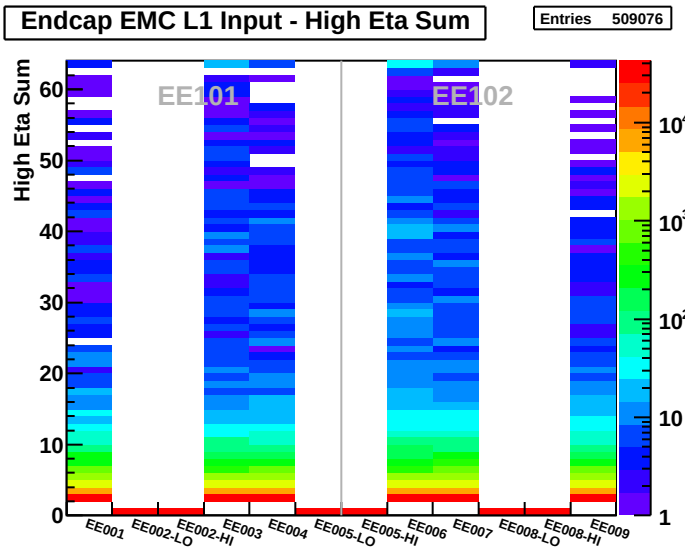

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

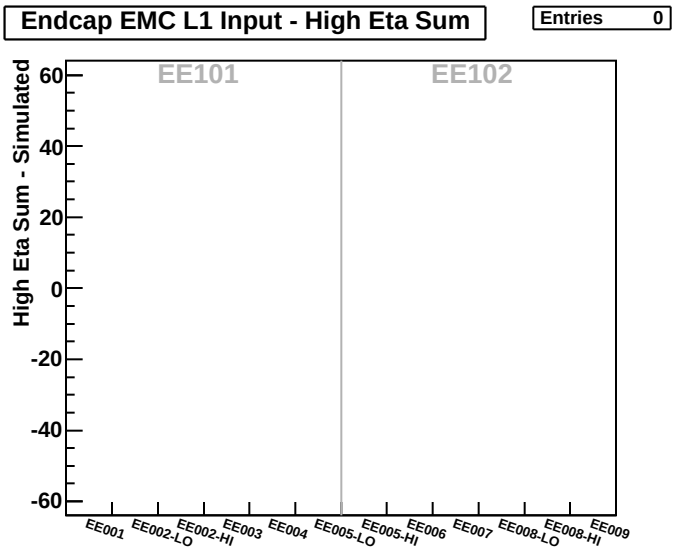

Barrel EMC L1 Input - High Tower Bits
Entries 1527228
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

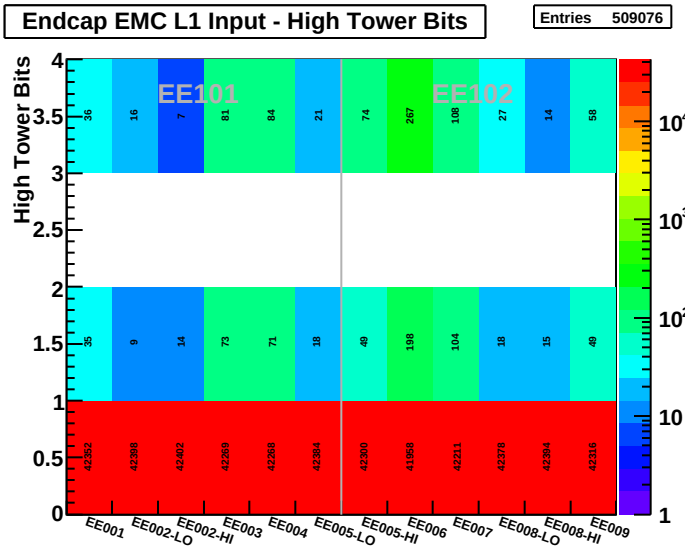

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

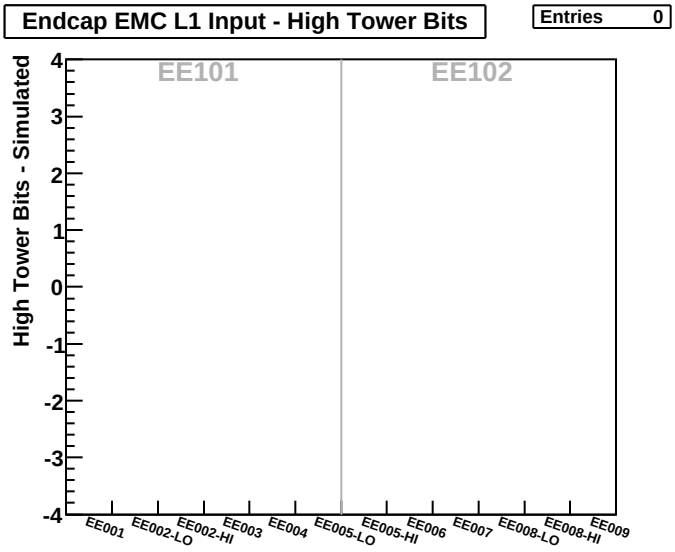

Endcap EMC L1 Input - Low Eta Sum
Entries 509076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

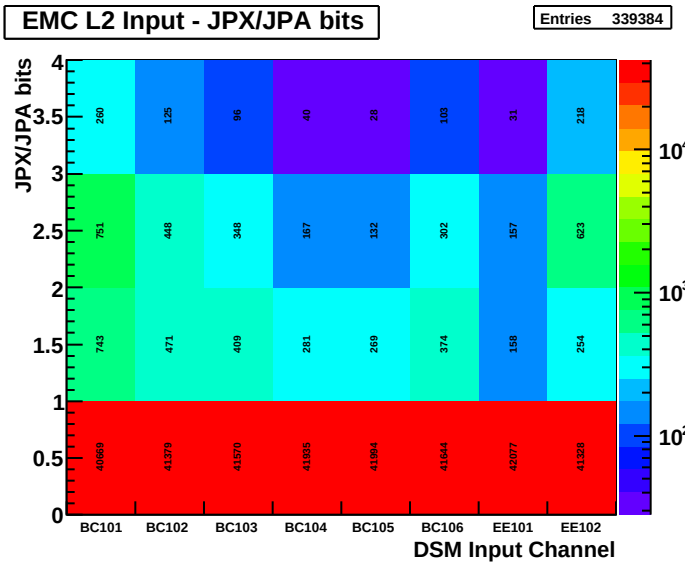

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

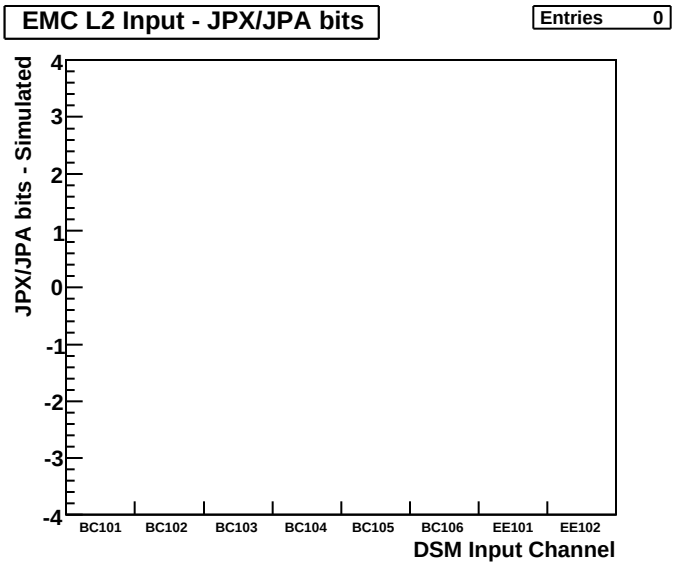

Endcap EMC L1 Input - High Eta Sum
Entries 509076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

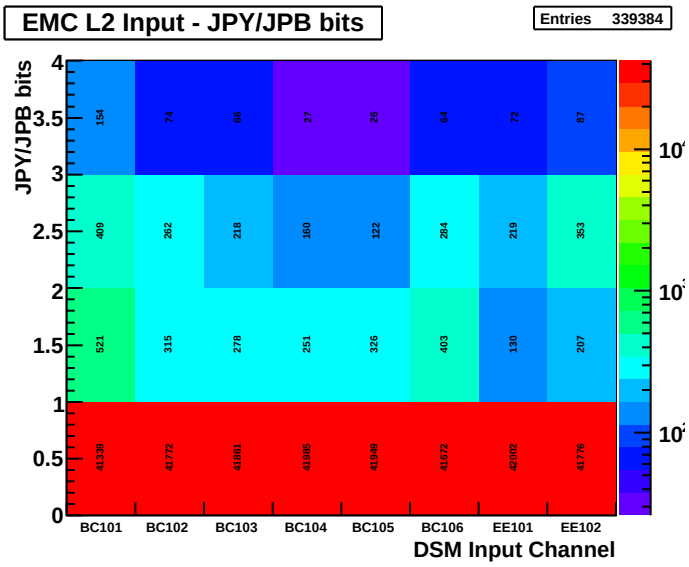

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

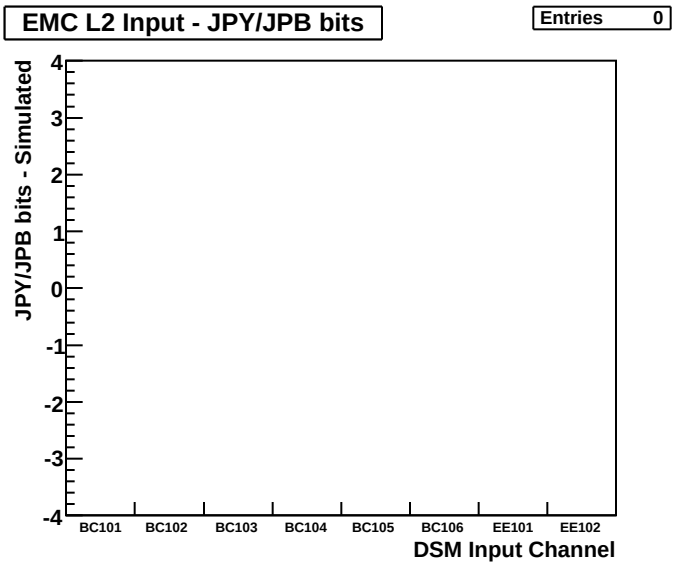

Endcap EMC L1 Input - High Tower Bits
Entries 509076
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

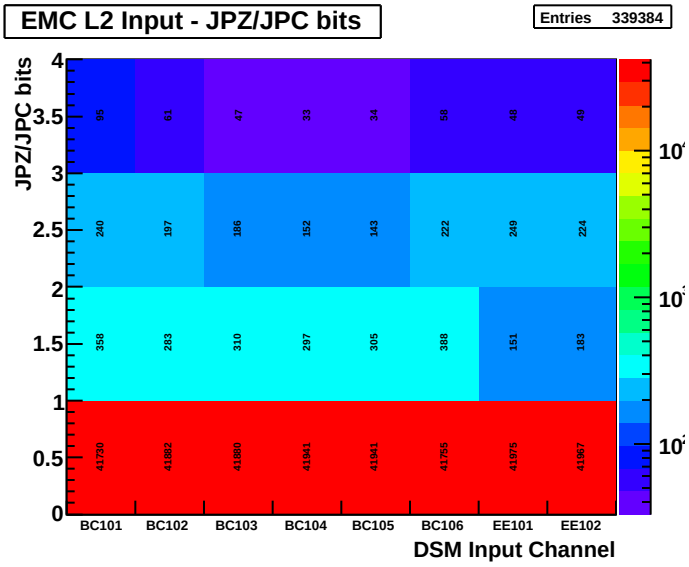

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

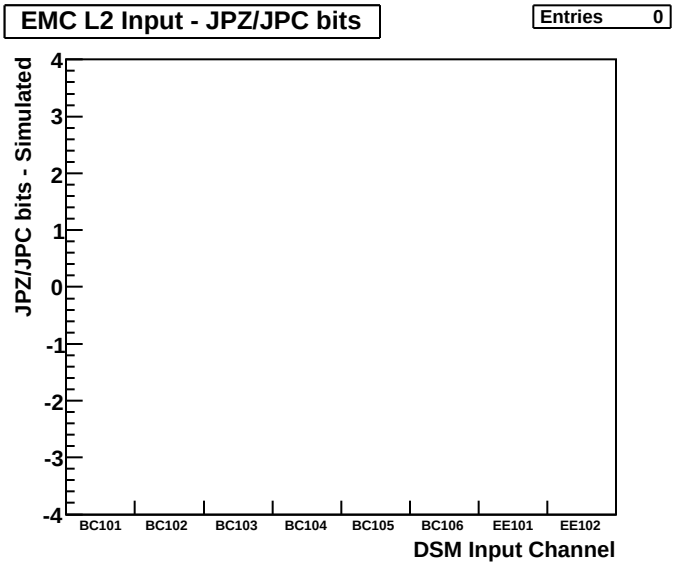

EMC L2 Input - JPX/JPA bits
Entries 339384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
280 125 96 40 28 103 31 218
751 448 348 167 132 302 157 623
743 471 409 281 269 374 158 254
40669 41379 41570 41935 41994 41644 42077 41328

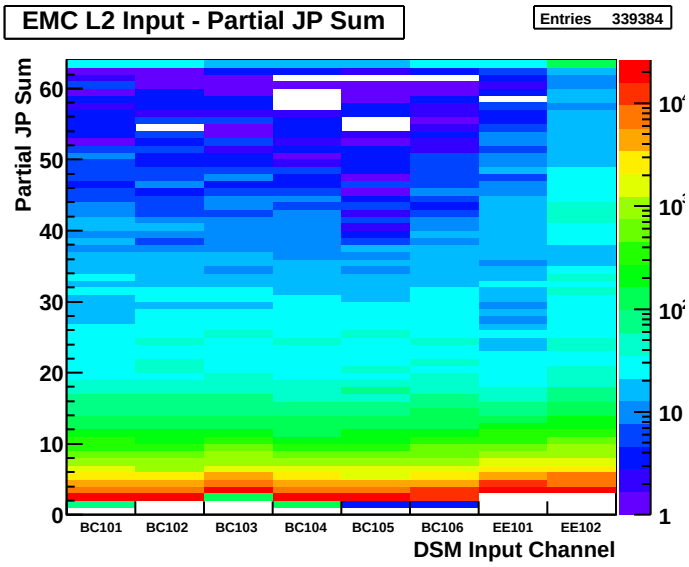

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

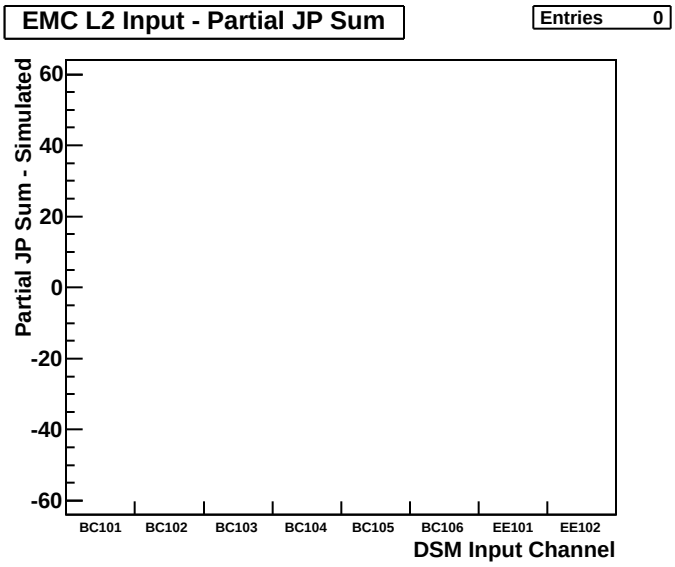

EMC L2 Input - JPY/JPB bits
Entries 339384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
154 74 66 27 26 64 72 87
409 262 218 160 122 284 219 353
521 315 278 251 326 403 130 207
41339 41772 41861 41985 41949 41672 42002 41776

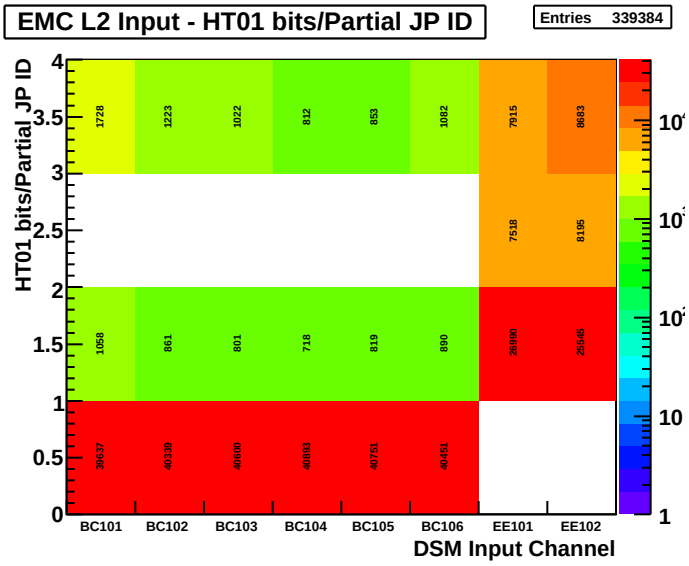

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

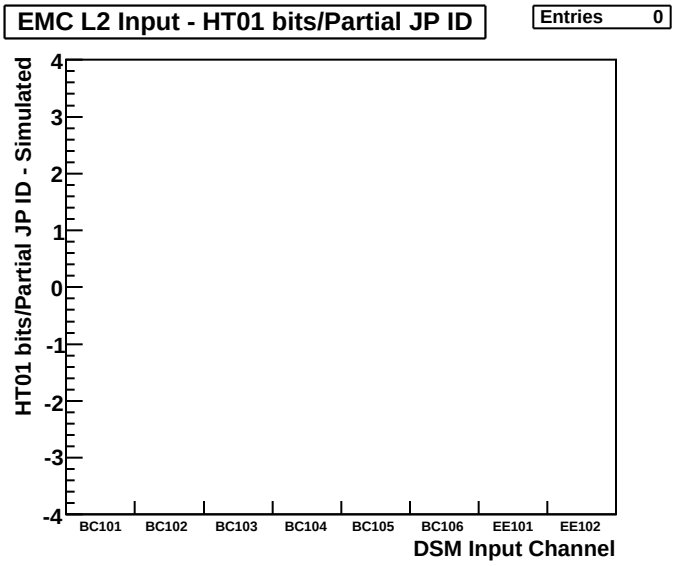

EMC L2 Input - JPZ/JPC bits
Entries 339384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
95 61 47 33 34 58 48 49
240 197 186 152 143 222 249 224
358 283 310 297 305 388 151 183
41730 41882 41880 41941 41941 41755 41975 41967

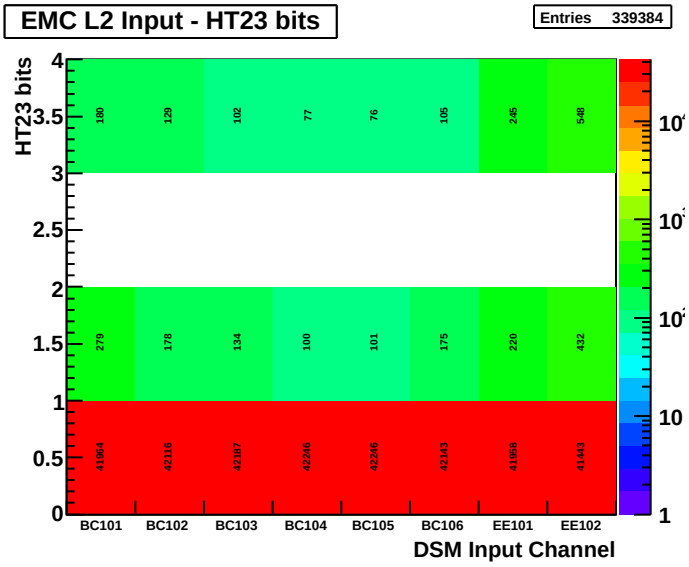

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

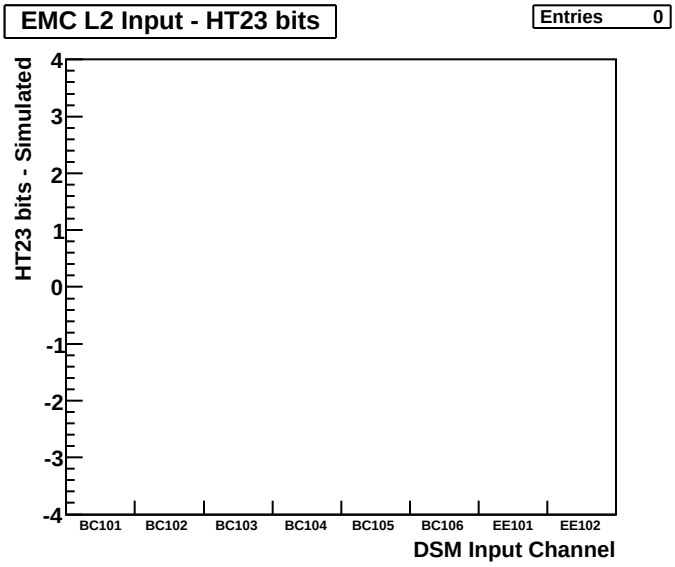

EMC L2 Input - Partial JP Sum
Entries 339384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

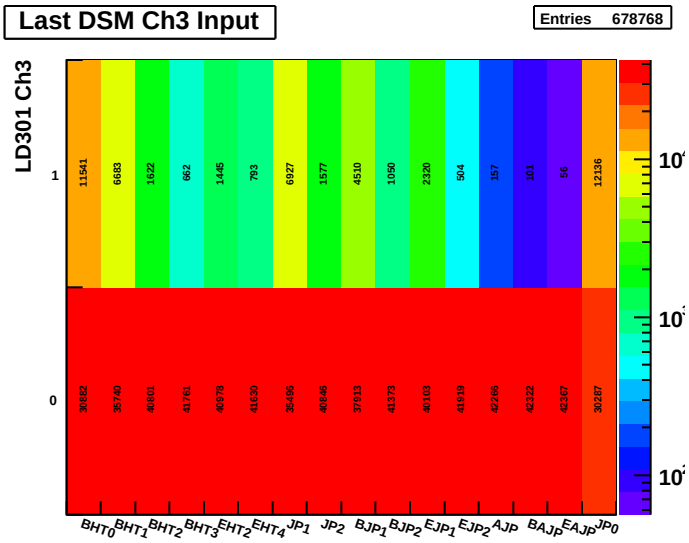

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

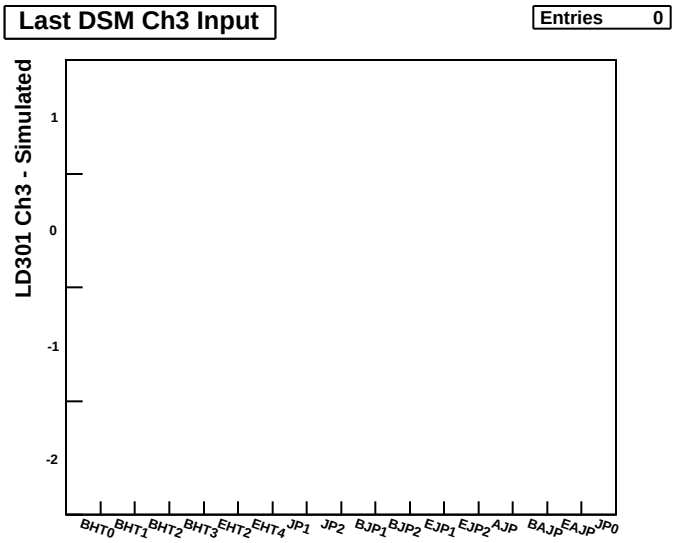

EMC L2 Input - HT01 bits/Partial JP ID
Entries 339384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

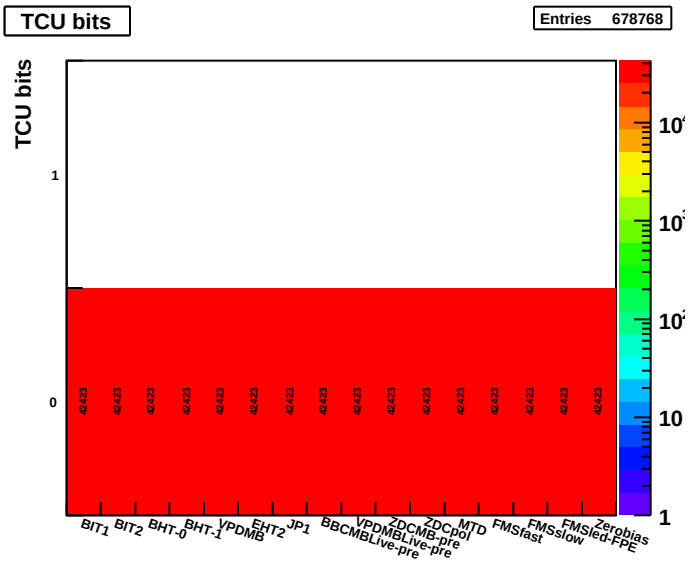

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

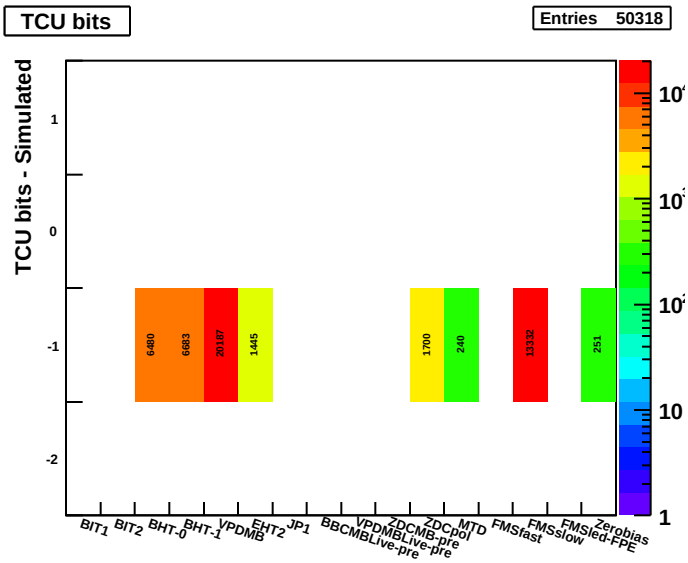

EMC L2 Input - HT23 bits
Entries 339384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

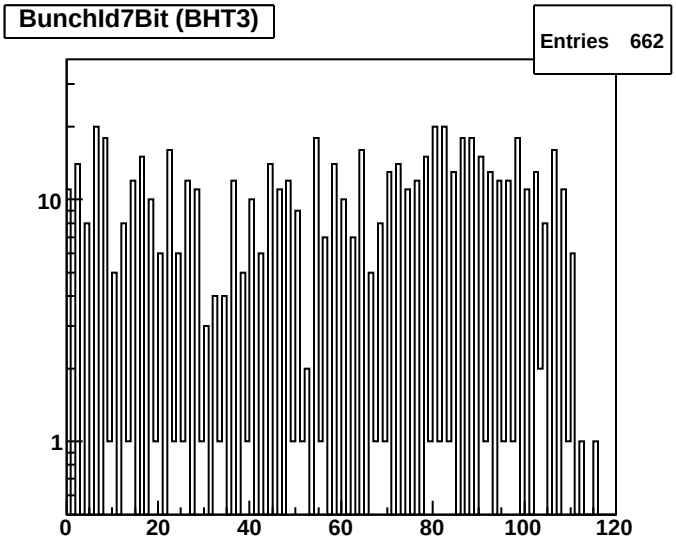

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

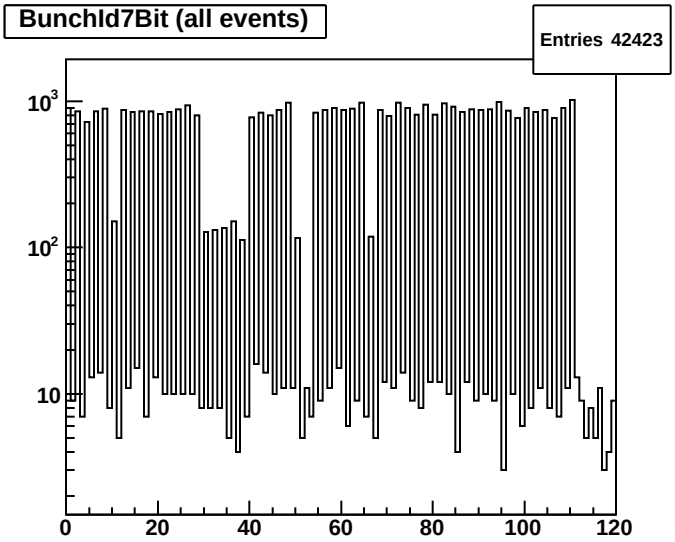

Last DSM Ch3 Input
Entries 678768
LD301 Ch3
11541 6683 1622 662 1445 793 6927 1577 4510 1050 2320 504 157 101 56 12136
30862 35740 40801 41761 40978 41630 35496 40846 37913 41373 40103 41919 42266 42322 42367 30287
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

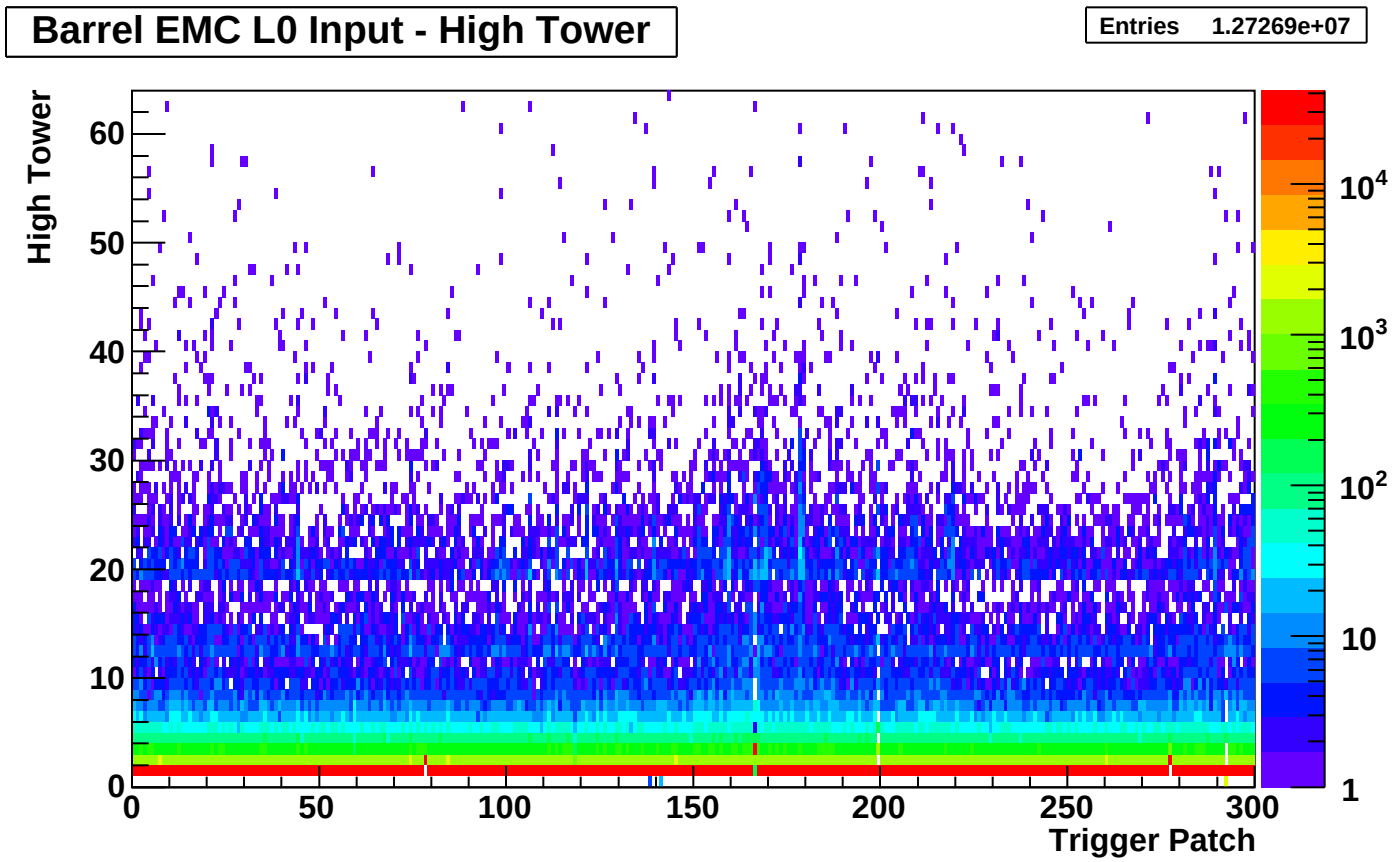

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

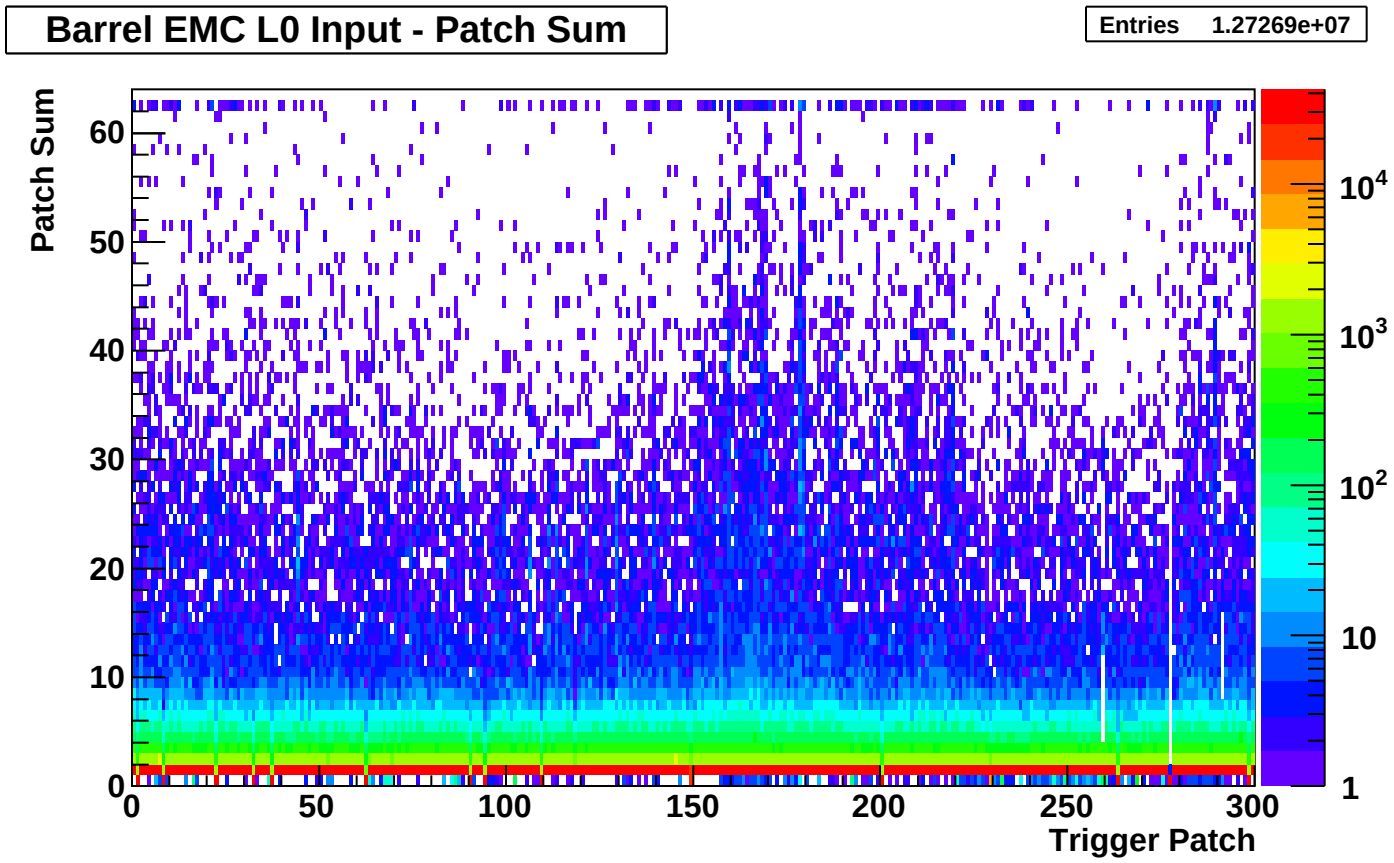

TCU bits
Entries 678768
TCU bits
42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423 42423
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

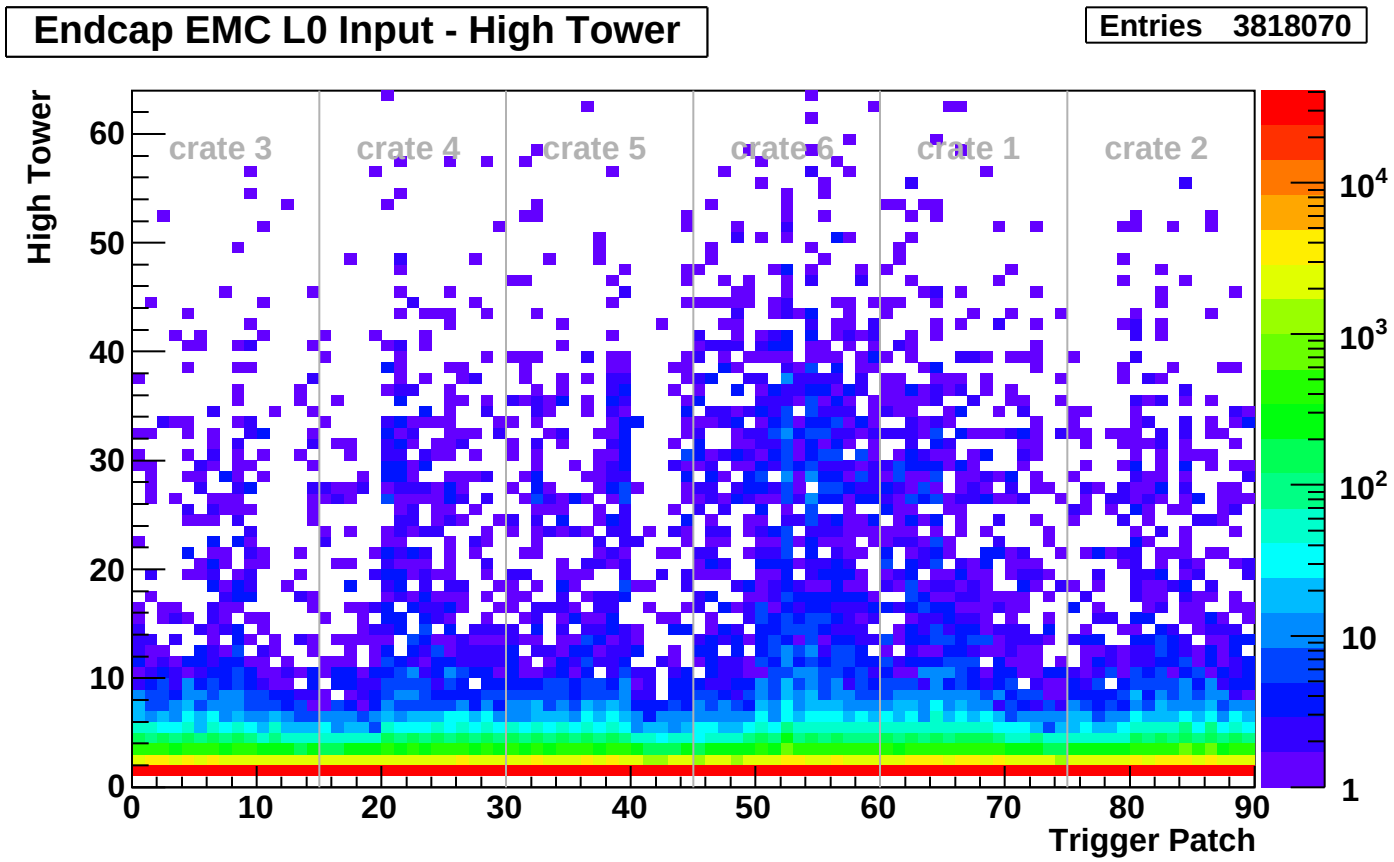

TCU bits
Entries 50318
TCU bits - Simulated
6480 6683 20187 1445 1700 240 13332 251
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

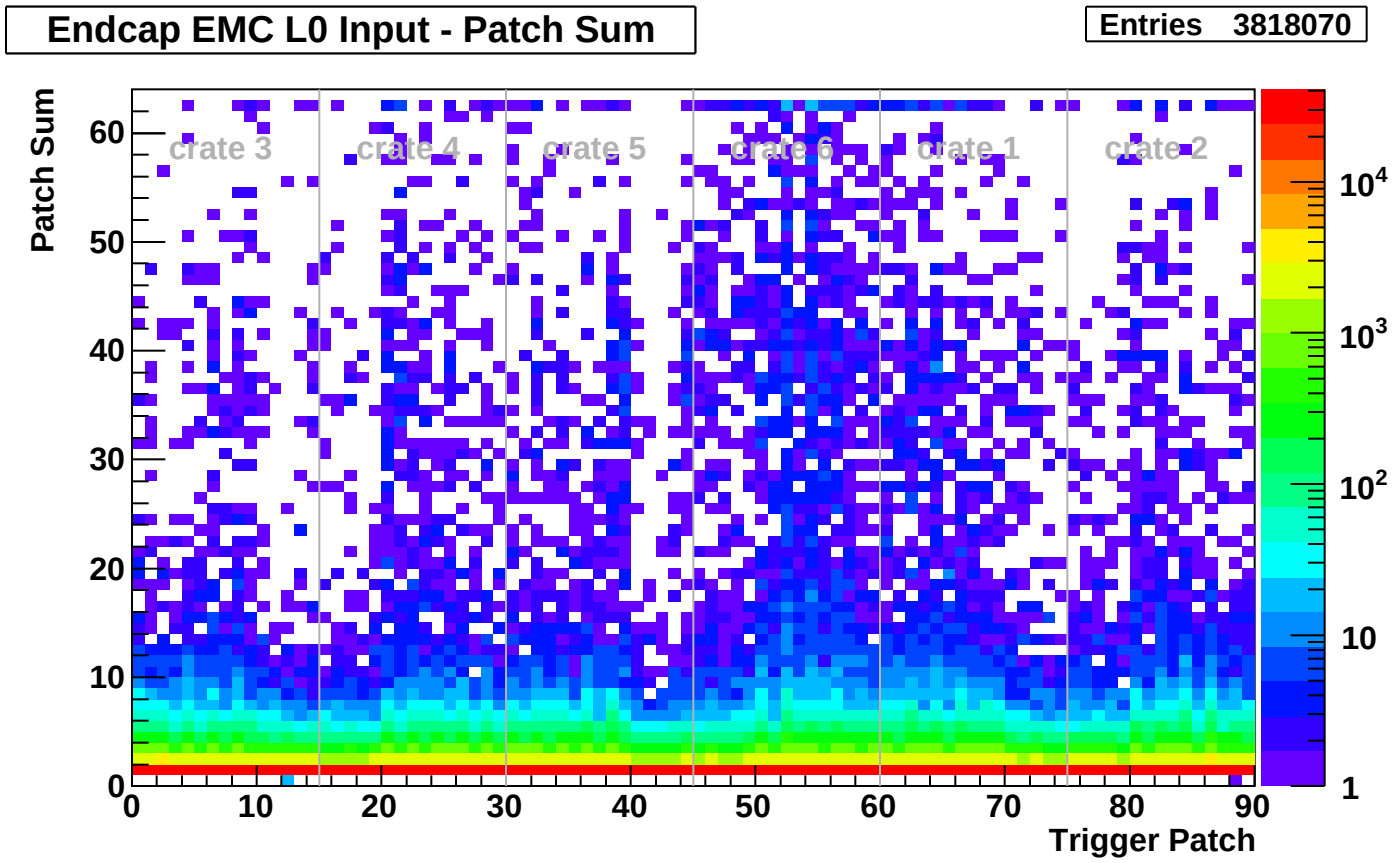

BunchId7Bit (BHT3)
Entries 662

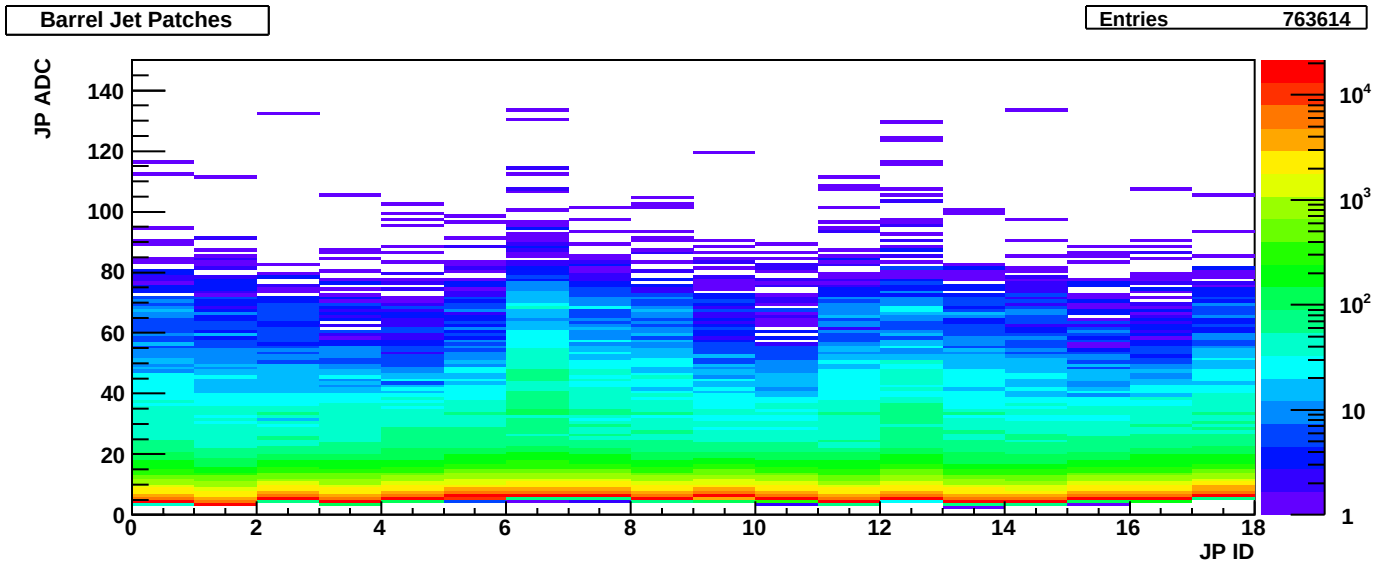

BunchId7Bit (all events)
Entries 42423

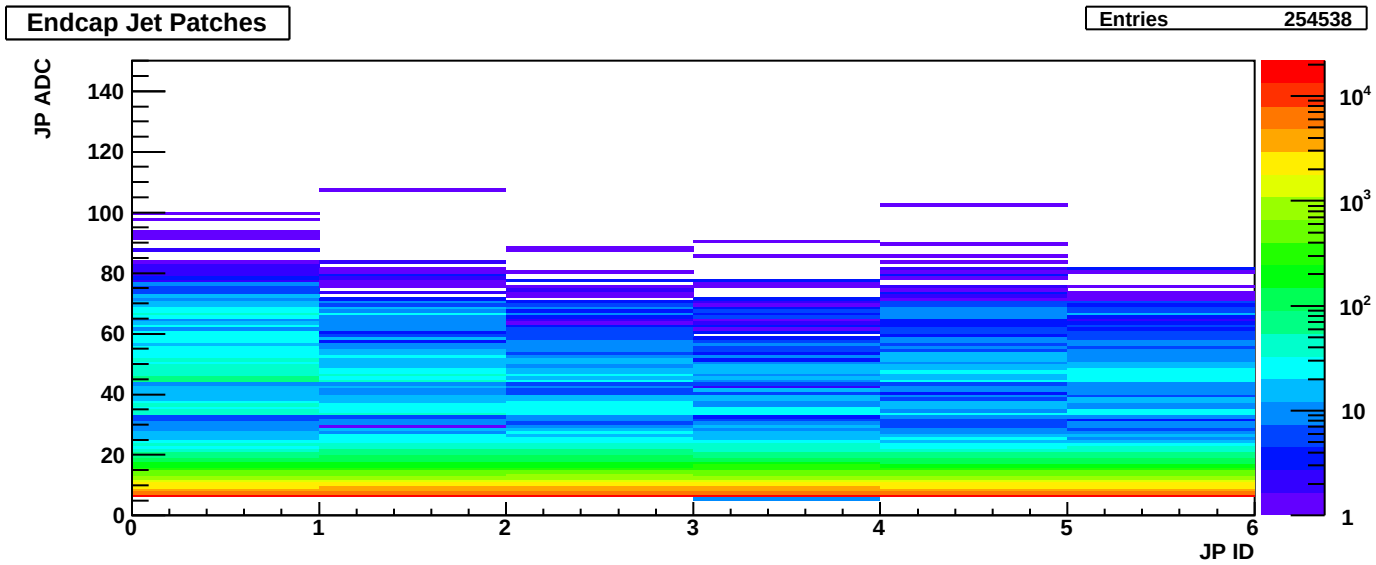

Barrel EMC L0 Input - High Tower
Entries 1.27269e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

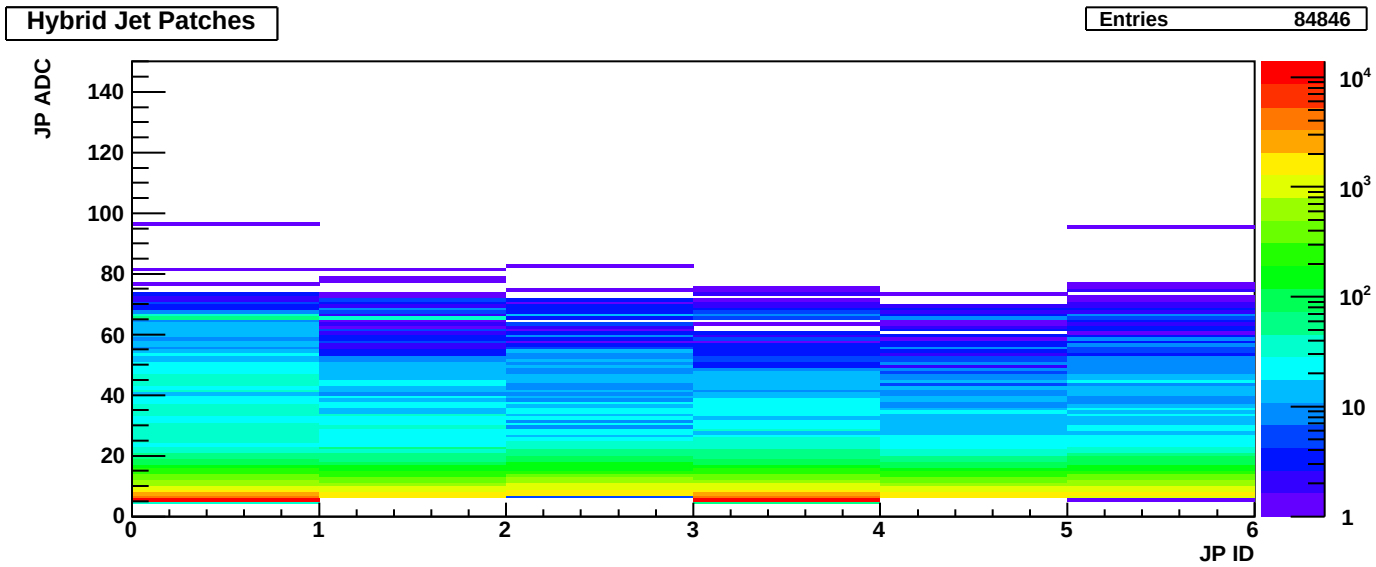

Barrel EMC L0 Input - Patch Sum
Entries 1.27269e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

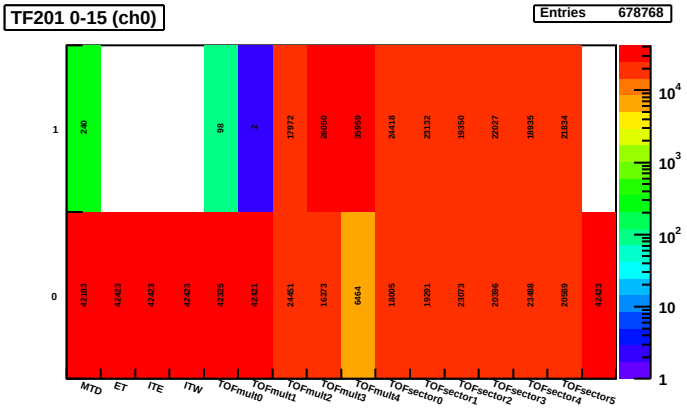

Endcap EMC L0 Input - High Tower
Entries 3818070
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

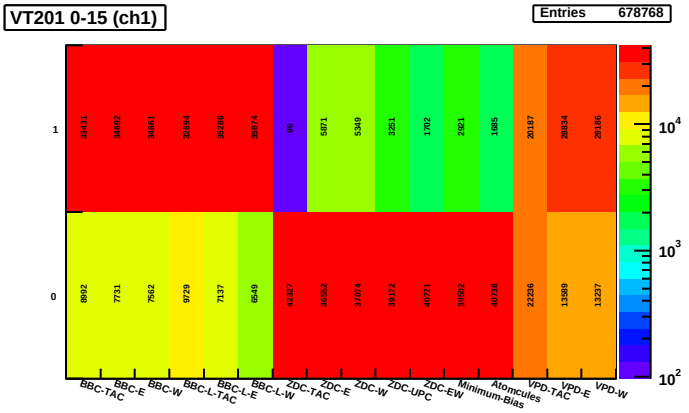

Endcap EMC L0 Input - Patch Sum
Entries 3818070
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

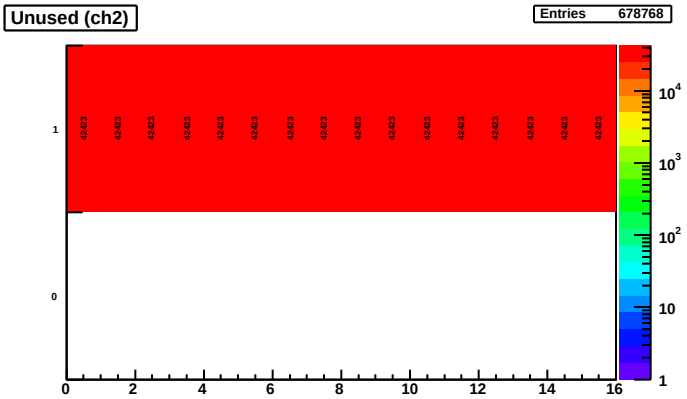

Barrel Jet Patches
Entries 763614
JP ADC
JP ID

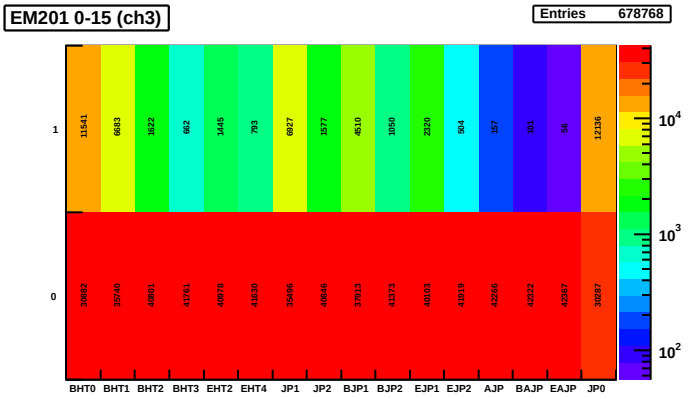

Endcap Jet Patches
Entries 254538
JP ADC
JP ID

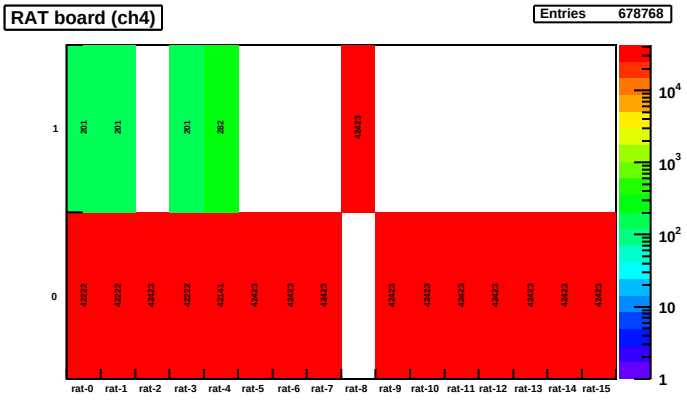

Hybrid Jet Patches
Entries 84846
JP ADC
JP ID

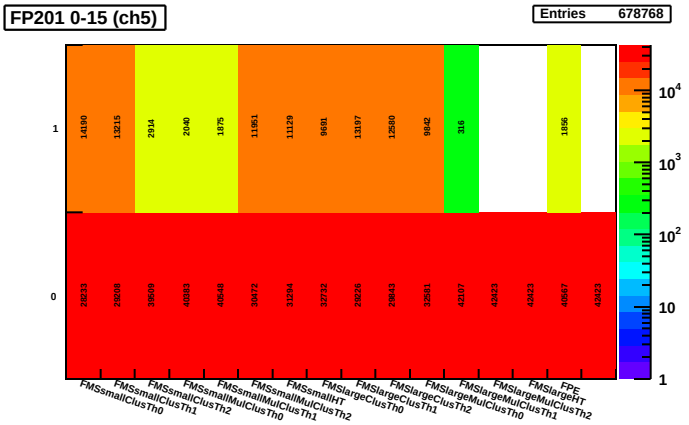

TF201 0-15 (ch0)
Entries 678768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

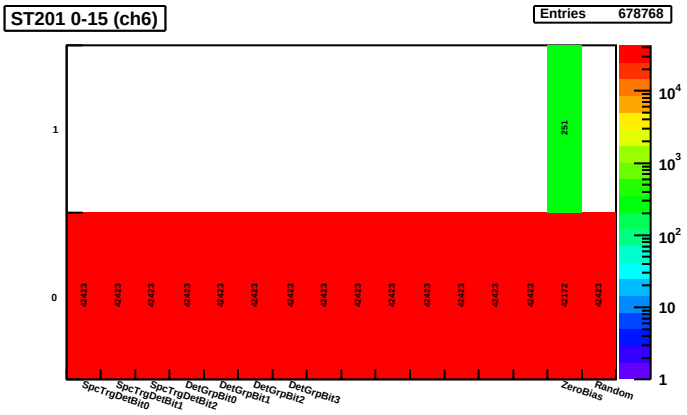

VT201 0-15 (ch1)
Entries 678768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

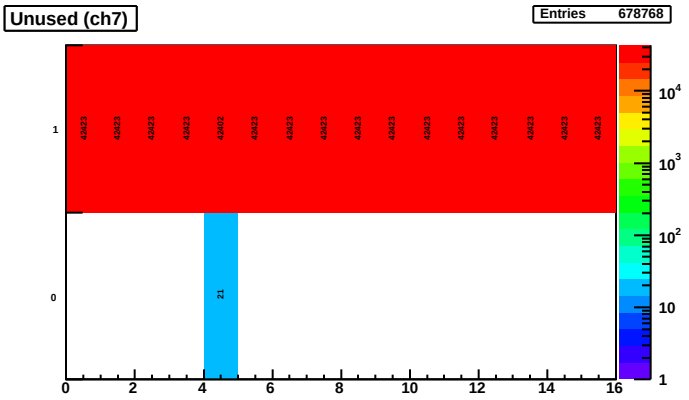

Unused (ch2)
Entries 678768

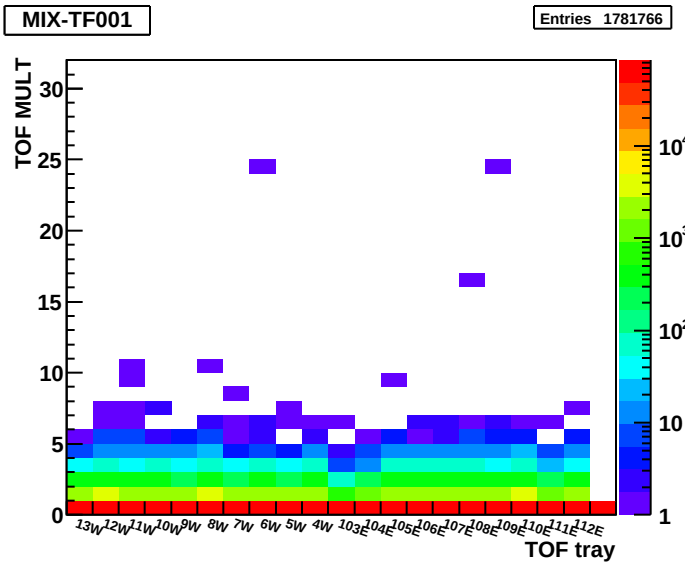

EM201 0-15 (ch3)
Entries 678768
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

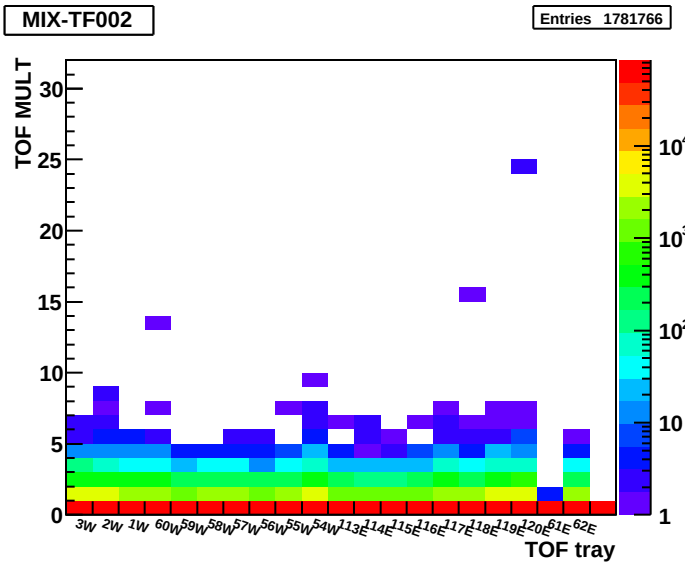

RAT board (ch4)
Entries 678768
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

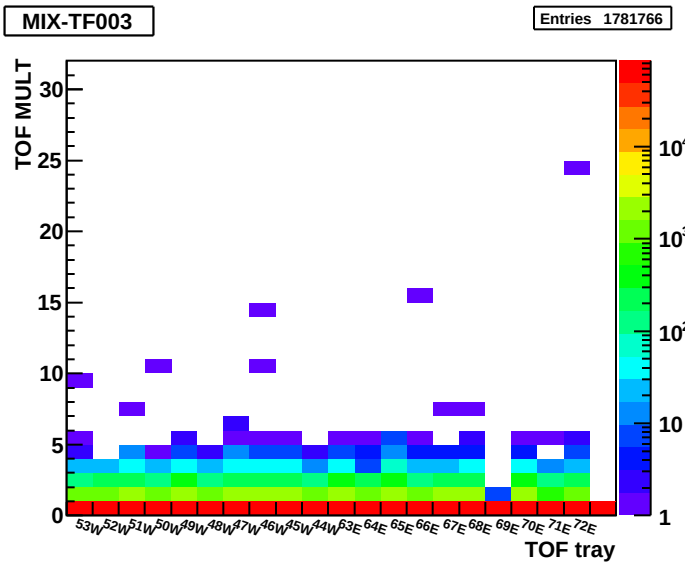

FP201 0-15 (ch5)
Entries 678768
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

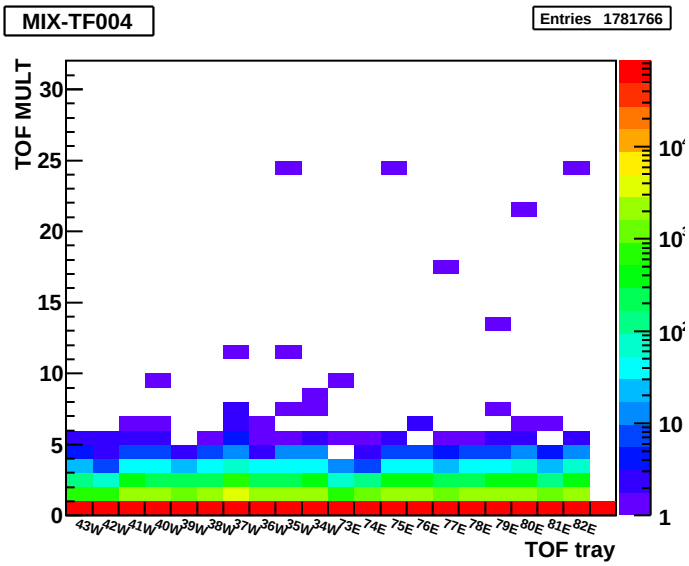

ST201 0-15 (ch6)
Entries 678768
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

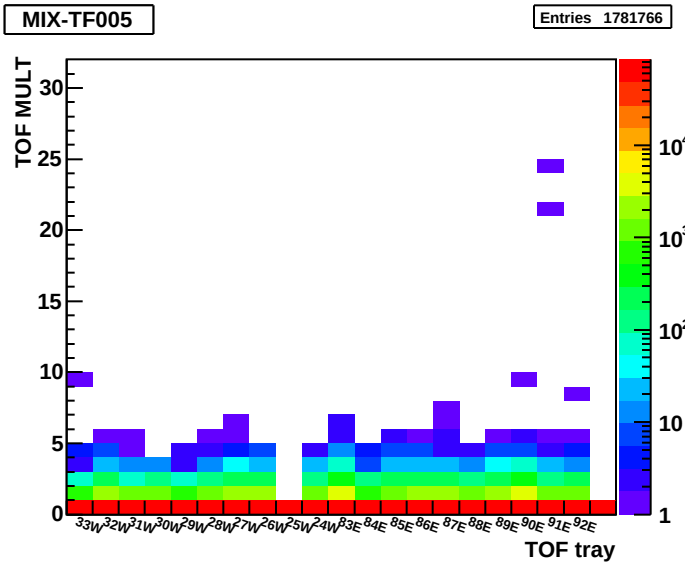

Unused (ch7)
Entries 678768

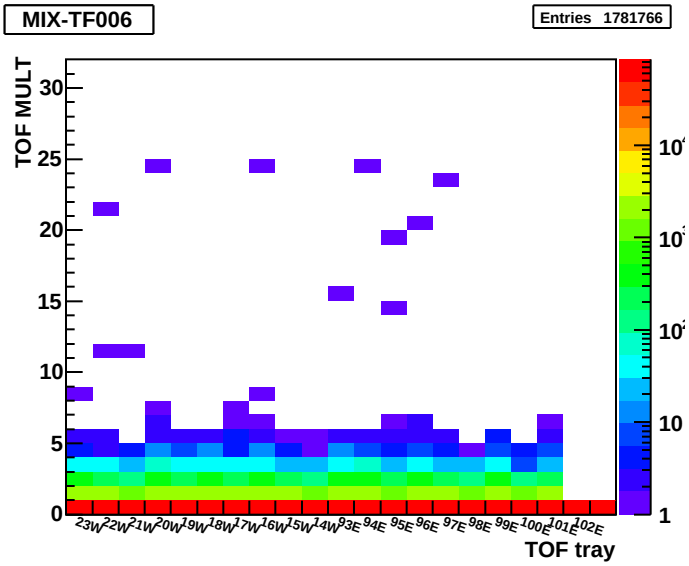

MIX-TF001
Entries 1781766
TOF MULT
TOF tray

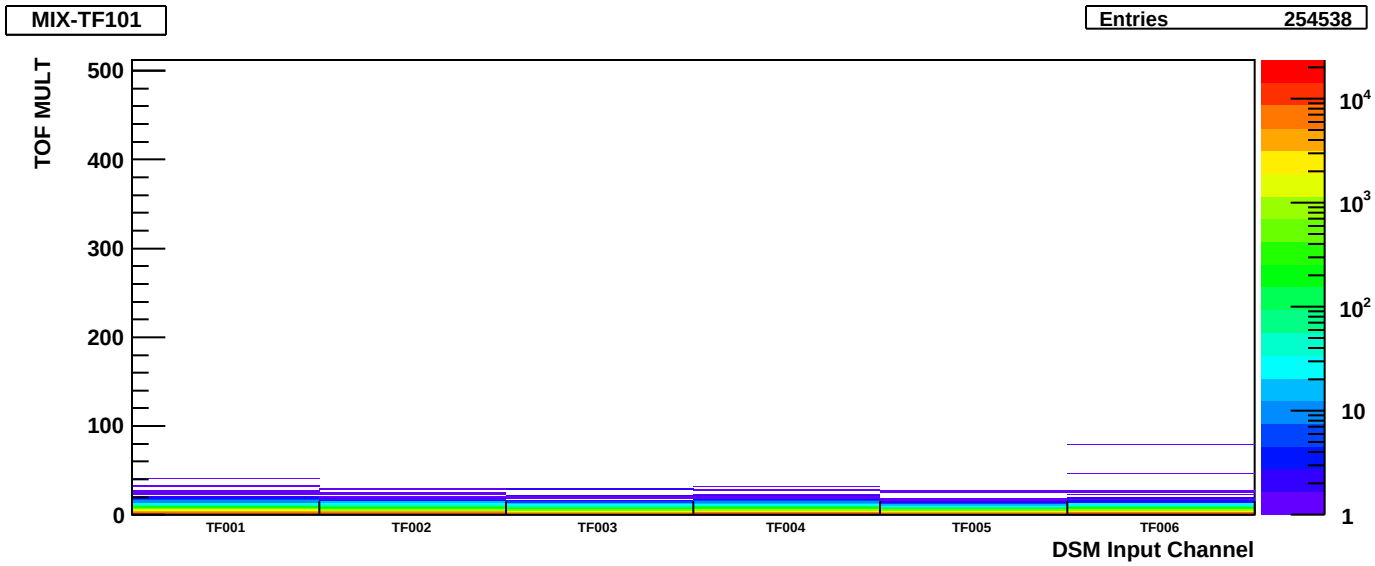

MIX-TF002
Entries 1781766
TOF MULT
TOF tray

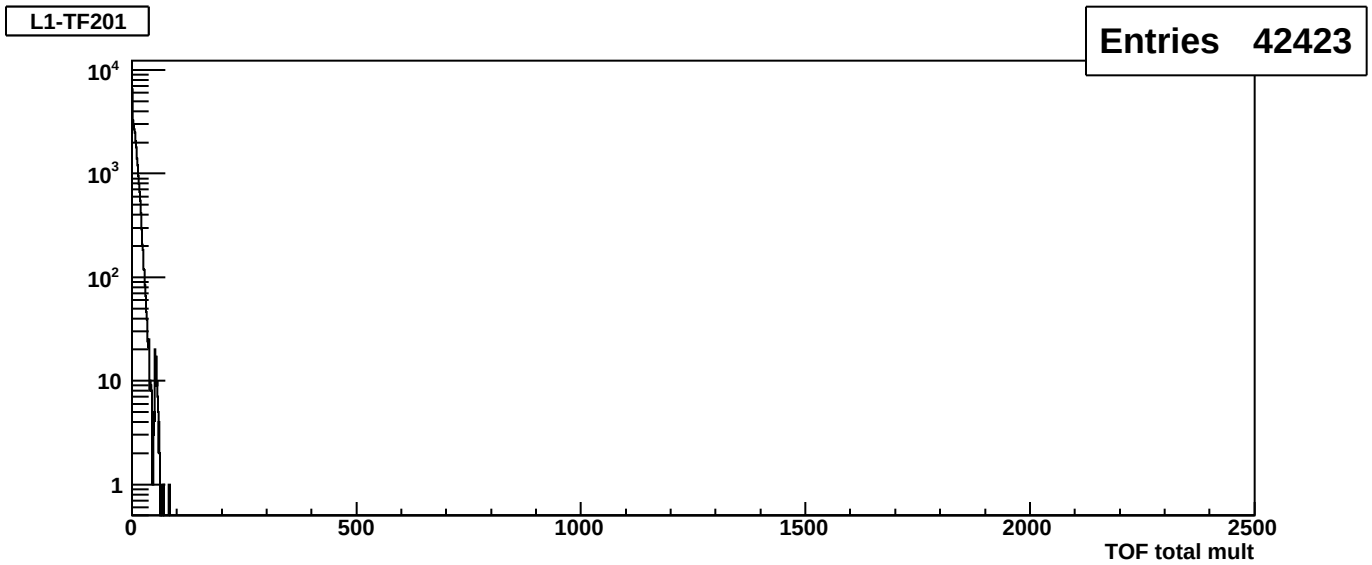

MIX-TF003
Entries 1781766
TOF MULT
TOF tray

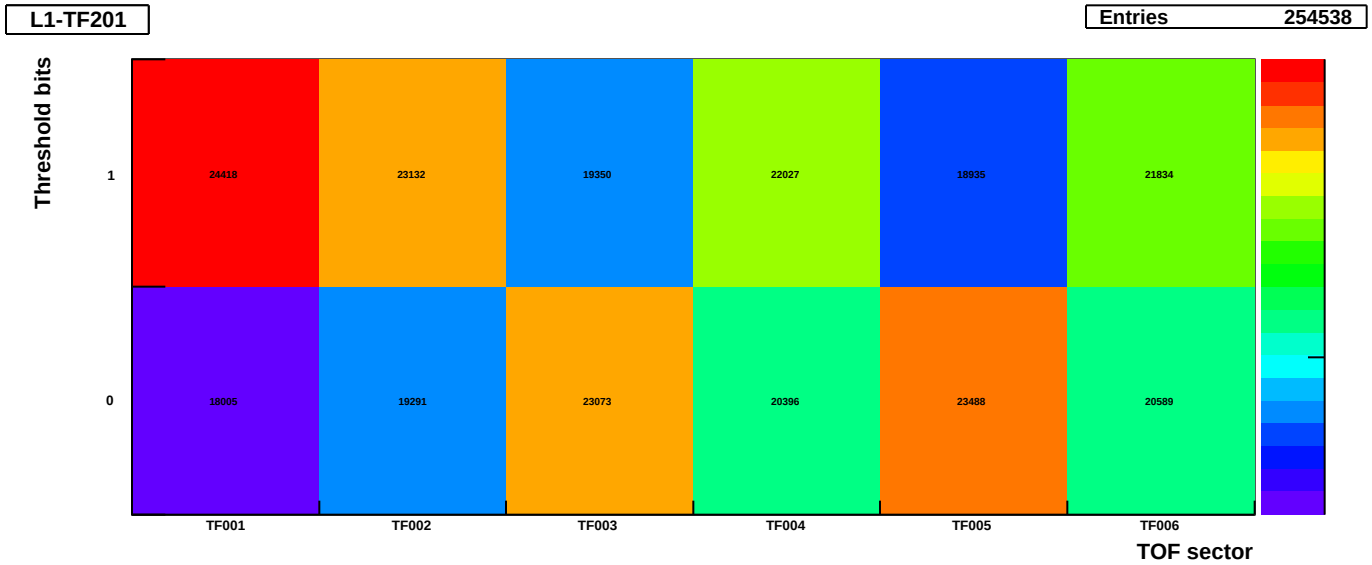

MIX-TF004
Entries 1781766
TOF MULT
TOF tray

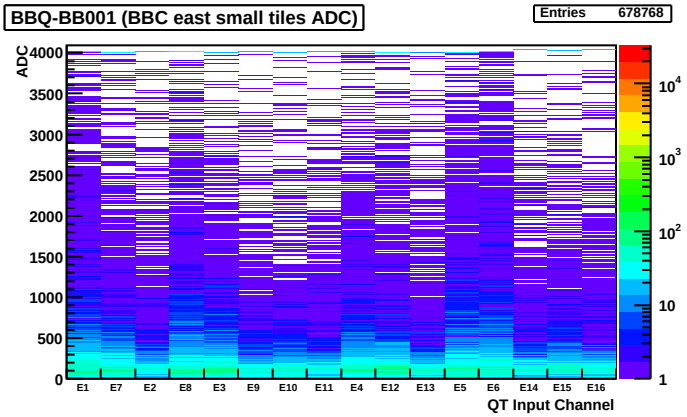

MIX-TF005
Entries 1781766
TOF MULT
TOF tray

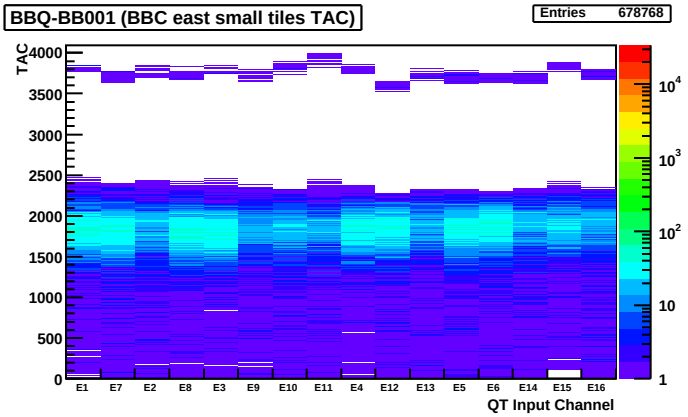

MIX-TF006
Entries 1781766
TOF MULT
TOF tray

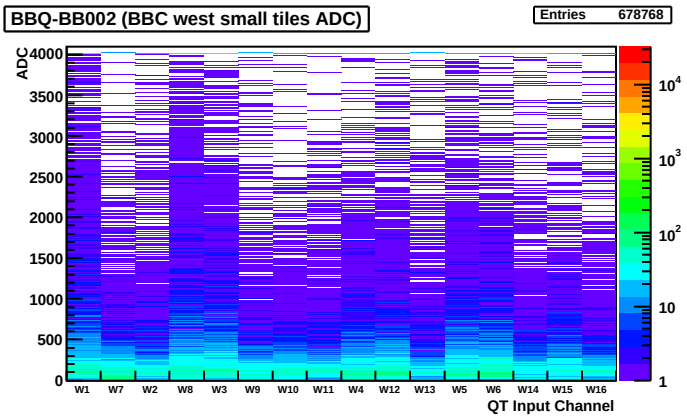

MIX-TF101
Entries 254538
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

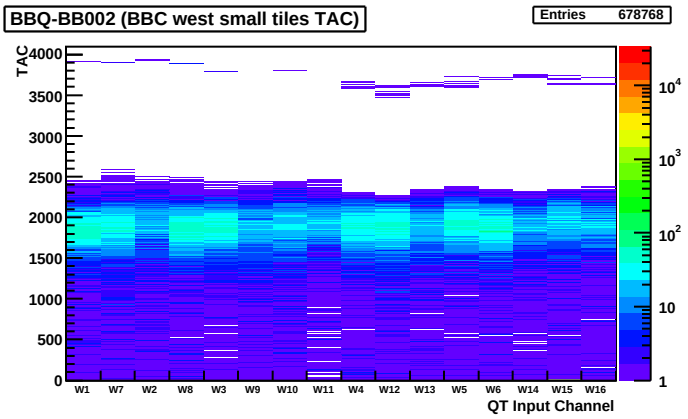

L1-TF201
Entries 42423
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

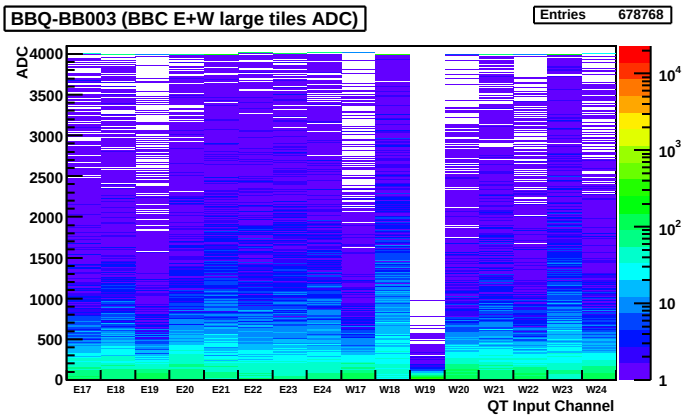

L1-TF201
Entries 254538
Threshold bits
1
0
24418
23132
19350
22027
18935
21834
18005
19291
23073
20396
23488
20589
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

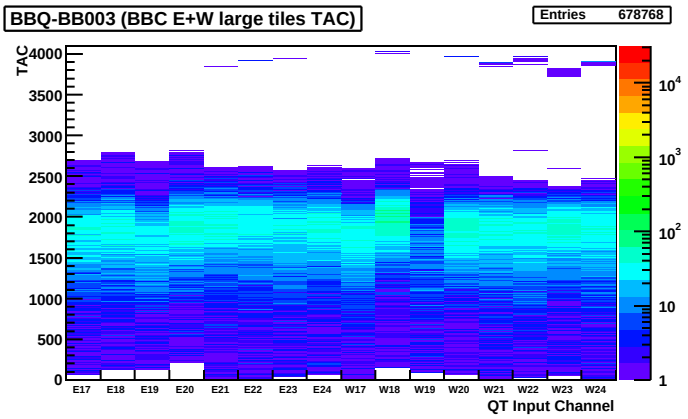

BBQ-BB001 (BBC east small tiles ADC)
Entries 678768
ADC
QT Input Channel

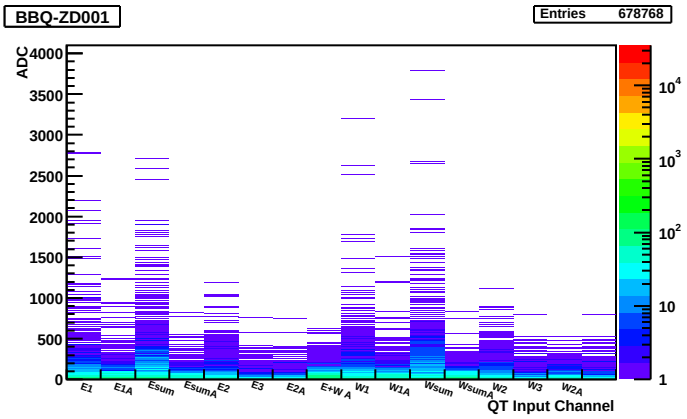

BBQ-BB001 (BBC east small tiles TAC)
Entries 678768
TAC
QT Input Channel

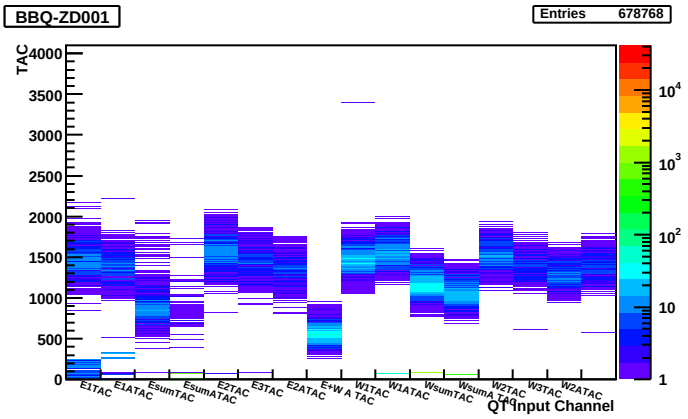

BBQ-BB002 (BBC west small tiles ADC)
Entries 678768
ADC
QT Input Channel

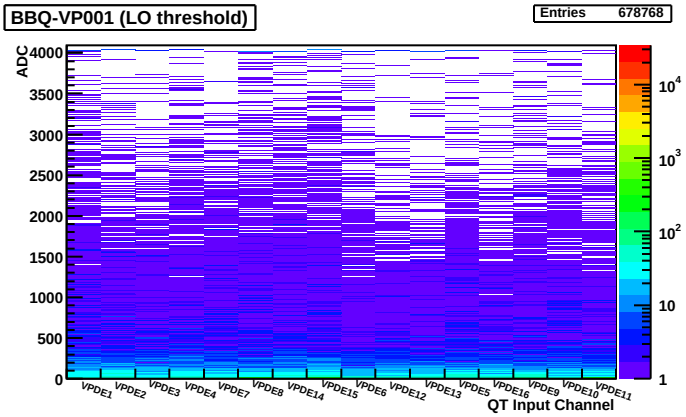

BBQ-BB002 (BBC west small tiles TAC)
Entries 678768
TAC
QT Input Channel

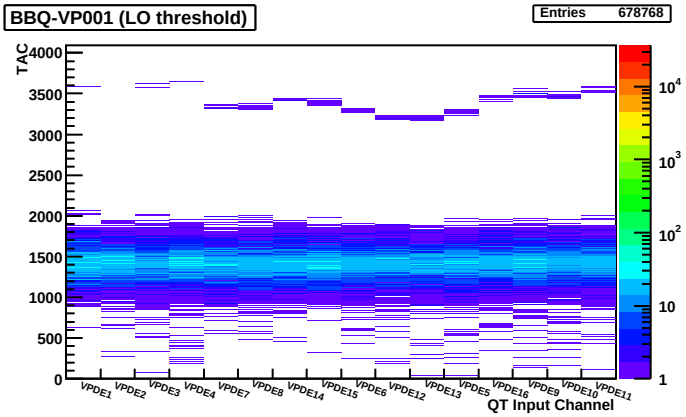

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 678768
ADC
QT Input Channel

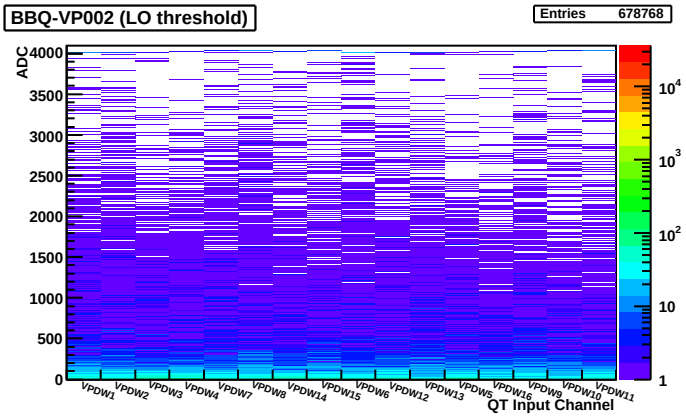

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 678768
TAC
QT Input Channel

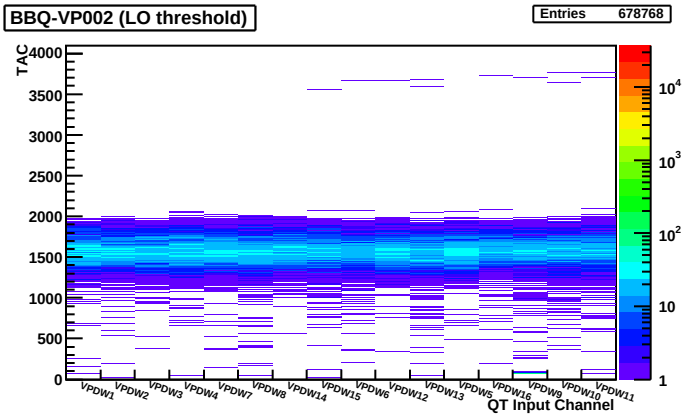

BBQ-ZD001
Entries 678768
ADC
QT Input Channel

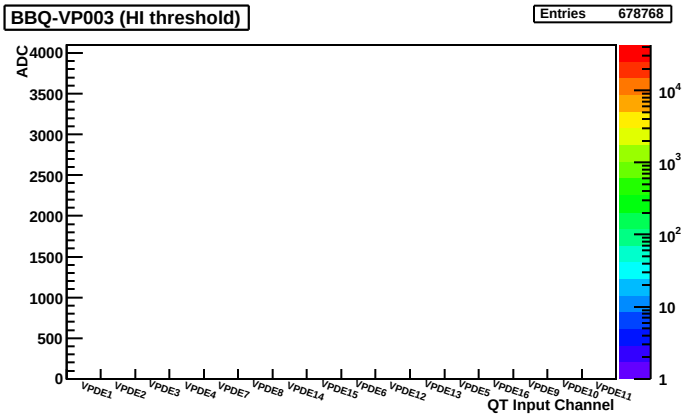

BBQ-ZD001
Entries 678768
TAC
QT Input Channel

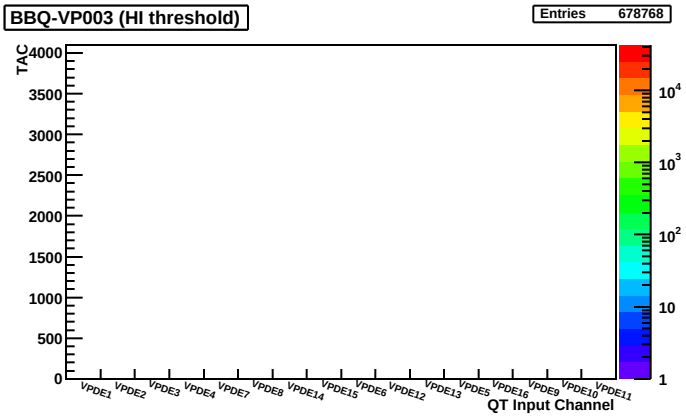

BBQ-VP001 (LO threshold)
Entries 678768
ADC
QT Input Channel

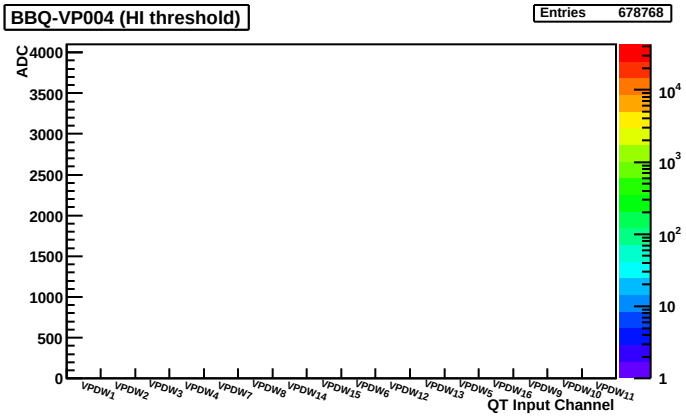

BBQ-VP001 (LO threshold)
Entries 678768
TAC
QT Input Channel

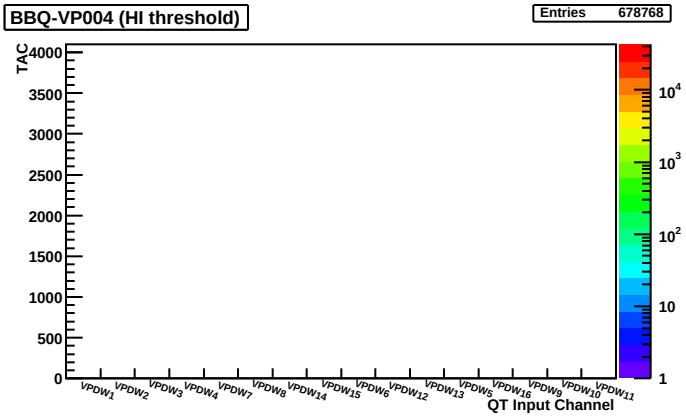

BBQ-VP002 (LO threshold)
Entries 678768
ADC
QT Input Channel

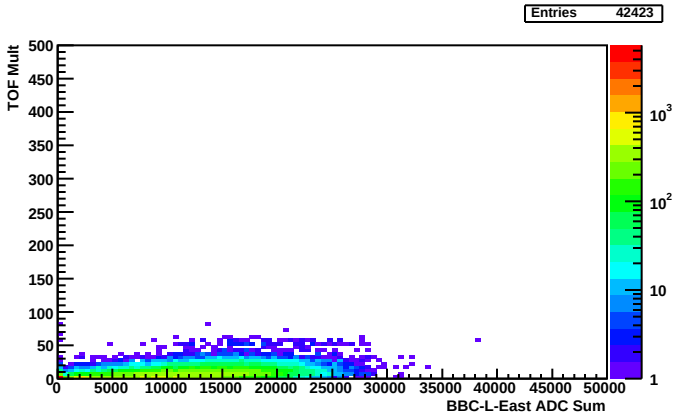

BBQ-VP002 (LO threshold)
Entries 678768
TAC
QT Input Channel

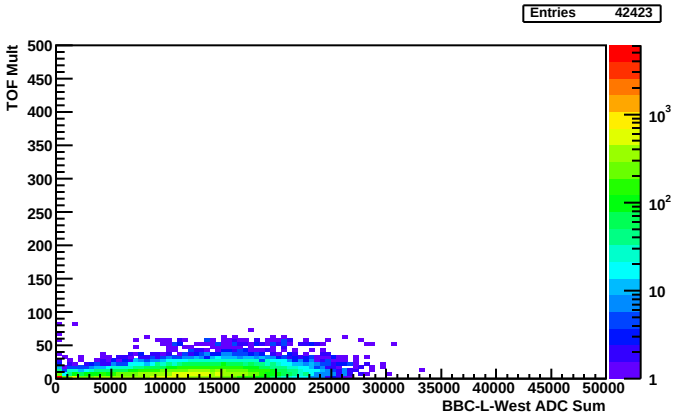

BBQ-VP003 (HI threshold)
Entries 678768
ADC
QT Input Channel

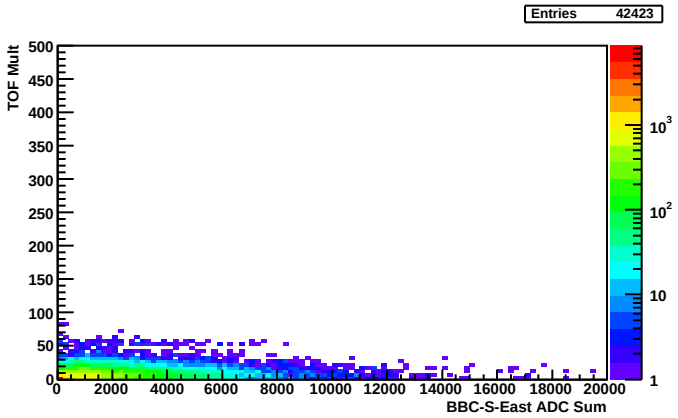

BBQ-VP003 (HI threshold)
Entries 678768
TAC
QT Input Channel

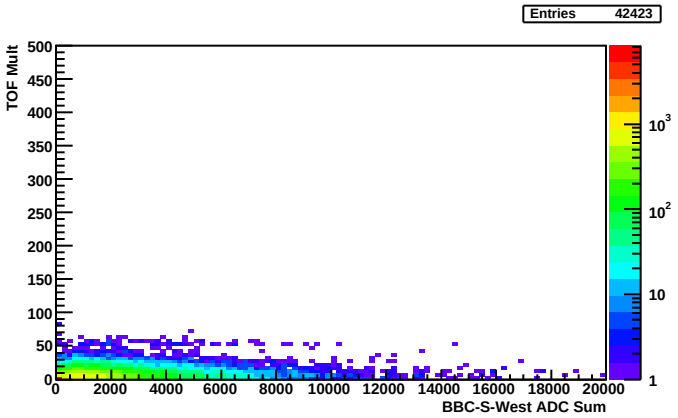

BBQ-VP004 (HI threshold)
Entries 678768
ADC
QT Input Channel

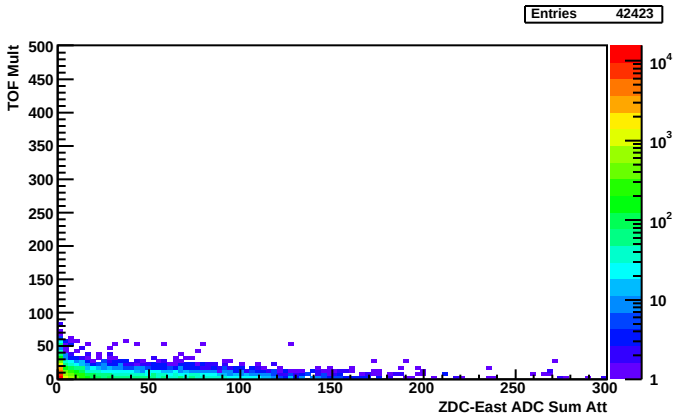

BBQ-VP004 (HI threshold)
Entries 678768
TAC
QT Input Channel

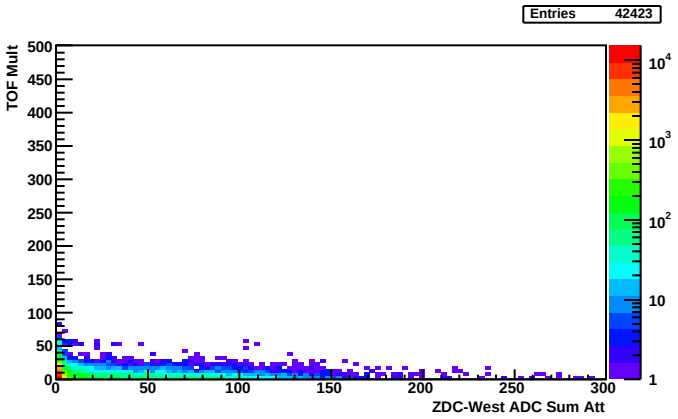

Entries 42423
TOF Mult
BBC-L-East ADC Sum

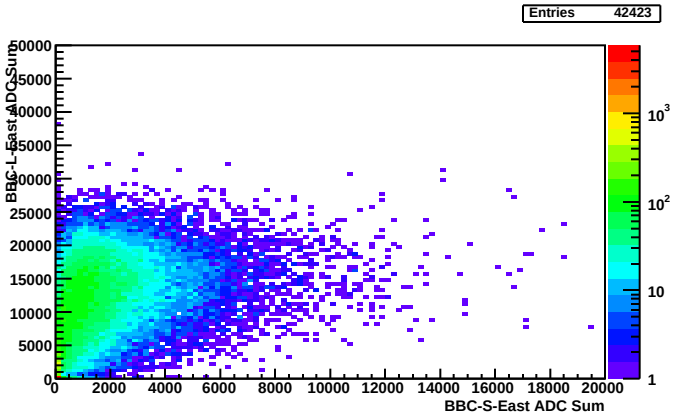

Entries 42423
TOF Mult
BBC-L-West ADC Sum

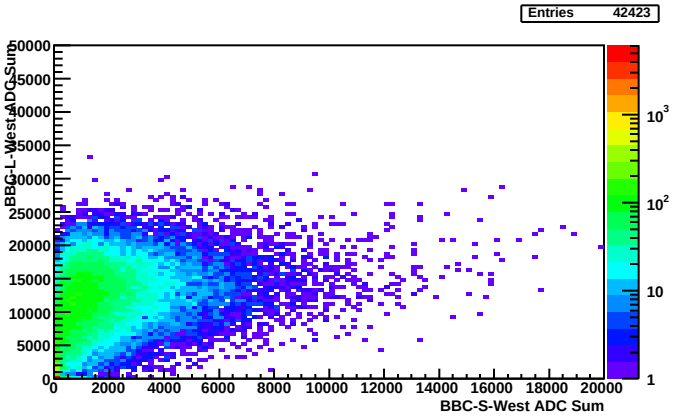

Entries 42423
TOF Mult
BBC-S-East ADC Sum

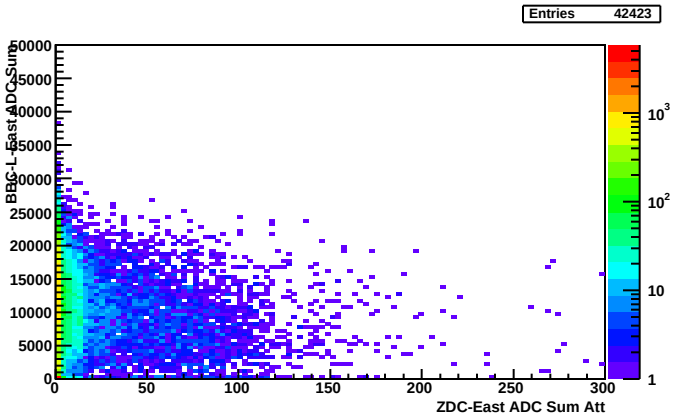

Entries 42423
TOF Mult
BBC-S-West ADC Sum

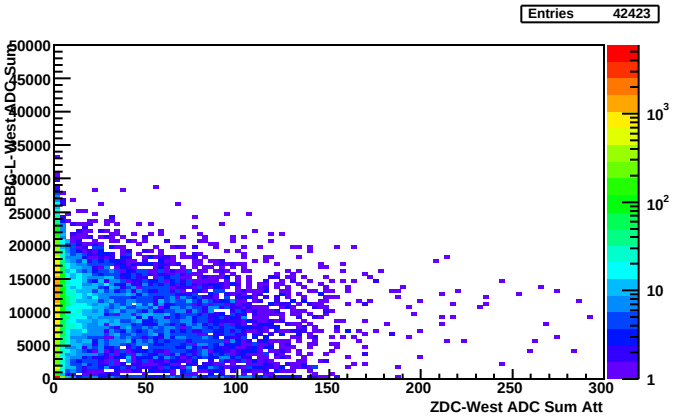

Entries 42423
TOF Mult
ZDC-East ADC Sum Att

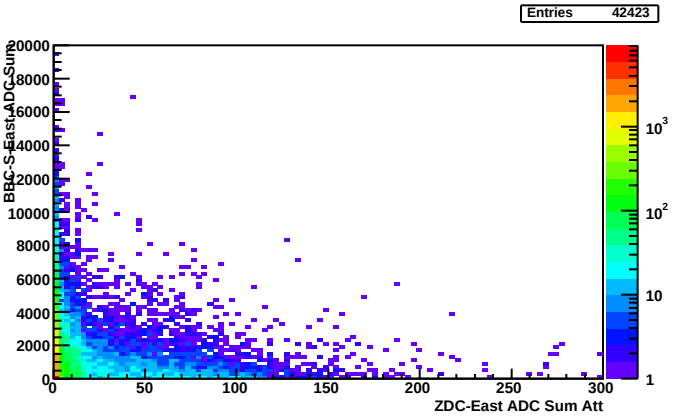

Entries 42423
TOF Mult
ZDC-West ADC Sum Att

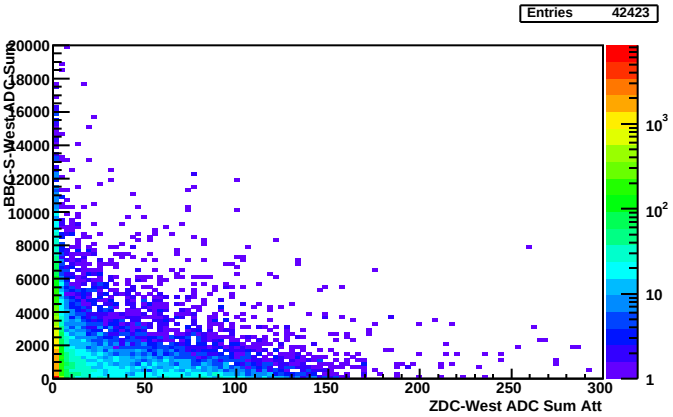

Entries 42423
BBC-L-East ADC Sum
BBC-S-East ADC Sum

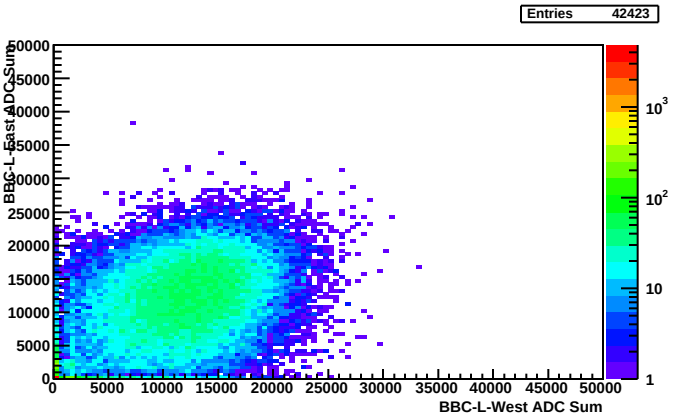

Entries 42423
BBC-L-West ADC Sum
BBC-S-West ADC Sum

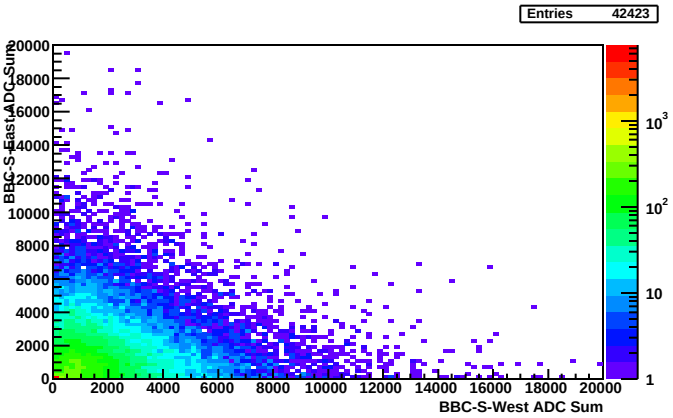

Entries 42423
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

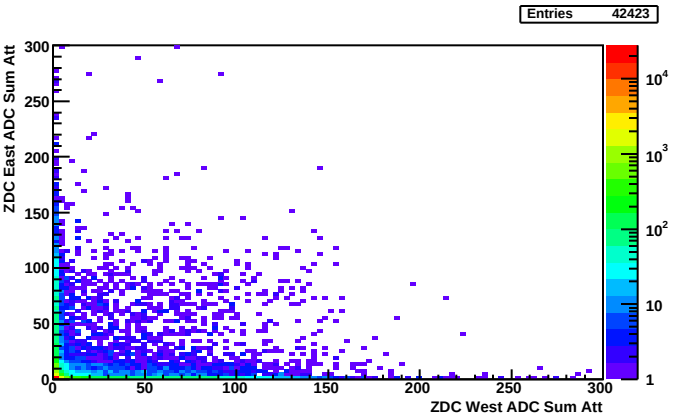

Entries 42423
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

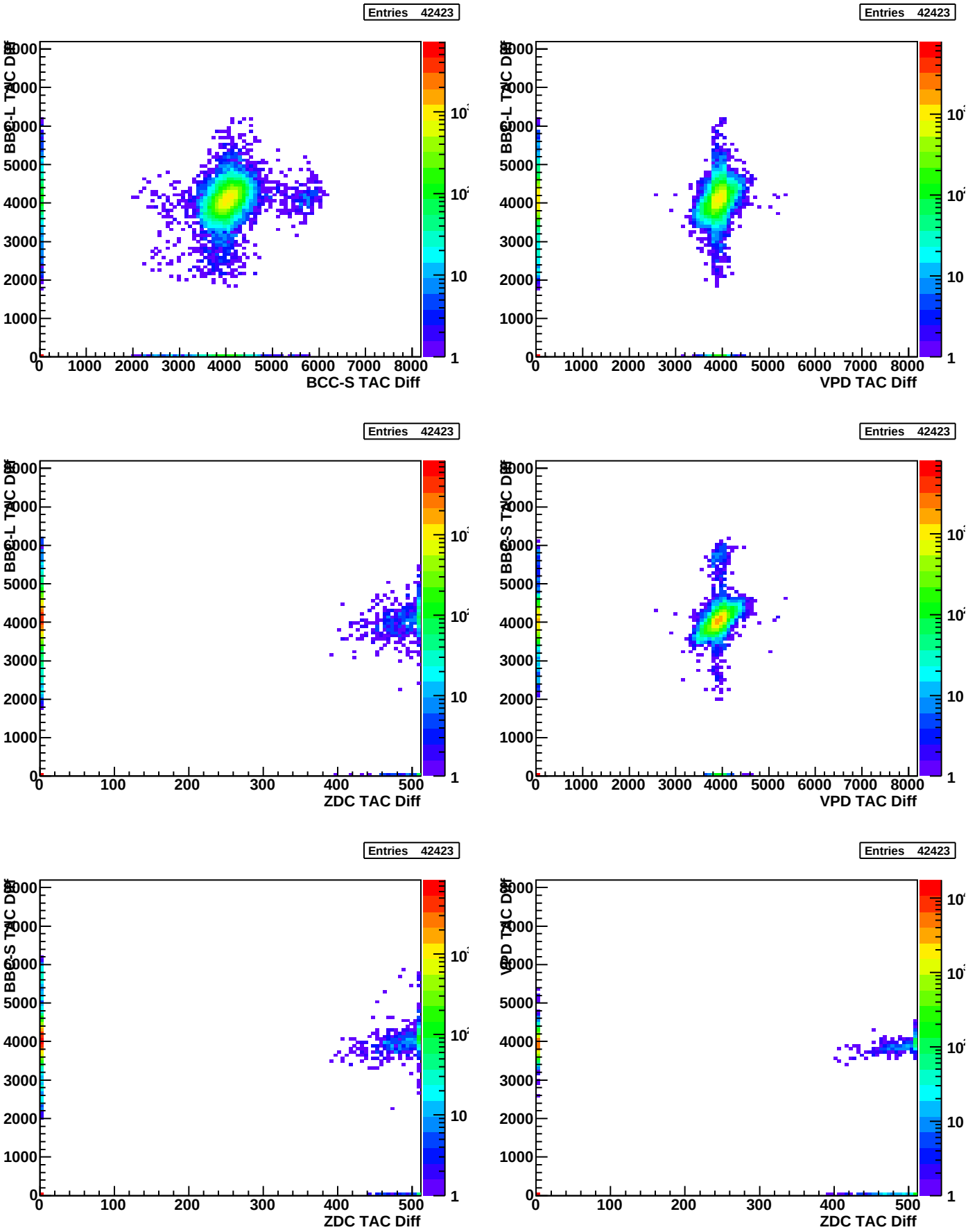

Entries 42423
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

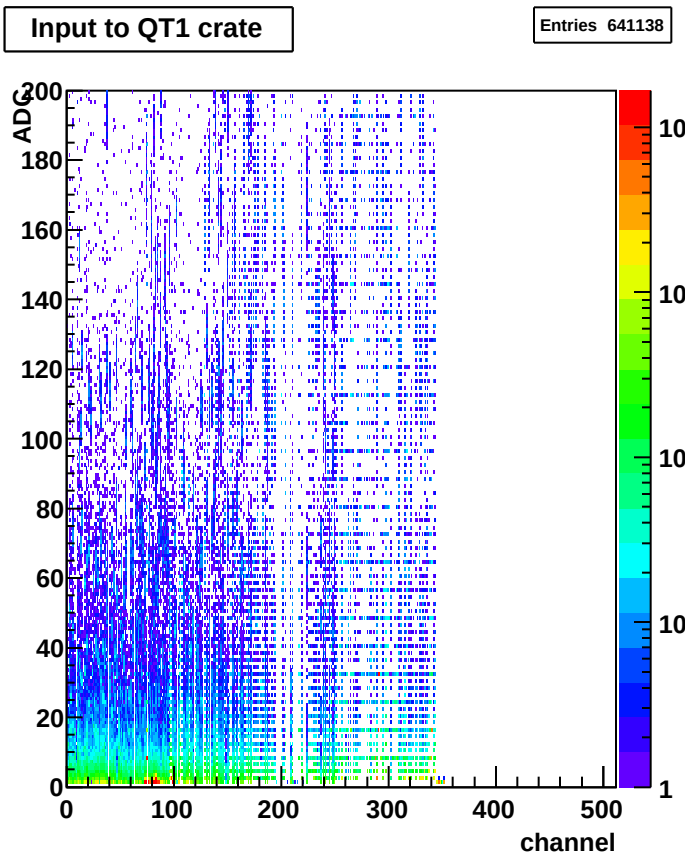

Entries 42423
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

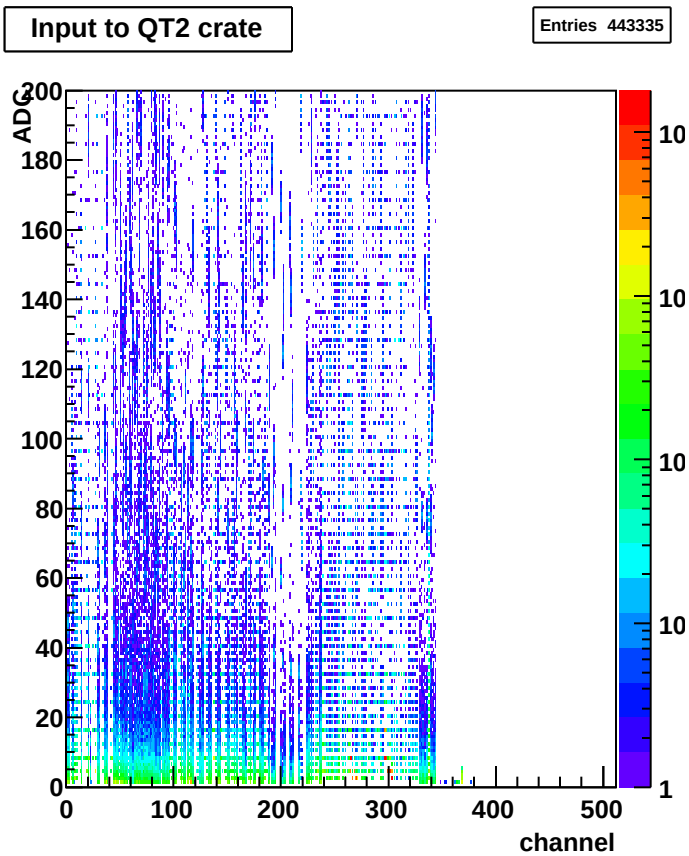

Entries 42423
BBC-L-East ADC Sum
BBC-L-West ADC Sum

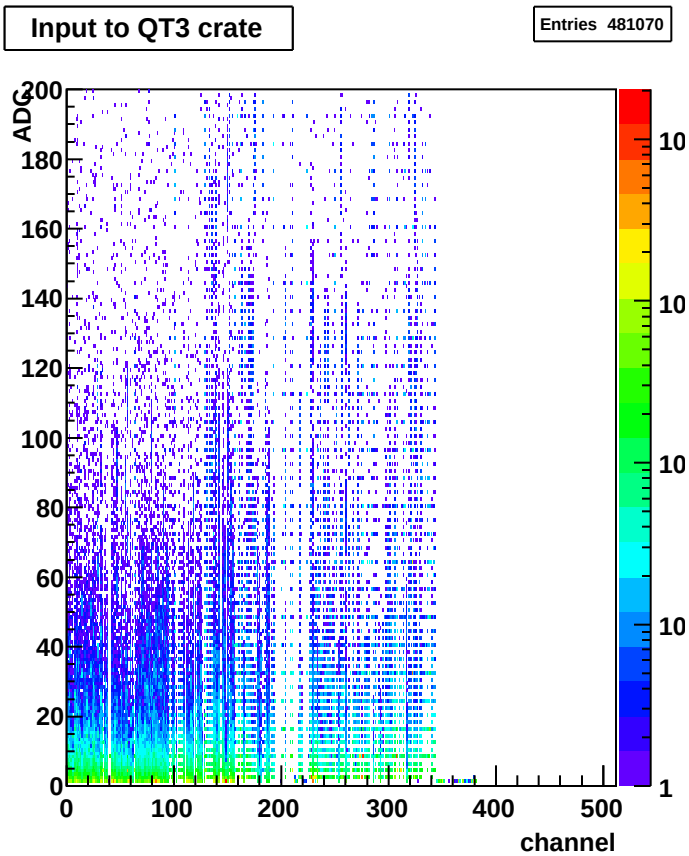

Entries 42423
BBC-S-East ADC Sum
BBC-S-West ADC Sum

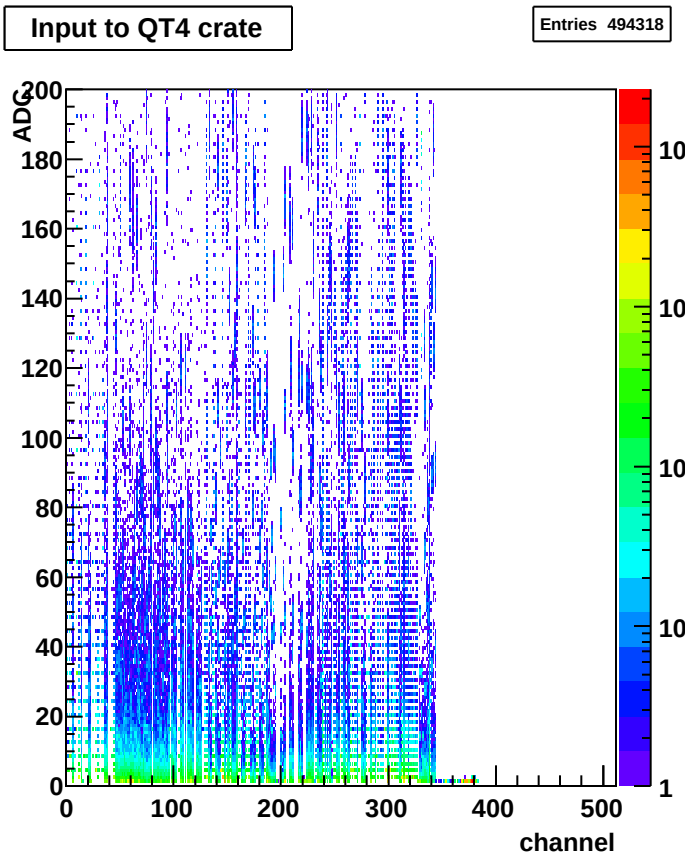

Entries 42423
ZDC East ADC Sum Att
ZDC West ADC Sum Att

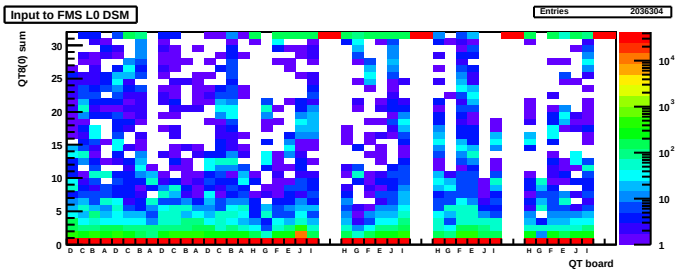

Entries 42423
BBC-L TAC Diff
BCC-S TAC Diff
Entries 42423
BBC-L TAC Diff
VPD TAC Diff
Entries 42423
BBC-L TAC Diff
ZDC TAC Diff
Entries 42423
BBC-S TAC Diff
VPD TAC Diff
Entries 42423
BBC-S TAC Diff
ZDC TAC Diff
Entries 42423
VPD TAC Diff
ZDC TAC Diff

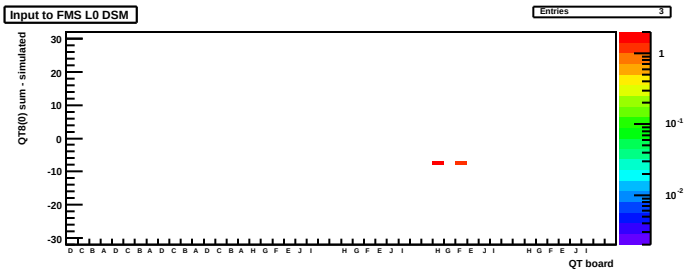

Input to QT1 crate
Entries 641138
ADC
channel

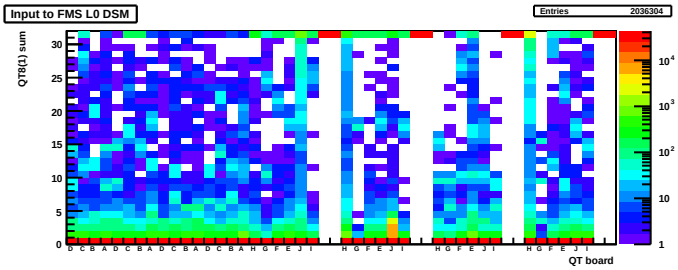

Input to QT2 crate
Entries 443335
ADC
channel

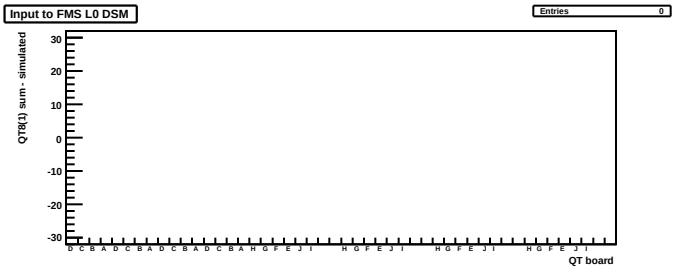

Input to QT3 crate
Entries 481070
ADC
channel

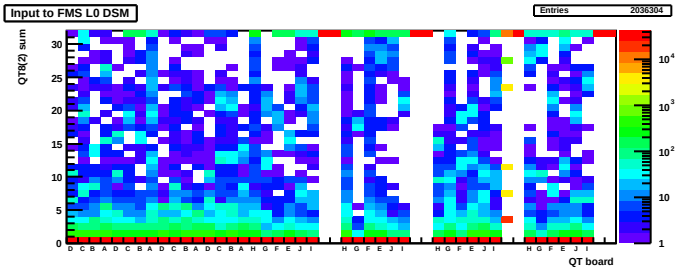

Input to QT4 crate
Entries 494318
ADC
channel

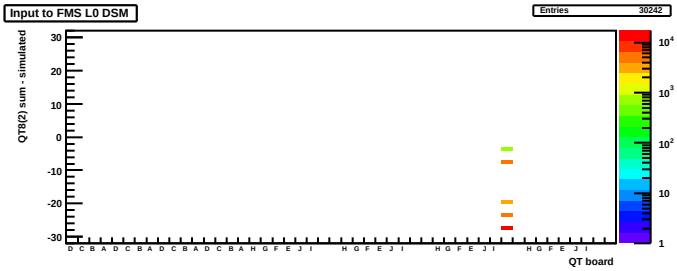

Input to FMS L0 DSM
Entries 2036304
QT8(0) sum
QT board

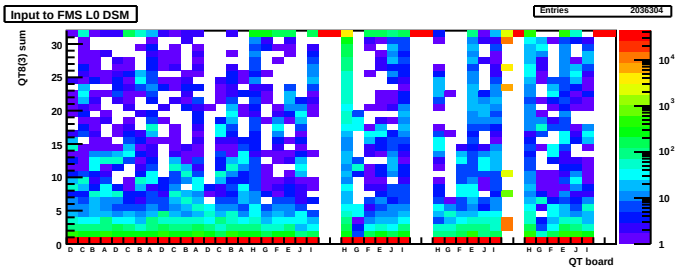

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

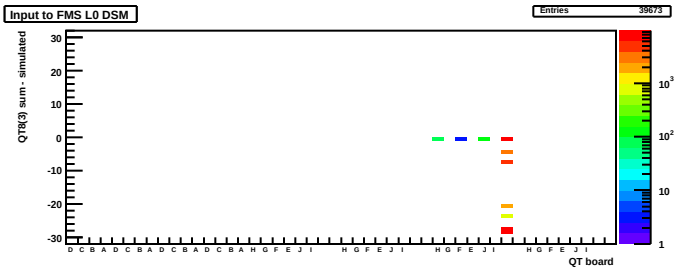

Input to FMS L0 DSM
Entries 2036304
QT8(1) sum
QT board

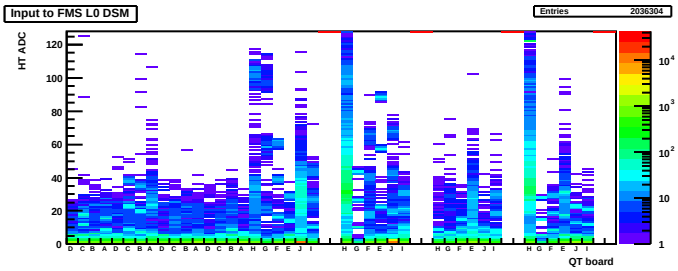

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

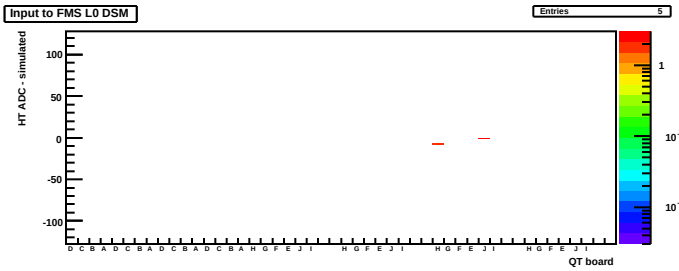

Input to FMS L0 DSM
Entries 2036304
QT8(2) sum
QT board

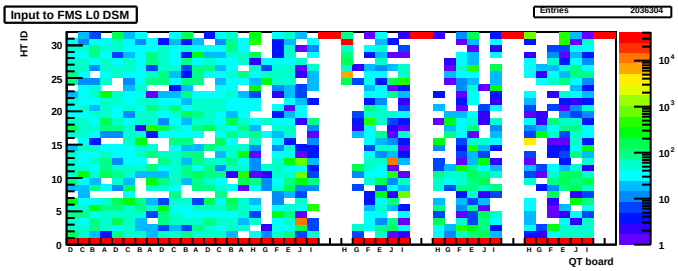

Input to FMS L0 DSM
Entries 30242
QT8(2) sum - simulated
QT board

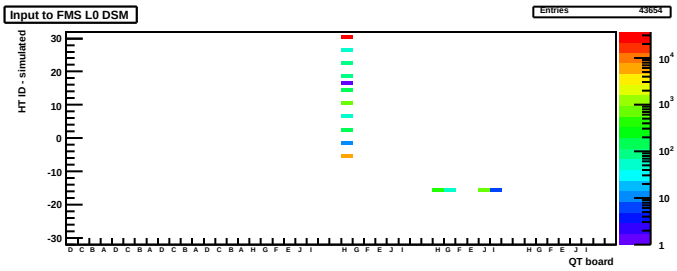

Input to FMS L0 DSM
Entries 2036304
QT8(3) sum
QT board

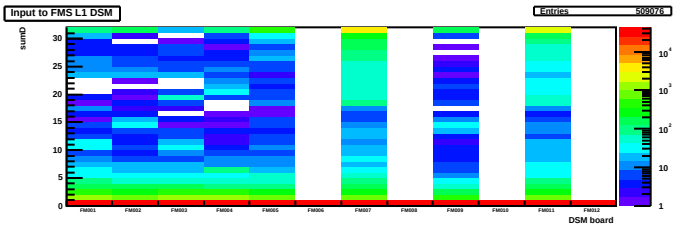

Input to FMS L0 DSM
Entries 39673
QT8(3) sum - simulated
QT board

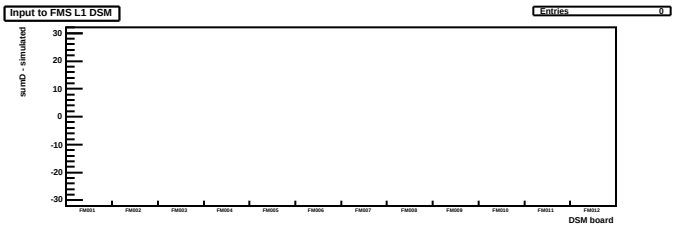

Input to FMS L0 DSM
Entries 2036304
HT ADC
QT board

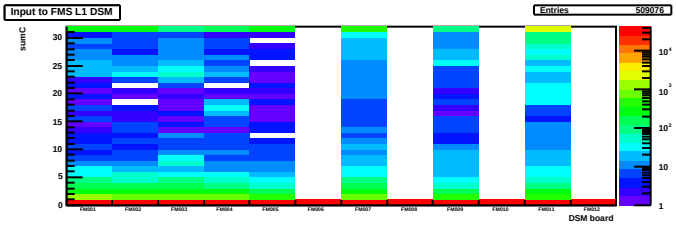

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

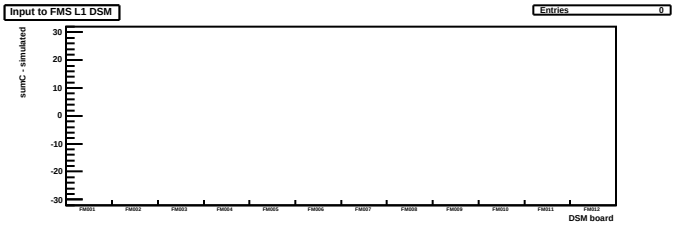

Input to FMS L0 DSM
Entries 2036304
HT ID
QT board

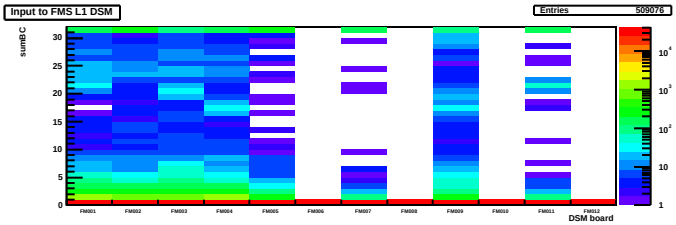

Input to FMS L0 DSM
Entries 43654
HT ID - simulated
QT board

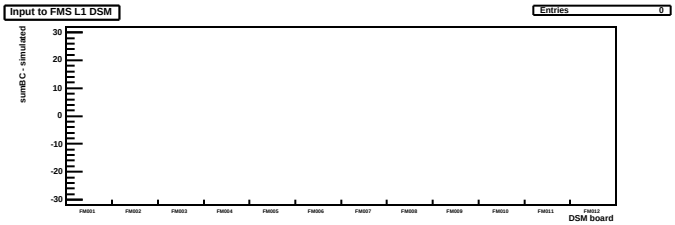

Input to FMS L1 DSM
Entries 509076
sumD
DSM board

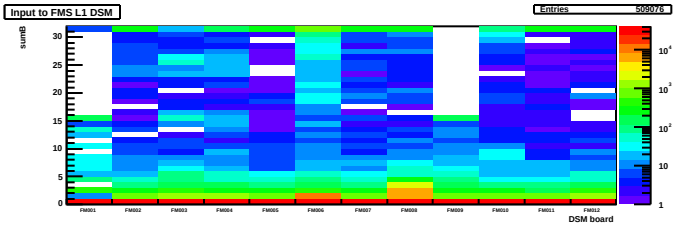

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

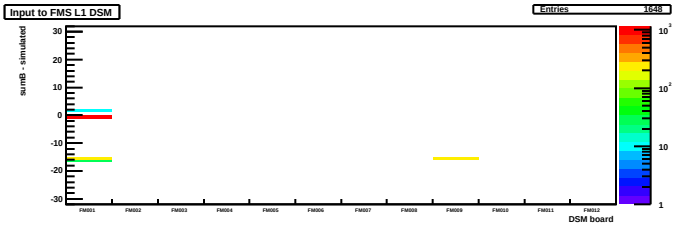

Input to FMS L1 DSM
Entries 509076
sumC
DSM board

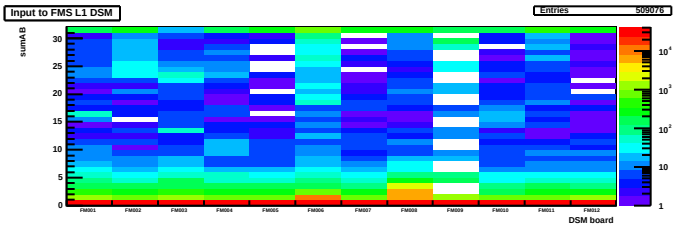

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

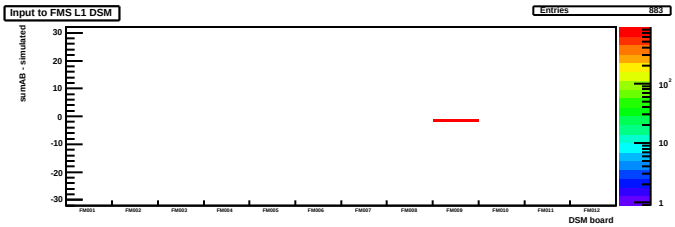

Input to FMS L1 DSM
Entries 509076
sumBC
DSM board

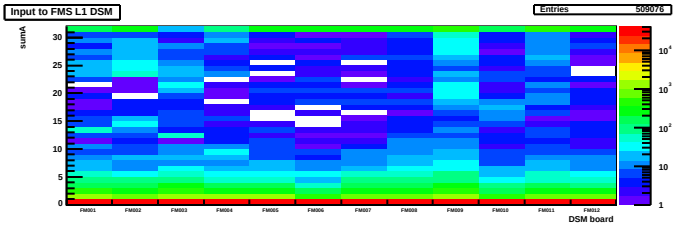

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

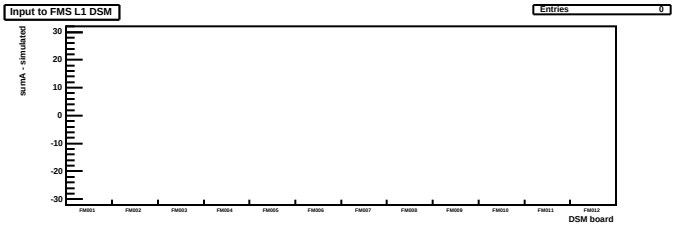

Input to FMS L1 DSM
Entries 509076
sumB
DSM board

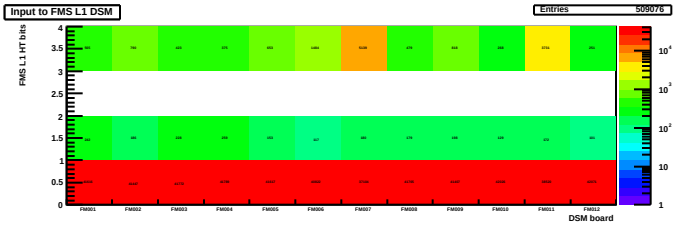

Input to FMS L1 DSM
Entries 1648
sumB - simulated
DSM board

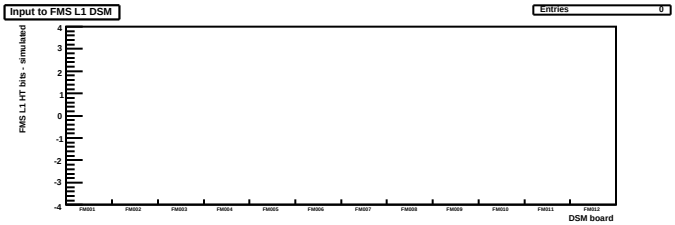

Input to FMS L1 DSM
Entries 509076
sumAB
DSM board

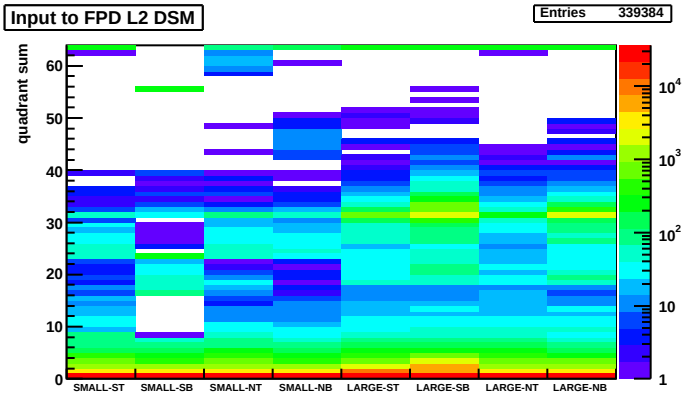

Input to FMS L1 DSM
Entries 883
sumAB - simulated
DSM board

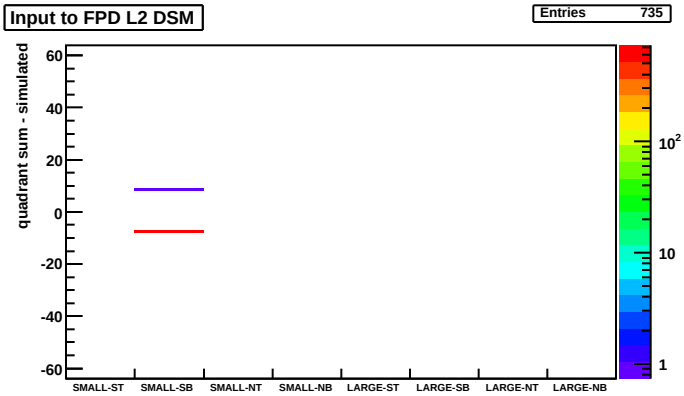

Input to FMS L1 DSM
Entries 509076
sumA
DSM board

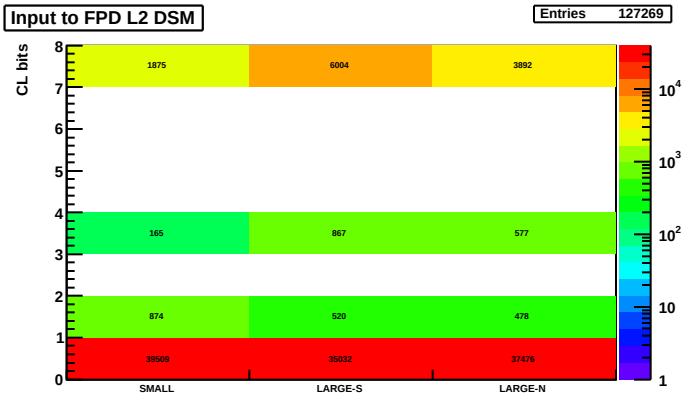

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

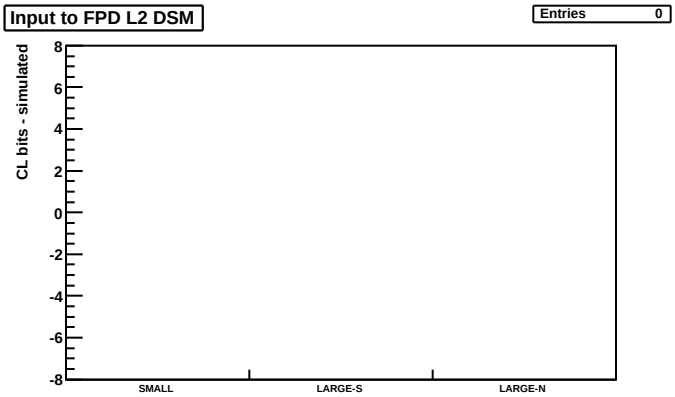

Input to FMS L1 DSM
Entries 509076
FMS L1 HT bits
DSM board

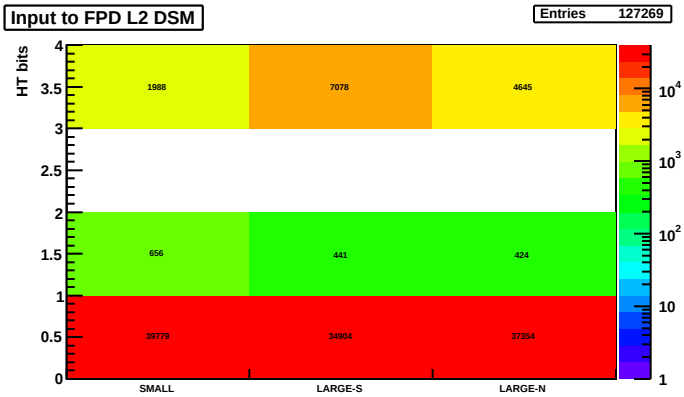

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

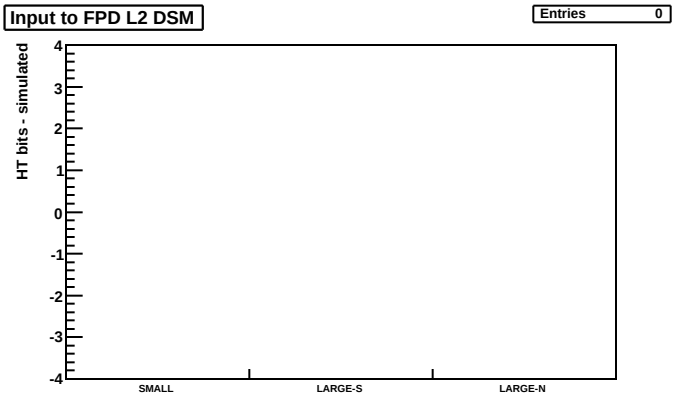

Input to FPD L2 DSM
Entries 339384
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

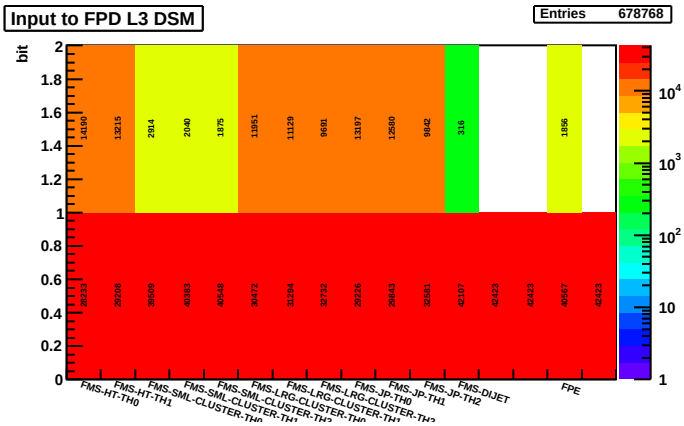

Input to FPD L2 DSM
Entries 735
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

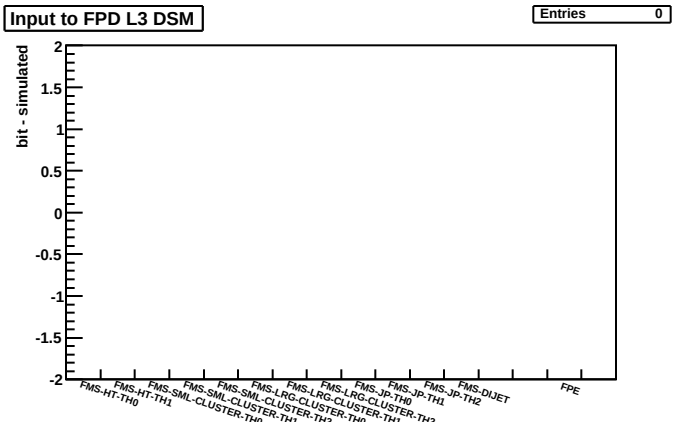

Input to FPD L2 DSM
Entries 127269
CL bits
1875
6004
3892
165
867
577
874
520
478
39509
35032
37476
SMALL
LARGE-S
LARGE-N

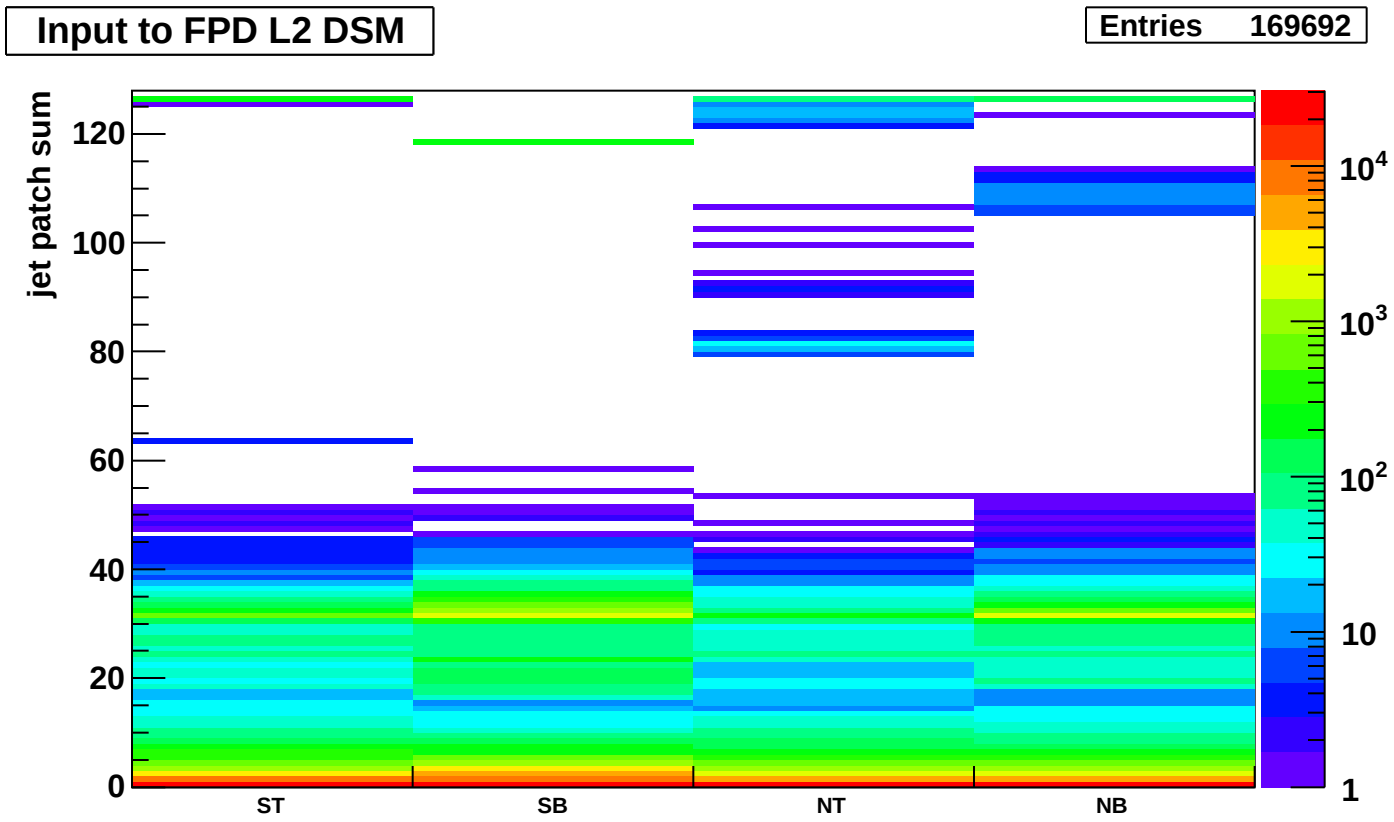

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

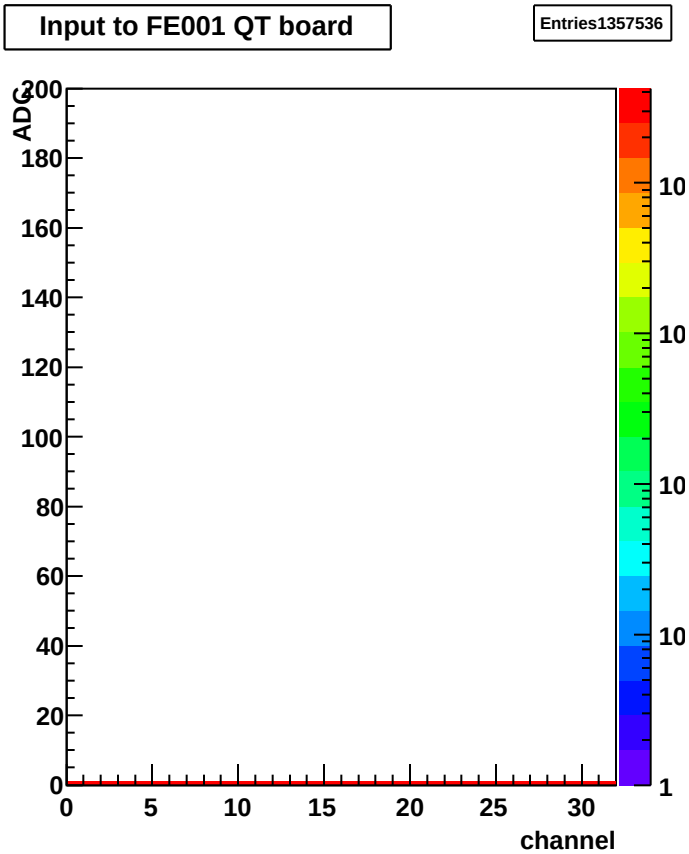

Input to FPD L2 DSM
Entries 127269
HT bits
1988
7078
4645
656
441
424
39779
34904
37354
SMALL
LARGE-S
LARGE-N

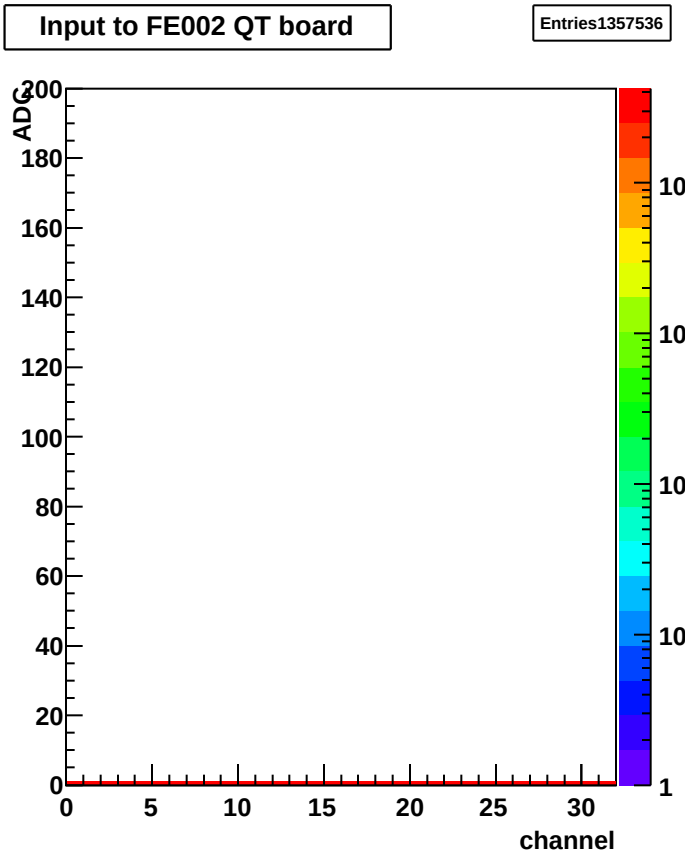

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

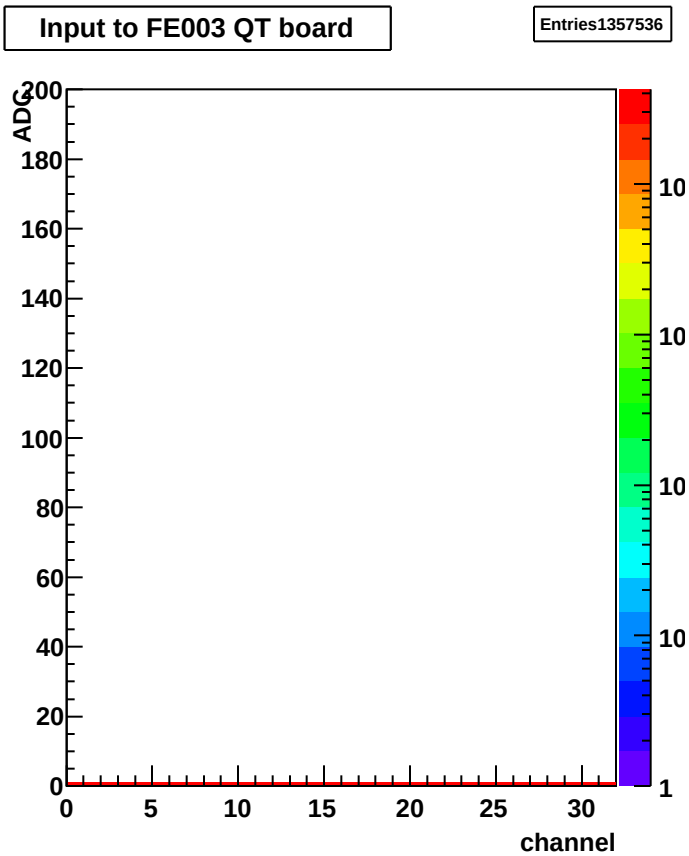

Input to FPD L3 DSM
Entries 678768
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

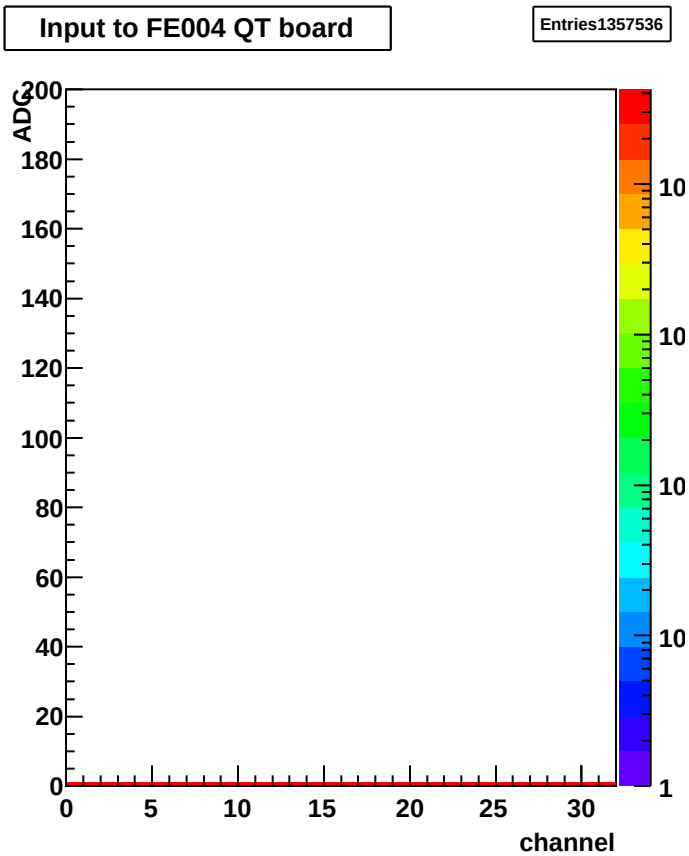

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

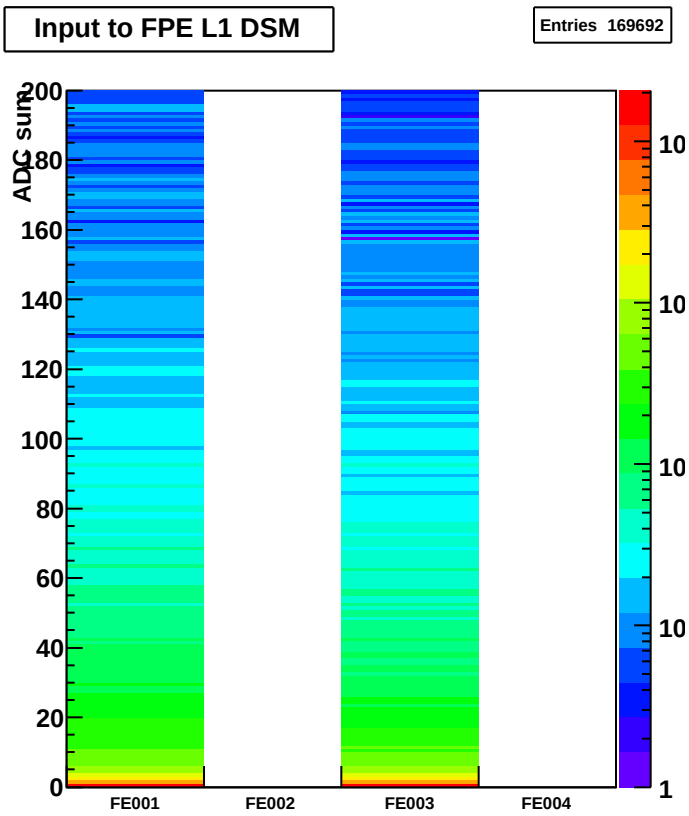

Input to FPD L2 DSM
Entries 169692
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

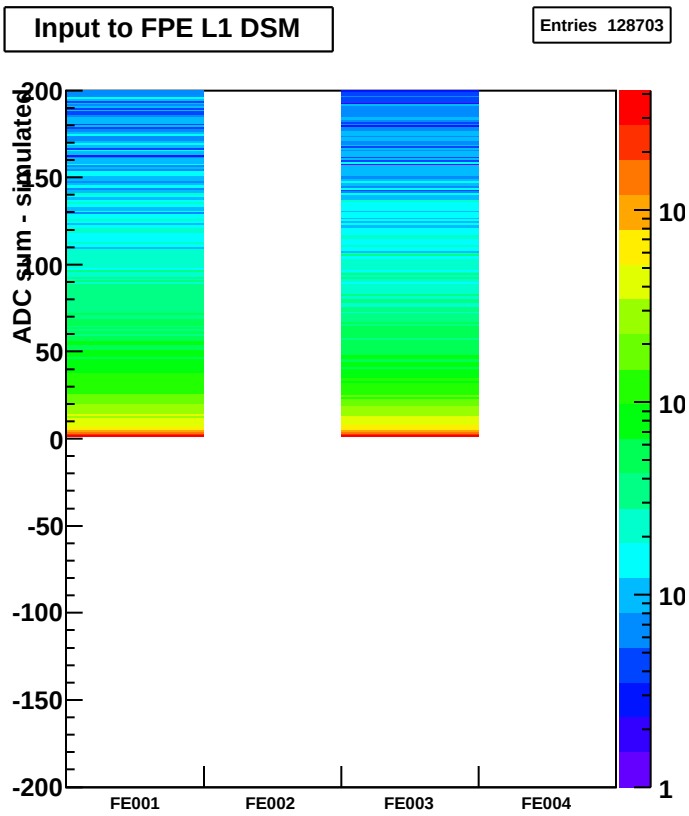

Input to FE001 QT board
Entries1357536
ADC
channel

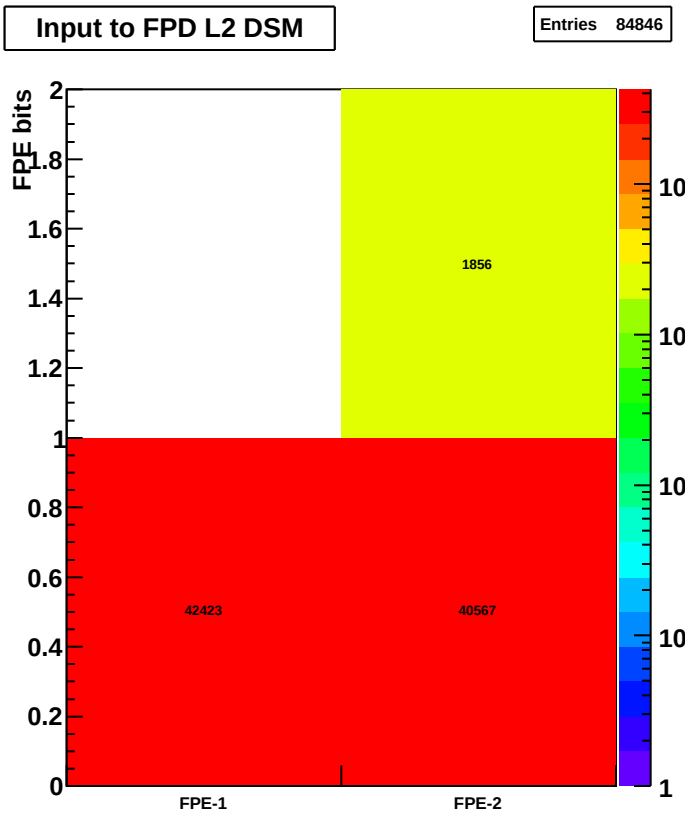

Input to FE002 QT board
Entries1357536
ADC
channel

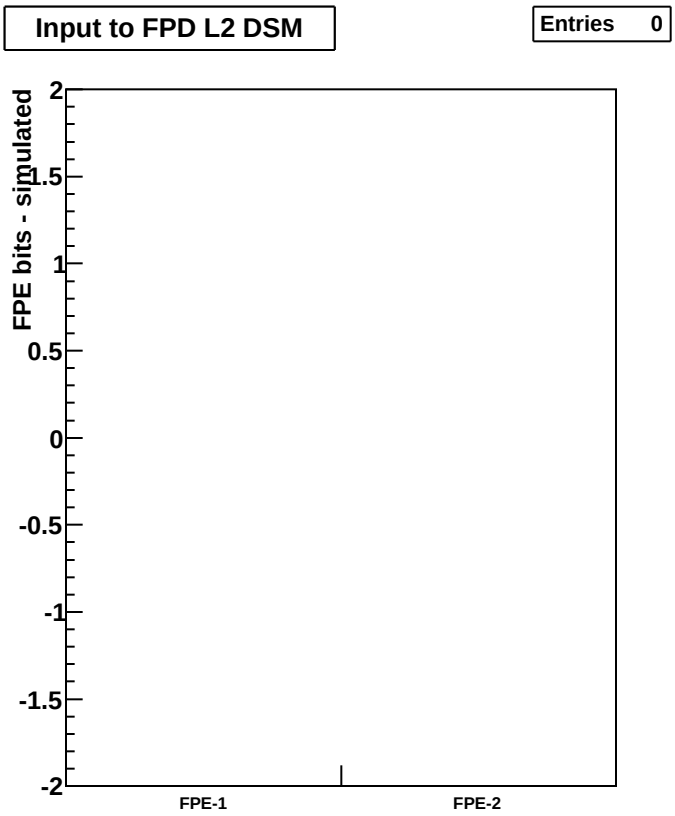

Input to FE003 QT board
Entries1357536
ADC
channel


Input to FE004 QT board
Entries1357536
ADC
channel


Input to FPE L1 DSM
Entries 169692
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 128703
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 84846
FPE bits
1856
42423
40567
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2